The DDC-I Ada Compiler System (DACS*)

- DACS*-80x86, DACS-Pentium® Processor, DACS-PC for Windows* NT, DACS-PC/LynxOS, DACS-PC/UNIX, DACS-PC/Bare, DACS-95
- Support for Ada-83 and Ada-95
- Compact, Efficient ROMable Code
- Advanced Symbolic Ada Debugger
- Easily Configurable to Hardware
- Stack Size Analysis Tool
- Supports Rate Monotonic Scheduling
- Fast and Normal Interrupt Handling
- Fast Ethernet Downloading
- Selective Linking
- Bare PC Target
- FAA Certification Packages

The DDC-I Ada Cross Compiler System, DACS, has been designed to fully utilize the features of each processor, with special emphasis on the needs of true real-time embedded systems. Continuously validated since 1987, DACS are mature development systems, field proven by hundreds of applications.

DDC-I will introduce it's Ada 95 cross compiler systems, DACS-95, with validated support for the full core language, the systems programming annex, the real-time systems annex, and will feature an Ada 83 "switch". DACS-95 will include many benefits of the language revision including object-oriented programming, new system construction features supporting "top down" development, and synchronization of access to shared data.

The DACS Ada cross debugger offers a full-featured multi-windowed symbolic debugging environment with Motif-style GUI, ethernet downloading, and break one tasking. The stand-alone Ada run-time system requires no underlying kernel and

ranges from 4 to 24 Kbytes in size.

DDC-I's Native Ada Compiler Systems include PC running LynxOS, DACS-PC/LynxOS, and PC 386 running UNIX system V or AIX , DACS-PC/UNIX. They offer complete production quality Ada compiler systems including a UNIX compatible, POSIX conforming operating system for real-time applications.

DACS-PC/Bare turns your everyday PC into a bare machine for embedded development. The DDC-I run-time system circumvents DOS and offers the same real-time behavior you would expect from a single board computer. Use any personal computer as a preliminary target board to develop the greater part of any embedded application. Development teams are able to test algorithms, timing and actually code and debug the greater part of an embedded application before the actual target hardware becomes available. The result is lower development costs, reduced technical risks and faster time-to-market. DACS-PC/Bare works with 80286, 80386, 80486 and Pentium processor based PC's.

Many additional tools and options for DACS complete the environment. Tools include assembler, librarian, linker (with locate/build facility), OMF utilities, disassembler, refather tool for configuration of libraries, make tool, object map tool for ICE support, stand-alone ether and serial downloader, GUI integrator for integration of external tools, multibus II communications packages, TCP/IP, rate monotonic scheduling, FAA RTS certification support, and RTS source code.

DDC-I also provides full consultation, installation, training and custom software tailoring for applications and development environments.

HOST SYSTEMS SUPPORTED:

Cross Compilers: Sun SPARC/Solaris,
Sun SPARC/SunOS,
VAX/VMS

Native Compilers: PC/LynxOS,
PC/UNIX V,
PC/AIX
PC/Windows NT

PROCESSORS SUPPORTED:

80186, 80C186, 80C186 XL/EA/EB/EC, 80L186 EA/EB/EC, 80188, 80C188, 80C188 XL/EA/EB/EC, 80L188 EA/EB/EC, 80286, Intel386™ CXSA/CXSB, Intel386™ EX/DX, IntelDX2™/IntelDX4™, and Pentium Processors

AVAILABILITY:

DACS-80x86, DACS-Pentium Processor, DACS-PC/LynxOS, DACS-PC/UNIX and DACS-PC/Bare are all available now

DACS-95 for the Intel Architecture will be available Q3, 1997.

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