

## MICE-IIIS-80C186 In-Circuit Emulator

- 1 Mbyte, Zero Wait State Overlay
- 32K Trace With 100ns Timestamp
- Paradigm or hyperSOURCE Source-Level Interface
- Access Trace Display On-the-Fly
- Map Memory and Enable Status From Keyboard
- Trigger on Executed Instructions, Not Pre-Fetch
- Define Events on Address, Data and Status
- Small Probe Drains No Target Power

Microtek's MICE-IIIS-80C186 in-circuit emulator supports the unique advantages of the Intel 186 architecture. Sophisticated event triggering and full-speed, source-level trace speed up product development. Choose between the familiar Turbo C debugger customized by Paradigm or the SSI hyperSOURCE-186 source-level interface. Both support compilers which output the Intel OMF-86 format.

You can define bus events on address, data, status or eight logic probes. Event definitions include ranges, masks and negation. Specify logical or sequential relationships between these events to trigger data capture into the trace buffer. The system lets you trigger breakpoints on executed instructions, since the emulator discriminates between pre-fetched and executed instructions. Synchronize the emulator with instrumentation using input and output triggers. Or enable control signals, including INTx, NMI, DRQx, RESET, Preq and Hold.

Pinpoint problems with the 32K frame by 104-bit trace buffer. Access the trace without halting the processor. Select pre-, center-, or post-trigger display. Measure real-time intervals with a 100 nanosecond time stamp on every captured bus cycle. The emulator displays the full-speed target system, unlike ROM monitors which continuously single-step to display status.



Map memory in 4K segments to the target or to one megabyte of zero wait state overlay memory from the keyboard. No jumpers required. You can assign each memory segment access rights, including read/write, read-only, and guarded.

Plug into small targets with the compact probe head, which measures 1.2" x 1.2" x 0.3" and drains no power from the target. The emulator operates in stand-alone mode, with self-test on powerup.

**HOST SYSTEMS SUPPORTED:**  
IBM PC with MS-DOS 3.3 or later

**PROCESSORS SUPPORTED:**  
80186EA/XL 3V and 5V to 20 MHz,  
80186EB 5V to 16 MHz,  
80188EA/XL 3V and 5V to 20 MHz,  
80188EB 5V to 16 MHz

**AVAILABILITY:**  
Now

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For international contacts see Appendix B.

**MICROTEK**  
IN-CIRCUIT EMULATORS