# Manageability HW Sensors and Controls

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# Role of HW Sensor in Manageability

- Provide data on HW conditions
- Provide data on HW inventory
- Provide data on SW conditions
- Provide data on SW inventory
- Proactively warn of impending problems



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## **Types of Sensors**

- Cooling
  - Temperature probe
  - Fan rotation
- Power
  - Power suppy approaching failure
- Chassis
  - Intrusion detection
- Equipment status
- intel®

LAN leash (detect missing or inoperative systems)

## How Sensors tie to Mgmt App

- discussion of busses and data flow
- instrumentation (sensors and controls)
- alerts
- service provider
- mgmt apps

### **Platform Considerations**

 How sensors are wired in HW (light on platform specifics (present the concepts)



## Commonly Used Sensors

- Typical sensors (light on platform specifics)
- LM75\*
- LM78\*
- LM79\*
- AD8001
- Magic Packet\*
- Alert Pack

#### **Platform Sensors**

The following information is routed to the platform management subsystem, and corresponding events logged in the event logs on each class of systems:

		<u>Servers</u>	<u>Desktop</u>	<u>Mobile</u>
•	Voltage/Analog Sensors:			
	<ul><li>+12V, +5V, -5V, +3.3V, -12V</li></ul>	Υ	Υ	Y
	<ul><li>CPU Voltage (per CPU)</li></ul>	Y	Υ	Υ
	<ul><li>Chipset logic voltage</li></ul>	Υ	Y	Y
	<ul><li>SCSI Termination voltage</li></ul>	Υ	N	N
•	Temperature Sensors:			
	<ul><li>Baseboard Chipset Area</li></ul>	Υ	Υ	Y
	♦ I/O area	Υ	NA	NA
	<ul><li>RAID Backplane</li></ul>	Υ	NA	NA
	<ul><li>CPU (per CPU)</li></ul>	Υ	Y	Y
•	Fans:			
	<ul><li>Fan RPM (tach) low speed</li></ul>	Υ	Υ	Υ
•	Chassis:			
	<ul><li>Chassis Intrusion</li></ul>	Υ	Υ	Y
	<ul><li>Electrical Interlock Status</li></ul>	Y	Y	Y



#### **Platform Sensors**

	<u>Servers</u>	<u>Desktop</u>	<u>Mobile</u>
ECC Memory:			
<ul><li>SIMM/DIMM count/pres.</li></ul>	Υ	Υ	Υ
<ul><li>DIMM module size</li></ul>	Υ	Υ	Υ
<ul><li>Single bit</li></ul>	Υ	Υ	Υ
<ul><li>Double bit ECC errors</li></ul>	Υ	Υ	Υ
• Bus Errors:			
◆ PCI - PERR	Υ	Υ	Υ
◆ PCI - SERR	Υ	Υ	Υ
♦ EISA/ISA NMIs	Υ	Υ	Υ
Processor Monitoring:			
<ul><li>IERR (Internal Error)</li></ul>	Υ	Υ	Υ
<ul><li>Thermal Trip</li></ul>	Υ	Υ	Υ
Machine Check Exception	Υ	Υ	Υ
<ul><li>Processor Presence</li></ul>	Y	Υ	Y
<ul><li>CPU voltage ID @ CPU</li></ul>	Υ	Υ	Υ
<ul><li>BIST Failure</li></ul>	Y	Υ	Y



#### **Platform Sensors**

	<u>Servers</u>	<u>Desktop</u>	<u>Mobile</u>
<ul><li>Power Supply</li></ul>			
<ul><li>Power Supply Fan RPM</li></ul>	Υ	Υ	Υ
<ul> <li>Redundant power supplies</li> </ul>			
<ul><li>Power Supply Presence</li></ul>	Υ	N	N
<ul><li>Power Supply Fault</li></ul>	Υ	N	N
<ul><li>Fault Resilient Booting</li></ul>	Υ	N	N
<ul><li>Current Overload</li></ul>	Υ	N	N
<ul><li>Redundancy State</li></ul>	Y	N	N
<ul> <li>Hot-swap Drive Backplane</li> </ul>			
<ul><li>Drive Presence</li></ul>	Υ	N	N
<ul><li>Drive array status</li></ul>	Υ	N	N
<ul><li>Drive Power Status</li></ul>	Y	N	N
Disk Drive			
S.M.A.R.T.	Y	S	N

SMART indicates predictive failure from IDE and SCSI disk drives

Drive Array Status (Fault, Rebuilding, Identify, etc. - extracted from information set to backplane via SCSI)



### Trends/Recommendations

- Sensor interfaces will evolve
  - Abstract via SW as much as possible
- Sensor populations and types of sensors will increase
  - Plan for extensibility
- Hot plug becomes more important (ACPI, SMBIOS, USB, 1394)
  - Build self-descriptive platforms and dynamically-configurable SW