



# PIC12C671/PIC12C672

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## Errata Sheet for Rev. A0 PIC12C671/PIC12C672 Engineering Sample Silicon

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The PIC12C671/PIC12C672 (Rev A0) engineering sample parts conform functionally to the PIC12C67X data sheet (DS30561A), except for the anomaly described below.

This problem will be addressed in the next revision of the PIC12C671/PIC12C672 silicon.

- Power on Reset

In order to ensure proper device operation upon power-up, the Power-up Timer (PWRT) must be enabled.

**Clarifications/Corrections to the Data Sheet:**

In the PIC12C67X Data Sheet (DS30561A), the following clarifications and corrections should be noted.

**NONE**



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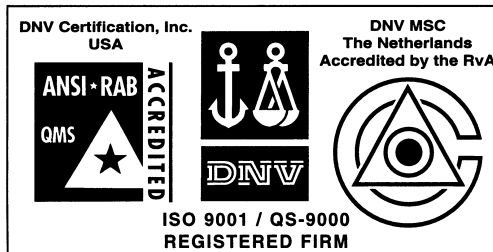
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