



PIC16CR54B

Errata Sheet for PIC16CR54B (Rev. A Silicon)

The PIC16C5X parts conform functionally to the PIC16C5X Data Sheet (DS30453B), except for the anomalies described below:

None

Clarifications/Corrections to the Data Sheet:

Section 19: Corrections for the DC characteristics Table 19.4 in the data sheet DS30453B are shown in Table 1. All other parameters are not affected in the data sheet.

PIC16CR54B

**TABLE 1: PIC16C54B/C54C/C55A/C56A/C57C/C58B-04, 20 (COMMERCIAL, INDUSTRIAL, EXTENDED)
 PIC16LC54B/LC54C/LC55A/LC56A/LC57C/LC58B-04 (COMMERCIAL, INDUSTRIAL)
 PIC16CR54B/CR54C/CR56A/CR57C/CR58B-04, 20 (COMMERCIAL, INDUSTRIAL, EXTENDED)
 PIC16LCR54B/LCR54C/LCR56A/LCR57C/LCR58B-04 (COMMERCIAL, INDUSTRIAL)**

DC Characteristics All Pins Except Power Supply Pins		Standard Operating Conditions (unless otherwise specified)				
		Operating Temperature 0°C ≤ TA ≤ +70°C (commercial) -40°C ≤ TA ≤ +85°C (industrial) -40°C ≤ TA ≤ +125°C (extended) Operating Voltage VDD range is described in Section 19.1, Section 19.2 and Section 19.3.				
Characteristic	Sym	Min	Typ ⁽¹⁾	Max	Units	Conditions
Input Low Voltage I/O Ports I/O Ports MCLR (Schmitt Trigger) T0CKI (Schmitt Trigger) OSC1 (Schmitt Trigger) OSC1	VIL	VSS VSS VSS VSS VSS	— — — — —	0.8 0.15 VDD 0.15 VDD 0.15 VDD 0.15 VDD 0.3 VDD	V V V V V —	4.5V < VDD ≤ 5.5V otherwise RC option only ⁽⁴⁾ XT, HS and LP options
Input High Voltage I/O ports MCLR (Schmitt Trigger) T0CKI (Schmitt Trigger) OSC1 (Schmitt Trigger) OSC1	VIH	2.0 0.25 VDD+0.8V 0.85 VDD 0.85 VDD 0.7 VDD	— — — — —	VDD VDD VDD VDD VDD	V V V V V	4.5V < VDD ≤ 5.5V ⁽⁵⁾ otherwise RC option only ⁽⁴⁾ XT, HS and LP options
Hysteresis of Schmitt Trigger inputs	VHYS	0.15VDD*	—	—	V	
Input Leakage Current⁽³⁾ I/O ports MCLR T0CKI OSC1	IIL	-1.0 -5.0 -3.0 -3.0	0.5 — 0.5 0.5	+1.0 +5.0 +3.0 +3.0 —	μA μA μA μA μA	For VDD ≤ 5.5V VSS ≤ VPIN ≤ VDD, Pin at hi-impedance VPIN = VSS + 0.25V ⁽²⁾ VPIN = VDD ⁽²⁾ VSS ≤ VPIN ≤ VDD VSS ≤ VPIN ≤ VDD, XT, HS and LP options
Output Low Voltage I/O ports OSC2/CLKOUT	VOL	— —	— —	0.6 0.6	V V	IOL = 8.7 mA, VDD = 4.5V IOL = 1.6 mA, VDD = 4.5V, RC option only
Output High Voltage I/O ports ⁽³⁾ OSC2/CLKOUT	VOH	VDD-0.7 VDD-0.7	— —	— —	V V	IOH = -5.4 mA, VDD = 4.5V IOH = -1.0 mA, VDD = 4.5V, RC option only

* These parameters are characterized but not tested.

- Note 1: Data in the Typical ("Typ") column is based on characterization results at 25°C. This data is for design guidance only and is not tested.
- 2: The leakage current on the MCLR/VPIN pin is strongly dependent on the applied voltage level. The specified levels represent normal operating conditions. Higher leakage current may be measured at different input voltage.
- 3: Negative current is defined as coming out of the pin.
- 4: For the RC option, the OSC1/CLKIN pin is a Schmitt Trigger input. It is not recommended that the PIC16C5X be driven with external clock in RC mode.



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