



# PIC16C712/716

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## PIC16C712/716 Errata Sheet

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The PIC16C712/716 parts you have received conform functionally to the Device Data Sheet (DS41106A), except for the anomalies described below.

**None.**

**Note:** As with any windowed EPROM device, please cover the window at all times, except when erasing.

# PIC16C712/716

## Clarifications/Corrections to the Data Sheet:

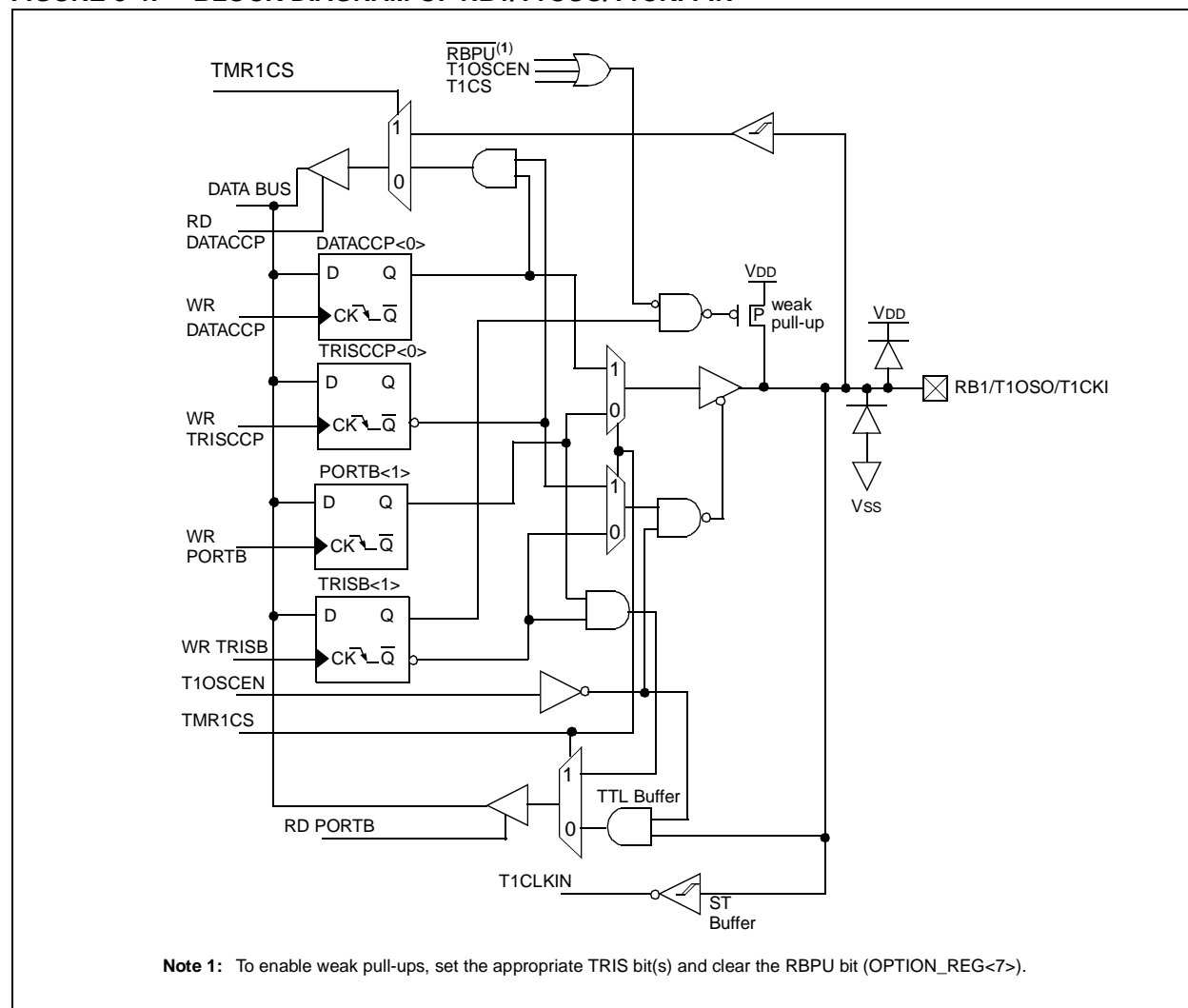
In the Device Data Sheet (DS41106A), the following clarifications and corrections should be noted.

- The following register and bit names need the following corrections:

Page	Where	Occurrence	Data Sheet Name	Correct Name
10	Figure 2-3, Address 0Fh	1	TRM1H	TMR1H
10	Figure 2-3, Address 11h	1	TRM2	TMR2
41	Table 7-2, Address 07h, bit 0	1	T1CK	DT1CK
44	Table 7-5, Control Bits column	4	TR1SCCP	TR1SCCP

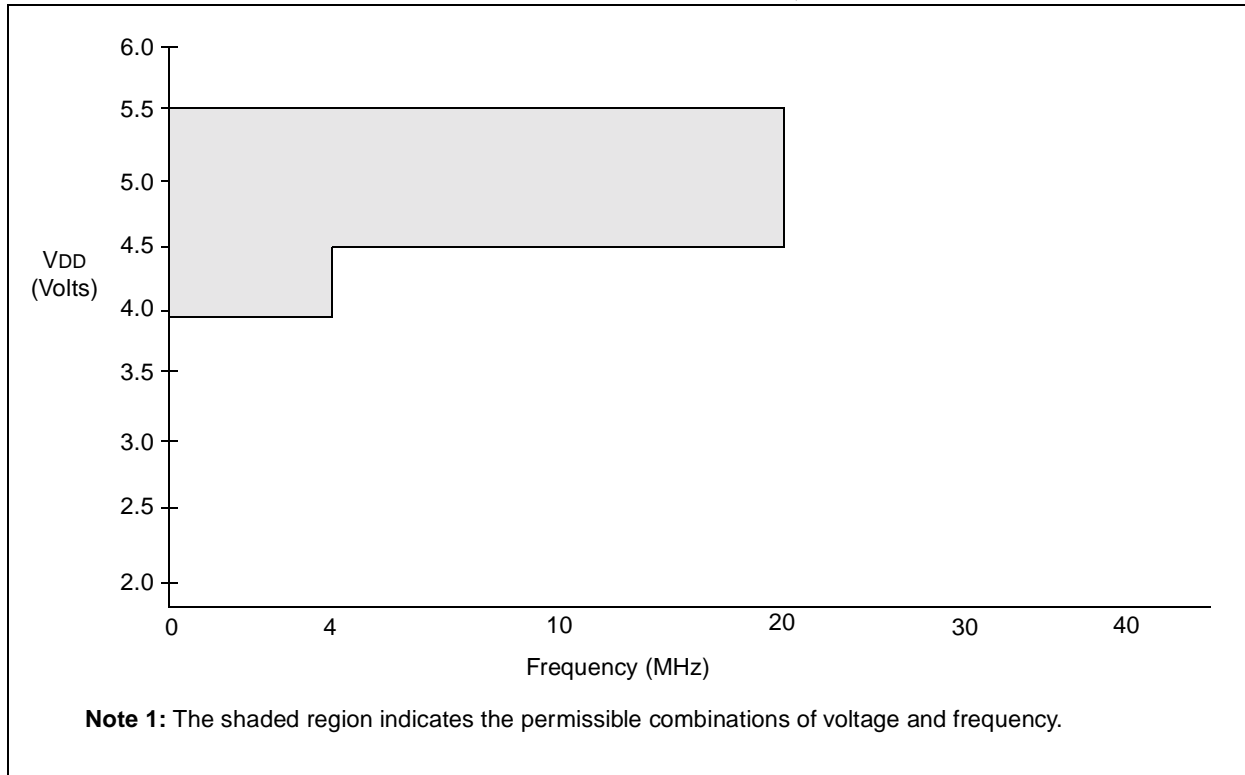
- Figure 3-4, page 24, of the Device Data Sheet (DS41106A), should show the following block diagram for RB1/T1OSO/T1CK1:

**FIGURE 3-4: BLOCK DIAGRAM OF RB1/T1OSO/T1CKI PIN**



3. Figure 12-1, page 76, of the Device Data Sheet (DS41106A) should show the following valid region of operation:

**FIGURE 12-1: PIC16C712/716 VOLTAGE-FREQUENCY GRAPH,  $-40^{\circ}\text{C} \leq T_A \leq +125^{\circ}\text{C}$**



4. Parameter No. D001, page 77, of the Device Data Sheet (DS41106A) should show the following values:

**12.1 DC Characteristics: PIC16C712/716-04 (Commercial, Industrial, Extended)  
PIC16C712/716-20 (Commercial, Industrial, Extended)**

DC CHARACTERISTICS		Standard Operating Conditions (unless otherwise stated)					
		Operating temperature $0^{\circ}\text{C} \leq T_A \leq +70^{\circ}\text{C}$ for commercial $-40^{\circ}\text{C} \leq T_A \leq +85^{\circ}\text{C}$ for industrial $-40^{\circ}\text{C} \leq T_A \leq +125^{\circ}\text{C}$ for extended					
Param No.	Sym	Characteristic	Min	Typ†	Max	Units	Conditions
D001	VDD	Supply Voltage	4.0	-	5.5	V	XT, RC and LP osc mode
D001A			4.5	-	5.5	V	HS osc mode
			VBOR*	-	5.5	V	BOR enabled (Note 7)



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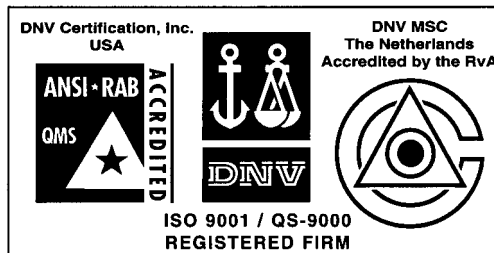
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