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## 24XX128 Rev. D2 Silicon Errata Sheet

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The 24XX128 parts you have received conform functionally to the Device Data Sheet (**DS21191**), except for the anomalies described below.

The anomalies are only found in the 24XX128 devices designed on Microchip's "150K" technology and does not affect older technology products. These 150K devices are currently available in 8-lead TSSOP packages. (The older technology parts are not available in the 8-lead TSSOP packages.)

All the problems listed here will be addressed in future revisions of the 24XX128 silicon.

### 1. Module: Memory

A read immediately after a write may provide the wrong data.

If a read of any location is performed within one second after a write of any location, the leading bit (MSb) will read as a logic "1" under the following conditions. (All three must be true):

- Serial Clock (SCL) > 200 kHz
- Device voltage (Vcc) < 4.5V
- Device Temperature < 25°C

#### Work around

Using *any* of the following work-arounds will eliminate the issue:

- Clock the device (SCL) < 200 kHz
- Increase device voltage (VCC) > 4.5V
- Wait more than one second after a write before reading.

### 2. Module: Interface Logic (I<sup>2</sup>C™)

A read from the device may give the wrong data.

If a read is performed under the following conditions, the leading bit (MSb) of the data will read as a logic "1" regardless of the actual MSb value:

- Serial Clock (SCL) > 200 kHz
- Device voltage (Vcc) < 4.5V

#### Work around

Using any one of the following work-arounds will eliminate the issue:

- Clock the device (SCL) < 200 kHz
- Increase device voltage (VCC) > 4.5V

# 24XX128

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## Clarification/Corrections to the Data Sheet

In the Device Data Sheet (DS21191), the following clarifications and corrections should be noted.

None

## REVISION HISTORY

Rev A Document

First Revision of this document.

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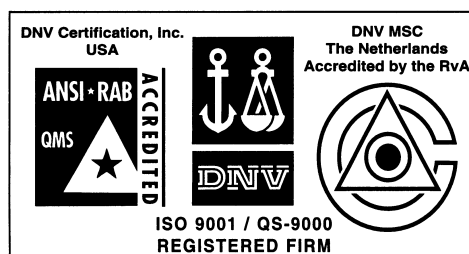
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