

DMV11
M8053 M8064

DMV11 MCTRL DIAG# 2
CVD MBAO

AH-F265A MC
FICHE 1 OF 2

MAY 1981
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A dense grid of approximately 100 small, individual diagrams or data pages, each containing technical information, likely related to the control system mentioned in the header. The diagrams are arranged in a regular grid pattern across the page.

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FICHE 2 OF 2

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.TITLE CVD MBA0 DMV11 MCTRL DIAG #2
.SBTTL PROGRAM DOCUMENT
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I D E N T I F I C A T I O N

PRODUCT CODE: AC-F264A-MC
PRODUCT NAME: CVD MBA0 DMV11 MICRO-CONTROLLER STATIC DIAGNOSTIC PART 2
PRODUCT DATE: JANUARY 1981
MAINTAINER: DIAGNOSTICS MERRIMACK CC:38P
AUTHORS: CHRIS BRIENEN
RAY MARSHALL
PURPOSE: THIS DIAGNOSTIC IS DESIGNED TO PERFORM STATIC LOGIC TESTS FOR
THE M8053 OR M8064 (HEREAFTER REFERRED TO AS THE DMV OR DMV-11)

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1.0 INTRODUCTION

THE M8053 AND M8064 ARE SINGLE-LINE SYNCHRONOUS, MICRO-PROCESSOR BASED COMMUNICATIONS INTERFACES WHICH CAN SUPPORT BOTH CHARACTER-ORIENTED (DDCMP, BSC, ETC.) AND BIT-ORIENTED (SDLC, HDLC, ETC.) PROTOCOLS. THE PURPOSE OF THIS PROGRAM IS TO PERFORM DIAGNOSTIC TESTING OF THE CSRS, RAM, AND BASIC MICRO-PROCESSOR LOGIC ON THESE BOARDS. THE FOLLOWING FUNCTIONS WILL BE PERFORMED: DMV RESIDENT U-DIAG EXECUTION CSR ADDRESSING, VIA REGISTER STATIC BIT INTERACTION AND READ/WRITE TESTING, AND ON-BOARD RAM TESTING.

THE STATIC LOGIC TESTS WILL PROVIDE EXTENSIVE TROUBLESHOOTING CAPABILITIES, SUCH AS TIGHT SCOPE LOOPS, SWITCH OPTIONS, AND ABILITY TO 'LOCK' ONTO INTERMITTENT ERRORS. IN ADDITION TESTS ARE DESIGNED AND STRUCTURED TO ACHIEVE MAXIMUM FAULT RESOLUTION AND FACILITATE REPLACEMENT OF THE SMALLEST FIELD REPLACEABLE UNIT.

THIS PROGRAM IS IMPLEMENTED USING THE DIAGNOSTIC SUPERVISOR AND A STRUCTURED PROGRAMMING APPROACH. BECAUSE THE DESIGN CONFORMS TO THE SUPERVISOR (STANDALONE VERSION) THE PROGRAM IS COMPATIBLE WITH ACT, APT, XXDP+, AND SLIDE.

THROUGH DIALOGUE WITH THE OPERATOR, THE PROGRAM ALLOWS MODIFICATION OF DEVICE PARAMETERS, SUCH AS LSI-BUS ADDRESS, VECTOR ADDRESSES AND DEVICE PRIORITY. IN ADDITION, THE OPERATOR CAN SPECIFY PARTICULAR TESTS TO BE RUN AND A VARIETY OF LOOPING, RUNNING, AND REPORTING MODES.

DEVICE ERRORS WILL BE REPORTED AS THEY OCCUR. THE REPORT WILL INCLUDE A TEST NUMBER AND DESCRIPTION OF THE ERROR, GOOD AND BAD TEST DATA, AND APPLICABLE DEVICE REGISTER CONTENTS.

2.0 HARDWARE REQUIREMENTS

THE FOLLOWING HARDWARE IS REQUIRED TO RUN THE M8053/8064 STATIC LOGIC TESTS:

PDP-11/03 OR PDP-11/23
16K WORDS OF MEMORY
CONSOLE TERMINAL
M8053 OR M8064 COMMUNICATIONS INTERFACE

3.0 PRELIMINARY PROGRAM REQUIREMENTS

THIS PROGRAM (CVDMB) SHOULD BE THE SECOND OF THE FIVE DMV-11 STATIC DIAGNOSTICS TO BE RUN (CVDMA SHOULD BE RUN FIRST). ERRORS FOUND IN THIS PROGRAM SHOULD BE CORRECTED BEFORE RUNNING ANY OF THE LINE UNIT DIAGNOSTICS (CVDMC, CVDMD, OR CVDME).

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4.0 GENERAL PROGRAM CONSIDERATIONS

4.1 DIAGNOSTIC SUPERVISOR

THIS PROGRAM IS COMPATIBLE WITH THE STANDALONE DIAGNOSTIC SUPERVISOR, AND MUST BE LOADED TO BE CO-RESIDENT WITH THE SUPERVISOR, OR BE PREVIOUSLY COMBINED WITH THE SUPERVISOR AND LOADED AS A SINGLE FILE. IN EITHER CASE, THE COMBINED PROGRAM WILL NOT EXCEED 16K OF MEMORY.

4.2 EXECUTION TIME

THE MAXIMUM TIME REQUIRED TO RUN THIS PROGRAM IS ABOUT ONE MINUTE PER PASS FOR EACH UNIT.

4.3 XXDP+

THIS PROGRAM MAY BE LOADED UNDER XXDP+, AND MAY BE RUN IN DUMP MODE OR CHAIN MODE.

4.4 ACT/SLIDE

THIS PROGRAM MAY BE LOADED UNDER ACT OR SLIDE AND MAY BE RUN IN DUMP MODE OR CHAIN MODE.

4.5 APT

THIS PROGRAM MAY BE LOADED BY THE APT SYSTEM (INCLUDING APT-RD) AND RUN IN PROGRAM MODE OR SCRIPT MODE.

4.6 MEMORY MANAGEMENT

MEMORY MANAGEMENT IS UTILIZED IN THIS PROGRAM TO VERIFY THE DMV-11'S ABILITY TO NPR INTO (AND OUT OF) EXTENDED MEMORY.

4.7 ERROR LOGGING

AT THE END OF EACH PASS ON ALL UNITS, THE PROGRAM PRINTS OUT THE CUMULATIVE TOTAL NUMBER OF ERRORS SINCE THE LAST START OR RESTART COMMAND.

5.0 PROGRAM LOAD MEDIA

THIS PROGRAM CAN BE LOADED FROM PAPER TAPE USING THE ABSOLUTE LOADER OR FROM ACT, SLIDE, OR APT SYSTEMS, OR FROM ANY MEDIA SUPPORTED BY XXDP+. WHEN USING THE PAPER TAPE ABSOLUTE LOADER, THE PROGRAM SHOULD BE LOADED FIRST.

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FOLLOWED BY THE DIAGNOSTIC SUPERVISOR. WHEN USING XXDP+, THE DIAGNOSTIC SUPERVISOR SHOULD BE LOADED FIRST, FOLLOWED BY THE DIAGNOSTIC PROGRAM.

6.0 OPERATING INSTRUCTIONS

6.1 LOADING AND STARTING PROCEDURES

6.1.1 LOADING PROCEDURES

THIS PROGRAM MAY BE LOADED FROM PAPER TAPE USING THE ABSOLUTE LOADER. IT MAY ALSO BE LOADED FROM ANY XXDP+ LOAD MEDIA. WHEN LOADED UNDER XXDP+, THE DIAGNOSTIC SUPERVISOR WILL BE LOADED AUTOMATICALLY.

6.1.2 STARTING PROCEDURES

THE PROGRAM STARTS AT LOCATION 200. USE STANDARD DEC PROCEDURES TO START THE PROGRAM.

6.1.3 STEPS FOR QUICK AND SIMPLE EXECUTION

THE DIAGNOSTIC CAN BE EXECUTED STANDALONE UNDER XXDP+, WITHOUT READING THE REMAINDER OF THIS DOCUMENT, AS FOLLOWS:

- A) LOAD AND START DIAGNOSTIC USING RUN COMMAND
- B) RECEIVE DIAGNOSTIC SUPERVISOR IDENTIFICATION AND PROMPT (DRS-C>)
- C) ENTER STA<CR>
- D) ANSWER HARDWARE AND SOFTWARE QUESTIONS
- E) GET END OF PASS MESSAGES OR ERROR MESSAGES
- F) TO END EXECUTION, ENTER CONTROL/C

6.2 INITIAL DIALOGUE

AFTER THE PROGRAM AND THE SUPERVISOR ARE LOADED AND THE PROGRAM IS STARTED, THE FOLLOWING IDENTIFICATION IS TYPED :

```
DRS LOADED
DIAG. RUN-TIME SERVICES
CVDMA-A-0
DMV-11 U-CONTRL LOGIC DIAG - PART 2 OF 2
UNIT IS M8053 OR M8064
DR>
```

THE OPERATOR THEN PROCEEDS BY TYPING ONE OR MORE OF THE COMMANDS DESCRIBED IN THE FOLLOWING SECTION 6.3. (FOR MORE DETAILED INFORMATION, REFER TO THE DIAGNOSTIC SUPERVISOR FUNCTIONAL SPECIFICATION).

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6.3 PROGRAM OPTIONS

6.3.1 START COMMAND

```
*****
STA(RT)/TESTS:<TEST-LIST>/PASS:<PASS-CNT>/FLAGS:
<FLAG-LIST>/EOP:<INCR>
*****
```

6.3.1.1 TESTS SWITCH (/TESTS:<TEST-LIST>)

<TEST-LIST> IS A SEQUENCE OF DECIMAL NUMBERS (1:2 ETC.) OR RANGES OF DECIMAL NUMBERS (1-5:8-10 ETC.) THAT SPECIFY THE TESTS TO BE EXECUTED. THE NUMBERS ARE SEPARATED BY COLONS. THE NUMBERS RANGE FROM 1 TO THE LARGEST TEST NUMBER IN THE DIAGNOSTIC. THEY MAY BE SPECIFIED IN ANY ORDER. TESTS WILL BE EXECUTED IN NUMERICAL ORDER REGARDLESS OF THE ORDER OF SPECIFICATION. THE DEFAULT IS TO EXECUTE ALL TESTS. ON THIS AND ALL SWITCHES, THE ANGLE BRACKETS <> ARE PUNCTUATION USED IN THE DEFINITION ONLY, AND ARE NOT TO BE TYPED BY THE OPERATOR. SEE EXAMPLE AT END OF 6.3.1.5.

6.3.1.2 PASS SWITCH (/PASS:<PASS-CNT>)

<PASS-CNT> IS A DECIMAL NUMBER INDICATING THE DESIRED NUMBER OF PASSES. A PASS IS DEFINED AS THE EXECUTION OF THE FULL DIAGNOSTIC (ALL SELECTED TESTS) AGAINST ALL UNITS SUBMITTED. THE DEFAULT IS NON-ENDING EXECUTION. IN THIS CASE EXIT FROM THE PROGRAM IS ACCOMPLISHED EITHER BY TYPING A CONTROL/C OR BY OCCURANCE OF AN ERROR WITH THE HALT ON ERROR FLAG BEING SET. THE EXIT IS A RETURN TO COMMAND MODE. SEE EXAMPLE AT END OF 6.3.1.5.

6.3.1.3 FLAGS SWITCH (/FLAGS:<FLAG-LIST>)

<FLAG-LIST> IS A SEQUENCE OF ELEMENTS OF THE FORM <FLAG>, <FLAG=1>, OR <FLAG=0>, SEPARATED BY COLONS, WHERE <FLAG> HAS ONE OF THE FOLLOWING VALUES:

HOE	HALT ON ERROR, CAUSING COMMAND MODE TO BE ENTERED WHEN AN ERROR IS ENCOUNTERED
LOE	LOOP ON ERROR, CAUSING THE DIAGNOSTIC TO LOOP CONTINUOUSLY WITHIN THE SMALLEST DEFINED BLOCK OF CODING (SEGMENT, SUBTEST, OR TEST) CONTAINING THE ERROR
IER	INHIBIT ERROR REPORTING
IBE	INHIBIT BASIC ERROR REPORTS
IXE	INHIBIT EXTENDED ERROR REPORTS
PRI	DIRECT ALL MESSAGES TO A LINE PRINTER
PNT	PRINT NUMBER OF TEST BEING EXECUTED
BOE	BELL ON ERROR
UAM	RUN IN UNATTENDED MODE, BYPASSING MANUAL

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326 INTERVENTION TESTS
 327 ISR INHIBIT STATISTICAL REPORTS
 328 IDU INHIBIT DROPPING OF UNITS BY DIAGNOSTIC
 329 LOT LOOP ON TEST
 330

331 THE FLAGS NAMED OR EQUATED TO 1 ARE SET, THOSE EQUATED TO 0
 332 ARE CLEARED. A FLAG NOT SPECIFIED IS CLEARED. IF THE FLAGS
 333 SWITCH IS NOT GIVEN ALL FLAGS ARE CLEARED. SEE EXAMPLE AT
 334 END OF 6.3.1.5.
 335

6.3.1.4 END OF PASS SWITCH (/EOP:<INCR>)

336
 337
 338
 339 <INCR> IS A DECIMAL NUMBER INDICATING HOW OFTEN (IN TERMS OF
 340 PASSES) IT IS DESIRED THAT THE END OF PASS MESSAGE BE
 341 PRINTED. THE DEFAULT IS AT THE END OF EVERY PASS. SEE
 342 EXAMPLE AT END OF 6.3.1.5.
 343

6.3.1.5 EFFECT OF START COMMAND

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 347
 348 THE EFFECT OF THE START COMMAND IS TO INITIATE THE HARDWARE
 349 PARAMETER DIALOGUE, THE SOFTWARE PARAMETER DIALOGUE, AND
 350 THEN THE DIAGNOSTIC TESTS THEMSELVES.

351
 352 THE HARDWARE PARAMETER DIALOGUE COMMENCES WITH THE QUESTION
 353 '# UNITS?' TO WHICH THE OPERATOR REPLIES WITH A DECIMAL
 354 NUMBER N FROM 1 TO 16. THE TERM 'UNIT' REFERS TO THE DEVICE
 355 TO WHICH THIS SERIES OF DIAGNOSTICS IS DEDICATED. FOLLOWING
 356 THIS ARE THE QUESTIONS WHEREBY THE P-TABLES THEMSELVES WILL
 357 BE BUILT. EACH P-TABLE IS A CORE-RESIDENT TABLE CONTAINING
 358 ALL THE HARDWARE INFORMATION FOR ONE UNIT. THE OPERATOR
 359 MUST SUPPLY N (NUMBER OF UNITS) VALUES FOR EACH QUESTION.
 360 HE MAY DO THIS BY GIVING ONE ANSWER TO EACH QUESTION (IN
 361 WHICH CASE THE SERIES OF QUESTIONS WILL BE POSED N TIMES) OR
 362 BY GIVING N VALUES, SEPARATED BY COMMAS, TO EACH QUESTION
 363 (SERIES WILL BE POSED ONCE). EACH QUESTION IS FOLLOWED BY
 364 THE RESPONSE RADIX (D FOR DECIMAL, B FOR BINARY, O FOR
 365 OCTAL, L FOR YES/NO) IN PARENTHESES AND THE DEFAULT VALUE
 366 AFTER THE PARENTHESES.

367
 368 FOLLOWING THE HARDWARE QUESTIONS ARE THE SOFTWARE QUESTIONS
 369 TO BUILD THE SOFTWARE TABLES, WHICH DEFINE THE MODE (QUICK
 370 VERIFY ETC.) THAT THE DIAGNOSTIC WILL EXECUTE IN.

371
 372 WHEN THE QUESTION '# UNITS?' IS ANSWERED, MEMORY STORAGE IS
 373 ALLOCATED FOR THE P-TABLES, AND IF THERE IS NOT ENOUGH TO
 374 ACCOMMODATE THEM THE MESSAGE 'TOO MANY UNITS' IS ISSUED. IN
 375 THIS CASE THE DIAGNOSTIC MUST BE EXECUTED MORE THAN ONCE TO
 376 TEST ALL UNITS.

EXAMPLE:

377
 378 STA/TESTS:1:2-4:6:8-10/PASS:3/FLAGS:IER:MOE=1:UAM:LOE
 379

380
 381 THIS COMMAND WILL CAUSE THREE PASSES TO BE MADE, EACH PASS

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CONSISTING OF TESTS 1,2,3,4,6,8,9, AND 10 EXECUTED AGAINST ALL UNITS. THERE IS NO DIFFERENCE BETWEEN SAYING <FLAG> AND SAYING <FLAG=1>. THE NOTATION <FLAG=0> IS MEANINGFUL ONLY ON A COMMAND OTHER THAN START TO CLEAR A FLAG THAT WAS PREVIOUSLY SET. NOTE THAT ON ALL COMMANDS ONLY THE FIRST THREE LETTERS ARE SCANNED.

6.3.2 RESTART COMMAND

```
*****
RES(TART)/TESTS:<TEST-LIST>/PASS:<PASS-CNT>/FLAGS:
<FLAG-LIST>/UNITS:<UNIT-LIST>
*****
```

6.3.2.1 TESTS, PASS, AND FLAGS SWITCHES

<TEST-LIST>, <PASS-CNT>, AND <FLAG-LIST> ARE AS IN THE START COMMAND.

6.3.2.2 UNITS SWITCH (/UNITS:<UNIT-LIST>)

<UNIT-LIST> IS A SEQUENCE OF DECIMAL NUMBERS (0,1 ETC.) OR RANGES OF DECIMAL NUMBERS (0-5, 8-10 ETC.) THAT SPECIFY THE UNITS TO BE TESTED. THE NUMBERS ARE SEPARATED BY COLONS. THE NUMBERS MAY RANGE FROM 0 THRU N-1 (N IS THE NUMBER OF UNITS SPECIFIED IN THE PREVIOUS START COMMAND). THE NUMBER INDICATES THE POSITION OF THE P-TABLE AS THE DATA WAS ENTERED DURING THE HARDWARE DIALOGUE. THE UNITS WHICH ARE SELECTED MUST NOT HAVE BEEN DROPPED BY THE DROP COMMAND. SEE THE DISCUSSION OF ADD AND DROP COMMANDS BELOW. DEFAULT IS TO TEST ALL UNITS WHICH HAVE NOT BEEN DROPPED BY A DROP COMMAND.

6.3.2.3 EFFECT OF RESTART COMMAND

THE RESTART COMMAND DIFFERS FROM THE START COMMAND IN THAT THE P-TABLES FROM THE PREVIOUS START COMMAND (THERE MUST HAVE BEEN ONE) ARE USED, INSTEAD OF NEW ONES BEING BUILT. THE UNITS SWITCH GIVES THE ABILITY TO SELECT A SUBSET OF THESE. THE SOFTWARE DIALOGUE MAY OPTIONALLY BE REEXECUTED (OPERATOR WILL BE ASKED). THE COMMAND CAN BE USED AFTER COMMAND MODE HAS BEEN REENTERED IN ANY OF THE THREE NORMAL WAYS: A) THE REQUESTED NUMBER OF PASSES HAVE BEEN MADE B) AN ERROR WAS ENCOUNTERED WITH THE HALT ON ERROR FLAG SET C) A CONTROL/C WAS ENTERED BY THE OPERATOR.

6.3.3 CONTINUE COMMAND

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*****
CON(TINUE)/PASS:<PASS-CNT>/FLAGS:<FLAG-LIST>
*****
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6.3.3.1 PASS SWITCH (/PASS:<PASS-CNT>)

<PASS-CNT> IS SAME AS IN START COMMAND, BUT THE DEFAULT IS THE UNSATISFIED PASS-CNT FROM THE PREVIOUS START OR RESTART. IF NONE REMAINS, THE DEFAULT IS NON-ENDING EXECUTION.

6.3.3.2 FLAG SWITCH (/FLAGS:<FLAG-LIST>)

<FLAG-LIST> IS SAME AS IN START COMMAND, BUT UNSPECIFIED FLAGS RETAIN THEIR CURRENT VALUE.

6.3.3.3 EFFECT OF CONTINUE COMMAND

CONTINUE MUST FOLLOW A START OR RESTART, AND COMMAND MODE MUST HAVE BEEN ENTERED DUE TO A HALT ON ERROR OR A CONTROL/C. THE EFFECT OF THE COMMAND IS TO GO TO THE BEGINNING OF THE TEST THAT WAS BEING EXECUTED WHEN THE HALT OR CONTROL/C TOOK PLACE. SOFTWARE DIALOGUE MAY OPTIONALLY BE REEXECUTED. HARDWARE PARAMETERS MAY NOT BE CHANGED.

6.3.4 PROCEED COMMAND

PRO(CEED)/FLAGS:<FLAG-LIST>

6.3.4.1 FLAGS SWITCH (/FLAGS:<FLAG-LIST>)

<FLAG-LIST> IS AS IN THE START COMMAND, BUT UNSPECIFIED FLAGS RETAIN THEIR CURRENT VALUE.

6.3.4.2 EFFECT OF PROCEED COMMAND

PROCEED MUST FOLLOW A START, RESTART, OR CONTINUE. COMMAND MODE MUST HAVE BEEN ENTERED VIA A HALT ON ERROR. THE EFFECT OF THE COMMAND IS TO BEGIN EXECUTION AT THE LOCATION FOLLOWING THE ERROR CALL. NEITHER HARDWARE NOR SOFTWARE PARAMETERS MAY BE ALTERED.

6.3.5 ADD COMMAND

ADD/UNITS:<UNIT-LIST>

6.3.5.1 UNITS SWITCH (/UNITS:<UNIT-LIST>)

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<UNIT-LIST> IS AS IN THE RESTART COMMAND.

6.3.5.2 EFFECT OF ADD COMMAND

THE UNITS SPECIFIED ARE ADDED TO THE TEST SEQUENCE. EACH UNIT MUST HAVE A P-TABLE IN MEMORY DUE TO AN EARLIER HARDWARE DIALOGUE. THIS COMMAND MUST BE FOLLOWED BY A RESTART OR CONTINUE. THE UNITS SWITCH MUST BE SPECIFIED. THE ADD COMMAND IS MEANINGFUL ONLY FOR UNITS THAT WERE PREVIOUSLY DROPPED.

6.3.6 DROP COMMAND

DRO(P)/UNITS:<UNIT-LIST>

6.3.6.1 UNITS SWITCH (/UNITS:<UNIT-LIST>)

<UNIT-LIST> IS AS IN THE RESTART COMMAND.

6.3.6.2 EFFECT OF DROP COMMAND

THE UNITS SPECIFIED WILL BE DROPPED FROM TESTING. THE UNITS WILL BE RESELECTED ONLY BY THE EXECUTION OF AN ADD OR START COMMAND. THE UNITS SWITCH MUST BE ENTERED. THIS COMMAND MUST BE FOLLOWED BY A RESTART OR A CONTINUE COMMAND.

6.3.7 PRINT COMMAND

PRI(NT)

6.3.7.1 EFFECT OF PRINT COMMAND

THE TOTAL NUMBER OF ERRORS FOR EACH UNIT SINCE THE LAST START OR RESTART COMMAND ARE PRINTED. THE ISR (INHIBIT STATISTICAL REPORTING) FLAG IS CLEARED.

6.3.8 DISPLAY COMMAND

DIS(PLAY)/UNITS:<UNIT-LIST>

6.3.8.1 UNITS SWITCH (/UNITS:<UNIT-LIST>)

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<UNIT-LIST> IS AS IN THE RESTART COMMAND

6.3.8.2 EFFECT OF DISPLAY COMMAND

THE HARDWARE P-TABLES FOR ALL UNITS UNDER TEST ARE PRINTED OUT IN THE FORMAT IN WHICH THEY WERE ENTERED. ANY UNITS THAT WERE DROPPED BY THE OPERATOR 'DROP' COMMAND ARE SO DESIGNATED.

6.3.9 FLAGS COMMAND

FLA(GS)

6.3.9.1 EFFECT OF FLAGS COMMAND

THE CURRENT SETTINGS OF ALL FLAGS ARE PRINTED.

6.3.10 ZFLAGS COMMAND

ZFL(AGS)

6.3.10.1 EFFECT OF ZFLAGS COMMAND

ALL FLAGS ARE CLEARED.

6.3.11 CONTROL CHARACTERS

A CONTROL C (C) ENTERED DURING THE EXECUTION OF A DIAGNOSTIC CAUSES A RETURN TO COMMAND MODE.

A CONTROL Z (Z) ENTERED DURING ONE OF THE THREE OPERATOR DIALOGUES- HARD CORE QUESTIONS (SEE 6.2), HARDWARE DIALOGUE (SEE 6.3.1.5), OR SOFTWARE DIALOGUE (SEE 6.3.1.5) CAUSES THE DEFAULTS TO BE TAKEN FOR THE REMAINDER OF THAT DIALOGUE.

A CONTROL O (O) ENTERED DURING THE EXECUTION OF A DIAGNOSTIC CAUSES ALL TELETYPE OUTPUT TO BE SUPPRESSED FOR THE REMAINDER OF THE DIAGNOSTIC OR UNTIL ANOTHER O IS TYPED, WHICH RESTORES NORMAL TELETYPE OUTPUT.

6.3.12 HARDWARE PARAMETERS

THE FOLLOWING 3 QUESTIONS WILL BE ASKED ON A START COMMAND. THE VALUE LOCATED TO THE LEFT OF THE QUESTION MARK IS THE DEFAULT VALUE THAT WILL BE TAKEN ON A CARRIAGE RETURN

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PROGRAM DOCUMENT

RESPONSE.

1. DEVICE CSR ADDRESS : (0) 160020?

THIS IS THE ADDRESS AT WHICH THE CSR REGISTERS (SEL0) RESIDE ON THE LSI-BUS. THE ALLOWABLE RANGE IS 160020-177760 (OCTAL), AND THE DEFAULT VALUE IS 160020.

2. DEVICE VECTOR ADDRESS : (0) 300 ?

THIS IS THE ADDRESS OF THE INPUT INTERRUPT VECTOR FOR THIS DEVICE. THE ALLOWABLE RANGE IS 000-674 (OCTAL), AND THE DEFAULT VALUE IS 300.

3. DEVICE PRIORITY LEVEL : (0) 4 ?

THIS IS THE CPU PRIORITY AT WHICH THE INTERRUPT HANDLERS OF THIS DEVICE WILL BE EXECUTED. THE ALLOWABLE RANGE IS 0-7, AND THE DEFAULT VALUE IS 4.

4. IS THE PROCESSOR STRAPPED TO MODE 0 ON POWER UP : (L) Y ?

THIS IS THE CPU'S POWER UP STRAPPING. 'MODE 0' INDICATES THAT THE PROCESSOR WILL POWER UP USING LOCATIONS 24 AND 26. IF THE ANSWER TO THIS QUESTION IS NO, TESTS WHICH USE 'DCOK' WILL BE SKIPPED.
(NOTE: MODE 0 IS SELECTED WHEN JUMPERS W5 AND W6 ARE 'REMOVED'- SEE MICROCOMPUTER PROCESSOR HANDBOOK FOR MORE INFORMATION).

5. BOARD TYPE (0=M8064, 1=M8053-V35, 2=M8053-EIA) : (0) 0 ?

THIS IS THE TYPE OF DMV-11 CURRENTLY INSTALLED. NOTE THAT THE M8053 IS SWITCH SELECTABLE BETWEEN V.35 AND EIA.

6. IS THIS A MANUFACTURING TEST STAND : (L) N ?

THIS QUESTION REFERS TO A SPECIFIC MEMORY CONFIGURATION THAT IS REQUIRED TO RUN TEST #8 (SEE SEC. 7.0).

6.3.13 SOFTWARE PARAMETERS

NO SOFTWARE PARAMETER QUESTIONS ARE ASKED BY THIS PROGRAM.

6.3.14 EXTENDED DISCUSSION OF P-TABLE DIALOGUE

THE FULL CAPABILITY OF THE HARDWARE DIALOGUE IS REVEALED BY THE FOLLOWING DISCUSSION OF WHAT HAPPENS INTERNALLY.

AS SOON AS THE QUESTION '# UNITS?' IS ANSWERED (WITH THE NUMBER N, SAY) SPACE IN CORE IS ALLOCATED FOR N P-TABLES. ALL OF THE P-TABLES ARE OF THE SAME FORMAT, AND THERE IS A ONE-TO ONE CORRESPONDENCE BETWEEN THE HARDWARE PARAMETER QUESTIONS AND THE SLOTS IN THE P-TABLE FORMAT.

ON THE FIRST TRIP THRU THE QUESTIONS, ALL OF THE SLOTS IN

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ALL OF THE P-TABLES ARE FILLED. IF THE OPERATOR TYPES IN LESS THAN N EXPLICIT VALUES IN RESPONSE TO A PARTICULAR QUESTION, THESE VALUES ARE PLACED IN THE P-TABLES (ONE VALUE GOING INTO THE PROPER SLOT OF EACH P-TABLE BEGINNING WITH THE FIRST P-TABLE) UNTIL THE STRING OF VALUES IS EXHAUSTED. THE LAST VALUE IN THE STRING BECOMES THE NEW DEFAULT AND IS USED TO FILL THAT SLOT IN THE REMAINING P-TABLES.

ON SUBSEQUENT TRIPS THRU THE QUESTIONS, THE SAME PROCESS IS CARRIED OUT, EXCEPT THAT THE EARLIEST P-TABLE NOT TO HAVE RECEIVED AN EXPLICIT VALUE IN ANY OF ITS SLOTS NOW ASSUMES THE ROLE THAT TABLE NUMBER ONE PLAYED IN THE FIRST TRIP.

THE SERIES OF QUESTIONS IS REISSUED UNTIL AT LEAST ONE QUESTION HAS RECEIVED N EXPLICIT VALUES FROM THE OPERATOR.

IN GIVING A STRING OF VALUES, COMMAS WITHOUT INTERVENING VALUES MAY BE USED TO INDICATE A REPETITION OF THE LAST NAMED VALUE.

A STRING OF VALUES MAY BE GIVEN AS A RANGE (6-10 FOR EXAMPLE). IF THE VALUES REPRESENT PURE NUMERICAL DATA, THIS SAMPLE RANGE TRANSLATES TO THE STRING 6,7,8,9,10 (AN INCREMENT OF 1). IF THE VALUES ARE ADDRESSES, THE SAMPLE RANGE TRANSLATES TO THE STRING 6,8,10 (AN INCREMENT OF 2).

NOW LET US SEE HOW WE COULD USE THESE CAPABILITIES TO CONSTRUCT A SET OF P-TABLES. ASSUME THAT WE HAVE 16 UNITS, AND THAT THERE ARE THREE HARDWARE PARAMETERS FOR EACH (THREE SLOTS IN THE P-TABLE, THREE HARDWARE QUESTIONS IN THE DIALOGUE). LET THE DESIRED VALUE FOR THE FIRST PARAMETER BE THE NUMBER 75 FOR ALL 16 TABLES. LET THE DESIRED VALUE FOR THE SECOND PARAMETER BE EQUAL TO THE UNIT NUMBER (0,1,2,...,15) EXCEPT FOR UNIT 12, WHICH SHOULD RECEIVE THE VALUE 11. LET THE DESIRED VALUE FOR THE THIRD PARAMETER BE THE NUMBER 76 FOR THE FIRST 7 UNITS AND THE NUMBER 77 FOR THE LAST 9 UNITS.

THE FOLLOWING DIALOGUE WOULD ACCOMPLISH THIS GOAL:

```
# UNITS (D) ? 16
UNIT 0
<QUESTION 1> ? 75
<QUESTION 2> ? 0-6
<QUESTION 3> ? 76
```

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UNIT 7
<QUESTION 1> ?
<QUESTION 2> ? 7-11,,13-15
<QUESTION 3> ? 77
```

THE FIRST TIME THE SERIES IS ASKED, SLOT ONE RECEIVES A 75 IN ALL 16 TABLES. SLOT TWO RECEIVES THE VALUES 0,1,2,...,6 IN TABLES 0 THRU 6 AND A CONSTANT 6 IN TABLES 7 THRU 15. SLOT THREE RECEIVES A CONSTANT 76 IN ALL 16 TABLES.

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THE SECOND TIME THRU THE SERIES, TABLES 7 THRU THE END ARE GOING TO BE AFFECTED (NOTE THAT THIS PIECE OF INFORMATION IS PRINTED OUT FOR THE THE OPERATOR IN THE FORM 'UNIT XX' AT THE BEGINNING OF EACH SERIES). QUESTION 1 IS RESPONDED TO BY A <CR>, SO SLOT ONE STAYS AT CONSTANT 75 IN TABLES 7 THRU 15, SINCE NO NEW EXPLICIT VALUES ARE TYPED IN. SLOT TWO GETS THE VALUES 7,8,9,10,11 IN TABLES 7 THRU 11, AND GETS AN 11 IN SLOT 12, AND GETS THE VALUES 13,14,15 IN TABLES 13 THRU 15. SLOT THREE GETS THE VALUE 77 IN TABLES 7 THRU 15.

THE DIALOGUE IS TERMINATED WHEN THE SOFTWARE RECOGNIZES THAT 16 EXPLICIT VALUES HAVE BEEN GIVEN FOR AT LEAST ONE QUESTION (NAMELY QUESTION 2).

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7.0 TEST DESCRIPTIONS

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*****
* TEST 1 <VIA TIMER 2 ONE SHOT MODE>
*
* THIS TEST VERIFIES THAT THE TIMER 2 COUNTER IS OPERATIONAL IN
* INTERVAL-TIMER (ONE-SHOT) MODE.
*
* THE FOLLOWING IS PERFORMED :
*
* A MASTER CLEAR IS DONE & THE TIMER IS PLACED IN INTERVAL-TIMER MODE
* BY SETTING ACR5 = 0 AND THE PROGRAM CHECKS FOR 'T2' (BIT 5 IN IFR)
* TO BE INITIALLY CLEARED.
*
* T2L-L (ADR 08) & T2C-H (ADR 09) ARE BOTH LOADED WITH 252 (OCTAL).
* (THIS IS EQUIVALENT TO AAAA (HEX) OR 43,690 (DECIMAL).) LOADING
* T2C-H STARTS THE COUNTER.
*
* T2L-L IS LOADED WITH 001 AND T2C-H IS LOADED WITH 000 IN ORDER TO
* SET 'T2' WITH A QUICK UNDERFLOW. THE 'T2' FLAG BIT IN IFR IS READ
* AND CHECKED TO BE SET.
*
* T2C-H IS CHECKED TO = 0. CHECKING T2C-H SHOULD NOT HAVE CLEARED 'T2'
* -- THIS IS VERIFIED.
*
* T2C-L IS CHECKED TO = 0. CHECKING T2C-L SHOULD HAVE CLEARED 'T2' --
* THIS TOO IS VERIFIED.
*
* T2C-H IS LOADED WITH 0 AGAIN TO INITIATE A NEW COUNT DOWN (WHICH
* SHOULD UNDERFLOW ALMOST IMMEDIATELY) AND THE 'T2' BIT IN IFR IS
* CHECKED TO BE SET AGAIN.
*
* T2L-L IS LOADED WITH 125 (OCTAL) AND 'T2' BIT IS CHECKED TO BE STILL
* SET.
*
* T2C-H IS LOADED WITH 125, AND THE 'T2' BIT IS READ AND CHECKED TO BE
* CLEARED BY THE LOADING OF T2C-L.
*****
*
*****
* TEST 2 <VIA'S SR INPUT (MODE 2) - SYSTEM CLOCK MODE>
*
* A MASTER CLEAR IS DONE. THEN THE SHIFT REG IS PLACED IN INPUT MODE
* UNDER CONTROL OF VIA CLK, BY SETTING ACR BIT 4 TO 0, BIT 3 TO 1, AND BIT 2
* TO 0. THE PROGRAM CHECKS FOR THE SR FLAG (BIT 2) IN THE IFR TO BE INITIALLY
* CLEARED. THEN, THE SR IS LOADED TO INITIALIZE THE SR OPERATION, AND THE
* PROGRAM CHECKS FOR SR FLAG = 1 AFTER ABOUT 8 US. AND READS SR REGISTER TO
* VERIFY THAT SHIFTING OCCURRED.
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*****
* TEST 3 <NPR CONTROL REGISTER - MASTER CLEAR>
*
* THE PROGRAM SETS THE FOLLOWING BITS IN THE NPR CONTROL REGISTER :
* IN/OUT, BYTE OPER, AND DISABL INIT. THE REGISTER IS READ AND VERIFIED.
* THEN, A MASTER CLEAR IS PERFORMED, AND THE REGISTER IS READ AND CHECKED FOR
* 000.
*****

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*****
* TEST 4 <NPR DATA-OUT>
*
* FIRST SUBTEST :
* THE NPR OUTPUT ADDRESS REGISTER IS LOADED WITH THE ADDRESS OF A 2 BYTE
* BUFFER IN THE PROGRAM. THEN, EACH WORD OF DATA PATTERN F IS LOADED INTO THE
* NPR OUTPUT DATA REGISTER, A FULLWORD NPR OUTPUT REQUEST IS PERFORMED,
* AND THE PROGRAM CHECKS FOR THE CORRECT DATA IN THE PROGRAM BUFFER. ALSO,
* THE PROGRAM CHECKS THAT THE ABORT XFER BIT IN THE NPR CONTROL REGISTER
* NEVER GETS SET.
* DATA PATTERN F = 125252, 052525, 000000, 177777, 000001, 000002, 000004,
* 000010, 000020, 000040, 000100, 000200, 000400, 001000,
* 002000, 004000, 010000, 020000, 040000, 100000, 177776,
* 177775, 177773, 177767, 177757, 177737, 177677, 177577,
* 177377, 176777, 175777, 173777, 167777, 157777, 137777,
* 077777, 000000
*
* SECOND SUBTEST:
* THE ABOVE OPERATIONS ARE REPEATED IN BYTE NPR TRANSFER MODE, USING THE DATA
* BYTES IN DATA PATTERN B. THE LOW BYTE OF THE PROGRAM BUFFER IS USED, AND
* THE UPPER BYTE IS CLEARED AT THE START, AND IS CHECKED TO REMAIN UNCHANGED
* THROUGHOUT THE SUBTEST.
* DATA PATTERN B = 125, 252, 000, 377, 001, 002, 004, 010, 020, 040, 100,
* 200, 376, 375, 373, 367, 357, 337, 277, 177, 000
*****

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*****
* TEST 5 <NPR DATA-IN>
*
* THE NPR INPUT ADDRESS REGISTER IS LOADED WITH THE ADDRESS OF A 2 BYTE
* BUFFER IN THE PROGRAM. THEN, EACH WORD OF DATA PATTERN F IS LOADED INTO THE
* PROGRAM BUFFER, A FULLWORD NPR INPUT REQUEST IS ISSUED AND PERFORMED,
* AND THE PROGRAM CHECKS FOR THE CORRECT DATA IN THE NPR INPUT DATA REG.
* ALSO, THE PROGRAM CHECKS THAT THE ABORT XFER BIT IN THE NPR CONTROL
* REGISTER NEVER GETS SET.
* DATA PATTERN F = 125252, 052525, 000000, 177777, 000001, 000002, 000004,
* 000010, 000020, 000040, 000100, 000200, 000400, 001000,
* 002000, 004000, 010000, 020000, 040000, 100000, 177776,
* 177775, 177773, 177767, 177757, 177737, 177677, 177577,
* 177377, 176777, 175777, 173777, 167777, 157777, 137777,
* 077777, 000000
*****

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*****
* TEST 6 <NPR XFER ABORT>
*
* FIRST SUBTEST :
* THE PROGRAM PERFORMS AN OUTPUT NPR REQUEST TO A NON-EXISTENT MEMORY
* LOCATION, AND CHECKS FOR THE ASSERTION OF ABORT XFER BIT IN THE NPR CONTROL
* REGISTER. THEN, AN OUTPUT NPR IS DONE AND CHECKED, TO A LOCATION IN THE
* PROGRAM, USING 125252 FOR DATA, AND THE PROGRAM CHECKS FOR ABORT XFER TO
* BE CLEARED BY SETTING THE DONE BIT.
* SECOND SUBTEST :
* THE ABOVE SUBTEST IS REPEATED USING INPUT NPR'S.
*****

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*****
* TEST 7 <NPR EXTENDED ADDRESS BIT TEST>
*
* THIS TEST WILL ONLY BE RUN IF THERE IS AT LEAST 32K WORDS OF MEMORY ON THE
* SYSTEM. IF THERE IS, THE PROGRAM CHOOSES A LOCATION TO USE IN THE ADDRESS
* RANGE 200000-377776 (OCTAL). THEN, THE FOLLOWING 2 SUBTESTS ARE PERFORMED :
*
* FIRST SUBTEST :
* AN INPUT NPR IS PERFORMED AND CHECKED USING THE MEMORY LOCATION, WITH
* 125252 FOR DATA. THE PROGRAM CHECKS THAT THE ABORT XFER BIT REMAINS
* CLEARED.
* SECOND SUBTEST :
* AN OUTPUT NPR IS PERFORMED AND CHECKED USING THE MEMORY LOCATION, WITH
* 125252 FOR DATA. THE PROGRAM CHECKS THAT THE ABORT XFER BIT REMAINS
* CLEARED.
*****

```

```

*****
* TEST 8 <SPECIAL MFG EXTENDED BIT TEST>
*
* THIS TEST WAS DESIGNED SPECIFICALLY TO ALLOW MANUFACTURING TO CHECK THE
* NPRAIX/NPRAOX BITS WITHOUT A FULL 4 M. OF MEMORY.
*
* IT WILL CHECK THE 12 DMV EXTENDED ADDRESS BITS (6:NPRAIX/6:NPRAOX) ON
* A Q22 SYSTEM IF MEMORY IS PRESENT AT THE FOLLOWING PHYSICAL ADDRESSES:
*
*      17600000      17400000      17200000
*      16600000      15600000      13600000
*      7600000
*
* FIRST SUBTEST :      TEST 'NPRAIX' EXTENDED ADDRESS BITS
* SECOND SUBTEST :      TEST 'NPRAOX' EXTENDED ADDRESS BITS
*****

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*****
* TEST 9 <Q-BUS INTERRUPT 'A' & 'B' SELECTION>
*
* THIS TEST CONTAINS SUBTESTS IN WHICH A SEQUENCE OF STEPS IS

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: * PERFORMED. IN GENERAL, EACH SUBTEST PERFORMS THE FOLLOWING:
: *
: *   1. INTERRUPTS ARE DISABLED FOR BOTH 'A' & 'B'
: *
: *   2. THE INTERRUPT REQUEST REGISTER IS WRITTEN INTO
: *
: *   3. A TEST IS MADE TO BE SURE THAT NEITHER INTERRUPT OCCURS
: *
: *   4. BOTH INTERRUPTS ARE ENABLES
: *
: *   5. A TEST IS MADE TO BE SURE THAT IF AN INTERRUPT IS EXPECTED, IT IS
: *      RECEIVED AND IF IT ISN'T EXPECTED IT DOESN'T HAPPEN.
: *
: * ALL TESTING IS DONE HERE WITH THE PROCESSOR'S PRIORITY SET AT 0.
: *****
:
: *****
: * TEST 10 <BUS RESET WITH DISABLE INIT SET> .PAGE
: *
: * A BYTE SELECT REGISTER (BSEL3) IS LOADED WITH 377, DISABLE INIT BIT IS SET
: * IN THE NPR CONTROL REGISTER, AND A BUS RESET INSTRUCTION IS EXECUTED. THE
: * PROGRAM THEN CHECKS THAT THE DMV-11 WAS NOT CLEARED, BY CHECKING FOR 377
: * STILL IN BSEL3
: *****
:
: *****
: * TEST 11 <MASTER CLEAR WITH DISABLE INIT SET>
: *
: * THE 'DISABL INIT' BIT IN THE NPR CONTROL REGISTER IS SET AND A MASTER CLEAR
: * IS ISSUED. IF THE MASTER CLEAR SUBROUTINE DETECTS AN ERROR, THE MASTER
: * CLEAR WILL NOT HAVE FUNCTIONED PROPERLY. WHERE THE NORMAL ERROR MESSAGE
: * (QUEUED UP BY 'MASCLR') IS NORMALLY PRINTED, THIS TEST WILL PRINT ITS OWN
: * INSTEAD.
: *****
:
: *****
: * TEST 12 <DCOK H LO BIT>
: *
: * DCOK H LO IS SET IN THE NPR CONTROL REGISTER WHICH SHOULD CAUSE A VECTOR TO
: * THE FIRST INTERRUPT HANDLER WHERE THE VECTOR IS CHANGED TO POINT TO THE
: * SECOND HANDLER. THIS SECOND HANDLER WILL THEN STALL FOR A WHILE WAITING FOR
: * THE POWER-UP INTERRUPT WHICH SHOULD KICK US INTO THE SECOND HANDLER. IN
: * BOTH HANDLERS FLAGS ARE SET TO SAY THAT WE GOT THERE. WHEN WE FINALLY
: * RETURN TO OUR MAINLINE CODE, WE WILL RESUME THE DELAY FUNCTION WE WERE IN
: * AND THEN CHECK THE FLAGS.
: *
: * IN SUBTEST # 1, WE EXPECT THE DMV TO BE RESET.
: *****
:
: *****
: * TEST 13 <HALT MODE VERIFICATION>

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: *
: * THIS TEST CONTAINS TWO (2) SUBTESTS DESIGNED TO VERIFY THE FUNCTIONALITY
: * OF THE 'HALT' CONTROL CONTAINED WITHIN THE NPR CONTROL REGISTER. IN EACH
: * CASE, MICROCODE IS LOADED INTO THE DMV IN ORDER TO CONTROL THE TESTING
: * FROM THERE.
: *
: *-----
: *
: * SUBTEST # 1:
: *
: * HERE = VERIFY THAT WE CAN CONTROL NPR'S AND DCOK PROPERLY WHILE THE 11 CPU
: * IS HALTED.
: *
: *      11 CPU'S OPERATIONS:                                DMV-11'S OPERATIONS:
: *
: * THE MICROCODE IS MOVED INTO THE DMV.
: *
: * CLEAR TMP0. THIS WILL BE OUR TEST
: * LOCATION FOR THE NPR OPERATION.
: *
: * SETUP FOR POWER-FAIL VECTORING THROUGH
: * LOCATION 24.
: *
: * THE MICROCODE IS INITIATED & BSEL7 IS
: * SET TO -1 AS A FLAG.
: *
: * WAIT FOR BSEL7 TO BE CLEARED                                CLEAR BSEL7 AND WAIT FOR IT TO GO
: *                                                                NON-ZERO AGAIN. THIS PUTS THE
: *                                                                DMV IN SYNC. WITH THE 11 CPU
: *
: * SAVE R6 IN OLDSP FOR RECOVERY LATER.
: * CLEAR TMP0, LOAD INTO SEL4 THE
: * ADDRESS OF TMP0, AND SET BSEL7 TO -1.
: *
: * START LOOPING -- INCREMENTING TMP0                            GET THE ADDRESS OF TMP0 FROM SEL6
: *                                                                AND SAVE IT FOR LATER
: *
: *                                                                HALT THE 11 CPU.
: *
: * CONSOLE 'ODT' SHOULD BE ENTERED.                            NPR-IN THE CURRENT CONTENTS OF TMP0
: *                                                                & PUT IT INTO SEL4 (THE FULL WORD).
: *
: *                                                                DELAY FOR ABOUT 100 MICROSECONDS
: *                                                                (THE TIME ISN'T CRITICAL).
: *
: * THE 11 CPU SHOULD NOT BE EXECUTING
: * ANYTHING NOW -- NOT EVEN 'ODT'
: *
: *                                                                DROP THE 'HALT' SIGNAL TO RELEASE
: *                                                                THE 11 CPU AND SET 'DCOK H LO' &
: *                                                                'DISABL INIT'. DROP 'DCOK H LO'
: *
: * WE SHOULD GO
: * THROUGH A POWER-UP SEQUENCE. R6 IS
: * RESTORED FROM OLDSP, INTERRUPT

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: * PRIORITY LEVEL IS RESTORED TO 0, &
: * INTERRUPT VECTOR 24 IS RETURNED TO
: * THE DIAGNOSTIC SUPERVISOR. SEL4 IS
: * COMPARED AGAINST TMPO -- THEY SHOULD
: * BE EQUAL.
: *
: * NOW CLEAR BSEL7.
: *
: *-----
: *
: * SUBTEST # 2:
: *
: * HERE THE HALT MODE IS USED IN A WAY WHICH VERY CLOSELY MATCHES THE DMV-11
: * MICROCODE'S UTILIZATION DURING A 'MOP BOOT' OPERATION. THE INTERRUPT
: * VECTOR AREA IS COMPLETELY OVERWRITTEN BY THE DMV NPR'S AND IS THEREFORE
: * BACKED UP ELSEWHERE IN THE 11 CPU'S MEMORY. THERE IS ALSO THE POSSIBLE
: * CONTENTION WITH THE DIAGNOSTIC SUPERVISOR -- TO HELP HERE, AS MUCH AS
: * POSSIBLE WILL BE DONE AT INTERRUPT LEVEL 7.
: *
: *      11 CPU'S OPERATIONS:
: *
: * THE MICROCODE IS MOVED INTO THE DMV.
: *
: * THE INTERRUPT VECTOR AREA IS BACKED-
: * UP IN AN I/O BUFFER FOLLOWING THE
: * PROGRAM
: *
: * THE MICROCODE IS INITIATED & BSEL7 IS
: * SET TO -1 AS A FLAG.
: *
: * WAIT FOR BSEL7 TO BE CLEARED
: *
: * CLEAR TMPO AND SAVE R6 FOR RECOVERY
: * LATER. SET BSEL7 AGAIN AND WAIT FOR
: * TMPO TO BE SET.
: *
: * ENTRY INTO THE CONSOLE 'ODT' WILL
: * BE INITIATED.
: *
: *
: * THE ENTRY INTO THE CONSOLE 'ODT'
: * WILL BE ABORTED!
: *
: *      DMV-11'S OPERATIONS:
: *
: * CLEAR BSEL7 AND WAIT FOR IT TO GO
: * NON-ZERO AGAIN PUTTING BOTH
: * PROCESSORS IN SYNC. WITH EACH
: * OTHER
: *
: * SET HALT, 'DCOK H LO', & 'DISABL
: * INIT' AND PERFORM 2 NOP'S AS A
: * 1 MICROSECOND DELAY
: *
: * CLEAR 'DCOK H LO', SET 'HALT' &
: * 'DISABL INIT'
: *
: * NPR-OUT THE FOLLOWING:
: * LOC: CONTENTS
: * 24 000000 VECTOR TO LOC 0
: * 26 000340 @ PRIORITY 7
: * 0 012700 MOV #-1,R0
: * 2 177777
: * 4 000777 BR .
: *
: *-----
: * SET 'DCOK H LO' & 'DISABL INIT'
: * AND CLEAR 'HALT'

```


PROGRAM DOCUMENT

8.0 ERROR INFORMATION

8.1 ERROR REPORTING

ERRORS ARE REPORTED BY THE PROGRAM AS THEY OCCUR (IF NOT INHIBITED). THE REPORT CONFORMS TO THE DIAGNOSTIC SUPERVISOR ERROR REPORT FORMAT, AND CONSISTS OF A DESCRIPTION OF THE ERROR, THE TEST NUMBER, SUBTEST NUMBER, PC OF THE ERROR CALL, DEVICE ADDRESS, AND BASIC AND EXTENDED ERROR INFORMATION.

THE FOLLOWING EXAMPLE PROVIDES A TYPICAL ERROR REPORT, WHICH DESCRIBES A 'MASTER CLEAR FAILURE' ERROR, AND PROVIDES THE PC OF THE ERROR CALL AND THE DEVICE REGISTER CONTENTS :

CVDMB DVC FTL ERR 00001 ON UNIT 00 TST 002 SUB 000 PC: 021122
MASTER CLEAR FAILURE

THE CONTENTS OF ALL BYTE SELECT REG'S ARE:

BSEL0	BSEL1	BSEL2	BSEL3
000	000	000	000
BSEL4	BSEL5	BSEL6	BSEL7
000	000	121	000
BSEL10	BSEL11	BSEL12	BSEL13
000	000	000	000
BSEL14	BSEL15	BSEL16	BSEL17
000	000	000	000

FOR OTHER ERRORS, THE REPORT MAY BE MORE EXTENSIVE, AND REQUIRE ADDITIONAL DATA TO BE REPORTED.

IF EXTENDED ERROR INFORMATION HAD BEEN INHIBITED USING THE IXE FLAG PRIOR TO RUNNING THE TEST, THE ABOVE ERROR WOULD HAVE BEEN REPORTED IN THE FOLLOWING SHORTENED FORM :

CVDMB DVC FTL ERR 00001 ON UNIT 00 TST 002 SUB 000 PC: 021122
MASTER CLEAR FAILURE

1122
1123
1124
1125
1126
1127
1128
1129
1130
1131
1132
1133
1134
1135
1136
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1161
1162

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LISTING & ASSEMBLY CONTROL

```

1163
1164
1165      000000
1166
1167
1168
1169      002000
1170
1171
1172 002000
1173
1174 002000
1175
1176
1177      000001
1178      000001
1179      000001
1180      000001
1181      000001
1182      000001
1183      000001
1184
1185
1186
1187
1188
1189 002000
1191

```

```

.SBTTL LISTING & ASSEMBLY CONTROL
HELP=0      ; CONTROL LISTING OF HELP INFORMATION
            ; HELP=0  NO LIST
            ; HELP=1  LIST
.=2000
.MCALL SVC
SVC          ; INITIALIZE SUPERVISOR MACROS
BGNMOD LU1MOD
$LSTIN= 1
$LSTTAG= 1
SVCINS= 1   ; LIST INSTRUCTIONS, SHIFTED RIGHT
SVCTST= 1   ; LIST TEST TAGS, SHIFTED RIGHT
SVCSUB= 1   ; LIST SUBTEST TAGS, SHIFTED RIGHT
SVCGBL= 1   ; LIST GLOBAL TAGS, SHIFTED RIGHT
SVCTAG= 1   ; LIST OTHER TAGS, SHIFTED RIGHT
; CHANGE THE VALUES OF THE SVC... SYMBOLS TO BE ZERO IF YOU WISH
; TO ALIGN THE MACRO CALLS AND THEIR EXPANSIONS. CHANGE THE
; SYMBOLS TO BE MINUS-ONE TO NOT LIST THE EXPANSIONS. YOU MAY
; CHANGE THE SYMBOLS AT ANY POINT IN YOUR PROGRAM.
POINTER BGNAU,BGNDU,ERRTBL

```

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PROGRAM HEADER

.SBTTL PROGRAM HEADER

;++

:THE PROGRAM HEADER MACRO CHARACTERIZES THIS DIAGNOSTIC. THE
:HEADER MACRO'S ARGUMENTS ARE FILE NAME, RELEASE LEVEL, PATCH
:DISPOSITION OF THE MOST RECENT PATCH, MAXIMUM TEST TIME IN SEC.,
:AND THE TYPE OF DIAGNOSTIC (0-SEQUENTIAL, 1-EXERCISER). THESE
:ARGUMENTS ARE IN RESPECTIVE ORDER.

:--

HEADER CVD MB,A,0.60.,0

1192		
1193		
1194		
1195		
1196		
1197		
1198		
1199		
1200		
1201		
1202	002000	
1203	002000	
1204	002000	103
1205	002001	126
1206	002002	104
1207	002003	115
1208	002004	102
1209	002005	000
1210	002006	000
1211	002007	000
1212	002010	
1213	002010	101
1214	002011	
1215	002011	060
1216	002012	
1217	002012	000000
1218	002014	
1219	002014	000074
1220	002016	
1221	002016	036436
1222	002020	
1223	002020	000000
1224	002022	
1225	002022	002160
1226	002024	
1227	002024	000000
1228	002026	
1229	002026	037154
1230	002030	
1231	002030	000000
1232	002032	
1233	002032	000000
1234	002034	
1235	002034	000000
1236	002036	
1237	002036	000000
1238	002040	
1239	002040	002124
1240	002042	
1241	002042	000000
1242	002044	
1243	002044	000000
1244	002046	
1245	002046	000000
1246	002050	
1247	002050	003

```

LSNAME::
        .ASCII /C/
        .ASCII /V/
        .ASCII /D/
        .ASCII /M/
        .ASCII /B/
        .BYTE 0
        .BYTE 0
        .BYTE 0
LSREV::
        .ASCII /A/
LSDEPO::
        .ASCII /O/
LSUNIT::
        .WORD 0
LSTIML::
        .WORD 60.
LSHPCP::
        .WORD LSHARD
LSSPCP::
        .WORD 0
LSHPTP::
        .WORD LSHW
LSSPTP::
        .WORD 0
LSLADP::
        .WORD 0
LSSTA::
        .WORD LSLAST
LSCO::
        .WORD 0
LSDTYP::
        .WORD 0
LSAPT::
        .WORD 0
LSDTP::
        .WORD LSDISPATCH
LSPRIO::
        .WORD 0
LSENV1::
        .WORD 0
LSEXP1::
        .WORD 0
LSMREV::
        .BYTE CSREVISION

```

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PROGRAM HEADER

```

1248 002051 003
1249 002052
1250 002052 000000
1251 002054 000000
1252 002056
1253 002056 000000
1254 002060
1255 002060 003254
1256 002062
1257 002062 000000
1258 002064
1259 002064 000000
1260 002066
1261 002066 000000
1262 002070
1263 002070 023162
1264 002072
1265 002072 023156
1266 002074
1267 002074 000000
1268 002076
1269 002076 003274
1270 002100
1271 002100 104035
1272 002102
1273 002102 002202
1274 002104
1275 002104 022020
1276 002106
1277 002106 023140
1278 002110
1279 002110 023014
1280 002112
1281 002112 022012
1282 002114
1283 002114 000000
1284 002116
1285 002116 000000
1286 002120
1287 002120 000000
1288
1289

```

.EVEN

```

LSEF:: .BYTE CREDIT
        .WORD 0
LSSPC:: .WORD 0
LSDEVP:: .WORD 0
LSREPP:: .WORD LSDVTYP
LSEXP4:: .WORD 0
LSEXP5:: .WORD 0
LSAUT:: .WORD 0
LSDUT:: .WORD LSAU
LSLUN:: .WORD LSDU
LSDESP:: .WORD 0
LSLOAD:: .WORD LDESC
        EMT ESLOAD
LSETP:: .WORD LSERRTBL
LSICP:: .WORD LSINIT
LSCCP:: .WORD LSCLEAN
LSACP:: .WORD LSAUTO
LSPRT:: .WORD LSPROT
LSTEST:: .WORD 0
LSDLY:: .WORD 0
LSHIME:: .WORD 0

```

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DISPATCH TABLE

.SBTTL DISPATCH TABLE

```

:////////////////////
:// THE DISPATCH TABLE CONTAINS THE STARTING ADDRESS OF EACH TEST.
:// IT IS USED BY THE SUPERVISOR TO DISPATCH TO EACH TEST.
:////////////////////

```

1290		
1291		
1292		
1293		
1294		
1295		
1296		
1297	002122	
1298	002122	000015
1299	002124	
1300	002124	023164
1301	002126	024174
1302	002130	025062
1303	002132	025450
1304	002134	026160
1305	002136	026430
1306	002140	027036
1307	002142	031276
1308	002144	032626
1309	002146	034134
1310	002150	034230
1311	002152	034274
1312	002154	035046
1313		

DISPATCH 13.

```

.WORD 13
LSDISPATCH::
.WORD T1
.WORD T2
.WORD T3
.WORD T4
.WORD T5
.WORD T6
.WORD T7
.WORD T8
.WORD T9
.WORD T10
.WORD T11
.WORD T12
.WORD T13

```

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DEFAULT HARDWARE P-TABLE

.SBTTL DEFAULT HARDWARE P-TABLE

```

:////////////////////
:// THE DEFAULT HARDWARE P-TABLE CONTAINS DEFAULT VALUES OF
:// THE TEST-DEVICE PARAMETERS. THE STRUCTURE OF THIS TABLE
:// IS IDENTICAL TO THE STRUCTURE OF THE RUN-TIME P-TABLE.
:////////////////////

```

```

1314
1315
1316
1317
1318
1319
1320
1321
1322 002156
1323 002156 000010
1324 002160
1325 002160
1326
1327 00 160 160020
1328 002162 000300
1329 002164 004000
1330 002166 000000
1331 002170 000000
1332 002172 000000
1333 002174 000000
1334 002176 000111
1335
1336
1337
1338
1339
1340
1341
1342
1343
1344
1345
1346
1347
1348 002200
1349 002200

```

BGNHW DFPTBL

.WORD L10000-LSHW/2

LSHW::
DFPTBL::

```

.WORD 160020
.WORD 300
.WORD 4000
.WORD 000
.WORD 000
.WORD 0
.WORD 0
.WORD 000111

```

```

:DMV11 CSR UNIBUS ADDRESS
:DMV11 INTERRUPT VECTOR
:DMV11 INTERRUPT PRIORITY LEVEL = 4
:SWITCH REG. #1 (BOOT ADDRESS)
:SWITCH REG. #2 (DDCMP ADDRESS)
:MODULE IS M8064
:H3254&H3255 USED
:MISC. CONTROLS:

```

```

: POWER-UP MODE 0 MASK = 100
: 0 = NOT JUMPED FOR MODE 0 POWER-UP
: 1 = JUMPED FOR MODE 0 POWER-UP <=== DEFAULT SETTING
: BOTH W5 & W6 REMOVED

```

```

: MFG EXTENDED MEMORY CONFIGURATION MASK = 200
: 0 = NORMAL TESTING
: 1 = Q22 SYSTEM WITH MEMORY @ FOLLOWING LOCATIONS:
: 17600000 17400000 17200000
: 16600000 15600000 13600000
: 7600000

```

ENDHW

L10000:

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SOFTWARE P-TABLE

1350
1351
1352
1353
1354
1355
1356
1357 002200
1358 002200 000000
1359 002202
1360 002202
1361 002202
1362 002202

.SBTTL SOFTWARE P-TABLE
:////////////////////
:// THE SOFTWARE P-TABLE CONTAINS THE VALUES OF THE PROGRAM
:// PARAMETERS THAT CAN BE CHANGED BY THE OPERATOR.
:////////////////////

BGNSW SFPTBL

.LSSW: .WORD L10001-LSSW/2
SFPTBL:
L10001:

ENDSW

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GLOBAL EQUATES SECTION

.SBTTL GLOBAL EQUATES SECTION

```

://////
:/ THE GLOBAL EQUATES SECTION CONTAINS PROGRAM EQUATES THAT
:/ ARE USED IN MORE THAN ONE TEST.
://////

```

EQUALS

```

:
: BIT DIFINITIONS
:

```

```

1363
1364
1365
1366
1367
1368
1369
1370
1371 002202
1372
1373
1374
1375 100000
1376 040000
1377 020000
1378 010000
1379 004000
1380 002000
1381 001000
1382 000400
1383 000200
1384 000100
1385 000040
1386 000020
1387 000010
1388 000004
1389 000002
1390 000001
1391
1392 001000
1393 000400
1394 000200
1395 000100
1396 000040
1397 000020
1398 000010
1399 000004
1400 000002
1401 000001
1402
1403
1404
1405
1406 000040
1407 000037
1408 000036
1409 000035
1410 000034
1411
1412
1413
1414
1415 000340
1416 000300
1417 000240
1418 000200

```

```

BIT15== 100000
BIT14== 40000
BIT13== 20000
BIT12== 10000
BIT11== 4000
BIT10== 2000
BIT09== 1000
BIT08== 400
BIT07== 200
BIT06== 100
BIT05== 40
BIT04== 20
BIT03== 10
BIT02== 4
BIT01== 2
BIT00== 1

```

```

BIT9== BIT09
BIT8== BIT08
BIT7== BIT07
BIT6== BIT06
BIT5== BIT05
BIT4== BIT04
BIT3== BIT03
BIT2== BIT02
BIT1== BIT01
BIT0== BIT00

```

: EVENT FLAG DEFINITIONS

: EF32:EF17 RESERVED FOR SUPERVISOR TO PROGRAM COMMUNICATION

```

EF.START== 32. ; START COMMAND WAS ISSUED
EF.RESTART== 31. ; RESTART COMMAND WAS ISSUED
EF.CONTINUE== 30. ; CONTINUE COMMAND WAS ISSUED
EF.NEW== 29. ; A NEW PASS HAS BEEN STARTED
EF.PWR== 28. ; A POWER-FAIL/POWER-UP OCCURRED

```

: PRIORITY LEVEL DEFINITIONS

```

PRI07== 340
PRI06== 300
PRI05== 240
PRI04== 200

```


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GLOBAL EQUATES SECTION

```

1419      000140      PRI03== 140
1420      000100      PRI02== 100
1421      000040      PRI01== 40
1422      000000      PRI00== 0
1423
1424      ;OPERATOR FLAG BITS
1425
1426      000004      EVL==      4
1427      000010      LOT==      10
1428      000020      ADR==      20
1429      000040      IDU==      40
1430      000100      ISR==      100
1431      000200      UAM==      200
1432      000400      BOE==      400
1433      001000      PNT==     1000
1434      002000      PRI==     2000
1435      004000      IXE==     4000
1436      010000      IBE==    10000
1437      020000      IER==    20000
1438      040000      LOE==    40000
1439      100000      HOE==   100000
1440
1441      .SBTTL DEFINE THE NUMBER OF CSR'S
1442      000010      CSREGS = 8.
1443
1444      ;-----
1445
1446      .SBTTL NPR ADDRESS REGISTER EQUATES
1447      000070      NPRAOL = 70      ;OUT NPR ADRS LO REG
1448      000071      NPRAOH = NPRAOL+1 ;OUT NPR ADRS HI REG
1449      000072      NPRAOX = NPRAOL+2 ;OUT NPR EXTENDED ADRS REG
1450      000074      NPRAIL = NPRAOL+4 ;IN NPR ADRS LO REG
1451      000075      NPRAIH = NPRAOL+5 ;IN NPR ADRS HI REG
1452      000076      NPRAIX = NPRAOL+6 ;IN NPR EXTENDED ADRS REG
1453      000200      NPRBS7 = BIT7    ;'BANK SELECT 7' BIT -- W/IN EXTENDED ADRS. REG.
1454
1455
1456
1457      .SBTTL NPR DATA REG EQUATES
1458      123000      NPRDRL = 123000 ;NPR DATA REGISTER -- LOW BYTE
1459      123001      NPRDRH = NPRDRL+1 ;NPR DATA REGISTER -- HIGH BYTE
1460
1461
1462
1463      .SBTTL NPR CONTROL REG EQUATES
1464      123004      NPRCTL = NPRDRL+4 ;NPR CONTROL REGISTER
1465      000200      NPRABT = BIT7    ;=1 IF BUS TIME-OUT ON NPR
1466      000100      NPRGO  = BIT6    ;SET FOR NOP, CLEAR TO 'GO' / 0=DONE, 1=BUSY
1467      000040      NPRIO  = BIT5    ;0 = (LSI ==> DMV); 1 = (DMV ==> LSI)
1468      000020      LSIHLT = BIT4    ;SETTING THIS WILL 'HALT' THE LSI-11 !!
1469      000010      NPRBYT = BIT3    ;SET TO 1 TO WRITE BYTE ONLY TO LSI-11
1470      000004      DMVPU  = BIT2    ;SET BY MICRO-DIAG. MUST REMAIN SET!!!
1471      000002      LSIIDCL = BIT1   ;IF SET, WILL CAUSE POWER DOWN CONDITION IN LSI!
1472      000001      DMVDAI = BIT0   ;'DISABLE INIT' FROM EFFECTING DMV-11
1473
1474

```

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NPR REQUEST FUNCTIONS

```

1475 .SBTTL NPR REQUEST FUNCTIONS
1476 NPRLD = DMVPU ;WORD XFER: LSI ==> DMV
1477 000004 NPRDL = DMVPU!NPRIO ;WORD XFER: DMV ==> LSI
1478 000044 NPRDLB = DMVPU!NPRIO.NPRBYT ;BYTE XFER: DMV ==> LSI
1479 000054
1480 ;-----
1481
1482 .SBTTL INTERRUPT REG EQUATES
1483 IRQREG = 123005 ;INTERRUPT REQUEST REG
1484 000004 IRQA = BIT2 ;REQUEST BIT FOR XX0 INTERRUPT -- 'A'
1485 000002 IRQB = BIT1 ;REQUEST BIT FOR XX4 INTERRUPT -- 'B'
1486
1487 ;-----
1488
1489 .SBTTL CONTROL FLAGS FROM P-TABLE ENTRIES
1490 PU24 = BIT0 ;POWER-FAIL VECTORING MODE. 1 = MODE 0
1491 ; (I.E. JUMPERS W5 & W6 BOTH REMOVED)

```

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SWITCH PACKS

1492
1493
1494
1495
1496
1497
1498
1499
1500

121000
121400

.SBTTL SWITCH PACKS

::*****
:* SWITCH PACKS
::~*****

SWPBOT = 121000
SWPDDCMP = 121400

::'BOOT ADDRESS' SWITCH PACK [A200]
::~'DDCMP ADDRESS' SWITCH PACK [A300]

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CSR REG. DEFINITION FOR MAINT. LOOP

1501
1502
1503
1504
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1517
1518
1519
1520
1521
1522
1523
1524
1525
1526
1527
1528
1529
1530
1531
1532
1533

000001
000020

000200
000100
000001

000001
000002
000003
000004
000005

000200

```
.SBTTL CSR REG. DEFINITION FOR MAINT. LOOP
;+*****
.SBTTL MAINTENANCE REGISTER - BSEL0
;-----*****
; INTERRUPT ENABLE BITS
IENBA = BIT0 ;INTERRUPT ENABLE 'A'
IENBB = BIT4 ;INTERRUPT ENABLE 'B'

;+*****
.SBTTL MAINTENANCE REGISTER - BSEL1
;-----*****
; MAINT. LOOP CONTROL BITS:
RUN = BIT7
MCLR = BIT6
MREQ = BIT0

;+*****
.SBTTL MAINTENANCE REGISTER - BSEL2
;-----*****
; MAINTENANCE FUNCTION CODES
REDLOC = 1 ;FUNCTION CODE FOR READ A 6502 LOCATION
WRILOC = 2 ;FUNCTION CODE FOR WRITE A 6502 LOCATION
REDPAG = 3 ;FUNCTION CODE FOR READ A 6502 MEMORY PAGE
WRIPAG = 4 ;FUNCTION CODE FOR WRITE A 6502 RAM PAGE
EXECUT = 5 ;FUNCTION CODE FOR EXECUTE AT GIVEN PC

MRDY = BIT7 ;M-LOOP REDY FOR A COMMAND WHEN SET
```

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DMV INTERNAL ADDRESSES

1534
1535
1536
1537
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1541
1542
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1544
1545
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1580
1581
1582
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1585
1586
1587
1588
1589

.SBTTL DMV INTERNAL ADDRESSES

```
:+*****  
: DMV INTERNAL ADDRESSES  
:-----*****
```

:***** << MICROPROCESSOR REGISTER ADDRESS EQUATES >> *****

.SBTTL BYTE & WORD SELECT REGISTERS

000020	SLT0	=020
000020	BSLT0	=SLT0
000021	BSLT1	=SLT0+1
000022	SLT2	=SLT0+2
000022	BSLT2	=SLT0+2
000023	BSLT3	=SLT0+3
000024	SLT4	=SLT0+4
000024	BSLT4	=SLT0+4
000025	BSLT5	=SLT0+5
000026	SLT6	=SLT0+6
000026	BSLT6	=SLT0+6
000027	BSLT7	=SLT0+7

.SBTTL VIA'S REGISTERS

120000	ORB	=120000
120001	ORA	=ORB+1
120002	DDRB	=ORB+2
120003	DDRA	=ORB+3
120004	T1CL	=ORB+4
120005	T1CH	=ORB+5
120005	T1LHGO	=ORB+5
120006	T1LL	=ORB+6
120007	T1LH	=ORB+7
120010	T2LL	=ORB+10
120010	T2CL	=T2LL
120011	T2CH	=ORB+11
120012	SR	=ORB+12
120013	ACR	=ORB+13
120014	PCR	=ORB+14
120015	IFR	=ORB+15
120016	IENR	=ORB+16
120017	ORAM	=ORB+17

.SBTTL VIA'S 'IFR' REGISTER'S BIT ASSIGNMENTS

000200	IFRIRQ	=BIT7	::'IRQ' HAS BEEN ISSUED -- LOGICAL 'OR' OF BITS 0 --> 6
000100	IFRT1	=BIT6	::'T1' -- TIMER # 1 TIMED-OUT
000040	IFRT2	=BIT5	::'T2' -- TIMER # 1 TIMED-OUT
000020	IFRCB1	=BIT4	::'CB1' EDGE DETECTED ('K2 LINE UNIT STEP' O/P SIGNAL FROM SR)
000010	IFRCB2	=BIT3	::'CB2' EDGE DETECTED (UNUSED!)
000004	IFRSR	=BIT2	::'SR' REGISTER COMPLETED SHIFT OPERATION
000002	IFRCA1	=BIT1	::'CA1' EDGE DETECTED ('K6 MOD RDY H')
000001	IFRCA2	=BIT0	::'CA2' EDGE DETECTED ('K2 CTS H')

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VIA'S 'IFR' REGISTER'S BIT ASSIGNMENTS

1590
1591

;- - - - -

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GLOBAL DATA SECTION

1592
 1593
 1594
 1595
 1596
 1597
 1598
 1599
 1600
 1601
 1602
 1603 002202
 1604 002202
 1605 002202 000000
 1606 002204 000000
 1607 002206 000000
 1608 002210 000000
 1609
 1610
 1611
 1612
 1613 002212
 1614 002212 000000
 1615 002214
 1616 002214 000000
 1617 002216
 1618 002216 000000
 1619 002220
 1620 002220 000000
 1621 002222
 1622 002222 000000
 1623 002224
 1624 002224 000000
 1625 002226
 1626 002226 000000
 1627 002230
 1628 002230 000000
 1629 002232 000000
 1630 002234 000000
 1631 002236 000000
 1632 002240 000000
 1633 002242 000000
 1634 002244 000000
 1635 002246 000000
 1636 002250 000000
 1637
 1638
 1639
 1640
 1641 002252 000000
 1642 002254 000000
 1643 002256 000000
 1644 002260 000000
 1645 002262 110400
 1646 002264 000007
 1647 002266 000000

```

.SBTTL GLOBAL DATA SECTION
://////
:/ THE GLOBAL DATA SECTION CONTAINS DATA THAT ARE USED
:/ IN MORE THAN ONE TEST.
://////

:*****
.SBTTL CONTROL BLOCK FOR STACKED ERROR MESSAGES
:-----

                ERRRTBL                                LERRRTBL::

ERRTYP::        .WORD    0
ERRNBR::        .WORD    0
ERRMSG::        .WORD    0
ERRBLK::        .WORD    0

:*****
.SBTTL STORAGE FOR DEVICE REGISTERS
:-----

WSR0:
BSR0:  .WORD    0
WSR2:
BSR1:  .WORD    0
WSR4:
BSR2:  .WORD    0
WSR6:
BSR3:  .WORD    0
WSR10:
BSR4:  .WORD    0
WSR12:
BSR5:  .WORD    0
WSR14:
BSR6:  .WORD    0
WSR16:
BSR7:  .WORD    0
BSR10: .WORD    0
BSR11: .WORD    0
BSR12: .WORD    0
BSR13: .WORD    0
BSR14: .WORD    0
BSR15: .WORD    0
BSR16: .WORD    0
BSR17: .WORD    0

:*****
.SBTTL MISCELLANEOUS STORAGE
:-----

TDATA: .WORD    0           ;TEST DATA
GDATA: .WORD    0           ;EXPECTED DATA
BDATA: .WORD    0           ;ACTUAL DATA
XDATA: .WORD    0           ;EXCLUSIVE OR BETWEEN 'GDATA' & 'BDATA'
DELAY1: .WORD   110400      ;DELAY TIME, 3 INST., 500 MILLISEC.
DELAY2: .WORD    7         ;DELAY TIME FOR M-LOOP FUNCTION, 100 USEC.APPROX.
LOGDEV: .WORD    0         ;LOGICAL DEVICE NUMBER

```

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MISCELLANEOUS STORAGE

1648 002270 000000
 1649 002272 000000
 1650
 1651 002274 000000
 1652
 1653 002276 000000
 1654 002300 000000
 1655 002302 000000
 1656 002304 000000
 1657 002306 000000
 1658 002310 000000
 1659 002312 000000
 1660 002314 000000
 1661 002316 000000
 1662
 1663
 1664
 1665
 1666
 1667
 1668
 1669
 1670
 1671
 1672
 1673
 1674
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 1699
 1700
 1701
 1702
 1703

PSTACK: .WORD 0 :CONTAINS BASE LEVEL PROGRAM STACK POINTER
 INTFLG: .WORD 0 :INTERRUPT RECEIVED FLAG BYTES. ALLOCATION:
 : LOW BYTE FOR 'A' & HIGH BYTE FOR 'B'
 INTWCH: .WORD 0 :BYTE IS SET NON-ZERO WHEN HANDLER SHOULD BE
 : WATCHING FOR INT'S. ALLOCATION: SEE INTFLG
 ERRFLG: .WORD 0 :ERROR FLAG
 REGNUM: .WORD 0 :REGISTER NUMBER -- FOR PASSING ARG. TO 'ERRR'
 FRSTIM: .WORD 0 :FLAG=0 IF PROGRAM JUST LOADED
 FRSPAS: .WORD 0 :FLAG=0 IF FIRST PASS AFTER LOAD
 STARES: .WORD 0 :FLAG TO SHOW NO. OF PASSES SINCE STA OR RES
 DEVMAP: .WORD 0 :BIT MAP OF ACTIVE DEVICES
 DEVPTR: .WORD 0 :DEVICE MAP BIT POINTER
 CONSOL: .WORD 0 :CONSOLE DEVICE FLAG -- NON-ZERO = NONE PRESENT
 PFLAG: .WORD 0 :MISC. PROGRAM FLAGS

THE ABOVE WORD CONTAINS MISC. FLAGS WHICH CAN ONLY BE ACCESSED BY PATCHING.
 IT IS NOT INTENDED THAT THEY BE SET OR CLEARED EXCEPT UNDER VERY UNUSUAL
 CIRCUMSTANCES. THEREFORE, THEY WILL NOT BE DOCUMENTED ANY OTHER PLACE
 EXCEPT RIGHT HERE.

BIT 0 -- WHEN SET, THOSE TESTS WHICH DO A BUS RESET WILL NOT BE EXECUTED.
 THIS WAS IMPLEMENTED TO SAVE WEAR & TEAR ON THE RX01 IN THE
 DEVELOPMENT SYSTEM WHILE DOING LONG TERM TESTING OF ALL OTHER
 TESTS.

BIT 1 -- CPU TYPE (NOT USED).

BIT 2 -- CONTROLS PRINTING OF EXTENDED ERROR INFORMATION DURING 'MOVING
 INVERSIONS TEST' OF RAM. NORMALLY ONLY ADDRESS, GOOD & BAD
 DATA, AND XOR WILL BE PRINTED. IF THIS BIT IS SET HOWEVER,
 INFORMATION IDENTIFYING WHERE WITHIN THE ALGORITHM THE ERROR
 WAS DETECTED IS REPORTED. THE FOLLOWING ABBREVIATIONS ARE USED
 IN THE HEADING:

BIT --- IDENTIFIES THE INNERMOST LOOP. WHICH BIT IS
 BEING INVERTED AT EACH LOCATION. BITS ARE
 IDENTIFIED AS 0 THROUGH 7.

DATA --- IDENTIFIES THE VALUE TO WHICH THE ABOVE BIT IS
 BEING SET (I.E. 0 OR 1). IT IS FIRST READ AND
 CHECKED FOR EXPECTED CONTENTS; THEN THE BIT IS
 INVERTED TO THIS STATE (DATA) AND RE-WRITTEN;
 THEN IT IS AGAIN READ & CHECKED FOR THE NEW
 VALUE.

SEQ --- INDICATES THE DIRECTION (FWD OR BKWD) THE TEST
 WAS SCANNING THROUGH RAM WHEN THE ERROR OCCURED.

LSB --- THIS IS THE LOGICAL LEAST SIGNIFICANT BIT OF THE
 RAM ADDRESS AS WE SCAN THROUGH MEMORY. BY
 VARYING THIS, THE ALGORITHM GENERATES NON-SEQUEN-
 TIAL ADDRESSING OF RAM AND EFFECTS A MUCH MORE
 THOROUGH TEST OF MEMORY.

BIT 3 -- ENABLES PRINTOUT OF THE MESSAGE WITHIN THE INIT CODE THAT TELLS
 US HOW LONG IT TOOK (IN LOOPS WITHIN THIS CPU) TO PERFORM ONE
 NO-OP FUNCTION OF THE MAINTAINENCE LOOP.

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CURRENT DEVICE PARAMETERS

```

1704 .SBTTL CURRENT DEVICE PARAMETERS
1705
1706 SMPCSR == 160000 ;INITIAL ASSEMBLED IN CSR ADDRESS
1707
1708 MPCSRL: ;POINTER TO THE DMV11 CSR'S
1709 BSEL0: ;POINTER TO BSEL0
1710 BSEL: ;ALTERNATE NAME FOR BSEL0
1711 SEL0: .WORD SMPCSR ;POINTER TO SEL0
1712 BSEL1: .WORD SMPCSR+1 ;POINTER TO BSEL1
1713 BSEL2: ;POINTER TO BSEL2
1714 SEL2: .WORD SMPCSR+2 ;POINTER TO SEL2
1715 BSEL3: .WORD SMPCSR+3 ;POINTER TO BSEL3
1716 BSEL4: ;POINTER TO BSEL4
1717 SEL4: .WORD SMPCSR+4 ;POINTER TO SEL4
1718 BSEL5: .WORD SMPCSR+5 ;POINTER TO BSEL5
1719 BSEL6: ;POINTER TO BSEL6
1720 SEL6: .WORD SMPCSR+6 ;POINTER TO SEL6
1721 BSEL7: .WORD SMPCSR+7 ;POINTER TO BSEL7
1722 BSEL10: ;POINTER TO BSEL10
1723 SEL10: .WORD SMPCSR+10 ;POINTER TO SEL10
1724 BSEL11: .WORD SMPCSR+11 ;POINTER TO BSEL11
1725 BSEL12: ;POINTER TO BSEL12
1726 SEL12: .WORD SMPCSR+12 ;POINTER TO SEL12
1727 BSEL13: .WORD SMPCSR+13 ;POINTER TO BSEL13
1728 BSEL14: ;POINTER TO BSEL14
1729 SEL14: .WORD SMPCSR+14 ;POINTER TO SEL14
1730 BSEL15: .WORD SMPCSR+15 ;POINTER TO BSEL15
1731 BSEL16: ;POINTER TO BSEL16
1732 SEL16: .WORD SMPCSR+16 ;POINTER TO SEL16
1733 BSEL17: .WORD SMPCSR+17 ;POINTER TO BSEL17
1734
1735 MPIVEC: .WORD 300 ;DMV11 INPUT INTERRUPT VECTOR
1736 MPOVEC: .WORD 304 ;DMV11 OUTPUT INTERRUPT VECTOR
1737 MPRIOR: .WORD 340 ;DMV11 DEVICE PRIORITY
1738 BRDTYP: .WORD 0 ;0=M8064,1=M8053/V.35,2=M8053/EIA
1739 PT.CTL: .WORD 0 ;MISC. CONTROL FLAGS FROM P-TABLE
1740
1741 .SBTTL GEN'L PURPOSE SCRATCH STORAGE
1742
1743 REG0: .WORD 0
1744 REG1: .WORD 0
1745 REG2: .WORD 0
1746 REG3: .WORD 0
1747 REG4: .WORD 0
1748 REG5: .WORD 0
1749 REG6: .WORD 0
1750 REG7: .WORD 0

```

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SCRATCH STORAGE FOR MESSAGE REPORTING

1751
 1752
 1753 002412 000000
 1754 002414 000000
 1755 002416 000000
 1756 002420 000000
 1757 002422 000000
 1758 002424 000000
 1759 002426 000000
 1760 002430 000000
 1761 002432 000000
 1762 002434 000000
 1763 002436 000000
 1764 002440 000000
 1765 002442 000000
 1766 002444 000000
 1767 002446 000000
 1768 002450 000000
 1769 002452 000000
 1770 002454 000000
 1771
 1772
 1773
 1774
 1775
 1776 002456 000025
 1777 002460 125
 1778 002461 252
 1779 002462 000
 1780 002463 377
 1781 002464 001
 1782 002465 002
 1783 002466 004
 1784 002467 010
 1785 002470 020
 1786 002471 040
 1787 002472 100
 1788 002473 200
 1789 002474 376
 1790 002475 375
 1791 002476 373
 1792 002477 367
 1793 002500 357
 1794 002501 337
 1795 002502 277
 1796 002503 177
 1797 002504 000
 1798 002505

.SBTTL SCRATCH STORAGE FOR MESSAGE REPORTING

TMP0: .WORD 0
 TMP1: .WORD 0
 TMP2: .WORD 0
 TMP3: .WORD 0
 TMP4: .WORD 0
 TMP5: .WORD 0
 TMP6: .WORD 0
 TMP7: .WORD 0
 TMP8: .WORD 0
 TMP9: .WORD 0
 TMPA: .WORD 0
 TMPB: .WORD 0
 TMPD: .WORD 0
 TMPE: .WORD 0
 TMPF: .WORD 0
 NEWPC: .WORD 0 ;SAVE LOCATION FOR A 'PC' VALUE RESET
 OLDSP: .WORD 0 ;SAVE LOCATION FOR A STACK POINTER RESET VALUE

.SBTTL ***** DATA PATTERN B *****

.EVEN ;USAGE:
 PATB: .WORD 1\$-.-2 ;# OF BYTES IN PATTERN
 .BYTE 125
 .BYTE 252
 .BYTE 000
 .BYTE 377
 .BYTE 001
 .BYTE 002
 .BYTE 004
 .BYTE 010
 .BYTE 020
 .BYTE 040
 .BYTE 100
 .BYTE 200
 .BYTE 376
 .BYTE 375
 .BYTE 373
 .BYTE 367
 .BYTE 357
 .BYTE 337
 .BYTE 277
 .BYTE 177
 .BYTE 000
 1\$:

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***** DATA PATTERN F *****

.SBTTL ***** DATA PATTERN F *****

1799		
1800		
1801		002506
1802	002506	000045
1803	002510	125252
1804	002512	052525
1805	002514	000000
1806	002516	177777
1807	002520	000001
1808	002522	000002
1809	002524	000004
1810	002526	000010
1811	002530	000020
1812	002532	000040
1813	002534	000100
1814	002536	000200
1815	002540	000400
1816	002542	001000
1817	002544	002000
1818	002546	004000
1819	002550	010000
1820	002552	020000
1821	002554	040000
1822	002556	100000
1823	002560	177776
1824	002562	177775
1825	002564	177773
1826	002566	177767
1827	002570	177757
1828	002572	177737
1829	002574	177677
1830	002576	177577
1831	002600	177377
1832	002602	176777
1833	002604	175777
1834	002606	173777
1835	002610	167777
1836	002612	157777
1837	002614	137777
1838	002616	077777
1839	002620	000000
1840	002622	

```

.EVEN
PATF: <1$-. -2>/2
125252
052525
0
-1
1
2
4
10
20
40
100
200
400
1000
2000
4000
10000
20000
40000
100000
177776
177775
177773
177767
177757
177737
177677
177577
177377
176777
175777
173777
167777
157777
137777
077777
0

```

1\$:

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***** DATA PATTERN RESULTS TABLE FOR MASTER CLEAR (RESFMC) *****

.SBTTL ***** DATA PATTERN RESULTS TABLE FOR MASTER CLEAR (RESFMC) *****

```

1841
1842
1843
1844 002622 000
1845 002623 200
1846 002624 000
1847 002625 000
1848 002626 033
1849 002627 000
1850 002630 305
1851 002631 000

```

```

.EVEN
BSELRS: .BYTE 000           ;BSEL0
         .BYTE 200           ;BSEL1 -- 'RUN' BIT SET
         .BYTE 000           ;BSEL2
         .BYTE 000           ;BSEL3
         .BYTE 033           ;BSEL4 -- CODE FOR THE DMV-11
         .BYTE 000           ;BSEL5
         .BYTE 305           ;BSEL6 -- INDICATING VALID COMPLETION OF U-DIAG.
         .BYTE 000           ;BSEL7

```

```

1852
1853
1854
1855
1856
1857
1858
1859
1860

```

.SBTTL ***** DATA PATTERN OF NPR REG'S AFTER MASTER CLEAR *****

```

:   ALTHOUGH THE REGISTERS ARE ONLY 1 BYTE LONG, EACH TABLE ENTRY IS ONE
:   WORD LONG TO SIMPLIFY THE ERROR CHECKING & REPORTING. THE HIGH BYTE
:   OF EACH ENTRY MUST BE LEFT AT ZERO OR THE TESTING & PRINTING WILL BE
:   IN ERROR!

```

```

1861
1862 002632 000004
1863 002634 000000
1864 002636 000000
1865 002640 000000
1866 002642 000000
1867 002644 000000
1868 002646 000000
1869 002650 000000
1870 002652 000000

```

```

.EVEN
NPRMCR: .WORD DMVPU       ;ONLY THE 'POWER UP' IS SET (BY MICRO-DIAG.)
         .WORD 0         ;'DATA HI'
         .WORD 0         ;'DATA LO'
         .WORD 0         ;'OUT ADDR. EXTENDED'
         .WORD 0         ;'OUT ADDR. HI'
         .WORD 0         ;'OUT ADDR. LO'
         .WORD 0         ;'IN ADDR. EXTENDED'
         .WORD 0         ;'IN ADDR. HI'
         .WORD 0         ;'IN ADDR. LO'

```

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DATA BUFFER AREAS

.SBTTL DATA BUFFER AREAS

BUFAREA: .BLKB 256.

: THIS BUFFER HAS SOME ALTERNATE USES TOO. THE FOLLOWING LABELS ARE PROVIDED
: FOR THOSE USAGES.

1871
1872
1873 002654 000400
1874
1875
1876
1877
1878
1879 003054
1880 003056
1881 003060
1882 003062
1883 003064
1884 003066
1885 003070
1886 003072
1887 003074
1888 003076
1889 003100
1890 003102
1891 003104
1892 003106
1893 003110
1894 003112
1895
1896 002654
1897 002740

W0 = BUFAREA+128. ;THIS WORD TABLE STARTS IN THE MIDDLE OF 'BUFAREA'
W1 = W0+2 ;AND IS USED BY 'ERR6' FOR PRINTING BYTES
W2 = W1+2
W3 = W2+2
W4 = W3+2
W5 = W4+2
W6 = W5+2
W7 = W6+2
W8 = W7+2
W9 = W8+2
WA = W9+2
WB = WA+2
WC = WB+2
WD = WC+2
WE = WD+2
WF = WE+2

BT1 = BUFAREA ;BYTE TABLE # 1
BT2 = BUFAREA+64 ;BYTE TABLE # 2

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GLOBAL TEXT SECTION

1898
1899
1900
1901
1902
1903
1904
1905
1906
1907
1908
1909 003254
1910 003254
1911 003254 034115 032460 020063
1912 003262 051117 046440 030070
1913 003270 032066 000
1914 003274
1915
1916
1917
1918
1919
1920 000012
1921 003274
1922 003274
1923 003274 046504 026526 030461
1924 003302 052440 041455 047117
1925 003310 051124 020114 047514
1926 003316 044507 020103 044504
1927 003324 043501 026440 050040
1928 003332 051101 020124 020062
1929 003340 043117 031040 000
1930 003346
1931 000010
1932

.SBTTL GLOBAL TEXT SECTION

```

:XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
:X THE GLOBAL TEXT SECTION CONTAINS FORMAT STATEMENTS,
:X MESSAGES, AND ASCII INFORMATION THAT ARE USED IN
:X MORE THAN ONE TEST.
:XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX

```

```

:*****
:* NAMES OF DEVICES SUPPORTED BY PROGRAM
:-----
DEV TYP <M8053 OR M8064>

```

```

LSDVTYP::
.ASCIZ /M8053 OR M8064/

```

.EVEN

```

:*****
:* TITLE OF PROGRAM
:-----

```

```

.RADIX 10.
DESCRIPT <DMV-11 U-CONTRL LOGIC DIAG - PART 2 OF 2>

```

```

LSDDESC::
.ASCIZ /DMV-11 U-CONTRL

```

.EVEN

.RADIX 8.

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GLOBAL SUBROUTINES

```

1933 .SBTTL GLOBAL SUBROUTINES
1934
1935 :////////////////////////////////////////////////////////////////////
1936 :/ THE GLOBAL SUBROUTINES ARE CALLED BY MORE THAN ONE TEST
1937 :////////////////////////////////////////////////////////////////////
1938
1939 :+*****
1940 .SBTTL MASCLR - MASTER CLEAR SUBROUTINE
1941
1942 : FUNCTION:
1943
1944 : THIS SUBROUTINE FORCES THE 6502 MICROPROCESSOR TO EXECUTE A MINI 17 PART
1945 : DIAGNOSTIC OF THE MICRO-PROCESSOR INSTRUCTION SET, RAM DATA AND ADDRESSING
1946 : VALIDITY, AND A ROM CRC TEST. THE CLEAR SUBROUTINE EXECUTES IN
1947 : APPROXIMATELY 500 HUNDRED(S) MILLISECOND. THIS SUBROUTINE WILL SEND THE
1948 : MASTER CLEAR COMMAND AND DELAY FOR APPROX. 500 MSEC. AT WHICH PRINT IN
1949 : TIME, THE STATE OF THE CSR REGISTERS IS TESTED. IF ANY ONE OF THE
1950 : REGISTERS CONTAINS ANYTHING THAT IS NOT EXPECTED, AN ERROR IS QUEUE UP AND
1951 : THE CARRY BIT IS SET. ELSE, THE CARRY BIT IS CLEARED.
1952
1953 : CALLING SEQUENCE:
1954
1955 : JSR PC,MASCLR
1956 : BCC NS ;IF NO ERROR OCCURED, PROCEED WITH ROUTINE
1957 : ERROR ;AN ERROR MESSAGE HAS BEEN STACKED: PRINT IT
1958 : <ANY OTHER SPECIAL ERROR PROCESSING MAY BE DONE HERE (I E. CKLOOP)>
1959
1960 : NS: <RESUMPTION OF NORMAL PROCESSING>
1961
1962 :-----*****
1963
1964 003346 010146 MASCLR: MOV R1,-(SP) ; SAVE REGISTER ONE
1965
1966 003350 112777 000300 176744 MOVB #RUN!MCLR,@BSEL1 ;SET BOTH THE RUN AND MASTER CLEAR BITS
1967 ;TO INITIATE THE MICRODIAGNOSTIC
1968
1969 ;NOW DELAY LONG ENOUGH FOR THE MICRODIAGNOSTIC TO COMPLETE
1970
1971 003356 013701 002262 2$: MOV DELAY1,R1 ;INITIALIZE THE LOOP COUNTER FOR DELAY LOOP
1972 003362 001402 BEQ 1$ ; EXIT DELAY LOOP IF THE TIME HAS EXPIRED
1973 003364 005301 DEC R1 ; ELSE, DECREMENT THE LOOP COUNTER AND
1974 003366 000775 BR 2$ ; CONTINUE TO LOOP.
1975 003370 1$: ; TIME-UP!
1976 003370 132777 000200 176724 BITB #RUN,@BSEL1 ;CHECK THE RUN BIT --
1977 003376 001410 BEQ 3$ ;IF NOT SET, GO REPORT THE ERROR
1978
1979 ;IF THE RUN BIT IS SET, MICRODIAGNOSTICS ARE COMPLETE.
1980 ;CHECK IF ALL MICRODIAGNOSTICS PASSED.
1981
1982 003400 127737 176730 002630 4$: CMPB @BSEL6,BSELRS+6 ;THIS CHECKS THE BYTE IN B-SELECT 6 FOR THE
1983 ;VALID MICRODIAGNOSTIC COMPLETION CODE.
1984 003406 001004 BNE 3$ ;IF BAD, GO REPORT ERROR
1985
1986 003410 127737 176714 002626 CMPB @BSEL4,BSELRS+4 ;ELSE, CHECK FOR THE VALID CODE FOR A DMV-11
1987 003416 001420 BEQ 6$ ;IF THIS TOO IS CORRECT THEN NO ERROR EXISTS
1988 ;ELSE, FALL INTO THE ERROR REPORTING CODE

```

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MASCLR - MASTER CLEAR SUBROUTINE

```

1989
1990 003420 004737 004232 3$: JSR PC,GETBSR ;GET THE BSEL REGISTERS FOR DUMPING
1991 003424 GTDF 20$,ERR3 ;MASTER CLEAR ERROR
1992 ; QUEUE 'DEVICE FATAL' ERROR # 1
1993 003424 012737 000001 002202 MOV #T.EDF,ERRTYP
1994 003432 012737 000001 002204 MOV #1,ERRNBR
1995 003440 012737 003466 002206 MOV #20$,ERRMSG
1996 003446 012737 006220 002210 MOV #ERR3,ERRBLK
1997 003454 000261 SEC ;INDICATE TO THE CALLING ROUTINE THAT
1998 003456 000401 BR 7$ ; AN ERROR WAS DETECTED
1999
2000 003460 000241 6$: CLC ;CLEAR THE CARRY BIT TO INDICATE NO ERROR
2001 003462 012601 7$: MOV (SP)+,R1 ;RESTORE REGISTER ONE
2002 003464 000207 RTS PC ; RETURN TO THE CALLER
2003 003466 040515 052123 051105 .NLIST BEX
;ASCIZ /MASTER CLEAR FAILURE/
.LIST BEX
.EVEN
2004 003514

```


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M-LOOP -- MSTCLR -- MASTER CLEAR & ENTER M-LOOP

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003514 012777 140400 176576
003522 010346
003524 012703 000014
003530 077301
003532 012603
003534 132777 000200 176562
003542 001023
003544 004737 004374
003550 012737 000301 002254
003556
003556 012737 000001 002202
003564 012737 000002 002204
003572 012737 016115 002206
003600 012737 006232 002210
003606 000261
003610 000401
003612 000241
003614 000207

```

.SBTTL M-LOOP -- MSTCLR -- MASTER CLEAR & ENTER M-LOOP
*****
MSTCLR -- MASTER CLEAR & ENTER M-LOOP

CALLING SEQUENCE:

      JSR      PC,MSTCLR
      BCC     NS          ;IF NO ERROR OCCURED, PROCEED WITH ROUTINE
      ERROR   ;AN ERROR MESSAGE HAS BEEN STACKED: PRINT IT
      <ANY OTHER SPECIAL ERROR PROCESSING MAY BE DONE HERE (I.E. CKLOOP)>

NS:   <RESUMPTION OF NORMAL PROCESSING>
-----*****

MSTCLR: MOV      #<RUN!MCLR!MREQ>*256.,@SELO ;INITIATE M-LOOP

      MOV      R3,-(SP)
      MOV      #12.,R3          ;WAIT FOR THE M-LOOP TO FINISH THE OPERATION
1$:   SOB      R3,1$
      MOV      (SP)+,R3

      BITB    #MRDY,@BSEL2      ;DID THE M-LOOP FINISH
      BNE     5$                ;YES, GOOD. RETURN
      JSR     PC,GETWSR         ;GET BYTE SELECT REGISTERS
      MOV     #RUN!MCLR!MREQ,GDATA ;IDENTIFY REQUESTED FUNCTION
      GTDF   EM3,ERR4          ;'MRDY' TIMEOUT
      ;      QUEUE 'DEVICE FATAL' ERROR # 2
                                MOV     #T.EDF,ERRTYP
                                MOV     #2,ERRNBR
                                MOV     #EM3,ERRMSG
                                MOV     #ERR4,ERRBLK

      SEC     ;SET CARRY TO INDICATE ERROR
      BR     9$                ;EXIT WITH THE 'ERROR' FLAG (CARRY BIT) SET
5$:   CLC     ;CLEAR C BIT FOR NO ERRORS
9$:   RTS     PC                ;RETURN

```

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M-LOOP -- READ

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003616 012577 176506
003622 112777 000001 176474
003630 010346
003632 012703 000032
003636 077301
003640 012603
003642 132777 000200 176454
003650 001023
003652 004737 004374
003656 012737 000001 002254
003664
003664 012737 000001 002202
003672 012737 000003 002204
003700 012737 016135 002206
003706 012737 006232 002210
003714 000261
003716 000401
003720 000241
003722 117735 176406
003726 000205

```
.SBTTL M-LOOP -- READ
*****
READ - READ THE SPECIFIED ADDRESS WITHIN THE DMV-11
CALLING SEQUENCE:
      JSR      R5,READ
      .WORD   <ADDRESS OF REGISTER WITHIN DMV-11>
      .WORD   <DESTINATION ADDRESS WITHIN LSI-11>
      BCC     NS          ;IF NO ERROR OCCURED, PROCEED WITH ROUTINE
      ERROR   ;AN ERROR MESSAGE HAS BEEN STACKED: PRINT IT
      <ANY OTHER SPECIAL ERROR PROCESSING MAY BE DONE HERE (I.E. CKLOOP)>
NS:    <RESUMPTION OF NORMAL PROCESSING>
*****
READ:  MOV      (R5)+,@SEL4      ;SETUP SOURCE POINTER
        MOVB    #REDLOC,@BSEL2  ;TELL M-LOOP TO GIVE US THE REQUESTED DATA
1$:    MOV      R3,-(SP)
        MOV     #26,R3          ;WAIT FOR THE M-LOOP TO FINISH THE OPERATION
        SOB    R3,1$
        MOV     (SP)+,R3
        BITB   #MRDY,@BSEL2     ;DID THE M-LOOP FINISH
        BNE    5$              ;YES, GOOD. RETURN
        JSR    PC,GETWSR        ;GET BYTE SELECT REGISTERS
        MOV    #REDLOC,GDATA    ;IDENTIFY REQUESTED FUNCTION
        GTDF   EM4,ERR4        ;'MRDY' TIMEOUT
        ;      QUEUE 'DEVICE FATAL' ERROR # 3
        MOV    #T.EDF,ERRTYP
        MOV    #3,ERRNBR
        MOV    #EM4,ERRMSG
        MOV    #ERR4,ERRBLK
        SEC
        BR     6$              ;INDICATE AN ERROR HAS BEEN STACKED
        ;RETURN WITH THAT INDICATION
5$:    CLC
6$:    MOVB    @BSEL6,@(R5)+    ;INDICATE 'NO ERROR'
        RTS    R5              ;PUT DATA WHERE CALLER WANTS IT
        ;RETURN
```

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M-LOOP -- READ IMMEDIATE

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2110
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003730
003730 012577 176374
003734 112777 000001 176362

003742 010346
003744 012703 000015
003750 077301
003752 012603

003754 132777 000200 176342
003762 001023

003764 004737 004374
003770 012737 000001 002254
003776

003776 012737 000001 002202
004004 012737 000004 002204
004012 012737 016135 002206
004020 012737 006232 002210
004026 000261
004030 000401

004032 000241
004034 017725 176274
004040 000205

.SBTTL M-LOOP -- READ IMMEDIATE

: READI - READ IMMEDIATE THE SPECIFIED ADDRESS WITHIN THE DMV-11
:
: CALLING SEQUENCE:
:
: JSR R5,READI
: .WORD <ADDRESS OF REGISTER WITHIN DMV-11>
: .WORD <DESTINATION -- CONTENTS OF REG. IS PUT HERE>
: BCC NS ;IF NO ERROR OCCURED, PROCEED WITH ROUTINE
: ERROR ;AN ERROR MESSAGE HAS BEEN STACKED: PRINT IT
: <ANY OTHER SPECIAL ERROR PROCESSING MAY BE DONE HERE (I.E. CKLOOP)>
:
: NS: <RESUMPTION OF NORMAL PROCESSING>
:
:-----*****

READI:
MOV (R5)+,@SEL4 ;SETUP SOURCE POINTER
MOVB #REDLOC,@BSEL2 ;TELL M-LOOP TO GIVE US THE REQUESTED DATA

MOV R3,-(SP)
MOV #1\$,R3 ;WAIT FOR THE M-LOOP TO FINISH THE OPERATION
1\$: SOB R3,1\$
MOV (SP)+,R3

BITB #MRDY,@BSEL2 ;DID THE M-LOOP FINISH
BNE 5\$;YES, GOOD. RETURN

JSR PC,GETWSR ;GET BYTE SELECT REGISTERS
MOV #REDLOC,GDATA ;IDENTIFY REQUESTED FUNCTION
GTDF EM4,ERR4 ;'MRDY' TIMEOUT
: QUEUE 'DEVICE FATAL' ERROR # 4
: MOV #T EDF,ERRTYP
: MOV #4,ERRNBR
: MOV #EM4,ERRMSG
: MOV #ERR4,ERRBLK

SEC ;INDICATE AN ERROR HAS BEEN STACKED
BR 6\$;RETURN WITH THAT INDICATION

5\$: CLC ;INDICATE 'NO ERROR'
6\$: MOV @SEL6,(R5)+ ;PUT DATA WHERE CALLER WANTS IT
RTS R5 ;RETURN

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M-LOOP -- WRITE

2126
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2142
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2145

004042 012577 176262
004046 113577 176262
004052 000404

```

.SBT _ M-LOOP -- WRITE
*****
WRITE - WRITE THE SPECIFIED DATA INTO THE SPECIFIED DMV-11 ADDRESS
CALLING SEQUENCE:
      JSR      R5,WRITE
      .WORD   <ADDRESS OF REGISTER WITHIN DMV-11>
      .WORD   <ADDRESS OF DATA BYTE>
      BCC     NS          ;IF NO ERROR OCCURED, PROCEED WITH ROUTINE
      ERROR   ;AN ERROR MESSAGE HAS BEEN STACKED: PRINT IT
              <ANY OTHER SPECIAL ERROR PROCESSING MAY BE DONE HERE (I.E. CKLOOP)>
      NS:    <RESUMPTION OF NORMAL PROCESSING>
*****
WRITE:  MOV     (R5)+,@SEL4      ;SETUP SOURCE POINTER
        MOVB   @(R5)+,@SEL6     ;MAKE DATA AVAILABLE TO M-LOOP
        BR     MLWRI            ;THE REST OF THIS ROUTINE IS THE SAME AS 'WRITEI'

```

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M-LOOP -- WRITE IMMEDIATE

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2168
2169
2170
2171
2172
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004054
004054 012577 17625C
004060 012577 176250
004064 112777 000002 176232

004072 010346
004074 012703 000050
004100 077301
004102 012603

004104 132777 000200 176212
004112 001023
004114 004737 004374
004120 012737 000002 002254
004126

004126 012737 000001 002202
004134 012737 000005 002204
004142 012737 016135 002206
004150 012737 006232 002210
004156 000261
004160 000401

004162 000241
004164 000205

.SBTTL M-LOOP -- WRITE IMMEDIATE

WRITEI - WRITE IMMEDIATE THE SPECIFIED DATA INTO THE SPECIFIED DMV-11 ADDRESS
CALLING SEQUENCE:
JSR R5,WRITEI
.WORD <ADDRESS OF REGISTER WITHIN DMV-11>
.WORD <DATA FIELD -- DATA TO BE WRITTEN IN DMV-11>
BCC NS ;IF NO ERROR OCCURED, PROCEED WITH ROUTINE
ERROR ;AN ERROR MESSAGE HAS BEEN STACKED: PRINT IT
<ANY OTHER SPECIAL ERROR PROCESSING MAY BE DONE HERE (I.E. CKLOOP)>
NS: <RESUMPTION OF NORMAL PROCESSING>

WRITEI:
MOV (R5)+,@SEL4 ;SETUP SOURCE POINTER
MOV (R5)+,@SEL6 ;MAKE DATA AVAILABLE TO M-LOOP
MLWRI: MOVB #WRILOC,@SEL2 ;TELL M-LOOP TO WRITE THE DATA

MOV R3,-(SP)
MOV #40,R3 ;WAIT FOR THE M-LOOP TO FINISH THE OPERATION
1\$: SOB R3,1\$
MOV (SP)+,R3

BITB #MRDY,@SEL2 ;DID THE M-LOOP FINISH
BNE 5\$;YES, GOOD. RETURN
JSR PC,GETWSR ;GET BYTE SELECT REGISTERS
MOV #WRILOC,GDATA ;IDENTIFY REQUESTED FUNCTION
GDF EM4,ERR4 ;'MRDY' TIMEOUT
; QUEUE 'DEVICE FATAL' ERROR # 5
MOV #T.EDF,ERRTYP
MOV #5,ERRNER
MOV #EM4,EKRMSG
MOV #ERR4,ERRBLK

SEC ;INDICATE AN ERROR HAS BEEN STACKED
BR 6\$;RETURN WITH THAT INDICATION

5\$: CLC ;INDICATE 'NO ERROR'
6\$: RTS R5 ;RETURN

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M-LOOP -- WRITE IMMEDIATE

2188
2189
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2197
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2200
2201
2202
2203
2204
2205
2206
2207
2208
2209
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2211
2212
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2214
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2216
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2219

004166 010146
004170 012737 000077 004210
004176 012537 004212
004202 012501
004204
004204 004537 004042
004210 000077
004212 000000
004214 005237 004210
004220 005237 004212
004224 077111
004226 012601
004230 000205

```

.SBTTL M-LOOP --- BLOCK MOVE (11 CPU ==> DMV-11)
*****
: MOVE A BLOCK OF BYTES FROM THE 11 CPU'S MEMORY TO THE DMV-11'S RAM
: THE DESTINATION ADDRESS IS ALWAYS THE SAME -- 77 (OCT) OR 003F (HEX)
: CALLING SEQUENCE:
: JSR R5,MOVLTD
: .WORD <SOURCE ADDRESS OF DATA>
: .WORD <# OF BYTE TO BE MOVED>
*****
MOVLTD: MOV R1,-(SP) ;SAVE THE REGISTER WE'LL BE A NEED'N
MOV #77,10$ ;DESTINATION ADDRESS IS ALWAYS THE SAME
MOV (R5)+,11$ ;SETUP THE SOURCE ADDRESS
MOV (R5)+,R1 ;INITIALIZE THE BYTE COUNT

5$: JSR R5,WRITE ;WRITE ONE BYTE INTO THE DMV-11'S RAM
10$: 77 ;THE RAM'S LOCATION
11$: 0 ;THE DATA BYTE'S LOCATION
INC 10$ ;POINT TO NEXT RAM BYTE
INC 11$ ;POINT TO NEXT DATA BYTE
SOB R1,5$ ;IF NOT DONE, LOOP
;ELSE, CLEAN-UP AND RETURN
MOV (SP)+,R1 ;RESTORE R1
RTS R5 ; & RETURN

```

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GETBSR -- GET BYTE SELECT REGISTERS

.SBTTL GETBSR -- GET BYTE SELECT REGISTERS

```

*****
GET THE CONTENTS OF ALL CONTROL AND STATUS REGISTERS

FUNCTION - THIS SUBROUTINE COLLECTS THE CONTENTS OF THE
          BYTE SELECT REGISTERS FOR THE PURPOSE OF DISPLAY.

ENTRY CONDITIONS - NONE
EXIT CONDITIONS - NONE
REGISTER'S DESTROYED - NONE
*****

```

2220
2221
2222
2223
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2225
2226
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2236

2237	004232	117737	176062	002212
2238	004240	117737	176056	002214
2239	004246	117737	176052	002216
2240	004254	117737	176046	002220
2241	004262	117737	176042	002222
2242	004270	117737	176036	002224
2243	004276	117737	176032	002226
2244	004304	117737	176026	002230
2245	004312	117737	176022	002232
2246	004320	117737	176016	002234
2247	004326	117737	176012	002236
2248	004334	117737	176006	002240
2249	004342	117737	176002	002242
2250	004350	117737	175776	002244
2251	004356	117737	175772	002246
2252	004364	117737	175766	002250
2253	004372	000207		

```

GETBSR:  MOV  @BSSEL0,BSR0      ;PUT THE CURRENT CSR VALUES INTO THE PRINT-OUT
         MOV  @BSSEL1,BSR1      ;TABLE
         MOVE @BSSEL2,BSR2
         MOV  @BSSEL3,BSR3
         MOV  @BSSEL4,BSR4
         MOV  @BSSEL5,BSR5
         MOV  @BSSEL6,BSR6
         MOV  @BSSEL7,BSR7
         MOV  @BSSEL10,BSR10
         MOV  @BSSEL11,BSR11
         MOV  @BSSEL12,BSR12
         MOV  @BSSEL13,BSR13
         MOV  @BSSEL14,BSR14
         MOV  @BSSEL15,BSR15
         MOV  @BSSEL16,BSR16
         MOV  @BSSEL17,BSR17
         RTS   PC                ;RETURN TO CALLER

```

.SBTTL GETWSR -- GET WORD SELECT REGISTERS
: 'WORD' VERSION OF ABOVE SUBROUTINE

2257				
2258	004374	017737	175720	002212
2259	004402	017737	175716	002214
2260	004410	017737	175714	002216
2261	004416	017737	175712	002220
2262	004424	017737	175710	002222
2263	004432	017737	175706	002224
2264	004440	017737	175704	002226
2265	004446	017737	175702	002230
2266	004454	000207		

```

GETWSR:  MOV  @WSEL0,WSR0      ;MOVE THE 4 WORD REGISTERS TO THE OTHERWISE
         MOV  @WSEL2,WSR2      ;BYTE TABLE
         MOV  @WSEL4,WSR4
         MOV  @WSEL6,WSR6
         MOV  @WSEL10,WSR10
         MOV  @WSEL12,WSR12
         MOV  @WSEL14,WSR14
         MOV  @WSEL16,WSR16
         RTS   PC                ;RETURN TO CALLER

```

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2321
2322

004456 010146
004460 112537 002427
004464 112537 002431
004470 111537 002441
004474 142737 177477 002441
004502 012501

004504 106301
004506 042701 177677
004512 140177 175610
004516 106301
004520 052701 000100
004524 110137 002447

004530 004537 004042
004534 120016
004536 002447
004540 103431

004542 004537 003616
004546 120013
004550 002440

.INITT1 -- INITIALIZE TIMER # 1

.SBTTL .INITT1 -- INITIALIZE TIMER # 1

* INITT1 - INITIALIZE TIMER # 1

* CALLING SEQUENCE:

* JSR R5,INITT1
* .WORD <VALUE LOADED INTO THE T1 LATCH @ T1LL & T1LH>
* .WORD <BITS 6 & 7 WILL BE LOADED INTO 'ACR', BIT 5 WILL BE
* USED TO SET OR CLEAR BIT 6 ('T1') OF THE INTERRUPT
* ENABLE REGISTER ('IER')>

* SEQUENCE OF EVENTS HEREIN:

* SET THE VIA'S INTERRUPT ENABLE REGISTER ('IER')
* SET THE VIA'S 'ACR'
* SET T1L-L (ADDR 06)
* SET T1L-H (ADDR 07)
* RETURN WITHOUT ANY ERROR CHECKING

INITT1: MOV R1, -(SP) ;SAVE THE REGISTER WE WILL BE USING
MOV (R5)+,TMP6+1 ;SETUP VALUES TO BE LOADED INTO THE LATCHES
MOV (R5)+,TMP7+1
MOV (R5),TMPB+1 ;GET & PROCESS BITS FOR ACR 6 & 7
BIC #^C<BIT6+BIT7>,TMPB+1 ;EXTRACT BITS 6 & 7 & SAVE THEM FOR LATER
MOV (R5)+,R1 ;NOW, GET THE BIT TO BE USED IN SETTING OR
;CLEARING BIT 6 OF 'IER'

; THE PASSED BIT IS IN THE WRONG POSITION BUT, IT SHOULD CONTROL THE OPERATION.
; WE KNOW WE ARE SETTING OR CLEARING BIT 6 -- THUS, THE PASSED BIT WILL BECOME
; THE CONTROLLING BIT 7 AND WE WILL 'JR' IN THE BIT WE WISH TO BE CONTROLLED
; (BIT 6).

ASLB R1 ;THIS PUTS THE PASSED BIT INTO BIT 6.
BIC #^C<BIT6>,R1 ;WHILE HERE, CLEAR ALL OTHER BITS AND
BICB R1,@SEL3 ;CLEAR THE INTERRUPT FLAG IN THE SELECT REG.
ASLB R1 ;NOW THE BIT IS IN THE CONTROLLING POSITION
BIS #BIT6,R1 ;SET BIT 6
MOV R1,TMPE+1 ;THE CALL WILL NOW WRITE THE APPROPRIATE VALUE

JSR R5,WRITE ;WRITE TO
IENR ;THE VIA'S IER
TMPE+1 ;INTERRUPT ENABLE/DISABLE INFORMATION
BCS 63\$;EXIT ON ERROR

JSR R5,READ ;READ THE CURRENT SETTING OF
ACR ;THE VIA'S ACR
TMPB

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.INITT1 -- INITIALIZE TIMER # 1

```

2323 004552 103424          BCS      63$           ;EXIT ON ERROR
2324
2325 004554 013701 002440    MOV      TMPB,R1       ;GET THAT VALUE
2326 004560 042701 177477    BIC      #^C<B;T6+BIT7>,R1 ;CLEAR BITS 6 & 7
2327 004564 150137 002441    BISB     R1,TMPB+1     ;ADD CURRENT BITS 0 --> 5 TO NEW BITS 6 & 7
2328
2329 004570 004537 004042    JSR      R5,WRITE     ;WRITE THE NEW REGISTER SETTING TO VIA'S ACR
2330 004574 120013
2331 004576 002441          ACR      TMPB+1
2332 004600 103411          BCS      63$           ;EXIT ON ERROR
2333
2334 004602 004537 004042    JSR      R5,WRITE     ;WRITE TO
2335 004606 120006          T1LL     ;LOW ORDER LATCH REGISTER (T1L-L)
2336 004610 002427          TMP6+1   ;THE VALUE PASSED
2337 004612 103404          BCS      63$           ;EXIT ON ERROR
2338
2339 004614 004537 004042    JSR      R5,WRITE     ;WRITE TO
2340 004620 120007          T1LH     ;HIGH ORDER LATCH REGISTER (T1L-H)
2341 004622 002431          TMP7+1   ;THE VALUE PASSED
2342
2343
2344
; DON'T WAIT AROUND FOR ANYTHING TO HAPPEN -- JUST (JEST) RETURN!
2345 004624 012601
2346 004626 000205
63$:  MOV      (SP)+,R1     ;BUT FIRST RESTORE R1
      RTS      R5         ;THEN RETURN

```

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.INITT2 -- INITIALIZE TIMER # 2

.SBTTL .INITT2 -- INITIALIZE TIMER # 2

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2402

```

:*****
* INITT2 - INITIALIZE TIMER # 2
*
*   CALLING SEQUENCE:
*
*       JSR     R5,INITT2
*       .WORD  <VALUE LOADED INTO 'T2L-L' & 'T2C-H'>
*       .BYTE  <BIT 5 WILL BE LOADED INTO 'ACR', BIT 4 WILL BE USED
*               TO SET OR CLEAR BIT 5 ('T2') OF THE INTERRUPT ENABLE
*               REGISTER ('IER')>
*       .BYTE  <UNUSED>
*
* SEQUENCE OF EVENTS HEREIN:
*
*   SET THE VIA'S INTERRUPT ENABLE REGISTER ('IER')
*
*   SET THE VIA'S 'ACR'
*
*   SET T2L-L (ADDR 08)
*
*   SET T2C-H (ADDR 09)
*
*   RETURN WITHOUT ANY ERROR CHECKING
:*****

```

```

004630 010146
004632 112537 002433
004636 112537 002435
004642 111537 002441
004646 142737 177737 002441
004654 012501
004656 106301
004660 042701 177737
004664 140177 175436
004670 106301
004672 106301
004674 052701 000040
004700 110137 002447
004704 004537 004042
004710 120016
004712 002447
004714 103431
004716 004537 003616

```

```

INITT2: MOV     R1, -(SP)           ;SAVE THE REGISTER WE WILL BE USING
        MOVB   (R5)+,TMP8+1    ;SETUP VALUES TO BE WRITTEN INTO COUNTER
        MOVB   (R5)+,TMP9+1
        MOVB   (R5),TMP8+1     ;GET & PROCESS BIT FOR ACR 5
        BICB   #^CBITS,TMP8+1
        MOV    (R5)+,R1        ;NOW, GET THE BIT TO BE USED IN SETTING OR
                                ;CLEARING BIT 5 OF 'IER'

; THE PASSED BIT IS IN THE WRONG POSITION BUT, IT SHOULD CONTROL THE OPERATION.
; WELL, WE KNOW WE ARE SETTING OR CLEARING BIT 5.  THUS, THE PASSED BIT WILL
; BECOME THE CONTROLLING BIT 7 AND WE'LL 'OR' IN THE BIT WE WISH TO BE
; CONTROLLED (BIT 5).

        ASLB   R1              ;THE PASSED BIT IS NOW IN POSITION TO
        BIC    #^CBITS,R1      ;CLEAR ALL UNWANTED BITS AND
        BICB   R1,@SEL3        ;CLEAR THE INT. FLAG IN THE SELECT REGISTER
        ASLB   R1              ;NOW PUT THE BIT INTO THE CONTROL POSITION
        ASLB   R1
        BIS    #BITS,R1        ;THEN SET BIT 5
        MOVB   R1,TMPE+1       ;THE CALL WILL NOW WRITE THE APPROPRIATE VALUE

        JSR    R5,WRITE        ;WRITE TO
                                ;THE VIA'S IER
                                ;INTERRUPT ENABLE/DISABLE INFORMATION
        BCS    63$             ;EXIT ON ERROR

        JSR    R5,READ         ;READ THE CURRENT SETTING OF

```

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.INITT2 -- INITIALIZE TIMER # 2

```

2403 004722 120013          ACR          ;THE VIA'S ACR
2404 004724 002440          TMPB
2405 004726 103424          BCS          63$          ;EXIT ON ERROR
2406
2407 004730 113701 002440    MOVB        TMPB,R1      ;GET THAT VALUE
2408 004734 042701 000040    BIC        #BIT5,R1     ;CLEAR THE CURRENT SETTING OF BIT 5
2409 004740 150137 002441    BISB        R1,TMPB+1   ;SET REMAINING BITS IN THE VALUE TO BE WRITTEN
2410
2411 004744 004537 004042    JSR        R5,WRITE     ;WRITE TO
2412 004750 120013          ACR          ;THE VIA'S ACR
2413 004752 002441          TMPB+1
2414 004754 103411          BCS          63$          ;EXIT ON ERROR
2415
2416 004756 004537 004042    JSR        R5,WRITE     ;WRITE TO
2417 004762 120010          T2LL        ;LOW ORDER LATCH & COUNTER (T2L-L)
2418 004764 002433          TMPB+1     ;THE PASSED VALUE
2419 004766 103404          BCS          63$          ;EXIT ON ERROR
2420
2421 004770 004537 004042    JSR        R5,WRITE     ;WRITE TO
2422 004774 120011          T2CH        ;HIGH ORDER COUNTER (T2C-H) <ALSO STARTS CTR>
2423 004776 002435          TMP9+1     ;THE PASSED VALUE
2424
2425          ; DON'T WAIT AROUND FOR ANYTHING TO HAPPEN -- JUST (JEST) RETURN!
2426
2427 005000 012601          63$: MOV     (SP)+,R1     ;BUT FIRST RESTORE R1
2428 005002 000205          RTS        R5          ;THEN RETURN
2429

```

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MOVSW -- MOVE A STRING OF WORDS

2430
 2431
 2432
 2433
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 2436
 2437
 2438
 2439
 2440
 2441
 2442
 2443 005004 010146
 2444 005006 010246
 2445 005010 010346
 2446
 2447 005012 012501
 2448 005014 012502
 2449 005016 012503
 2450
 2451 005020 012122
 2452 005022 077302
 2453
 2454
 2455 005024 012603
 2456 005026 012602
 2457 005030 012601
 2458
 2459 005032 000205
 2460

```

.SBTTL MOVSW -- MOVE A STRING OF WORDS
*****
: MOVSW -- MOVE A STRING OF WORDS
:
: CALLING SEQUENCE:
:
: JSR    R5,MOVSW
: .WORD <ADDRESS OF SOURCE STRING>
: .WORD <ADDRESS OF DESTINATION STRING>
: .WORD <# OF WORDS TO MOVE>
:
:-----*****
MOVSW:  MOV    R1,-(SP)      ;SAVE THE REGISTERS WE'LL BE USING
        MOV    R2,-(SP)
        MOV    R3,-(SP)
        MOV    (R5)+,R1   ;INITIALIZE SOURCE POINTER
        MOV    (R5)+,R2   ;          DESTINATION POINTER
        MOV    (R5)+,R3   ;          COUNTER
1$:     MOV    (R1)+,(R2)+ ;MOVE IN 1 WORD OF DATA
        SOB   R3,1$      ;IF MORE DATA, LOOP
                          ;ELSE, RESTORE REGISTERS AND RETURN
        MOV    (SP)+,R3   ;RESTORE REGISTERS
        MOV    (SP)+,R2
        MOV    (SP)+,R1
        RTS   R5         ;RETURN TO CALLING ROUTINE

```

CVD MBA.P11 18-DEC-80 15:53

MOVSB -- MOVE A STRING OF BYTES

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2474 005034 010146
2475 005036 010246
2476 005040 010346
2477
2478 005042 012501
2479 005044 012502
2480 005046 012503
2481
2482 005050 112122
2483 005052 077302
2484
2485
2486 005054 012603
2487 005056 012602
2488 005060 012601
2489
2490 005062 000205

```
.SBTTL MOVSB -- MOVE A STRING OF BYTES
:*****
: MOVSB -- MOVE A STRING OF BYTES
:
: CALLING SEQUENCE:
:
: JSR    R5,MOVSB
: .WORD <ADDRESS OF SOURCE STRING>
: .WORD <ADDRESS OF DESTINATION STRING>
: .WORD <# OF BYTES TO MOVE>
:-----*****
MOVSB:  MOV    R1,-(SP)      ;SAVE THE REGISTERS WE'LL BE USING
        MOV    R2,-(SP)
        MOV    R3,-(SP)
:
        MOV    (R5)+,R1   ;INITIALIZE SOURCE POINTER
        MOV    (R5)+,R2   ;          DESTINATION POINTER
        MOV    (R5)+,R3   ;          COUNTER
1$:     MOVB   (R1)+,(R2)+ ;MOVE IN 1 BYTE OF DATA
        SOB    R3,1$      ;IF MORE DATA, LOOP
:ELSE, RESTORE REGISTERS AND RETURN
:
        MOV    (SP)+,R3   ;RESTORE REGISTERS
        MOV    (SP)+,R2
        MOV    (SP)+,R1
:
        RTS    R5        ;RETURN TO CALLING ROUTINE
```

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XORSW -- XOR TWO WORD TABLES

2491
2492
2493
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2500
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2536
2537
2538
2539

005064 010146
005066 010246
005070 010346
005072 010446
005074 012501
005076 012502
005100 012503
005102 012504
005104 010546
005106 012113
005110 012205
005112 074523
005114 077404
005116 012605
005120 012604
005122 012603
005124 012602
005126 012601
005130 000205
005132 000207

```

.SBTTL XORSW -- XOR TWO WORD TABLES
;*****
; XORSW -- DEVELOP THE EXCLUSIVE OR'S BETWEEN TWO STRINGS OF WORDS
;
; CALLING SEQUENCE:
;
; JSR    R5,XORSW
; .WORD  <ADDRESS OF FIRST SOURCE STRING>
; .WORD  <ADDRESS OF SECOND SOURCE STRING>
; .WORD  <ADDRESS OF 'XOR' STRING>
; .WORD  <# OF BYTES TO MOVE>
;-----
XORSW:  MOV    R1,-(SP)          ;SAVE THE REGISTERS WE'LL BE USING
        MOV    R2,-(SP)
        MOV    R3,-(SP)
        MOV    R4,-(SP)
;
        MOV    (R5)+,R1       ;INITIALIZE SOURCE POINTER # 1
        MOV    (R5)+,R2       ; SOURCE POINTER # 2
        MOV    (R5)+,R3       ; 'XOR' STRING POINTER
        MOV    (R5)+,R4       ; COUNTER
;
        MOV    R5,-(SP)       ;NOW WE CAN SAVE R5 FOR THE RETURN
;
1$:     MOV    (R1)+,(R3)      ;MOVE ONE WORD TO THE DESTINATION FIELD
        MOV    (R2)+,R5       ;GET SECOND WORD & SETUP FOR XOR INSTRUCTION
        XOR    R5,(R3)+      ;PERFORM ACTUAL XOR
        SOB   R4,1$          ;IF MORE DATA, LOOP
;ELSE, RESTORE REGISTERS AND RETURN
;
        MOV    (SP)+,R5       ;RESTORE REGISTERS
        MOV    (SP)+,R4
        MOV    (SP)+,R3
        MOV    (SP)+,R2
        MOV    (SP)+,R1
;
        RTS    R5            ;RETURN TO CALLING ROUTINE
;-----
.SBTTL STALL -- DELAY FOR 10.5 MICRO-SEC'S (ON LSI-11)
;*****
; STALL -- THIS SUBROUTINE STALLS FOR ABOUT 10.5 MICRO-SECONDS
;-----
STALL:  RTS    PC

```

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NPREAD -- 'READ' CONTENTS OF ALL NPR REGISTERS

.SBTTL NPREAD -- 'READ' CONTENTS OF ALL NPR REGISTERS

:+*****
: NPREAD -- READ ALL NPR REGISTERS INTO LOC'S STARTING @ 'W0'
:--*****

2540			
2541			
2542			
2543			
2544			
2545			
2546	005134	004537	003616
2547	005140	123004	
2548	005142	002740	
2549	005144	103447	
2550	005146	004537	003616
2551	005152	123001	
2552	005154	002742	
2553	005156	103442	
2554	005160	004537	003616
2555	005164	123000	
2556	005166	002744	
2557	005170	103435	
2558	005172	004537	003616
2559	005176	000072	
2560	005200	002746	
2561	005202	103430	
2562	005204	004537	003616
2563	005210	000071	
2564	005212	002750	
2565	005214	103423	
2566	005216	004537	003616
2567	005222	000070	
2568	005224	002752	
2569	005226	103416	
2570	005230	004537	003616
2571	005234	000076	
2572	005236	002754	
2573	005240	103411	
2574	005242	004537	003616
2575	005246	000075	
2576	005250	002756	
2577	005252	103404	
2578	005254	004537	003616
2579	005260	000074	
2580	005262	002760	
2581			
2582	005264	000207	

```

NPREAD: JSR      R5,READ
          NPRCTL
          BT2
          BCS      10$           ;ON ERROR, EXIT
          JSR      R5,READ
          NPRDRH
          BT2+2
          BCS      10$           ;ON ERROR, EXIT
          JSR      R5,READ
          NPRDRL
          BT2+4
          BCS      10$           ;ON ERROR, EXIT
          JSR      R5,READ
          NPRAOX
          BT2+6
          BCS      10$           ;ON ERROR, EXIT
          JSR      R5,READ
          NPRAOH
          BT2+8.
          BCS      10$           ;ON ERROR, EXIT
          JSR      R5,READ
          NPRAOL
          BT2+10.
          BCS      10$           ;ON ERROR, EXIT
          JSR      R5,READ
          NPRAIX
          BT2+12.
          BCS      10$           ;ON ERROR, EXIT
          JSR      R5,READ
          NPRAIH
          BT2+14.
          BCS      10$           ;ON ERROR, EXIT
          JSR      R5,READ
          NPRAIL
          BT2+16.
10$:    RTS      PC           ;RETURN

```

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NPRMOV -- WORD/BYTE BLOCK MOVE USING THE NPR HARDWARE

```

2583 .SBTTL NPRMOV -- WORD/BYTE BLOCK MOVE USING THE NPR HARDWARE
2584 :*****
2585 : NPRMOV -- MOVE A BLOCK OF DATA THROUGH THE DMV'S NPR LOGIC
2586 :-----
2587
2588 : - - - - - I N I T I A L I Z A T I O N - - - - -
2589
2590 NPRMOV: MOV R1,-(SP) ;SAVE THE REGISTERS WE USE
2591 MOV R2,-(SP)
2592 MOV R3,-(SP)
2593
2594 1$: MOV (R5)+,R1 ;POINT TO TEST PATTERN
2595 MOV (R5)+,R2 ;POINT TO THE LSI-11 BUFFER AREA
2596 MOV (R5)+,R3 ;GET COUNT OF # OF WORDS IN TEST PATTERN
2597 MOV (R5)+,42$ ;LOAD UP THE COMMAND TO BE USED
2598
2599 CLR ERRFLG ;INITIALIZE ERROR FLAG
2600
2601 2601 005312 032737 000040 005612 BIT #NPRIO,42$ ;DETERMINE DIRECTION:
2602 005320 001403 BEQ 3$ ;LSI ==> DMV -- USE 'NPRAIL'
2603 005322 012746 000070 MOV #NPRACL,-(SP) ;DMV ==> LSI -- USE 'NPRACL'
2604 005326 000402 BR 4$
2605 005330 012746 000074 3$: MOV #NPRAIL,-(SP)
2606 005334 011637 005400 4$: MOV (SP),10$ ;SETUP LOW BYTE ADDRESS POINTER
2607 005340 005216 INC (SP) ;INCREMENT TO NEXT DMV ADDRESS
2608 005342 011637 005422 MOV (SP),14$ ;SETUP HIGH BYTE ADDRESS POINTER
2609 005346 005216 INC (SP) ;INCREMENT TO NEXT DMV ADDRESS
2610 005350 012637 005434 MOV (SP)+,17$ ;SETUP EXT. BYTE ADDRESS POINTER & RESTORE SP
2611
2612 : - - - - - R E - I N I T I A L I Z E L O O P ' S V A R I A B L E S - - - - -
2613
2614 6$: 005354
2615
2616 : - - - - - S E T U P A P P R O P R I A T E A D D R E S S - - - - -
2617
2618 2618 005354 032737 000040 005612 BIT #NPRIO,42$ ;DIRECTION OF TRANSFER?
2619 005362 001002 BNE 8$ ;DMV ==> LSI -- ADDR. SHOULD POINT TO BUFFER
2620 005364 010246 MOV R2,-(SP) ;LSI ==> DMV -- SAVE BUFFER POINTER
2621 005366 010102 MOV R1,R2 ; & POINT TO TEST DATA TO BE READ
2622 005370 010237 005402 8$: MOV R2,11$ ;SETUP LOW BYTE OF ADDRESS
2623 005374 004537 004054 JSR R5,WRITEI ;LOAD UP DESTINATION ADDRESS
2624 005400 000070 10$: NPRAOL ; NPR ADDRESS LOW BYTE
2625 005402 000000 11$: 0 ;*** MODIFIED FROM ABOVE ***
2626 005404 103510 BCS 45$ ;ON ERROR, EXIT
2627
2628 2628 005406 000302 SWAB R2 ;SETUP HIGH BYTE OF DESTINATION ADDRESS
2629 005410 010237 005424 MOV R2,15$
2630 005414 000302 SWAB R2 ; (RESTORE ADDRESS)
2631 005416 004537 004054 JSR R5,WRITEI ; SEND IT TO THE DMV
2632 005422 000071 14$: NPRAOH ; NPR ADDRESS HIGH BYTE
2633 005424 000000 15$: 0 ;*** MODIFIED FROM ABOVE ***
2634 005426 103477 BCS 45$ ;ON ERROR, EXIT
2635
2636 2636 005430 004537 004054 JSR R5,WRITEI ;MAKE SURE 'EXTENDED' BITS ARE CLEARED
2637 005434 000072 17$: NPRAOX ; NPR ADDRESS EXTENDED BYTE
2638 005436 000000 0 ; ACTUAL VALUE LOADED INTO THIS BYTE (WE HOPE!)

```


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NPRMOV -- WORD/BYTE BLOCK MOVE USING THE NPR HARDWARE

```

2639 005440 103472          BCS      45$          ;ON ERROR, EXIT
2640
2641          ; - - - - - S E T U P   D A T A - - - - -
2642
2643 005442 112137 005556      MOVB     (R1)+,30$      ;SETUP ONE BYTE OF DATA TO BE PASSED TO THE DMV
2644 005446 032737 000040 005612  BIT     #NPRIO,42$    ;DIRECTION OF TRANSFER?
2645 005454 001010          BNE     19$          ;DMV ==> LSI -- 'DATA-OUT'
2646          ;LSI ==> DMV -- 'DATA-IN'
2647
2648          ; ON AN 'LSI ==> DMV', R2 WAS STACKED & IT SHOULD NOW BE RESTORED:
2649
2650 005456 012602          MOV     (SP)+,R2      ;RESTORE R2
2651
2652          ; WHEN WE FALL THROUGH TO HERE, THE DIRECTION IS LSI ==> DMV -- ('DATA-IN').
2653          ; THIS MUST ALWAYS BE A WORD TRANSFER SO THAT BOTH HIGH & LOW BYTES MUST BE
2654          ; LOADED WITH THE BACKGROUND PATTERN (THE ONE'S COMPLEMENT OF THE TEST PATTERN):
2655
2656 005460 005137 005556      COM     30$          ;ONE'S COMPLEMENT THE LOW BYTE
2657 005464 112137 005600      MOVB   (R1)+,35$    ;SETUP THE HIGH BYTE AND
2658 005470 005137 005600      COM     35$          ; COMPLEMENT IT ALSO
2659 005474 000425          BR      28$          ;NO GO AROUND THE 'DATA-OUT' SETUP AND PASS
2660          ;THE BACKGROUND PATTERN TO THE DMV'S NPR DATA REG'S
2661
2662          ; IF WE GET TO HERE, THE DIRECTION IS DMV ==> LSI -- ('DATA-OUT').
2663          ; IF THIS IS A WORD XFER, BOTH HIGH & LOW BYTES MUST BE SETUP; IF BYTE, THEN
2664          ; WE MUST DETERMINE WHICH ONE (HIGH OR LOW) IS TO BE DONE AND ONLY LOAD THAT
2665          ; BYTE.  CONTRARY TO NORMAL PDP-11 OPERATION FOR A BYTE OPERATION FROM A
2666          ; REGISTER, THE ODD ADDRESSED BYTE WILL ALWAYS BE WRITTEN FROM THE HIGH DATA
2667          ; BYTE REGISTER AND THE EVEN ADDRESSED BYTE WILL ALWAYS BE WRITTEN FROM THE
2668          ; LOW DATA BYTE REGISTER.
2669
2670
2671 005476 113712 005556      19$:   MOVB   30$, (R2)    ;SETUP THE BACKGROUND PATTERN IN LSI'S MEM.
2672 005502 105112          COMB   (R2)          ; (THIS MAKES IT A 'BACKGROUND' PATTERN)
2673
2674 005504 032737 000010 005612  BIT     #NPRBYT,42$  ;IS THIS A BYTE OR WORD TRANSFER?
2675 005512 001007          BNE     21$          ;BYTE, GO DETERMINE WHICH ONE
2676 005514 111137 005600      MOVB   (R1),35$     ;WORD, SETUP THE HIGH BYTE OF NPR DATA
2677 005520 112162 000001      MOVB   (R1)+,1(R2)  ;ALSO, SETUP THE HIGH BYTE'S BACKGROUND PATTERN
2678 005524 105162 000001      COMB   1(R2)        ; (THIS MAKES IT A 'BACKGROUND' PATTERN)
2679 005530 000407          BR      28$          ;GO LOAD UP THE NPR DATA REG'S NOW
2680
2681 005532 032702 000001      21$:   BIT     #1,R2     ;IS LSI ADDRESS ODD OR EVEN (HIGH OR LOW BYTE)?
2682 005536 001404          BEQ     28$          ;EVEN (LOW BYTE), EVERYTHING'S OK -- GO LOAD IT
2683 005540 013737 005556 005600  MOV     30$,35$     ;ODD (HIGH BYTE). WE SETUP THE WRONG ONE --
2684          ; PUT THE DATA BYTE IN THE RIGHT PLACE
2685 005546 000411          BR      33$          ; AND GO LOAD IT INTO THE DMV'S NPR HIGH DATA BYTE REG.
2686
2687
2688 005550 004537 004054      28$:   JSR     R5,WRITEI   ;LOAD UP THE
2689 005554 123000          NPRDRL              ; NPR DATA REG. LOW BYTE
2690 005556 000000      30$:   0              ; DATA BYTE -- LOW
2691 005560 103422          BCS     45$          ;ON ERROR, EXIT
2692
2693 005562 032737 000010 005612  BIT     #NPRBYT,42$  ;IS THIS A BYTE OR WORD TRANSFER?
2694 005570 001005          BNE     40$          ;BYTE, THEN LEAVE THE NEXT BYTE OF DATA FOR LATER

```

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NPRMOV -- WORD/BYTE BLOCK MOVE USING THE NPR HARDWARE

```

2695
2696 005572 004537 004054
2697 005576 123001
2698 005600 000000
2699 005602 103411
2700
2701
2702
2703
2704 005604 004537 004054
2705 005610 123004
2706 005612 000044
2707 005614 103404
2708
2709
2710
2711 005616 004537 003730
2712 005622 123004
2713 005624 000000
2714 005626 103513
2715
2716
2717
2718
2719 005630 132737 000200 005624
2720 005636 001436
2721 005640 005237 002412
2722 005644 023727 002412 000001
2723 005652 001030
2724
2725
2726 005654 013737 005612 002414
2727 005662 013737 005624 002416
2728 005670 113737 005424 002421
2729 005676 113737 005402 002420
2730 005704
2731
2732 005704 012737 000001 002202
2733 005712 012737 000006 002204
2734 005720 012737 016316 002206
2735 005726 012737 007314 002210
2736 000006
2737
2738
2739
2740 005734 032737 000040 005612
2741 005742 001025
2742
2743
2744 005744 010237 005756
2745 005750 004537 003616
2746 005754 123000
2747 005756 000000
2748 005760 103436
2749 005762 005202
2750

```

```

33$: JSR R5,WRITEI ;WORD, LOAD UP THE
      NPRDRH ; NPR DATA REG. HIGH BYTE WITH
35$: 0 ; WHAT WE HOPE IS THE APPROPRIATE DATA!
      BCS 45$ ;ON ERROR, EXIT
; - - - - - INITIATE ONE TRANSFER - - - - -
40$: JSR R5,WRITEI ;MAKE THE 'NPR' LOGIC DO IT'S THING
      NPRCTL ; BY LOADING THE CONTROL REGISTER WITH
42$: NPRDL ; THE PASSED COMMAND
      BCS 45$ ;ON ERROR, EXIT
; - - - - - MAKE SURE THE XFER RAN OK - - - - -
      JSR R5,READI ;GET THE CONTROL REG. FOR IT'S STATUS
      NPRCTL
44$: 0
45$: BCS 63$ ;ON ERROR, EXIT
; BY NOW, THE TRANSFER SHOULD BE COMPLETE SO BIT 6 SHOULD BE = 0. THEREFOR
; THE ONLY SIGNIFICANT BIT IS BIT 7 WHICH SHOULD = 0 IF EVERYTHING WENT OK.
      BITB #NPRABT,44$ ;DID THE TRANSFER ABORT?
      BEQ 50$ ;NO, PROCEED WITH TESTING
      INC TMP0 ;YES, COUNT THE TIME-OUT
      CMP TMP0,#1 ;IS THIS THE FIRST OCCURANCE OF A TIMEOUT?
      BNE 50$ ;NO, THEN DON'T STACK THE ERROR MESSAGE
      ;YES, QUEUE UP A FATAL ERROR MESSAGE
      ;PASS TO ERROR HANDLER:
      ; CONTROL REGISTER AS WE SET IT
      ; CONTROL REGISTER AS READ
      ; LSI'S MEMORY ADDRESS
      MOV 42$,TMP
      MOV 44$,TP 2
      MOVB 15$,TMP3+1
      MOVB 11$,TMP3
      GTDF EM26E,ERR11 ;IN ORDER TO REPORT THE TIME-OUT ERROR,
      ; QUEUE 'DEVICE FATAL' ERROR # 6
      MOV #T.EDF,ERRTYP
      MOV #6,ERRNER
      MOV #EM26E,ERRMSG
      MOV #ERR11,ERRBLK
NPRTOE = $E ;THIS GETS THE ERROR # FOR TESTING LATER
; - - - - - IF 'LSI ==> DMV', RETRIEVE DATA - - - - -
50$: BIT #NPRIO,42$ ;DIRECTION?
      BNE 54$ ;DMV ==> LSI -- DATA ALREADY IN LSI-11
      ;LSI ==> DMV -- DATA IN REG'S, RETRIEVE IT
      MOV R2,51$ ;POINT TO LSI'S INPUT BUFFER
      JSR R5,READ ;GET ONE BYTE
      ; FROM THE LOW ORDER HALF OF THE DATA REG.
51$: () ;*** MODIFIED FROM ABOVE *** DESTINATION ADDR.
      EICS 63$ ;ON ERROR, EXIT
      INC R2 ;POINT TO NEXT BYTE OF THE BUFFER

```

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NPRMOV -- WORD/BYTE BLOCK MOVE USING THE NPR HARDWARE

```

2751 005764 032737 000010 005612      BIT    #NPRBYT,42$    ;WAS A BYTE OF WORD 'NPR' PERFORMED?
2752 005772 001022                      BNE    56$           ;BYTE -- EVERYTHING'S 'KOOL'!!
2753                                     ;WORD -- MOVE HIGH BYTE INTO BUFFER
2754 005774 010237 006006      MCV    R2,52$        ;POINT TO LSI'S I/P BUFFER
2755 006000 004537 003616      JSR    R5,READ      ;GET ONE BYTE
2756 006004 123001                      NPRDRH
2757 006006 000000      52$:    0           ;    FROM THE HIGH ORDER HALF OF THE DATA REG.
2758 006010 103422                      BCS    63$          ;*** MODIFIED FROM ABOVE *** DESTINATION ADDR.
2759 006012 005202                      INC    R2           ;ON ERROR, EXIT
2760 006014 000411                      BR     56$          ;POINT TO NEXT BYTE OF THE BUFFER
2761                                     ;DONE RETRIEVING DATA -- CHECK FOR MORE
2762                                     ; - - - - - DMV ==> LSI -- JUST ADVANCE LSI-11 ADDRESS - - - - -
2763
2764 006016 005202      54$:    INC    R2           ;BUMP THE LSI-11 ADDRESS
2765 006020 032737 000010 005612      BIT    #NPRBYT,42$    ;IS THIS A BYTE OR WORD TRANSFER?
2766 006026 001004                      BNE    56$           ;BYTE, THEN ADDRESS IS OK AS IS
2767 006030 005202                      INC    R2           ;WORD, BUMP ADDR. -- WE ALREADY DID THE HIGH BYTE
2768 006032 000402                      BR     56$
2769
2770                                     ; - - - - - TEST FOR MORE - - - - -
2771
2772 006034 000137 005354      55$:    JMP     6$           ;THIS LITTLE BIT IF CUTE LOGIC IS NECESSARY
2773                                     ;BECAUSE '6$' IS TOO FAR AWAY FOR A BRANCH!
2774 006040 077303      56$:    SOB    R3,55$      ;DO IT AGAIN IF THERE IS MORE DATA
2775 006042 005737 002276      TST    ERRFLG        ;WAS AN ERROR DETECTED?
2776 006046 001402                      BEQ    61$          ;NO, TAKE NORMAL EXIT
2777
2778                                     ; - - - - - CLEAN UP & EXIT - - - - -
2779
2780 006050 000261      60$:    SEC           ;INDICATE ERROR CONDITION
2781 006052 000401                      BR     63$
2782
2783 006054 000241      61$:    CLC           ;INDICATE NO ERROR
2784
2785 006056 012603      63$:    MOV    (SP)+,R3      ;RESTORE THE REGISTERS AGAIN
2786 006060 012602                      MOV    (SP)+,R2
2787 006062 012601                      MOV    (SP)+,R1
2788
2789 006064 000205                      RTS     R5          ;RETURN

```

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INTERRUPT HANDLER -- MPIHAN

2790
 2791
 2792
 2793
 2794
 2795
 2796
 2797
 2798
 2799
 2800 006066
 2801 006066
 2802 006066 010046
 2803 006070 105737 002274
 2804 006074 001007
 2805 006076 004737 004232
 2806 006102
 2807
 2808 006102 104455
 2809 006104 000007
 2810 006106 016526
 2811 006110 006220
 2812 006112 000407
 2813
 2814 006114 105237 002272
 2815 006120 005737 006136
 2816 006124 001402
 2817 006126 004777 000004
 2818 006132 012600
 2819 006134
 2820 006134
 2821 006134 000002
 2822
 2823 006136 000000

.SBTTL INTERRUPT HANDLER -- MPIHAN

```

:*****
:MPIHAN -- COUNT INTERRUPTS -- USUALLY INTERRUPT 'A'
:
:   THIS ROUTINE WILL INCREMENT THE LOW BYTE OF 'INTFLG' EACH TIME IT IS
:   ENTERED.  IF 'IHILNK' IS NON-ZERO, VECTOR TO THE ADDRESS THEREIN USING
:   A 'JSR PC'
:-----*****

```

BGNSRV MPIHAN

```

MOV   RO, -(SP)
TSTB INTWCH
BNE   S$
JSR   PC, GETBSR
GEDF  EM34, ERR3

```

MPIHAN::

```

;SAVE RO
;HAVE WE BEEN TOLD TO WATCH FOR TYPE 'A' INT'S?
;YES, DO NORMAL INTERRUPT PROCESSING
;NO, DUMP REGISTERS AND
;   REPORT 'UNEXPECTED INTERRUPT'
;   'DEVICE FATAL' ERROR # 7

```

```

TRAP  C$ERDF
.WORD 7
.WORD EM34
.WORD ERR3

```

BR 10\$;GO TO EXIT

```

S$:  INCB  INTFLG ;INCREMENT LOW BYTE OF INTERRUPT COUNTER
     TST  IHILNK ;ARE WE EXPECTED TO EXECUTE ANOTHER ROUTINE?
     BEQ  10$ ;NO, GET OUT
     JSR  PC, @IHILNK ;YES, GO TO IT -- I HOPE IT'S VALID!
10$: MOV  (SP)+, RO ;RESTORE RO
     ENDSRV ;RETURN TO INTERRUPTED PROCESS

```

L10002:

RTI

IHILNK: .WORD 0

;POINTER TO AUXILIARY INT. HANDLING ROUTINE

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INTERRUPT HANDLER -- MPOHAN

2824
2825
2826
2827
2828
2829
2830
2831
2832
2833
2834 006140
2835 006140
2836 006140 010046
2837 006142 105737 002275
2838 006146 001007
2839 006150 004737 004232
2840 006154
2841
2842 006154 104455
2843 006156 000010
2844 006160 016540
2845 006162 006220
2846 006164 000407
2847
2848 006166 105237 002273
2849 006172 005737 006210
2850 006176 001402
2851 006200 004777 000004
2852 006204 012600
2853 006206
2854 006206
2855 006206 000002
2856
2857 006210 000000

```
.SBTTL INTERRUPT HANDLER -- MPOHAN
:*****
:MPOHAN -- SIMPLY COUNT INTERRUPTS -- USUALLY INTERRUPT 'B'
:
:   THIS ROUTINE WILL INCREMENT THE HIGH BYTE OF 'INTFLG' EACH TIME IT IS
:   ENTERED.  IF 'IHOLNK' IS NON-ZERO, VECTOR TO THE ADDRESS THEREIN USING
:   A 'JSR PC'
:*****
:-----
BGNSRV  MPOHAN
MOV     R0, -(SP)           ;SAVE R0
TSTB   INTWCH+1           ;HAVE WE BEEN TOLD TO WATCH FOR TYPE 'B' INT'S?
BNE    5$                  ;YES, DO NORMAL INTERRUPT PROCESSING
JSR    PC, GETBSR         ;NO, DUMP REGISTERS AND
GEDF   EM34B, ERR3        ;REPORT 'UNEXPECTED INTERRUPT'
:                               ;'DEVICE FATAL' ERROR # 8
:                               TRAP   CSERDF
:                               .WORD  8
:                               .WORD  EM34B
:                               .WORD  ERR3
BR     10$                 ;GO TO EXIT
5$:    INCB   INTFLG+1      ;INCREMENT HIGH BYTE OF INTERRUPT COUNTER
TST    IHOLNK             ;ARE WE EXPECTED TO EXECUTE ANOTHER ROUTINE?
BEQ    10$                 ;NO, GET OUT
JSR    PC, @IHOLNK        ;YES, GO TO IT -- I HOPE IT'S VALID!
10$:   MOV    (SP)+, R0     ;RESTORE R0
ENDSRV                      ;RETURN TO INTERRUPTED PROCESS
:                               L10003:
:                               RTI
IHOLNK: .WORD  0           ;POINTER TO AUXILIARY INT. HANDLING ROUTINE
```

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GLOBAL ERROR REPORT REPORT SECTION

2858
 2859
 2860
 2861
 2862
 2863
 2864
 2865
 2866
 2867
 2868 006212
 2869 006212
 2870 006212 004737 013076
 2871 006216
 2872 006216
 2873 006216 104423
 2874
 2875
 2876
 2877 006220
 2878 006220
 2879 006220 004737 012206
 2880 006224 004737 013076
 2881 006224
 2882 006230
 2883 006230 104423
 2884
 2885
 2886
 2887 006232
 2888 006232
 2889 006232 010146
 2890 006234 113701 002254
 2891 006240 122701 000017
 2892 006244 103013
 2893 006246
 2894 006246 005046
 2895 006250 150116
 2896 006252 012746 013346
 2897 006256 012746 000002
 2898 006262 010600
 2899 006264 104415
 2900 006266 062706 000006
 2901 006272 000425
 2902
 2903 006274 001001
 2904 006276 005001
 2905 006300 022701 000007
 2906 006304 002002
 2907 006306 012701 000006
 2908 006312 006301
 2909 006314
 2910 006314 016146 021746
 2911 006320 005046
 2912 006322 153716 002254
 2913 006326 012746 013411

.SBTTL GLOBAL ERROR REPORT REPORT SECTION

:/ THE GLOBAL ERROR REPORT SECTION CONTAINS ERROR MESSAGES
 :/ THAT ARE USED IN MORE THAN ONE TEST.
 :/ .EVEN

.SBTTL ERROR HANDLER -- ERR1 -- 'NO NOTHING' HANDLER

BGNMSG ERR1
 JSR PC,MULERR ;USE COMMON ROUTINE TO TERMINATE ERROR MESSAGE
 ENDMSG
 ERR1::
 L10004: TRAP C\$MSG

.SBTTL ERROR HANDLER -- ERR3 -- DUMP THE BYTE SELECT REGISTERS

BGNMSG ERR3
 JSR PC,ERR4\$
 JSR PC,MULERR ;USE COMMON ROUTINE TO TERMINATE ERROR MESSAGE
 ENDMSG
 ERR3::
 L10005: TRAP C\$MSG

.SBTTL ERROR HANDLER -- ERR4 -- M-LOOP TIMEOUT ERROR HANDLING

BGNMSG ERR4
 MOV R1,-(SP) ;SAVE THE WORKING REGISTER
 MOVB GDATA,R1 ;SAVE THIS FOR LATER
 CMPB #17,R1 ;WAS THIS AN M-LOOP REQUEST?
 BHIS 5\$;YES, THEN REPORT THE FUNCTION CODE
 PRINTX #FMT5,<B,R1> ;NO, THEN IT MUST BE A BSEL1 SETTING
 CLR -(SP)
 BISB R1,(SP)
 MOV #FMT5,-(SP)
 MOV #2,-(SP)
 MOV SP,R0
 TRAP C\$PNTX
 ADD #6,SP
 BR 20\$
 5\$: BNE 6\$;IF IT WAS A 17, THIS IS A 'NOP' AND
 CLR R1 ; THE TEXT POINTER MUST SO REFLECT.
 6\$: CMP #7,R1 ;IS FUNCTION CODE > 7?
 BGE 7\$;NO, THEN WE CAN HANDLE IT
 MOV #6,R1 ;YES, THEN IT'S UNDEFINED -- SAY SO
 7\$: ASL R1 ;CONVERT TO A WORD OFFSET
 PRINTX #FMT5A,<B,GDATA>,TXTMLT(R1) ;REPORT THE FAILING FUNCTION
 MOV TXTMLT(R1),-(SP)
 CLR -(SP)
 BISB GDATA,(SP)
 MOV #FMT5A,-(SP)

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ERROR HANDLER -- ERR4 -- M-LOOP TIMEOUT ERROR HANDLING

```

2914 006332 012746 000003
2915 006336 010600
2916 006340 104415
2917 006342 062706 000010
2918
2919 006346 012601
2920 006350
2921 006350 012746 015212
2922 006354 012746 015313
2923 006360 012746 013213
2924 006364 012746 000003
2925 006370 010600
2926 006372 104415
2927 006374 062706 000010
2928 006400
2929 006400 013746 002220
2930 006404 013746 002216
2931 006410 013746 002214
2932 006414 013746 002212
2933 006420 012746 013770
2934 006424 012746 000005
2935 006430 010600
2936 006432 104415
2937 006434 062706 000014
2938 006440
2939 006440 012746 015252
2940 006444 012746 013306
2941 006450 012746 000002
2942 006454 010600
2943 006456 104415
2944 006460 062706 000006
2945 006464
2946 006464 013746 002230
2947 006470 013746 002226
2948 006474 013746 002224
2949 006500 013746 002222
2950 006504 012746 013770
2951 006510 012746 000005
2952 006514 010600
2953 006516 104415
2954 006520 062706 000014
2955 006524 004737 013076
2956 006530
2957 006530
2958 006530 104423
2959
2960
2961
2962 006532
2963 006532
2964 006532 113701 002300
2965 006536
2966 006536 016146 021770
2967 006542 012746 015745
2968 006546 012746 013476
2969 006552 012746 000003

MOV #3,-(SP)
MOV SP,R0
TRAP C$PNTX
ADD #10,SP

20$: MOV (SP)+,R1 ;RESTORE THE WORKING REGISTER
PRINTX #FMT4,#TXT6,#TXT4

MOV #TXT4,-(SP)
MOV #TXT6,-(SP)
MOV #FMT4,-(SP)
MOV #3,-(SP)
MOV SP,R0
TRAP C$PNTX
ADD #10,SP

PRINTX #FMT11,WSR0,WSR2,WSR4,WSR6 ;DUMP THE SELECT REGISTERS
MOV WSR6,-(SP)
MOV WSR4,-(SP)
MOV WSR2,-(SP)
MOV WSR0,-(SP)
MOV #FMT11,-(SP)
MOV #5,-(SP)
MOV SP,R0
TRAP C$PNTX
ADD #14,SP

PRINTX #FMT4B,#TXT4A
MOV #TXT4A,-(SP)
MOV #FMT4B,-(SP)
MOV #2,-(SP)
MOV SP,R0
TRAP C$PNTX
ADD #6,SP

PRINTX #FMT11,WSR10,WSR12,WSR14,WSR16
MOV WSR16,-(SP)
MOV WSR14,-(SP)
MOV WSR12,-(SP)
MOV WSR10,-(SP)
MOV #FMT11,-(SP)
MOV #5,-(SP)
MOV SP,R0
TRAP C$PNTX
ADD #14,SP

JSR PC,MULERR ;USE COMMON ROUTINE TO TERMINATE ERROR MESSAGE
ENDMSG

L10006: TRAP C$MSG

-----
:SBTTL ERROR HANDLER -- ERR8 -- NPR REGISTER ERRORS
-----
BGNMSG ERR8
ERR8:
MOV REGNUM,R1 ;THIS WAS CALCULATED TO BE A WORD OFFSET
PRINTB #FMT07,#TXTNP,TXTNPT(R1)

MOV TXTNPT(R1),-(SP)
MOV #TXTNP,-(SP)
MOV #FMT07,-(SP)
MOV #3,-(SP)

```

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ERROR HANDLER -- ERR8 -- NPR REGISTER ERRORS

2970	006556	010600				MOV	SP,R0
2971	006560	104414				TRAP	C\$PNTB
2972	006562	062706	000010			ADD	#10,SP
2973	006566			PRINTX	#FMT06A		
2974	006566	012746	013542			MOV	#FMT06A,-(SP)
2975	006572	012746	000001			MOV	#1,-(SP)
2976	006576	010600				MOV	SP,R0
2977	006600	104415				TRAP	C\$PNTX
2978	006602	062706	000004			ADD	#4,SP
2979							
2980				:	PRINT FIRST SET OF REGISTERS: CONTROL & DATA		
2981							
2982	006606			PRINTX	#FMT06,#TXT11A		
2983	006606	012746	015437			MOV	#TXT11A,-(SP)
2984	006612	012746	013530			MOV	#FMT06,-(SP)
2985	006616	012746	000002			MOV	#2,-(SP)
2986	006622	010600				MOV	SP,R0
2987	006624	104415				TRAP	C\$PNTX
2988	006626	062706	000006			ADD	#6,SP
2989	006632	010146		MOV	R1,-(SP) :PRESERVE R1		
2990	006634	013701	002254	MOV	GDATA,R1 :POINTER TO EXPECTED DATA		
2991	006640			PRINTX	#FMT16,#TXT8A,<B,(R1)+>,<B,(R1)+>,<B,(R1)+>		
2992	006640	005046				CLR	-(SP)
2993	006642	152116				BISB	(R1)+,(SP)
2994	006644	005046				CLR	-(SP)
2995	006646	152116				BISB	(R1)+,(SP)
2996	006650	005046				CLR	-(SP)
2997	006652	152116				BISB	(R1)+,(SP)
2998	006654	012746	015336			MOV	#TXT8A,-(SP)
2999	006660	012746	014007			MOV	#FMT16,-(SP)
3000	006664	012746	000005			MOV	#5,-(SP)
3001	006670	010600				MOV	SP,R0
3002	006672	104415				TRAP	C\$PNTX
3003	006674	062706	000014			ADD	#14,SP
3004	006700	013701	002256	MOV	BDATA,R1 :POINTER TO ACTUAL DATA		
3005	006704			PRINTX	#FMT16,#TXT8B,<B,(R1)+>,<B,(R1)+>,<B,(R1)+>		
3006	006704	005046				CLR	-(SP)
3007	006706	152116				BISB	(R1)+,(SP)
3008	006710	005046				CLR	-(SP)
3009	006712	152116				BISB	(R1)+,(SP)
3010	006714	005046				CLR	-(SP)
3011	006716	152116				BISB	(R1)+,(SP)
3012	006720	012746	015353			MOV	#TXT8B,-(SP)
3013	006724	012746	014007			MOV	#FMT16,-(SP)
3014	006730	012746	000005			MOV	#5,-(SP)
3015	006734	010600				MOV	SP,R0
3016	006736	104415				TRAP	C\$PNTX
3017	006740	062706	000014			ADD	#14,SP
3018	006744	004537	005064	JSR	R5,XORSW :GENERATE XOR'S		
3019	006750	002254		.WORD	GDATA :BETWEEN GOOD DATA		
3020	006752	002256		.WORD	BDATA :AND BAD DATA		
3021	006754	003054		.WORD	W0 :AND PUT THEM HERE		
3022	006756	000011		.WORD	9. :ONLY DO THIS MANY		
3023	006760			PRINTX	#FMT16,#TXT8C,<B,W0>,<B,W1>,<B,W2>		
3024	006760	005046				CLR	-(SP)
3025	006762	153716	003060			BISB	W2,(SP)

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ERROR HANDLER -- ERR8 -- NPR REGISTER ERRORS

3026	006766	005046			CLR	-(SP)
3027	006770	153716	003056		BISB	W1,(SP)
3028	006774	005046			CLR	-(SP)
3029	006776	153716	003054		BISB	W0,(SP)
3030	007002	012746	015370		MOV	#TXT8C,-(SP)
3031	007006	012746	014007		MOV	#FMT16,-(SP)
3032	007012	012746	000005		MOV	#5,-(SP)
3033	007016	010600			MOV	SP,R0
3034	007020	104415			TRAP	C\$PNTX
3035	007022	062706	000014		ADD	#14,SP
3036						
3037						
3038						
3039	007026					
3040	007026	012746	015460			
3041	007032	012746	013530		MOV	#TXT11B,-(SP)
3042	007036	012746	000002		MOV	#FMT06,-(SP)
3043	007042	010600			MOV	#2,-(SP)
3044	007044	104415			MOV	SP,R0
3045	007046	062706	000006		TRAP	C\$PNTX
3046	007052	012701	002257		ADD	#6,SP
3047	007056					
3048	007056	005046			MOV	#GDATA+3,R1 ; POINTER TO EXPECTED DATA
3049	007060	152116			PRINTX	#FMT16A,#TXT8A,<B,(R1)+>,<B,(R1)+>,<B,(R1)+>,<B,(R1)+>,<B,(R1)+>,<B,(R1)+>,<B,(R1)+>
3050	007062	005046			CLR	-(SP)
3051	007064	152116			BISB	(R1)+,(SP)
3052	007066	005046			CLR	-(SP)
3053	007070	152116			BISB	(R1)+,(SP)
3054	007072	005046			CLR	-(SP)
3055	007074	152116			BISB	(R1)+,(SP)
3056	007076	005046			CLR	-(SP)
3057	007100	152116			BISB	(R1)+,(SP)
3058	007102	005046			CLR	-(SP)
3059	007104	152116			BISB	(R1)+,(SP)
3060	007106	012746	015336		MOV	#TXT8A,-(SP)
3061	007112	012746	014032		MOV	#FMT16A,-(SP)
3062	007116	012746	000010		MOV	#10,-(SP)
3063	007122	010600			MOV	SP,R0
3064	007124	104415			TRAP	C\$PNTX
3065	007126	062706	000022		ADD	#22,SP
3066	007132	012701	002261			
3067	007136					
3068	007136	005046			MOV	#BDATA+3,R1 ; POINTER TO ACTUAL DATA
3069	007140	152116			PRINTX	#FMT16A,#TXT8B,<B,(R1)+>,<B,(R1)+>,<B,(R1)+>,<B,(R1)+>,<B,(R1)+>,<B,(R1)+>,<B,(R1)+>
3070	007142	005046			CLR	-(SP)
3071	007144	152116			BISB	(R1)+,(SP)
3072	007146	005046			CLR	-(SP)
3073	007150	152116			BISB	(R1)+,(SP)
3074	007152	005046			CLR	-(SP)
3075	007154	152116			BISB	(R1)+,(SP)
3076	007156	005046			CLR	-(SP)
3077	007160	152116			BISB	(R1)+,(SP)
3078	007162	005046			CLR	-(SP)
3079	007164	152116			BISB	(R1)+,(SP)
3080	007166	012746	015353		MOV	#TXT8B,-(SP)
3081	007172	012746	014032		MOV	#FMT16A,-(SP)

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ERROR HANDLER -- ERR8 -- NPR REGISTER ERRORS

```

3082 007176 012746 000010
3083 007202 010600
3084 007204 104415
3085 007206 062706 000022
3086 007212 012701 003062
3087 007216
3088 007216 005046
3089 007220 152116
3090 007222 005046
3091 007224 152116
3092 007226 005046
3093 007230 152116
3094 007232 005046
3095 007234 152116
3096 007236 005046
3097 007240 152116
3098 007242 005046
3099 007244 152116
3100 007246 012746 015370
3101 007252 012746 014032
3102 007256 012746 000010
3103 007262 010600
3104 007264 104415
3105 007266 062706 000022
3106 007272 004737 013076
3107 007276
3108 007276
3109 007276 104423
3110
3111
3112
3113 007300
3114 007300
3115 007300 004737 012614
3116 007304
3117 007304
3118 007304 104423
3119
3120
3121
3122 007306
3123 007306
3124 007306 004737 012752
3125 007312
3126 007312
3127 007312 104423
3128
3129
3130
3131 007314
3132 007314
3133 007314 023727 002412 000001
3134 007322 001412
3135 007324
3136 007324 013746 002412
3137 007330 012746 014307

MOV #10,-(SP)
MOV SP,R0
TRAP C$PNTX
ADD #22,SP

MOV #W3,R1           ;POINT TO REST OF XOR'S
PRINTX #FMT16A,#TXT8C,<B,(R1)+>,<B,(R1)+>,<B,(R1)+>,<B,(R1)+>,<B,(R1)+>,<B,(R1)+>,<B,(R1)+>,<B,(R1)+>
CLR -(SP)
BISB (R1)+,(SP)
CLR -(SP)
BISB (R1)+,(SP)
CLR -(SP)
BISB (R1)+,(SP)
CLR -(SP)
BISB (R1)+,(SP)
CLR -(SP)
BISB (R1)+,(SP)
CLR -(SP)
BISB (R1)+,(SP)
CLR -(SP)
BISB (R1)+,(SP)
CLR -(SP)
BISB (R1)+,(SP)
CLR -(SP)
MOV #TXT8C,-(SP)
MOV #FMT16A,-(SP)
MOV #10,-(SP)
MOV SP,R0
TRAP C$PNTX
ADD #22,SP

JSR PC,MULERR       ;USE COMMON ROUTINE TO TERMINATE ERROR MESSAGE
ENDMSG

L10007:
TRAP C$MSG

-----
:SBTTL ERROR HANDLER -- ERR9 -- WORD NPR I/O ERRORS
-----
BGNMSG ERR9
JSR PC,ERR9.       ;USE COMMON 'ERROR 9' ROUTINE
ENDMSG

L10010:
TRAP C$MSG

-----
:SBTTL ERROR HANDLER -- ERR10 -- BYTE NPR I/O ERRORS
-----
BGNMSG ERR10
JSR PC,ERR10.     ;USE COMMON 'ERROR 10' ROUTINE
ENDMSG

L10011:
TRAP C$MSG

-----
:SBTTL ERROR HANDLER -- ERR11 -- NPR TIMEOUT ERRORS
-----
BGNMSG ERR11
ERR11::
CMP TMP0,#1       ;IF ONLY ONE TIMEOUT,
BEQ 1$            ;NO NEED TO TELL HOW MANY OCCURED
PRINTX #FMT17C,TMP0 ;ELSE, SAY HOW MANY WERE FOUND IN ALL
MOV TMP0,-(SP)
MOV #FMT17C,-(SP)

```

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ERROR HANDLER -- ERR11 -- NPR TIMEOUT ERRORS

3138	007334	012746	000002					MOV	#2,-(SP)
3139	007340	010600						MOV	SP,R0
3140	007342	104415						TRAP	C\$PNTX
3141	007344	062706	000006					ADD	#6,SP
3142	007350			1\$:	PRINTX	#FMT17,<B,TMP1>	;NPRCTL SENT		
3143	007350	005046						CLR	-(SP)
3144	007352	153716	002414					BISB	TMP1,(SP)
3145	007356	012746	014074					MOV	#FMT17,-(SP)
3146	007362	012746	000002					MOV	#2,-(SP)
3147	007366	010600						MOV	SP,R0
3148	007370	104415						TRAP	C\$PNTX
3149	007372	062706	000006					ADD	#6,SP
3150	007376				PRINTX	#FMT17A,<B,TMP2>	;NPRCTL READ		
3151	007376	005046						CLR	-(SP)
3152	007400	153716	002416					BISB	TMP2,(SP)
3153	007404	012746	014153					MOV	#FMT17A,-(SP)
3154	007410	012746	000002					MOV	#2,-(SP)
3155	007414	010600						MOV	SP,R0
3156	007416	104415						TRAP	C\$PNTX
3157	007420	062706	000006					ADD	#6,SP
3158	007424				PRINTX	#FMT17B,TMP3	;LSI-11'S MEMORY ADDRESS		
3159	007424	013746	002420					MOV	TMP3,-(SP)
3160	007430	012746	014234					MOV	#FMT17B,-(SP)
3161	007434	012746	000002					MOV	#2,-(SP)
3162	007440	010600						MOV	SP,R0
3163	007442	104415						TRAP	C\$PNTX
3164	007444	062706	000006					ADD	#6,SP
3165	007450	004737	013076		JSR	PC,NULERR	;USE COMMON ROUTINE TO TERMINATE ERROR MESSAGE		
3166	007454				ENDMSG				
3167	007454								L10012:
3168	007454	104423							TRAP C\$MSG
3169									
3170					SBTTL	ERROR HANDLER -- ERR12 -- NPR EXTENDED ADDRESSING ERROR HANDLER			
3171									
3172	007456				BGNMSG	ERR12			
3173	007456								ERR12::
3174	007456				PRINTX	#FMT12	;PRINT FIRST HEADING LINE		
3175	007456	012746	007672					MOV	#FMT12,-(SP)
3176	007462	012746	000001					MOV	#1,-(SP)
3177	007466	010600						MOV	SP,R0
3178	007470	104415						TRAP	C\$PNTX
3179	007472	062706	000004					ADD	#4,SP
3180	007476				PRINTX	#FMT12A	;PRINT SECOND HEADING LINE		
3181	007476	012746	007732					MOV	#FMT12A,-(SP)
3182	007502	012746	000001					MOV	#1,-(SP)
3183	007506	010600						MOV	SP,R0
3184	007510	104415						TRAP	C\$PNTX
3185	007512	062706	000004					ADD	#4,SP
3186									;PRINT ADDRESS, CONTROL, & EXPECTED DATA
3187	007516	004737	012162		JSR	PC,XORGB	;GENERATE EXCLUSIVE OR OF EXPECTED & READ DATA		
3188	007522				PRINTX	#FMT12D,<B,TMPF+1>,<B,TMP2>	,GDATA,BDATA,XDATA		
3189	007522	013746	002260					MOV	XDATA,-(SP)
3190	007526	013746	002256					MOV	BDATA,-(SP)
3191	007532	013746	002254					MOV	GDATA,-(SP)
3192	007536	005046						CLR	-(SP)
3193	007540	153716	002416					BISB	TMP2,(SP)

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ERROR HANDLER -- ERR12 -- NPR EXTENDED ADDRESSING ERROR HANDLER

```

3194 007544 012746 000000
3195 007550 012746 000000
3196 007554 005046
3197 007556 153716 002451
3198 007562 012746 010020
3199 007566 012746 000010
3200 007572 010600
3201 007574 104415
3202 007576 062706 000022
3203 007602 023737 002256 002420
3204 007610 001011
3205 007612
3206 007612 012746 010063
3207 007616 012746 000001
3208 007622 010600
3209 007624 104415
3210 007626 062706 000004
3211 007632 000414
3212
3213 007634 023737 002256 002414 4$:
3214 007642 001010
3215 007644
3216 007644 012746 010145
3217 007650 012746 000001
3218 007654 010600
3219 007656 104415
3220 007660 062706 000004
3221 007664 004737 013076 60$:
3222 007670
3223 007670
3224 007670 104423
3225
007672 047045 051445 031461 .NLIST BEX
007732 047045 051445 030461 FMT12: .ASCIZ /%N%13%ANPR REGISTERS%14%ADATA/
010020 047045 051445 022470 FMT12A: .ASCIZ /%N%11%ADDRESS CONTROL EXPECTED READ XOR%N/
010063 045 022516 033523 FMT12D: .ASCIZ /%N%8%03%03%03%03%03%010%09%09/
010145 045 022516 031123 FMT12E: .ASCIZ /%N%7%(NPR OPERATION ACCESSED WRONG MEMORY PAGE)/
FMT12F: .ASCIZ /%N%2%(NPR DATA REGISTER UN-CHANGED FROM BEFORE REQUEST)/
.LIST BEX
.EVEN
3226 010240
3227
3228
3229
3230 010240
3231 010240
3232 010240
3233 010240 012746 010546
3234 010244 012746 000001
3235 010250 010600
3236 010252 104415
3237 010254 062706 000004
3238 010260
3239 010260 012746 010611
3240 010264 012746 000001
3241 010270 010600
3242 010272 104415
3243 010274 062706 000004

```

```

MOV #0,-(SP)
MOV #0,-(SP)
CLR -(SP)
BISB TMPF+1,(SP)
MOV #FMT12D,-(SP)
MOV #10,-(SP)
MOV SP,R0
TRAP C$PNTX
ADD #22,SP
CMP BDATA,TMP3 ;DID WE READ THE BACKGROUND PATTERN?
BNE 4$ ;NO
PRINTX #FMT12E ;YES, INDICATE WRONG PAGE READ
MOV #FMT12E,-(SP)
MOV #1,-(SP)
MOV SP,R0
TRAP C$PNTX
ADD #4,SP
BR 60$ ; AND EXIT ERROR HANDLER
CMP BDATA,TMP1 ;DID WE EVEN PERFORM A READ?
BNE 60$ ;YES, THEN WE CAN GIVE ANY FURTHER ERROR INFO.
PRINTX #FMT12F ;NO, THEN WE CAN AT LEAST SAY THAT MUCH
MOV #FMT12F,-(SP)
MOV #1,-(SP)
MOV SP,R0
TRAP C$PNTX
ADD #4,SP
JSR PC,NULERR ;USE COMMON ROUTINE TO TERMINATE ERROR MESSAGE
ENDMSG
L10013:
TRAP C$MSG

```

```

.SBTTL ERROR HANDLER -- ERR13 -- 'MMU' ERROR HANDLER
BGNMSG ERR13
PRINTX #FMT13A
ERR13::
MOV #FMT13A,-(SP)
MOV #1,-(SP)
MOV SP,R0
TRAP C$PNTX
ADD #4,SP
PRINTX #FMT13B
MOV #FMT13B,-(SP)
MOV #1,-(SP)
MOV SP,R0
TRAP C$PNTX
ADD #4,SP

```

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ERROR HANDLER -- ERR13 -- 'MMU' ERROR HANDLER

3244	010300			PRINTX #FMT13C	
3245	010300	012746	010656		MOV #FMT13C,-(SP)
3246	010304	012746	000001		MOV #1,-(SP)
3247	010310	010600			MOV SP,R0
3248	010312	104415			TRAP C\$PNTX
3249	010314	062706	000004		ADD #4,SP
3250	010320			PRINTX #FMT13D	
3251	010320	012746	010721		MOV #FMT13D,-(SP)
3252	010324	012746	000001		MOV #1,-(SP)
3253	010330	010600			MOV SP,R0
3254	010332	104415			TRAP C\$PNTX
3255	010334	062706	000004		ADD #4,SP
3256	010340			PRINTX #FMT11,TMP0,TMP1,TMP2,TMP3	
3257	010340	013746	002420		MOV TMP3,-(SP)
3258	010344	013746	002416		MOV TMP2,-(SP)
3259	010350	013746	002414		MOV TMP1,-(SP)
3260	010354	013746	002412		MOV TMP0,-(SP)
3261	010360	012746	013770		MOV #FMT11,-(SP)
3262	010364	012746	000005		MOV #5,-(SP)
3263	010370	010600			MOV SP,R0
3264	010372	104415			TRAP C\$PNTX
3265	010374	062706	000014		ADD #14,SP
3266	010400			PRINTX #FMT13E,TMP4,TMP5,TMP6,TMP7	
3267	010400	013746	002430		MOV TMP7,-(SP)
3268	010404	013746	002426		MOV TMP6,-(SP)
3269	010410	013746	002424		MOV TMP5,-(SP)
3270	010414	013746	002422		MOV TMP4,-(SP)
3271	010420	012746	010764		MOV #FMT13E,-(SP)
3272	010424	012746	000005		MOV #5,-(SP)
3273	010430	010600			MOV SP,R0
3274	010432	104415			TRAP C\$PNTX
3275	010434	062706	000014		ADD #14,SP
3276	010440			PRINTX #FMT11,REG0,REG1,REG2,REG3	
3277	010440	013746	002400		MOV REG3,-(SP)
3278	010444	013746	002376		MOV REG2,-(SP)
3279	010450	013746	002374		MOV REG1,-(SP)
3280	010454	013746	002372		MOV REG0,-(SP)
3281	010460	012746	013770		MOV #FMT11,-(SP)
3282	010464	012746	000005		MOV #5,-(SP)
3283	010470	010600			MOV SP,R0
3284	010472	104415			TRAP C\$PNTX
3285	010474	062706	000014		ADD #14,SP
3286	010500			PRINTX #FMT13E,REG4,REG5,REG6,REG7	
3287	010500	013746	002410		MOV REG7,-(SP)
3288	010504	013746	002406		MOV REG6,-(SP)
3289	010510	013746	002404		MOV REG5,-(SP)
3290	010514	013746	002402		MOV REG4,-(SP)
3291	010520	012746	010764		MOV #FMT13E,-(SP)
3292	010524	012746	000005		MOV #5,-(SP)
3293	010530	010600			MOV SP,R0
3294	010532	104415			TRAP C\$PNTX
3295	010534	062706	000014		ADD #14,SP
3296	010540	004737	013076	JSR PC,NULERR ;USE COMMON ROUTINE TO TERMINATE ERROR MESSAGE	
3297	010544			ENDMSG	
3298	010544				L10014:
3299	010544	104423			TRAP C\$MSG

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ERROR HANDLER -- ERR13 -- 'MMU' ERROR HANDLER

```

3300      010546 047045 040445 020040 .NLIST BEX
          010611      045 020101 020040 FMT13A: .ASCIZ /XNZA SR0 SR1 SR2 SR3/
          010656 047045 040445 020040 FMT13B: .ASCIZ /XA KPAR6 KPDR6 V ADDR DATA?/
          010721      045 020101 020040 FMT13C: .ASCIZ /XNZA R0 R1 R2 R3/
          010764 047445 022470 034117 FMT13D: .ASCIZ /XA R4 R5 SP PC/
          FMT13E: .ASCIZ /X08X08X08X08/
          .LIST BEX
          .EVEN
3301      011002
3302
3303      -----
          :SBTTL ERROR HANDLER -- ERR14 -- NPR REGISTER LOAD ERROR HANDLER
          -----
3304
3305      011002      BGNMSG ERR14
3306      011002
3307      011002 010146      MOV R1,-(SP) ;SAVE GENERAL REGISTER ERR14::
3308      011004 013701 002300      MOV REGNUM,R1
3309      011010 006301      ASL R1 ;CONVERT REG # TO WORD INDEX
3310      011012      PRINTB #FMT07,#TXTNP,TXTNPT(R1)
3311      011012 016146 021770      MOV TXTNPT(R1),-(SP)
3312      011016 012746 015745      MOV #TXTNP,-(SP)
3313      011022 012746 013476      MOV #FMT07,-(SP)
3314      011026 012746 000003      MOV #3,-(SP)
3315      011032 010600      MOV SP,R0
3316      011034 104414      TRAP C$PNTB
3317      011036 062706 000010      ADD #10,SP
3318      011042 004737 012162      JSR PC,XORGB
3319
3320      011046 023727 002300 000006      CMP REGNUM,#6 ;DATA: GOOD, BAD, & XOR
3321      011054 001423      BEQ 5$ ;IF EXTENDED ADDRESS BYTE, USE BYTE PRINT
3322      011056 023727 002300 000003      CMP REGNUM,#3
3323      011064 001417      BEQ 5$
3324
3325      011066      PRINTX #FMT10,GDATA,BDATA,XDATA ;ELSE, USE WORD PRINTS
3326      011066 013746 002260      MOV XDATA,-(SP)
3327      011072 013746 002256      MOV BDATA,-(SP)
3328      011076 013746 002254      MOV GDATA,-(SP)
3329      011102 012746 013714      MOV #FMT10,-(SP)
3330      011106 012746 000004      MOV #4,-(SP)
3331      011112 010600      MOV SP,R0
3332      011114 104415      TRAP C$PNTX
3333      011116 062706 000012      ADD #12,SP
3334      011122 000421      BR 10$ ;BYPASS BYTE PRINTS IF WORD PRINTS USED
3335
3336      011124      5$: PRINTX #FMT02A,<B,GDATA>,<B,BDATA>,<B,XDATA>
3337      011124 005046      CLR -(SP)
3338      011126 153716 002260      BISB XDATA,(SP)
3339      011132 005046      CLR -(SP)
3340      011134 153716 002256      BISB BDATA,(SP)
3341      011140 005046      CLR -(SP)
3342      011142 153716 002254      BISB GDATA,(SP)
3343      011146 012746 013127      MOV #FMT02A,-(SP)
3344      011152 012746 000004      MOV #4,-(SP)
3345      011156 010600      MOV SP,R0
3346      011160 104415      TRAP C$PNTX
3347      011162 062706 000012      ADD #12,SP
3348      011166 004737 013076      10$: JSR PC,MULERR ;USE COMMON ROUTINE TO TERMINATE ERROR MESSAGE
3349      011172 012601      MOV (SP)+,R1 ;RESTORE GENERAL REGISTER

```

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ERROR HANDLER -- ERR14 -- NPR REGISTER LOAD ERROR HANDLER

3350 011174
 3351 011174
 3352 011174 104423
 3353
 3354
 3355
 3356
 3357 011176
 3358 011176
 3359 011176 010146
 3360 011200 113701 002441
 3361 011204 000241
 3362
 3363 011206 042701 177737
 3364 011212 106101
 3365 011214 106101
 3366 011216 106101
 3367 011220 106101
 3368
 3369
 3370
 3371
 3372 011222
 3373 011222 005046
 3374 011224 153716 002433
 3375 011230 005046
 3376 011232 153716 002435
 3377 011236 010146
 3378 011240 012746 014673
 3379 011244 012746 000004
 3380 011250 010600
 3381 011252 104415
 3382 011254 062706 000012
 3383 011260 004737 013076
 3384 011264 012601
 3385 011266
 3386 011266
 3387 011266 104423
 3388
 3389
 3390
 3391 011270
 3392 011270
 3393 011270 004537 003616
 3394 011274 120013
 3395 011276 002440
 3396 011300 004537 003616
 3397 011304 120014
 3398 011306 002442
 3399 011310
 3400 011310 012746 011514
 3401 011314 012746 000001
 3402 011320 010600
 3403 011322 104415
 3404 011324 062706 000004
 3405 011330

ENDMSG

L10015: TRAP CMSG

:SBTTL ERROR HANDLER -- ERR51 -- FOR REPORTING TIMER # 2 ERRORS

BGNMSG ERR51

ERR51::

```

MOV R1,-(SP)      ;SAVE R1 FOR CALLER
MOVB TMP8+1,R1    ;GET THE MODE LAST SETUP
CLC               ;SEEING AS THE CARRY BIT WILL BE ROTATED INTO
                  ;THE DATA, WE HAD BETTER CLEAR IT JUST IN CASE.
BIC #^C<BITS>,R1 ;LOOK @ JUST THE TIMER 2 MODE DEFINITION
ROLB R1           ;POSITION IT FOR PRINTOUT
ROLB R1
ROLB R1
ROLB R1

```

```

;IDENTIFY THE MODE BEING USED AT THE TIME, AND THE VALUES THAT WERE
;LOADED INTO THE LATCHES:

```

PRINTX #FMT51A,R1,<B,TMP9+1>,<B,TMP8+1>

```

CLR -(SP)
BISB TMP8+1,(SP)
CLR -(SP)
BISB TMP9+1,(SP)
MOV R1,-(SP)
MOV #FMT51A,-(SP)
MOV #4,-(SP)
MOV SP,R0
TRAP C$PNTX
ADD #12,SP

```

```

JSR PC,NULERR    ;USE COMMON ROUTINE TO TERMINATE ERROR MESSAGE
MOV (SP)+,R1     ;RESTORE R1 FOR CALLER
ENDMSG

```

L10016: TRAP CMSG

:SBTTL ERROR HANDLER -- ERR52 -- PROCESS SHIFT REGISTER ERROR MESSAGES

BGNMSG ERR52

ERR52::

```

JSR R5,READ      ;GET CURRENT VALUES WITHIN ACR & PCR
ACR
TMPB
JSR R5,READ
PCR
TMPC
PRINTX #FMT52H

```

```

MOV #FMT52H,-(SP)
MOV #1,-(SP)
MOV SP,R0
TRAP C$PNTX
ADD #4,SP

```

PRINTX #FMT52A,#TXT8D,<B,TMPA+1>,<B,TMPB+1>,<B,TMPC+1>

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ERROR HANDLER -- ERR52 -- PROCESS SHIFT REGISTER ERROR MESSAGES

```

3406 011330 005046
3407 011332 153716 002447
3408 011336 005046
3409 011340 153716 002441
3410 011344 005046
3411 011346 153716 002437
3412 011352 012746 015405
3413 011356 012746 011557
3414 011362 012746 000005
3415 011366 010600
3416 011370 104415
3417 011372 062706 000014
3418 011376
3419 011376 005046
3420 011400 153716 002446
3421 011404 005046
3422 011406 153716 002444
3423 011412 005046
3424 011414 153716 002442
3425 011420 005046
3426 011422 153716 002440
3427 011426 005046
3428 011430 153716 002436
3429 011434 012746 015422
3430 011440 012746 011607
3431 011444 012746 000007
3432 011450 010600
3433 011452 104415
3434 011454 062706 000020
3435 011460 005737 002252
3436 011464 001010
3437 011466
3438 011466 012746 011652
3439 011472 012746 000001
3440 011476 010600
3441 011500 104415
3442 011502 062706 000004
3443 011506 004737 013076
3444 011512
3445 011512
3446 011512 104423
3447
3448
011514 047045 051445 032061
011557 045 022516 030523
011607 045 022516 030523
011652 047045 051445 030061
3449 011720
3450
3451
3452
3453
3454
3455
3456

```

```

CLR -(SP)
BISB TMPE+1,(SP)
CLR -(SP)
BISB TMPB+1,(SP)
CLR -(SP)
BISB TMPA+1,(SP)
MOV #TXT8D,-(SP)
MOV #FMT52A,-(SP)
MOV #5,-(SP)
MOV SP,RO
TRAP C$PNTX
ADD #14,SP
PRINTX #FMT52B,#TXT8E,<B,TMPA>,<B,TMPB>,<B,TMPC>,<B,TMPD>,<B,TMPE>
CLR -(SP)
BISB TMPE,(SP)
CLR -(SP)
BISB TMPD,(SP)
CLR -(SP)
BISB TMPC,(SP)
CLR -(SP)
BISB TMPB,(SP)
CLR -(SP)
BISB TMPA,(SP)
MOV #TXT8E,-(SP)
MOV #FMT52B,-(SP)
MOV #7,-(SP)
MOV SP,RO
TRAP C$PNTX
ADD #20,SP
TST TDATA ;HAS 'SR' BEEN WRITTEN YET?
BNE 10$ ;NO, THEN JUST FINISH ERROR MESSAGE
PRINTX #FMT52C ;YES, ADD THAT INFORMATION TO MESSAGE
MOV #FMT52C,-(SP)
MOV #1,-(SP)
MOV SP,RO
TRAP C$PNTX
ADD #4,SP
10$: JSR PC,NULERR ;USE COMMON ROUTINE TO TERMINATE ERROR MESSAGE
ENDMSG
L10017: TRAP C$MSG
.NLIST BEX
FMT52H: .ASCIZ /%N%S14%ASR ACR PCR IFR IER/
FMT52A: .ASCIZ /%N%S1%T%03%S3%03%S15%03/
FMT52B: .ASCIZ /%N%S1%T%03%S3%03%S3%03%S3%03%S3%03/
FMT52C: .ASCIZ /%N%S10%(SR HASN'T BEEN LOADED YET!)/
.LIST BEX
.EVEN
SR ERROR FORMATS:
LOADED SR ACR PCR IFR IER
READ XXX XXX --- --- XXX

```


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ERROR HANDLER -- ERR52 -- PROCESS SHIFT REGISTER ERROR MESSAGES

3457
3458
3459
3460
3461 011720
3462 011720
3463 011720
3464 011720 012746 012034
3465 011724 012746 000001
3466 011730 010600
3467 011732 104415
3468 011734 062706 000004
3469 011740
3470 011740 012746 012073
3471 011744 012746 000001
3472 011750 010600
3473 011752 104415
3474 011754 062706 000004
3475 011760 005003
3476 011762
3477 011762 016346 032610
3478 011766 016346 032572
3479 011772 016346 032554
3480 011776 012746 012134
3481 012002 012746 000004
3482 012006 010600
3483 012010 104415
3484 012012 062706 000012
3485 012016 005723
3486 012020 020327 000016
3487 012024 001356
3488 012026 004737 013076
3489 012032
3490 012032
3491 012032 104423
3492
012034 047045 051445 022463
012073 045 022516 031523
012134 047045 051445 022462
3493 012162

```

-----
:SBTTL ERROR HANDLER -- ERR60 -- NPR WRITE-EXTENDED BIT ERRORS
-----

```

```

      BGNMSG  ERR60
      PRINTX  #FMT60
                                     ERR60::
      MOV     #FMT60,-(SP)
      MOV     #1,-(SP)
      MOV     SP,R0
      TRAP   C$PNTX
      ADD     #4,SP
      PRINTX  #FMT61
      MOV     #FMT61,-(SP)
      MOV     #1,-(SP)
      MOV     SP,R0
      TRAP   C$PNTX
      ADD     #4,SP
10$:  CLR     R3                      ;CLEAR INDEX
      PRINTX #FMT62,XLOC0(R3),XVAL0(R3),RXVAL0(R3)
      MOV     RXVAL0(R3),-(SP)
      MOV     XVAL0(R3),-(SP)
      MOV     XLOC0(R3),-(SP)
      MOV     #FMT62,-(SP)
      MOV     #4,-(SP)
      MOV     SP,R0
      TRAP   C$PNTX
      ADD     #12,SP
      TST     (R3)+                    ;BUMP INDEX
      CMP     R3,#14.
      BNE     10$
      JSR     PC,NULERR
      ENDMSG
                                     L10020:
      TRAP   C$MSG
      .NLIST BEX
      FMT60: .ASCIZ /%N%S3%AXLOC%S6%AXVAL%S6%ARXVAL/
      FMT61: .ASCIZ /%N%S3%A-----%S6%A-----%S6%A-----%N/
      FMT62: .ASCIZ /%N%S2%06XS4%06XS4%06/
      .LIST BEX
      .EVEN

```

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ERROR HANDLER SUBROUTINES

3494
3495
3496
3497
3498
3499
3500
3501
3502
3503
3504
3505 012162 010146
3506 012164 013701 002254
3507 012170 013737 002256 002260
3508 012176 074137 002260
3509 012202 012601
3510 012204 000207
3511
3512
3513
3514
3515
3516
3517 012206
3518 012206 012746 014761
3519 012212 012746 015162
3520 012216 012746 013213
3521 012222 012746 000003
3522 012226 010600
3523 012230 104415
3524 012232 062706 000010
3525 012236
3526 012236 005046
3527 012240 153716 002220
3528 012244 005046
3529 012246 153716 002216
3530 012252 005046
3531 012254 153716 002214
3532 012260 005046
3533 012262 153716 002212
3534 012266 012746 013253
3535 012272 012746 000005
3536 012276 010600
3537 012300 104415
3538 012302 062706 000014
3539 012306
3540 012306 012746 015017
3541 012312 012746 013306
3542 012316 012746 000002
3543 012322 010600
3544 012324 104415
3545 012326 062706 000006
3546 012332
3547 012332 005046
3548 012334 153716 002230
3549 012340 005046

.SBTTL ERROR HANDLER SUBROUTINES

```

-----
:----- SUBROUTINES USED ONLY BY ERROR HANDLERS -----
:-----

```

.SBTTL ERROR HANDLER SUBROUTINE -- XORGB

```

: PERFORM EXCLUSIVE OR BETWEEN 'GDATA' & 'BDATA' PUTTING
: THE RESULT IN 'XDATA'

```

```

XORGB: MOV R1,-(SP) ;PRESERVE WORKING REGISTER
MOV GDATA,R1 ;GET 'GOOD' DATA
MOV BDATA,XDATA ;AND 'BAD' DATA
XOR R1,XDATA ;PERFORM EXCLUSIVE OR
MOV (SP)+,R1 ;RESTORE R1
RTS PC ;RETURN

```

.SBTTL ERROR HANDLER SUBROUTINE -- ERR4\$

```

: IDENTIFY & DUMP THE BYTE SELECT REGISTERS

```

ERR4\$: PRINTX #FMT4,#TXT3,#TXT1

```

MOV #TXT1,-(SP)
MOV #TXT3,-(SP)
MOV #FMT4,-(SP)
MOV #3,-(SP)
MOV SP,R0
TRAP C$PNTX
ADD #10,SP

```

PRINTX #FMT4A,<B,BSR0>,<B,BSR1>,<B,BSR2>,<B,BSR3>

```

CLR -(SP)
BISB BSR3,(SP)
CLR -(SP)
BISB BSR2,(SP)
CLR -(SP)
BISB BSR1,(SP)
CLR -(SP)
BISB BSR0,(SP)
MOV #FMT4A,-(SP)
MOV #5,-(SP)
MOV SP,R0
TRAP C$PNTX
ADD #14,SP

```

PRINTX #FMT4B,#TXT2

```

MOV #TXT2,-(SP)
MOV #FMT4B,-(SP)
MOV #2,-(SP)
MOV SP,R0
TRAP C$PNTX
ADD #6,SP

```

PRINTX #FMT4C,<B,BSR4>,<B,BSR5>,<B,BSR6>,<B,BSR7>

```

CLR -(SP)
BISB BSR7,(SP)
CLR -(SP)

```

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ERROR HANDLER SUBROUTINE -- ERR4\$

```

3550 012342 153716 002226
3551 012346 005046
3552 012350 153716 002224
3553 012354 005046
3554 012356 153716 002222
3555 012362 012746 013313
3556 012366 012746 000005
3557 012372 010600
3558 012374 104415
3559 012376 062706 000014
3560 012402
3561 012402 012746 015061
3562 012406 012746 013306
3563 012412 012746 000002
3564 012416 010600
3565 012420 104415
3566 012422 062706 000006
3567 012426
3568 012426 005046
3569 012430 153716 002240
3570 012434 005046
3571 012436 153716 002236
3572 012442 005046
3573 012444 153716 002234
3574 012450 005046
3575 012452 153716 002232
3576 012456 012746 013253
3577 012462 012746 000005
3578 012466 010600
3579 012470 104415
3580 012472 062706 000014
3581 012476
3582 012476 012746 015120
3583 012502 012746 013306
3584 012506 012746 000002
3585 012512 010600
3586 012514 104415
3587 012516 062706 000006
3588 012522
3589 012522 005046
3590 012524 153716 002250
3591 012530 005046
3592 012532 153716 002246
3593 012536 005046
3594 012540 153716 002244
3595 012544 005046
3596 012546 153716 002242
3597 012552 012746 013313
3598 012556 012746 000005
3599 012562 010600
3600 012564 104415
3601 012566 062706 000014
3602 012572 000207
3603
3604
3605

```

PRINTX #FMT4B,#TXT2A

PRINTX #FMT4A,<B,BSR10>,<B,BSR11>,<B,BSR12>,<B,BSR13>

PRINTX #FMT4B,#TXT2B

PRINTX #FMT4C,<B,BSR14>,<B,BSR15>,<B,BSR16>,<B,BSR17>

RTS PC

```

BISB BSR6,(SP)
CLR -(SP)
BISB BSR5,(SP)
CLR -(SP)
BISB BSR4,(SP)
MOV #FMT4C,-(SP)
MOV #5,-(SP)
MOV SP,RO
TRAP C$PNTX
ADD #14,SP

MOV #TXT2A,-(SP)
MOV #FMT4B,-(SP)
MOV #2,-(SP)
MOV SP,RO
TRAP C$PNTX
ADD #6,SP

CLR -(SP)
BISB BSR13,(SP)
CLR -(SP)
BISB BSR12,(SP)
CLR -(SP)
BISB BSR11,(SP)
CLR -(SP)
BISB BSR10,(SP)
MOV #FMT4A,-(SP)
MOV #5,-(SP)
MOV SP,RO
TRAP C$PNTX
ADD #14,SP

MOV #TXT2B,-(SP)
MOV #FMT4B,-(SP)
MOV #2,-(SP)
MOV SP,RO
TRAP C$PNTX
ADD #6,SP

CLR -(SP)
BISB BSR17,(SP)
CLR -(SP)
BISB BSR16,(SP)
CLR -(SP)
BISB BSR15,(SP)
CLR -(SP)
BISB BSR14,(SP)
MOV #FMT4C,-(SP)
MOV #5,-(SP)
MOV SP,RO
TRAP C$PNTX
ADD #14,SP

```

:SBTTL

ERROR HANDLER SUBROUTINE -- ERR9\$ & ERR9.

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ERROR HANDLER SUBROUTINE -- ERR9\$ & ERR9.

```

3606
3607 :-----
3608 : COMMON ERROR 9 ROUTINE TO IDENTIFY THE FAILING ADDRESS & DATA
3609 ERR9$: PRINTX #NEWLIN ;WHEN CALLED FROM TEST, START A NEW LINE
3610 012574 012746 013124 MOV #NEWLIN,-(SP)
3611 012574 012746 000001 MOV #1,-(SP)
3612 012604 010600 MOV SP,R0
3613 012606 104415 TRAP C$PNTX
3614 012610 062706 000004 ADD #4,SP
3615 012614 005237 002300
3616 012620
3617 012620 013746 002300 MOV REGNUM,-(SP)
3618 012624 012746 013621 MOV #FMT09,-(SP)
3619 012630 012746 000002 MOV #2,-(SP)
3620 012634 010600 MOV SP,R0
3621 012636 104415 TRAP C$PNTX
3622 012640 062706 000006 ADD #6,SP
3623 012644 004737 012162
3624 012650 JSR PC,XORGB
3625 012650 013746 002260 PRINTX #FMT10,GDATA,BDATA,XDATA ;DATA: GOOD, BAD, & XOR
3626 012654 013746 002256 MOV XDATA,-(SP)
3627 012660 013746 002254 MOV BDATA,-(SP)
3628 012664 012746 013714 MOV GDATA,-(SP)
3629 012670 012746 000004 MOV #FMT10,-(SP)
3630 012674 010600 MOV #4,-(SP)
3631 012676 104415 MOV SP,R0
3632 012700 062706 000012 TRAP C$PNTX
3633 012704 PRINTX #FMT09A,TDATA ;LSI ADDRESS ADD #12,SP
3634 012704 013746 002252 MOV TDATA,-(SP)
3635 012710 012746 013673 MOV #FMT09A,-(SP)
3636 012714 012746 000002 MOV #2,-(SP)
3637 012720 010600 MOV SP,R0
3638 012722 104415 TRAP C$PNTX
3639 012724 062706 000006 ADD #6,SP
3640 012730 000207 RTS PC
3641
3642 :-----
3643 : SBTTL ERROR HANDLER SUBROUTINE -- ERR10$ & ERR10.
3644 :-----
3645 : COMMON ERROR 10 ROUTINE TO IDENTIFY THE FAILING ADDRESS & DATA
3646
3647 ERR10$: PRINTX #NEWLIN ;WHEN CALLED FROM TEST, START A NEW LINE
3648 012732 012746 013124 MOV #NEWLIN,-(SP)
3649 012736 012746 000001 MOV #1,-(SP)
3650 012742 010600 MOV SP,R0
3651 012744 104415 TRAP C$PNTX
3652 012746 062706 000004 ADD #4,SP
3653 012752 005237 002300
3654 012756
3655 012756 013746 002300 MOV REGNUM,-(SP)
3656 012762 012746 013621 MOV #FMT09,-(SP)
3657 012766 012746 000002 MOV #2,-(SP)
3658 012772 010600 MOV SP,R0
3659 012774 104415 TRAP C$PNTX
3660 012776 062706 000006 ADD #6,SP
3661 013002 004737 012162 JSR PC,XORGB

```

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ERROR HANDLER SUBROUTINE -- ERR10\$ & ERR10.

```

3662 013006          PRINTX #FMT02A,<B,GDATA>,<B,BDATA>,<B,XDATA> ;DATA: GOOD, BAD, & XOR
3663 013006 005046          CLR      -(SP)
3664 013010 153716 002260    BISB    XDATA,(SP)
3665 013014 005046          CLR      -(SP)
3666 013016 153716 002256    BISB    BDATA,(SP)
3667 013022 005046          CLR      -(SP)
3668 013024 153716 002254    BISB    GDATA,(SP)
3669 013030 012746 013127    MOV     #FMT02A,-(SP)
3670 013034 012746 000004    MOV     #4,-(SP)
3671 013040 010600          MOV     SP,R0
3672 013042 104415          TRAP   C$PNTX
3673 013044 062706 000012    ADD     #12,SP
3674 013050          PRINTX #FMT09A,TDATA ;LSI ADDRESS
3675 013050 013746 002252    MOV     TDATA,-(SP)
3676 013054 012746 013673    MOV     #FMT09A,-(SP)
3677 013060 012746 000002    MOV     #2,-(SP)
3678 013064 010600          MOV     SP,R0
3679 013066 104415          TRAP   C$PNTX
3680 013070 062706 000006    ADD     #6,SP
3681 013074 000207          RTS     PC
3682
3683 ;-----
3684 ;SBTTL      SUBROUTINE TO PERFORM 'PRINTB #ENDEMB'
3685 ;-----
3686
3687 NULERR: PRINTB #ENDEMB          ;TERMINATE ERROR MESSAGE
3688 013076 012746 013120    MOV     #ENDEMB,-(SP)
3689 013102 012746 000001    MOV     #1,-(SP)
3690 013106 010600          MOV     SP,R0
3691 013110 104414          TRAP   C$PNTB
3692 013112 062706 000004    ADD     #4,SP
3693 013116 000207          RTS     PC
3694 ;-----

```

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3695
3696
3697
3698
3699

FORMAT SPEC'S FOR ERROR HANDLERS -- 'FMT___'

.SBTTL FORMAT SPEC'S FOR ERROR HANDLERS -- 'FMT___'

----- FORMAT SPEC'S USED BY ERROR HANDLERS -----

013120 047045 000062
013124 047045 000

.NLIST BEX
ENDEMB: .ASCIZ /%N2/
NEWLIN: .ASCIZ /%N/

013127 045 022516 020101
013213 045 022516 020101
013253 045 022516 030523
013306 047045 052045 000
013313 045 022516 032523
013346 047045 040445 020040
013411 045 022516 020101
013476 040445 020040 042504
013530 047045 022462 030523
013542 047045 022462 020101
013621 045 020101 042040
013673 045 020101 046040

FMT02A: .ASCIZ /%NZA EXPECTED: %03XA ACTUAL: %03XA XOR: %03/
FMT4: .ASCIZ /%NZA THE CONTENTS OF ALL%T%N%T/
FMT4A: .ASCIZ /%N%S1%03%S5%03%S5%03%S5%03/
FMT4B: .ASCIZ /%N%T/
FMT4C: .ASCIZ /%N%S5%03%S5%03%S5%03%S5%03/
FMT5: .ASCIZ /%NZA WHEN %03XA LOADED INTO BSEL1/
FMT5A: .ASCIZ /%NZA ATTEMPTING 'M-LOOP' FUNCTION CODE %02XA (%T%XA)/
FMT07: .ASCIZ /%A DETECTED IN %T%T%XA --/
FMT06: .ASCIZ /%N2%S13%T/
FMT06A: .ASCIZ /%N2%XA N P R R E G I S T E R S :/
FMT09: .ASCIZ /%A DETECTED @ TEST PATTERN ELEMENT # %D2/
FMT09A: .ASCIZ /%A LSI ADDR: %08/

013714 047045 040445 020040
013770 047045 047445 022470
014007 045 022516 022524
014032 047045 052045 047445
014074 040445 020040 020040
014153 045 022516 020101
014234 047045 040445 020040
014307 045 022516 020101
014367 045 022516 020101
014441 045 022516 030523
014522 047045 051445 022463
014562 051445 022463 031517
014577 045 031523 047445
014614 047045 022462 030523
014673 045 022516 020101

FMT10: .ASCIZ /%NZA EXPECTED: %08XA ACTUAL: %08XA XOR: %08/
FMT11: .ASCIZ /%N%08%08%08%08/
FMT16: .ASCIZ /%N%T%03%S4%03%S%03/
FMT16A: .ASCIZ /%N%T%03%S%03%S%03%S4%03%S%03%S%03/
FMT17: .ASCIZ /%A VALUE SENT TO NPR CONTROL REGISTER: %03/
FMT17A: .ASCIZ /%NZA VALUE READ FROM CONTROL REGISTER: %03/
FMT17B: .ASCIZ /%NZA LSI-11 MEMORY ADDRESS ACCESSED: %08/
FMT17C: .ASCIZ /%NZA INFORMATION ON THE FIRST OF %D5XA ERRORS:/
FMT50A: .ASCIZ /%NZA TIMER # 1 MODE: %01XA REGISTERS:/
FMT50B: .ASCIZ /%N%S15%AT1CH T1CL T1LH T1LL ACR IFR IER/
FMT50C: .ASCIZ /%N%S3%T%S1%03%S3%03%S3%03%S3%03/
FMT50D: .ASCIZ /%S3%03%S9%03/
FMT50E: .ASCIZ /%S3%03%S3%03/
FMT50M: .ASCIZ /%N2%S10%XA(T1CH & T1CL HAVEN'T YET BEEN LOADED)/
FMT51A: .ASCIZ /%NZA TIMER # 2 MODE: %01XA T2CH & T2LL: %03%S%03/

.SBTTL TEXT STRINGS FOR ERROR HANDLERS -- 'TXT___'

----- TEXT USED BY ERROR HANDLERS -----

014761 102 042523 030114
015017 040 020040 041040
015061 102 042523 030514
015120 020040 041040 042523
015162 041040 052131 020105
015212 020040 051440 046105
015252 020040 051440 046105
015313 040 042523 042514
015336 042440 050130 041505
015353 040 041501 052524
015370 054040 051117 020072
015405 040 047514 042101
015422 051040 040505 035104

TXT1: .ASCIZ /BSEL0 BSEL1 BSEL2 BSEL3/
TXT2: .ASCIZ / BSEL4 BSEL5 BSEL6 BSEL7/
TXT2A: .ASCIZ /BSEL10 BSEL11 BSEL12 BSEL13/
TXT2B: .ASCIZ / BSEL14 BSEL15 BSEL16 BSEL17/
TXT3: .ASCIZ / BYTE SELECT REG'S ARE:/
TXT4: .ASCIZ / SEL0 SEL2 SEL4 SEL6/
TXT4A: .ASCIZ / SEL10 SEL12 SEL14 SEL16/
TXT6: .ASCIZ / SELECT REG'S ARE:/
TXT8A: .ASCIZ / EXPECTED: /
TXT8B: .ASCIZ / ACTUAL: /
TXT8C: .ASCIZ / XOR: /
TXT8D: .ASCIZ / LOADED: /
TXT8E: .ASCIZ / READ: /

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TEXT STRINGS FOR ERROR HANDLERS -- 'TXT___'

015437	103	047117	051124	TXT11A:	.ASCIZ	/CONTROL DATA/
015460	020040	047440	052125	TXT11B:	.ASCIZ	/ OUT ADDR. IN ADDR./
015513	021	000		TXTNUL:	.BYTE	21,0 ;CTL-Q -- THIS (WE HOPE) IS HARMLESS
015515	116	050117	000	TXTML0:	.ASCIZ	/NOP/
015521	122	040505	020104	TXTML1:	.ASCIZ	/READ 1 BYTE/
015535	127	044522	042524	TXTML2:	.ASCIZ	/WRITE 1 BYTE/
015552	050116	026522	052517	TXTML3:	.ASCIZ	/NPR-OUT 256 BYTES/
015574	050116	026522	047111	TXTML4:	.ASCIZ	/NPR-IN 256 BYTES/
015615	123	052105	046440	TXTML5:	.ASCIZ	/SET MICROPROCESSOR'S PC/
015645	125	042116	043105	TXTML6:	.ASCIZ	/UNDEFINED/
015657	015	051412	052105	TXTML7:	.ASCIZ	<15><12>/SET MAINT INTERRUPT & CLR INT DISABLE IN CPU STATUS/
015745	116	051120	000040	TXTNP:	.ASCIZ	/NPR /
015752	047503	052116	047522	TXTNP0:	.ASCIZ	/CONTROL/
015762	040504	040524	044040	TXTNP1:	.ASCIZ	/DATA HI/
015772	040504	040524	046040	TXTNP2:	.ASCIZ	/DATA LO/
016002	042101	051104	020056	TXTNP3:	.ASCIZ	/ADDR. OUT EX/
016017	101	042104	027122	TXTNP4:	.ASCIZ	/ADDR. OUT HI/
016034	042101	051104	020056	TXTNP5:	.ASCIZ	/ADDR. OUT LO/
016051	101	042104	027122	TXTNP6:	.ASCIZ	/ADDR. IN EX/
016065	101	042104	027122	TXTNP7:	.ASCIZ	/ADDR. IN HI/
016101	101	042104	027122	TXTNP8:	.ASCIZ	/ADDR. IN LO/

ERROR MESSAGES -- 'EM__'

.SBTTL ERROR MESSAGES -- 'EM__'

----- ERROR MESSAGES USED BY ERROR CALL'S -----

016115	125	042055	040511	EM3:	.ASCIZ	/U-DIAG. FAILURE/
016135	115	042122	020131	EM4:	.ASCIZ	/MRDY TIMEOUT/
016152	050116	020122	047514	EM26:	.ASCIZ	/NPR LOGIC M-CLEAR FAILURE/
016204	040502	020104	050116	EM26A:	.ASCIZ	/BAD NPR REG. LOAD/
016226	047527	042122	047040	EM26B:	.ASCIZ	/WORD NPR-OUT ERROR/
016251	102	052131	020105	EM26C:	.ASCIZ	/BYTE NPR-OUT ERROR/
016274	047527	042122	047040	EM26D:	.ASCIZ	/WORD NPR-IN ERROR/
016316	050116	020122	044524	EM26E:	.ASCIZ	/NPR TIMEOUT -- 'ABORT' SET/
016351	116	051120	041040	EM26F:	.ASCIZ	\NPR BS7 FAILURE ON WRITE\
016402	050116	026522	041101	EM26G:	.ASCIZ	/NPR-ABORT FAILURE/
016424	046515	020125	041101	EM27:	.ASCIZ	/MPU ABORT!/\
016437	130	040455	042104	EM27A:	.ASCIZ	/X-ADDR. NPR ABORT/
016461	130	040455	042104	EM27B:	.ASCIZ	/X-ADDR. NPR HUNG/
016502	026530	042101	051104	EM27C:	.ASCIZ	/X-ADDR. NPR FAILURE/
016526	040442	020042	047111	EM34:	.ASCIZ	/'A' INT. ?/\
016540	041042	020042	047111	EM34B:	.ASCIZ	/'B' INT. ?/\
016552	044515	051523	047111	EM35:	.ASCIZ	/MISSING 'A' INT./
016573	115	051511	044523	EM35B:	.ASCIZ	/MISSING 'B' INT./
016614	046504	020126	047111	EM40:	.ASCIZ	/DMV INIT'D BY 'BINIT' WITH 'DISABL INIT' SET/
016671	042	040515	052123	EM41:	.ASCIZ	/'MASTER RESET' FAILED WHEN 'DISABL INIT' SET/
016746	047516	021040	047520	EM42A:	.ASCIZ	/NO 'POWER UP' VECTOR ON 'DCOK' GOING HIGH/
017020	047516	044440	044516	EM42B:	.ASCIZ	/NO INIT ON 'DCOK' LOW & 'DISABL INIT' CLEAR/
017074	047111	040526	044514	EM42C:	.ASCIZ	/INVALID INIT ON 'DCOK' LOW & 'DISABL INIT' SET/
017153	104	053115	046440	EM43A:	.ASCIZ	\DMV MICRO-CODE HUNG\
017177	042	040510	052114	EM43B:	.ASCIZ	/'HALT' FAILED/
017215	116	020117	047520	EM43C:	.ASCIZ	/NO POWER-UP SEQUENCE/
017242	046442	050117	041055	EM43D:	.ASCIZ	/'MOP-BOOT' LOAD FAILED/
017271	042	030524	020042	EM50A:	.ASCIZ	\ 'T1' FLAG NOT CLEARED BY LOADING T1LH\
017337	042	030524	020042	EM50B:	.ASCIZ	\ 'T1' FLAG NOT CLEARED BY LOADING T1CH\
017405	042	030524	020042	EM50C:	.ASCIZ	\ 'T1' FLAG NOT CLEARED BY READING T1CL\
017453	126	040511	051447	EM50D:	.ASCIZ	\ VIA'S T1CL NOT DECREMENTING\
017507	126	040511	051447	EM50E:	.ASCIZ	\ VIA'S T1CH NOT DECREMENTING\
017543	042	030524	020042	EM50F:	.ASCIZ	\ 'T1' FLAG NOT SET ON TIMER 1 TIMEOUT\
017610	052042	021061	043040	EM50G:	.ASCIZ	\ 'T1' FLAG CLEARED BY READING T1CH\
017652	044526	023501	020123	EM50H:	.ASCIZ	\ VIA'S T1LL IMPROPERLY LOADED BY WRITING T1CL @ ADDR 4\
017740	052042	021061	043040	EM50I:	.ASCIZ	\ 'T1' FLAG CLEARED BY READING T1LL\
020002	044526	023501	020123	EM50J:	.ASCIZ	\ VIA'S T1LH IMPROPERLY LOADED BY WRITING T1CH @ ADDR 5\
020070	052042	021061	043040	EM50K:	.ASCIZ	\ 'T1' FLAG CLEARED BY READING T1LH\
020132	052042	021061	043040	EM50L:	.ASCIZ	\ 'T1' FLAG NOT SET AFTER RE-LOADING T1CH & TIMEOUT\
020214	052042	021061	043040	EM50M:	.ASCIZ	\ 'T1' FLAG CLEARED BY LOADING T1LL\
020256	052042	021061	043040	EM50N:	.ASCIZ	\ 'T1' FLAG NOT CLEARED BY LOADING T1CH\
020324	050042	033502	020042	EM50S:	.ASCIZ	\ 'PB7' W/IN VIA NOT SET ON TIMER 1 TIMEOUT\
020376	050042	033502	020042	EM50U:	.ASCIZ	\ 'PB7' NOT SET AFTER TIMER 1 TIMEOUT\
020442	050042	033502	020042	EM50V:	.ASCIZ	\ 'PB7' NOT DRIVEN LOW BY LOADING T1CH\
020507	042	041120	021067	EM50W:	.ASCIZ	\ 'PB7' UNEXPECTEDLY MODIFIED BY TIMER 1\
020556	052042	021061	047040	EM50X:	.ASCIZ	\ 'T1' NOT RESET AFTER BEING CLEARED\
020621	042	041120	021067	EM50Y:	.ASCIZ	\ 'PB7' PREMATURELY SET DURING T1 COUNTDOWN\
020673	042	041120	021067	EM50Z:	.ASCIZ	\ 'PB7' NOT SET AFTER SECOND CYCLE\
020734	052042	021062	043040	EM51B:	.ASCIZ	\ 'T2' FLAG NOT CLEARED BY LOADING T2CH\
021002	052042	021062	043040	EM51C:	.ASCIZ	\ 'T2' FLAG NOT CLEARED BY READING T2CL\
021050	044526	023501	020123	EM51E:	.ASCIZ	\ VIA'S T2CH NOT DECREMENTING\

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ERROR MESSAGES -- 'EM__'

021104	052042	021062	043040	EM51F:	.ASCIZ	\ 'T2' FLAG NOT SET ON TIMER 2 TIMEOUT\
021151	042	031124	020042	EM51G:	.ASCIZ	\ 'T2' FLAG CLEARED BY READING T2CH\
021213	042	031124	020042	EM51L:	.ASCIZ	\ 'T2' FLAG NOT SET AFTER RE-LOADING T2CH & TIMEOUT\
021275	042	031124	020042	EM51M:	.ASCIZ	\ 'T2' FLAG CLEARED BY LOADING T2LL\
021337	042	031124	020042	EM51N:	.ASCIZ	\ 'T2' FLAG NOT CLEARED BY LOADING T2CH\
021405	042	031124	020042	EM51P:	.ASCIZ	\ 'T2' FLAG NOT SET AFTER APPROPRIATE DELAY\
021457	042	051123	020042	EM52A:	.ASCIZ	\ 'SR' FLAG SET BEFORE ACCESSING SHIFT REGISTER\
021535	116	020117	051442	EM52B:	.ASCIZ	\ NO 'SR' INT. USING MODE 2\
021567	111	041516	046517	EM52C:	.ASCIZ	\ INCOMPLETE SHIFTING OPERATION IN MODE 2 -- GOT INT.\
021653	116	020117	051442	EM52D:	.ASCIZ	\ NO 'SR' INT. AFTER READING SR\
021711	104	053115	042440	EM60N:	.ASCIZ	\ DMV EXTENDED NPR WRITE ERROR\

.EVEN

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TEXT ADDRESS TABLES FOR ERROR HANDLERS -- 'TXT_T'

.SBTTL TEXT ADDRESS TABLES FOR ERROR HANDLERS -- 'TXT_T'

----- TEXT ADDRESS TABLES USED BY ERROR HANDLERS -----

021746 015515 015521 015535 TXTMLT: .WORD TXTML0,TXTML1,TXTML2,TXTML3,TXTML4,TXTML5,TXTML6,TXTML7

021766 015745 .WORD TXTNP

021770 015752 015762 015772 TXTNPT: .WORD TXTNP0,TXTNP1,TXTNP2,TXTNP3,TXTNP4,TXTNP5,TXTNP6,TXTNP7,TXTNP8

.LIST BEX

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LOAD DEVICE PROTECTION TABLE

```

3700
3701
3702
3703
3704
3705
3706
3707 022012
3708 022012
3709 022012 177777
3710 022014 177777
3711 022016 177777
3712 022020

```

.SBTTL LOAD DEVICE PROTECTION TABLE

```

:////////////////////
:// THIS TABLE IDENTIFIES THE LOAD DEVICE TO THE SUPERVISOR, SO THAT IT CAN BE
:// PROTECTED FROM TESTING. IF DESIRED.
:////////////////////

```

BGNPROT

```

.WORD -1 ;DON'T CHK CSR ADRS
.WORD -1 ;DON'T CHK MASSBUS UNIT NO.
.WORD -1 ;DON'T CHK DRIVE NO.
ENDPROT

```

LSPROT::

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INITIALIZE SECTION

.SBTTL INITIALIZE SECTION

:/ THE INITIALIZE SECTION CONTAINS THE CODING THAT IS PERFORMED
:/ AT THE BEGINNING OF THE TEST SEQUENCE ON THE NEXT UNIT.

3713
3714
3715
3716
3717
3718
3719
3720 022020
3721 022020
3722
3723 022020 010637 002270
3724
3725 022024
3726 022024 012700 000040
3727 022030 104447
3728 022032
3729 022032 103417
3730
3731 022034
3732 022034 012700 000037
3733 022040 104447
3734 022042
3735 022042 103454
3736
3737 022044
3738 022044 012700 000035
3739 022050 104447
3740 022052
3741 022052 103454
3742
3743 022054
3744 022054 012700 000036
3745 022060 104447
3746 022062
3747 022062 103401
3748 022064 000461
3749
3750 022066 000137 022456
3751
3752
3753
3754 022072 005037 002306
3755
3756
3757
3758 022076 005037 002314
3759 022102
3760 022102 012746 000000
3761 022106 012746 022556
3762 022112 012746 000004
3763 022116 012746 000003
3764 022122 104437
3765 022124 012706 000010
3766 022130 015737 177564
3767 022134
3768 022134 012700 000004

BGNINIT
LSINIT::
MOV SP,PSTACK ;SAVE BASE-LEVEL STACK POINTER
;SEE IF PROGRAM JUST STARTED, BR IF YES
READEF #EF.START
BCOMplete STARST
;SEE IF PROGRAM JUST RESTARTED, BR IF YES
READEF #EF.RESTART
BCOMplete RESTRT
;SEE IF THIS IS A NEW PASS, BR IF YES
READEF #EF.NEW
BCOMplete NEWST
;SEE IF PROGRAM WAS JUST CONTINUED
READEF #EF.CONTINUE
BCOMplete 10\$
BR GETPRM
10\$: JMP CONTIN ;(THIS IS TO FAR AWAY FOR A 'BR' INSTRUCTION)
;*** ENTER HERE IF 'START' COMMAND ISSUED
STARST: CLR STARES ;CLEAR FLAG TO SHOW JUST HAD STA OR RES
; TEST FOR THE PRESENCE OR ABSENCE OF A CONSOLE TERMINAL.
CLR CONSOL ;RESET THE CONSOLE TERMINAL FLAG
SETVEC #4,#CONST,#0 ;SETUP BUS TIMEOUT VECTER TO TEST FOR A CONSOLE
MOV #0,-(SP)
MOV #CONST,-(SP)
MOV #4,-(SP)
MOV #3,-(SP)
TRAP CSSVEC
ADD #10,SP
TST @#177564 ;TRY TO ACCESS THE CONSOLE TERMINAL'S 'XCSR'
CLRVEC #4 ;WE SHOULD BE THROUGH WITH THIS BY NOW
MOV #4,RO

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INITIALIZE SECTION

```

3769 022140 104436                                TRAP  C$CVEC
3770
3771
3772
3773
3774
3775 022142 005737 002314                        TST  CONSOL          ;IF CONSOLE TERMINAL ISN'T THERE,
3776 022146 001412                                BEQ  5$
3777 022150                                PRINTF #CFMTO,#NPROTS ;TELL THE OPERATOR WHAT TESTING WON'T BE DONE
3778 022150 012746 000004                                MOV  #NPROTS,-(SP)
3779 022154 012746 022566                                MOV  #CFMTO,-(SP)
3780 022160 012746 000002                                MOV  #2,-(SP)
3781 022164 010600                                MOV  SP,R0
3782 022166 104417                                TRAP C$PNTF
3783 022170 062706 000006                                ADD  #6,SP
3784 022174
3785
3786
3787 022174 005037 002306                        5$:
3788 022200 005037 002310                        ;*** ENTER HERE IF 'RESTART' COMMAND ISSUED
3789
3790 022204                                RESTRT: CLR  STARES          ;CLEAR FLAG TO SHOW JUST HAD STA OR RES
3791
3792 022204 012737 177777 002266                CLR  DEVMAP          ;CLEAR DEVICE MAP
3793 022212 005237 002306
3794 022216 005237 002304
3795 022222 012737 000001 002312                NEWST:
3796
3797                                ;ENTER HERE BEFORE EACH TEST
3798 022230
3799 022230 005237 002266
3800 022234
3801 022234 013700 002266
3802 022240 104442
3803 022242 010001
3804 022244
3805 022244 103403
3806 022246 006337 002312
3807 022252 000766
3808
3809 022254 053737 002312 002310 10$:
3810 022262 006337 002312
3811
3812
3813
3814 022266 012100
3815 022270 012703 000020
3816 022274 012702 002320
3817 022300 010022
3818 022302 005200
3819 022304 077303
3820
3821
3822
3823 022306 012100
3824 022310 010037 002360

```

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INITIALIZE SECTION

```

3825 022314 022020          CMP      (R0)+,(R0)+      ;ADD 4 TO VECTOR TO GET ADDRESS OF 'B' VECTOR
3826 022316 010037 002362  MOV      R0,MPOVEC      ;SETUP 'B' VECTOR POINTER
3827
3828 022322 012100          MOV      (R1)+,R0       ;GET DMV11 DEVICE PRIORITY
3829 022324 006200          ASR      R0             ;RE-POSITION IT
3830 022326 006200          ASR      R0
3831 022330 006200          ASR      R0
3832 022332 006200          ASR      R0
3833 022334 010037 002364  MOV      R0,MPRIOR     ;SETUP OUR VARIABLE FOR INT. VECTOR INIT'S
3834
3835 022340 022121          CMP      (R1)+,(R1)+     ;SKIP OVER SWITCH #'S 1 & 2
3836 022342 012137 002366  MOV      (R1)+,BRDTYP   ;GET DMV-11 BOARD TYPE
3837 022346 022111          CMP      (R1)+,(R1)     ;SKIP OVER CONNECTOR FLAG
3838
3839 022350 012100          MOV      (R1)+,R0       ;GET CONTROL FLAGS
3840 022352 012703 000006  MOV      #6,R3          ;POSITION THEM PROPERLY IN THE WORD
3841 022356 006200          15$: ASR      R0
3842 022360 077302          SOB      R3,15$
3843 022362 010037 002370  MOV      R0,PT.CTL     ;PUT IT WHERE TESTS EXPECT TO FIND IT
3844
3845          ; TEST THE VARIOUS CONTROL FLAGS & REPORT NON-STANDARD ACTION RESULTING FROM
3846          ; THEIR SETTINGS
3847
3848 022366 023727 002306 000001  CMP      STARES,#1      ;FIRST PASS SINCE STA OR RES ??
3849 022374 001030          BNE      40$           ; IF NO: SKIP POSSIBLE PRINTOUT
3850
3851 022376 032737 000001 002370  BIT      #PU24,PT.CTL   ;IF THE PROCESSOR ISN'T STRAPPED TO COME UP
3852 022404 001024          BNE      40$           ;THROUGH INTERRUPT VECTOR 24 & 26,
3853 022406          PRINTF  #CFMT2,#DCOKTS,#HLTEST ;TELL THE OPERATOR THAT NO DCOK TESTING
3854 022406 012746 000015          MOV      #HLTEST,-(SP)
3855 022412 012746 000014          MOV      #DCOKTS,-(SP)
3856 022416 012746 022655          MOV      #CFMT2,-(SP)
3857 022422 012746 000003          MOV      #3,-(SP)
3858 022426 010600          MOV      SP,R0
3859 022430 104417          TRAP    C$PNTF
3860 022432 062706 000010          ADD     #10,SP
3861 022436          PRINTF  #CFMT3          ;WILL BE DONE
3862 022436 012746 022735          MOV     #CFMT3,-(SP)
3863 022442 012746 000001          MOV     #1,-(SP)
3864 022446 010600          MOV     SP,R0
3865 022450 104417          TRAP    C$PNTF
3866 022452 062706 000004          ADD     #4,SP
3867 022456          40$:
3868
3869 022456          CONTIN: ;ENTER HERE WHEN A 'CONTINUE' COMMAND IS ISSUED
3870
3871 022456          SETVEC  @MPIVEC,@MPIHAN,@MPRIOR ;SETUP 'A' INT. VECTOR
3872 022456 013746 002364          MOV     @MPRIOR,-(SP)
3873 022462 012746 006066          MOV     @MPIHAN,-(SP)
3874 022466 013746 002360          MOV     @MPIVEC,-(SP)
3875 022472 012746 000003          MOV     #3,-(SP)
3876 022476 104437          TRAP    C$SVEC
3877 022500 062706 000010          ADD     #10,SP
3878 022504 005037 006136          CLR     IHILNK         ;WE DON'T WANT THE HANDLER TO LINK ELSEWHERE
3879 022510          SETVEC  @MPOVEC,@MPOHAN,@MPRIOR ;SETUP 'B' INT. VECTOR
3880 022510 013746 002364          MOV     @MPRIOR,-(SP)

```

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INITIALIZE SECTION

```

3881 022514 012746 006140
3882 022520 013746 002362
3883 022524 012746 006w03
3884 022530 104437
3885 022532 062706 000010
3886 022536 005037 006210
3887 022542 005037 002274
3888
3889 022546 012737 000001 002302
3890 022554
3891 022554
3892 022554 104411
3893
3894
3895
3896
3897
3898 022556 012737 177777 002314
3899 022564 000002
3900
3901
3902
022566 047045 040445 052040
022655 045 022516 020101
022735 045 022516 032123
3903 023014

```

```

MOV      @MPOHAN,-(SP)
MOV      @MPOVEC,-(SP)
MOV      #3,-(SP)
TRAP     CS$VEC
ADD      #10,SP
CLR      IHOLNK          ;WE DON'T WANT THE HANDLER TO LINK ELSEWHERE
CLR      INTWCH          ;RESET 'INTERRUPT WATCH' FLAGS (BOTH 'A' & 'B')
MOV      #1,FRSTIM       ;MARK FLAG FOR NEXT TIME THROUGH
ENDINIT   ;END OF 'INIT' CODE
L10022:
TRAP     CS$INIT

```

```

: ***** SUBROUTINES USED BY 'INIT' CODE *****
:
: INTERRUPT HANDLER FOR CONSOLE TERMINAL PRESENCE TESTING
CONTST: MOV      #-1,CONSOL      ;INDICATE THAT NO CONSOLE TERMINAL EXISTS!
RTI      ;RETURN

```

```

: FORMATS FOR FORCED MESSAGES
.NLIST
BEX
CFMT0: .ASCIZ  /%XA TEST %D2% SUBTEST 3 CAN'T RUN -- NO CSR @ 177564/
CFMT2: .ASCIZ  /%XA TESTS %D2% AND %D2% CAN'T RUN -- CPU NOT/
CFMT3: .ASCIZ  /%XS4%STRAPPED TO POWER-UP THROUGH VECTOR 24/
.LIST
BEX
.EVEN

```

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AUTO DROP UNIT SECTION

.SBTTL AUTO DROP UNIT SECTION

:/ THE AUTO DROP CODING DETERMINES WHETHER OR NOT THE DEVICE WHOSE P-TABLE WAS JUST OBTAINED IS READY FOR TESTING, AND IT IS DROPPED IF NOT READY.

THIS ALGORITHM IS THE SAME A CVDMA TEST # 1 EXCEPT THAT TEST WILL JUST REPORT THE FAILURE AND GO ON -- THIS ROUTINE WILL CAUSE THE DEVICE TO BE DROPPED IF A BUS-TIMEOUT OCCURS WHEN ANY OF THE CSR'S ARE ACCESSED WITH EITHER A 'TST' OR 'TSTB' INSTRUCTION.

3904
3905
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3959

023014
023014
023014
023014
023020
023024
023030
023034
023036
023042
023046
023052
023056
023060
023062
023064
023070
023074
023076
023100
023102
023104
023104
023110
023112
023116
023120
023120
023124
023126
023130
023130
023130

012746
012746
012746
012746
062706
005037
012702
013703
105723
006302
103375
013703
012702
005723
006302
006302
103374
012700
104436
005737
001403
013700
104451
000240
104461

BGNAUTO
LSAUTO::
SETVEC #4, #AD.HIT, #0
MOV #0, -(SP)
MOV #AD.HIT, -(SP)
MOV #4, -(SP)
MOV #3, -(SP)
TRAP C\$SVEC
ADD #10, SP
CLR TMO
MOV #1, R2
MOV BSEL0, R3
1\$: TSTB (R3)+
ASL R2
BCC 1\$
MOV BSEL0, R3
MOV #1, R2
2\$: TST (R3)+
ASL R2
ASL R2
BCC 2\$
CLRVEC #4
MOV #4, R0
TRAP C\$CVEC
TST TMO
BEQ AD.OK
DODU LOGDEV
AD.OK: NOP
ENDAUTO

:SETUP INVALID-ADDRESS TRAP VECTOR
:INITIALIZE TRAP FLAG REGISTER
:FLAG BIT
:INIT ADDRESS POINTER
:ACCESS THE CSR'S BY BYTES.
:RE-INIT ADDRESS POINTER
:RE-INIT FLAG BIT
:ACCESS THE CSR'S BY WORDS.
:RESTORE THE VECTOR TO DS
:DID WE GET HIT WITH AN INVALID ADDRESS TRAP?
:NO, EXIT TEST
:YES, DROP THIS LOGICAL DEV.
:(FOR PATCHING IN A HALT IF NECESSARY)

MOV #0, -(SP)
MOV #AD.HIT, -(SP)
MOV #4, -(SP)
MOV #3, -(SP)
TRAP C\$SVEC
ADD #10, SP
MOV #4, R0
TRAP C\$CVEC
MOV LOGDEV, R0
TRAP C\$DODU
TRAP C\$AUTO

L10023:

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AUTO DROP UNIT SECTION

3960 023132 050237 002412
3961 023136 000002
3962

AD.HIT: B!S R2, TMPO
RT!

;FLAG THE HIT IF WE GET IT!
;RETURN

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CLEANUP CODING SECTION

.SBTTL CLEANUP CODING SECTION

://
:// THE CLEANUP CODING SECTION CONTAINS THE CODING THAT IS PERFORMED
:// AT THE END OF THE TEST SEQUENCE ON A PARTICULAR UNIT.
://

3963
3964
3965
3966
3967
3968
3969
3970 023140
3971 023140
3972 023140
3973 023140 013700 002360
3974 023144 104436
3975 023146
3976 023146 013700 002362
3977 023152 104436
3978 023154
3979 023154
3980 023154 104412

BGNCLN

CLRVEC @MPIVEC

CLRVEC @MPOVEC

ENDCLN

L\$CLEAN::
;RETURN VECTORS TO SUPERVISOR

MOV @MPIVEC,RO
TRAP CSCVEC

MOV @MPOVEC,RO
TRAP CSCVEC

L10024:
TRAP \$CLEAN

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DROP UNIT SECTION

.SBTTL DROP UNIT SECTION

```

://////////////////
:// THE DROP-UNIT SECTION CONTAINS THE CODING THAT CAUSES A DEVICE
:// TO NO LONGER BE TESTED.
://////////////////

```

```

3981
3982
3983
3984
3985
3986
3987
3988 023156
3989 023156
3990
3991 023156
3992 023156 104433
3993 023160
3994 023160
3995 023160 104453

```

```

          BGNDU
          :ISSUE UNIBUS RESET TO CLEAN UP
          BRESET
          ENDDU
          LSDU::
          TRAP CSRESET
          L10025:
          TRAP CSDU

```

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ADD UNIT SECTION

.SBTTL ADD UNIT SECTION

```

:////////////////////
:// THE ADD-UNIT SECTION CONTAINS THE CODING THAT CAUSES A DEVICE
:// TO BE (A) TESTED FOR THE FIRST TIME, OR (B) RESUMED IN TESTING. IF
:// 'EF.AUNIT' IS SET, THE UNIT WILL BE TESTED AS A NEW UNIT.
:////////////////////

```

```

3996
3997
3998
3999
4000
4001
4002
4003
4004 023162
4005 023162
4006 023162
4007 023162
4008 023162 104452

```

BGNAU

ENDAU

LSAU::

L10026:

TRAP CSAU

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TEST 1 -- VIA TIMER 2 ONE SHOT MODE

.SBTTL TEST 1 -- VIA TIMER 2 ONE SHOT MODE

4009
4010
4011
4012
4013
4014
4015
4016
4017
4018
4019
4020
4021
4022
4023
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4025
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4057
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4059
4060
4061
4062
4063
4064

023164
023164
023164
023164 104402

023166 004737 003514
023172 103003
023174
023174 104460
023176
023176 104410

* TEST 1 -- VIA TIMER 2 ONE SHOT MODE
* THIS TEST VERIFIES THAT THE TIMER 2 COUNTER IS OPERATIONAL IN
* INTERVAL-TIMER (ONE-SHOT) MODE.
* THE FOLLOWING IS PERFORMED :
* A MASTER CLEAR IS DONE & THE TIMER IS PLACED IN INTERVAL-TIMER MODE
* BY SETTING ACR5 = 0 AND THE PROGRAM CHECKS FOR 'T2' (BIT 5 IN IFR)
* TO BE INITIALLY CLEARED.
* T2L-L (ADR 08) & T2C-H (ADR 09) ARE BOTH LOADED WITH 252 (OCTAL).
* (THIS IS EQUIVALENT TO AAAA (HEX) OR 43,690 (DECIMAL).) LOADING
* T2C-H STARTS THE COUNTER.
* T2L-L IS LOADED WITH 001 AND T2C-H IS LOADED WITH 000 IN ORDER TO
* SET 'T2' WITH A QUICK UNDERFLOW. THE 'T2' FLAG BIT IN IFR IS READ
* AND CHECKED TO BE SET.
* T2C-H IS CHECKED TO = 0. CHECKING T2C-H SHOULD NOT HAVE CLEARED 'T2'
* -- THIS IS VERIFIED.
* T2C-L IS CHECKED TO = 0. CHECKING T2C-L SHOULD HAVE CLEARED 'T2' --
* THIS TOO IS VERIFIED.
* T2C-H IS LOADED WITH 0 AGAIN TO INITIATE A NEW COUNT DOWN (WHICH
* SHOULD UNDERFLOW ALMOST IMMEDIATELY) AND THE 'T2' BIT IN IFR IS
* CHECKED TO BE SET AGAIN.
* T2L-L IS LOADED WITH 125 (OCTAL) AND 'T2' BIT IS CHECKED TO BE STILL
* SET.
* T2C-H IS LOADED WITH 125, AND THE 'T2' BIT IS READ AND CHECKED TO BE
* CLEARED BY THE LOADING OF T2C-H.

```

: BGNTST
: BGNSUB
: T1::
: T1.1: TRAP CSBSUB
-----
: JSR PC,MSTCLR ;INIT DMV & ENTER M-LOOP
: BCC 1$ ;IF NO ERROR, PROCEED WITH TESTING
: ERROR ;ELSE, REPORT ERROR
: ESCAPE TST ; & EXIT TEST
: TRAP CSERROR
: TRAP CSESCAPE
```

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TEST 1 -- VIA TIMER 2 ONE SHOT MODE

```

4065 023200 000700
4066 023202 004537 004630
4067 023206 002000
4068 023210 000000
4069 023212 103003
4070 023214 104460
4071 023214 104460
4072 023216
4073 023216 104410
4074 023220 000660
4075 023222 004737 024134
4076 023226 102002
4077 023230
4078 023230 104410
4079 023232 000644
4080 023234 103033
4081 023236
4082
4083 023236 104455
4084 023240 000011
4085 023242 020734
4086 023244 011176
4087
4088
4089
4090 023246 112737 000002 002435
4091 023254 004537 004042
4092 023260 120011
4093 023262 002435
4094 023264 103003
4095 023266
4096 023266 104460
4097 023270
4098 023270 104410
4099 023272 000606
4100 023274 004737 024134
4101 023300 102002
4102 023302
4103 023302 104410
4104 023304 000572
4105 023306 103006
4106 023310
4107
4108 023310 104455
4109 023312 000012
4110 023314 020734
4111 023316 011176
4112 023320
4113 023320 104410
4114 023322 000554
4115
4116
4117
4118 023324 004537 024102
4119 023330 252
4120 023331 252

1$: JSR R5,INITT2 ;INITIALIZE TIMER # 2
      2000 ; 2000 ==> LATCHES (PREVENTS IMMED. TIMEOUT)
      0 ; MODE 0 & 'T2' INT. ENABLE FLAG CLEARED
      BCC .+10 ;IF NO ERROR, PROCEED
      ERROR ;ELSE, REPORT IT
      ESCAPE TST ; AND EXIT THIS TEST
      TRAP C$ERROR
      .WORD L10027-.

      JSR PC,GETT2 ;IS 'T2' SET?
      BVC .+6 ;IF NO ERROR, PROCEED
      ESCAPE SUB ;ELSE, IT'S ALREADY BEEN REPORTED -- EXIT
      TRAP C$ESCAPE
      .WORD L10027-.

      BCC 6$ ;NO, GOOD.
      GEDF EM51B,ERR51 ;YES, REPORT IT'S NOT BEING CLEARED @ INIT.
      ; 'DEVICE FATAL' ERROR # 9
      TRAP C$ERDF
      .WORD 9
      .WORD EM51B
      .WORD ERR51

-----

      MOVB #2,TMP9+1
      JSR R5,WRITE ;INIT TIMER # 2 BY WRITING INTO
      T2CH ;T2C-H (ADDR 09)
      TMP9+1
      BCC .+10 ;IF NO ERROR, PROCEED
      ERROR ;ELSE, REPORT IT
      ESCAPE TST ; AND EXIT THIS TEST
      TRAP C$ERROR
      .WORD L10027-.

      JSR PC,GETT2 ;IS 'T2' SET?
      BVC .+6 ;IF NO ERROR, PROCEED
      ESCAPE SUB ;ELSE, IT'S ALREADY BEEN REPORTED -- EXIT
      TRAP C$ESCAPE
      .WORD L10030-.

      BCC 6$ ;NO, GOOD.
      GEDF EM51B,ERR51 ;YES, REPORT IT'S NOT BEING CLEARED @ INIT.
      ; 'DEVICE FATAL' ERROR # 10
      TRAP C$ERDF
      .WORD 10
      .WORD EM51B
      .WORD ERR51

      ESCAPE SUB ;AND EXIT SUBTEST
      TRAP C$ESCAPE
      .WORD L10030-.

-----

6$: JSR R5,LODT2C ;LOAD TIMER # 2
7$: .BYTE 252
8$: .BYTE 252

```

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TEST 1 -- VIA TIMER 2 ONE SHOT MODE

```

4121
4122
4123
4124 023332 004537 003616      JSR    R5,READ      ;READ THE LOW COUNTER
4125 023336 120010              T2CL
4126 023340 002432              TMP8
4127 023342 103003              BCC    .+10         ;IF NO ERROR, PROCEED
4128 023344              ERROR                ;ELSE, REPORT IT
4129 023344 104460              ESCAPE TST          ;      AND EXIT THIS TEST      TRAP    C$ERROR
4130 023346              ;                                     ;                                     TRAP    C$ESCAPE
4131 023346 104410              ;                                     ;                                     .WORD  L10027-.
4132 023350 000530              CMPB   TMP8,7$      ;MAKE SURE THE COUNTER IS DECREMENTING
4133 023352 123737 002432 023330  BNE    12$          ;IT IS, NOW SEE IF THE HIGH COUNTER IS TOO
4134 023360 001004              GEDF   EM50D,ERR51 ;IT WASN'T -- REPORT THE ERROR
4135 023362              ;      'DEVICE FATAL' ERROR # 11
4136
4137 023362 104455              ;                                     ;                                     TRAP    C$ERDF
4138 023364 000013              ;                                     ;                                     .WORD  11
4139 023366 017453              ;                                     ;                                     .WORD  EM50D
4140 023370 011176              ;                                     ;                                     .WORD  ERR51
4141 023372 012703 000100      12$:  MOV    #100,R3   ;INIT. TIMEOUT VALUE
4142 023376 004537 003616      13$:  JSR    R5,READ   ;READ THE HIGH COUNTER
4143 023402 120011              T2CH
4144 023404 002434              TMP9
4145 023406 103003              BCC    .+10         ;IF NO ERROR, PROCEED
4146 023410              ERROR                ;ELSE, REPORT IT
4147 023410 104460              ESCAPE TST          ;      AND EXIT THIS TEST      TRAP    C$ERROR
4148 023412              ;                                     ;                                     TRAP    C$ESCAPE
4149 023412 104410              ;                                     ;                                     .WORD  L10027-.
4150 023414 000464              CMPB   TMP9,8$      ;DID IT CHANGE FROM THE LOADED VALUE?
4151 023416 123737 002434 023331  BNE    14$          ;YES, PROCEED WITH TESTING
4152 023424 001007              SOB    R3,13$       ;NO, IF NO TIMEOUT, TRY AGAIN
4153 023426 077315              GEDF   EM51E,ERR51 ;ELSE, REPORT THAT HIGH COUNTER ISN'T RUNNING
4154 023430              ;      'DEVICE FATAL' ERROR # 12
4155
4156 023430 104455              ;                                     ;                                     TRAP    C$ERDF
4157 023432 000014              ;                                     ;                                     .WORD  12
4158 023434 021050              ;                                     ;                                     .WORD  EM51E
4159 023436 011176              ;                                     ;                                     .WORD  ERR51
4160 023440              ESCAPE SUB          ;      WE CAN'T PROCEED WITH TESTING EITHER
4161 023440 104410              ;                                     ;                                     TRAP    C$ESCAPE
4162 023442 000434              ;                                     ;                                     .WORD  L10030-.
4163
4164
4165 023444 005003 024134      14$:  CLR    R3        ;INITIALIZE TIMEOUT COUNTER
4166 023446 004737              15$:  JSR    PC,GETT2 ;WAIT FOR TIMER TO COUNT DOWN
4167 023452 102002              BVC    .+6         ;IF NO ERROR, PROCEED
4168 023454              ESCAPE SUB          ;ELSE, IT'S ALREADY BEEN REPORTED -- EXIT
4169 023454 104410              ;                                     ;                                     TRAP    C$ESCAPE
4170 023456 000420              ;                                     ;                                     .WORD  L10030-.
4171 023460 103406              BCS    16$          ;DONE.
4172 023462 077307              SOB    R3,15$       ;NOT YET, TIMEOUT?
4173 023464              GEDF   EM51P,ERR51 ;YES, REPORT NO 'T2' INT. FLAG
4174
4175 023464 104455              ;      'DEVICE FATAL' ERROR # 13
4176 023466 000015              ;                                     ;                                     TRAP    C$ERDF
                                     ;                                     .WORD  13

```

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TEST 1 -- VIA TIMER 2 ONE SHOT MODE

```

4177 023470 021405
4178 023472 011176
4179 023474 000445
4180
4181
4182
4183 023476 004537 003616
4184 023502 120011
4185 023504 002434
4186 023506 103003
4187 023510
4188 023510 104460
4189 023512
4190 023512 104410
4191 023514 000364
4192 023516 004737 024134
4193 023522 102002
4194 023524
4195 023524 104410
4196 023526 000350
4197 023530 103405
4198 023532
4199
4200 023532 104455
4201 023534 000016
4202 023536 021151
4203 023540 011176
4204 023542 000422
4205
4206
4207
4208 023544 004537 004042
4209 023550 120010
4210 023552 002433
4211 023554 103003
4212 023556
4213 023556 104460
4214 023560
4215 023560 104410
4216 023562 000316
4217 023564 004737 024134
4218 023570 102002
4219 023572
4220 023572 104410
4221 023574 000302
4222 023576 103404
4223 023600
4224
4225 023600 104455
4226 023602 000017
4227 023604 021275
4228 023606 011176
4229
4230
4231
4232 023610 004537 024102

```

```

                                .WORD  EM51P
                                .WORD  ERR51
BR      17$                      ;  & BYPASS 'T2'-RESET-ON-T2CH-READ CHECK
-----
16$:   JSR      R5,READ           ;READ T2C-H (ADDR 09)
        T2CH
        TMP9
        BCC     .+10             ;IF NO ERROR, PROCEED
        ERROR                    ;ELSE, REPORT IT
        ESCAPE TST              ;      AND EXIT THIS TEST
                                TRAP   C$ERROR
                                .WORD  C$ESCAPE
                                L10027-.
        JSR      PC,GETT2        ;IS 'T2' STILL SET?
        BVC     .+6              ;IF NO ERROR, PROCEED
        ESCAPE SUB              ;ELSE, IT'S ALREADY BEEN REPORTED -- EXIT
                                TRAP   C$ESCAPE
                                .WORD  L10030-.
        BCS     40$              ;YES, ALL'S OK
        GEDF    EM51G,ERR51     ;NO!  BAD VIA CHIP!
                                ;      'DEVICE FATAL' ERROR # 14
                                TRAP   C$ERDF
                                .WORD  14
                                .WORD  EM51G
                                .WORD  ERR51
BR      17$                      ;  & BYPASS 'T2'-RESET-ON-T2LL-WRITE CHECK
-----
40$:   JSR      R5,WRITE         ;RE-LOAD T2L-L (ADDR 08)
        T2LL
        TMP8+1
        BCC     .+10             ;IF NO ERROR, PROCEED
        ERROR                    ;ELSE, REPORT IT
        ESCAPE TST              ;      AND EXIT THIS TEST
                                TRAP   C$ERROR
                                .WORD  C$ESCAPE
                                L10027-.
        JSR      PC,GETT2        ;IS 'T2' STILL SET?
        BVC     .+6              ;IF NO ERROR, PROCEED
        ESCAPE SUB              ;ELSE, IT'S ALREADY BEEN REPORTED -- EXIT
                                TRAP   C$ESCAPE
                                .WORD  L10030-.
        BCS     17$              ;YES, ALL'S STILL OK
        GEDF    EM51M,ERR51     ;NO!  SOMETHING WENT WRONG! REPORT IT
                                ;      'DEVICE FATAL' ERROR # 15
                                TRAP   C$ERDF
                                .WORD  15
                                .WORD  EM51M
                                .WORD  ERR51
-----
17$:   JSR      R5,LODT2C        ;RE-LOAD TIMER # 2 WITH A VALUE WHICH CAUSE AN

```


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TEST 1 -- VIA TIMER 2 ONE SHOT MODE

```

4233 023614 001
4234 023615 000
4235
4236
4237 023616 004737 024134
4238 023622 102002
4239 023624
4240 023624 104410
4241 023626 000250
4242 023630 103406
4243 023632
4244
4245 023632 104455
4246 023634 000020
4247 023636 021104
4248 023640 011176
4249 023642
4250 023642 104410
4251 023644 000232
4252 023646 004537 003616
4253 023652 120011
4254 023654 002434
4255 023656 103003
4256 023660
4257 023660 104460
4258 023662
4259 023662 104410
4260 023664 000214
4261 023666 004737 024134
4262 023672 102002
4263 023674
4264 023674 104410
4265 023676 000200
4266 023700 103405
4267 023702
4268
4269 023702 104455
4270 023704 000021
4271 023706 017610
4272 023710 011176
4273 023712 000400
4274
4275 023714
4276
4277
4278 023714 004537 003616
4279 023720 120010
4280 023722 002432
4281 023724 103003
4282 023726
4283 023726 104460
4284 023730
4285 023730 104410
4286 023732 000146
4287 023734 004737 024134
4288 023740 102002

18$: .BYTE 1 ;ALMOST IMMEDIATE TIMEOUT
19$: .BYTE 0 ; (ADDRESS OF HIGH BYTE FOR T2C-H (ADDR 09))
-----
JSR PC,GETT2 ;WAS 'T2' SET BY THE ABOVE OPERATION?
BVC .+6 ;IF NO ERROR, PROCEED
ESCAPE SUB ;ELSE, IT'S ALREADY BEEN REPORTED -- EXIT
TRAP C$ESCAPE
.WORD L10030-.

BCS 20$ ;YES, OK -- CONTINUE ERROR CHECKING
GEDF EM51F,ERR51 ;NO, BAD NEWS! REPORT THE FAILURE
; 'DEVICE FATAL' ERROR # 16
TRAP C$ERDF
.WORD 16
.WORD EM51F
.WORD ERR51

ESCAPE SUB ; AND GET OUT OF SUBTEST
TRAP C$ESCAPE
.WORD L10030-.

20$: JSR R5,READ ;READ T2C-H (ADDR 09) TO SEE IF THIS CLEARS 'T2'
T2CH ;(THIS VALUE ISN'T CHECKED BECAUSE IT CAN BE
TMP9 ;ALMOST ANYTHING)
BCC .+10 ;IF NO ERROR, PROCEED
ERROR ;ELSE, REPORT IT
TRAP C$ERROR

ESCAPE TST ; AND EXIT THIS TEST
TRAP C$ESCAPE
.WORD L10027-.

JSR PC,GETT2 ;PUT THE CURRENT 'T2' VALUE INTO THE CARRY BIT
BVC .+6 ;IF NO ERROR, PROCEED
ESCAPE SUB ;ELSE, IT'S ALREADY BEEN REPORTED -- EXIT
TRAP C$ESCAPE
.WORD L10030-.

BCS 21$ ;IF SET, READING T2CH DIDN'T CLEAR IT -- OK!
GEDF EM50G,ERR51 ;IF CLEARED! BAD VIA CHIP!
; 'DEVICE FATAL' ERROR # 17
TRAP C$ERDF
.WORD 17
.WORD EM50G
.WORD ERR51

BR 28$ ;BYPASS THE REST OF THIS SECTION OF TESTING

21$:
-----
28$: JSR R5,READ ;READ T2C-L (ADDR 08)
T2CL ;(THIS VALUE ISN'T CHECKED BECAUSE IT CAN BE
TMP8 ;ALMOST ANYTHING)
BCC .+10 ;IF NO ERROR, PROCEED
ERROR ;ELSE, REPORT IT
TRAP C$ERROR

ESCAPE TST ; AND EXIT THIS TEST
TRAP C$ESCAPE
.WORD L10027-.

JSR PC,GETT2 ;IS 'T2' CLEARED NOW
BVC .+6 ;IF NO ERROR, PROCEED

```

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TEST 1 -- VIA TIMER 2 ONE SHOT MODE

```

4289 023742          ESCAPE SUB          ;ELSE, IT'S ALREADY BEEN REPORTED -- EXIT
4290 023742 104410          TRAP C$ESCAPE
4291 023744 000132          .WORD L10030-.
4292 023746 103004          BCC 29$
4293 023750          GEDF EM51C,ERR51 ;YES, ALL'S OK
;NO! BAD VIA CHIP!
; 'DEVICE FATAL' ERROR # 18
4295 023750 104455          TRAP C$ERDF
4296 023752 000022          .WORD 18
4297 023754 021002          .WORD EM51C
4298 023756 011176          .WORD ERR51
4299
4300
4301
-----
4302 023760 004537 004042 29$: JSR R5,WRITE ;RE-WRITE INTO T2C-H (ADDR 09) TO SET T2 AGAIN
4303 023764 120011          T2CH
4304 023766 002435          TMP9+1
4305 023770 103003          BCC .+10 ;IF NO ERROR, PROCEED
4306 023772          ERROR ;ELSE, REPORT IT
4307 023772 104460          ESCAPE TST ; AND EXIT THIS TEST TRAP C$ERROR
4308 023774          ; TRAP C$ESCAPE
4309 023774 104410          .WORD L10027-.
4310 023776 000102          JSR PC,GETT2 ;IS 'T2' SET AGAIN
4311 024000 004737 024134 BVC .+6 ;IF NO ERROR, PROCEED
4312 024004 102002          ESCAPE SUB ;ELSE, IT'S ALREADY BEEN REPORTED -- EXIT
4313 024006          TRAP C$ESCAPE
4314 024006 104410          .WORD L10030-.
4315 024010 000066          BCS 32$
4316 024012 103406          GEDF EM51L,ERR51 ;YES, ALL'S WELL (AGAIN?)
;NO! SOMETHING WENT WRONG! REPORT IT
; 'DEVICE FATAL' ERROR # 19
4317 024014          TRAP C$ERDF
4318          .WORD 19
4319 024014 104455          .WORD EM51L
4320 024016 000023          .WORD ERR51
4321 024020 021213          ESCAPE SUB ; AND EXIT FROM THIS SUBTEST TRAP C$ESCAPE
4322 024022 011176          .WORD L10030-.
4323 024024          ;
4324 024024 104410          TRAP C$ESCAPE
4325 024026 000050          .WORD L10030-.
4326
4327
-----
4328
4329 024030 004537 024102 32$: JSR R5,LODT2C ;AGAIN RE-LOAD TIMER # 2. THIS TIME WITH
4330 024034 125 125          .BYTE 125,125 ; LARGER BUT DIFFERENT VALUES
4331
4332
-----
4333
4334 024036 004737 024134 JSR PC,GETT2 ;'T2' SHOULD NOW BE CLEARED
4335 024042 102002          BVC .+6 ;IF NO ERROR, PROCEED
4336 024044          ESCAPE SUB ;ELSE, IT'S ALREADY BEEN REPORTED -- EXIT
4337 024044 104410          TRAP C$ESCAPE
4338 024046 000030          .WORD L10030-.
4339 024050 103004          BCC 34$
4340 024052          GEDF EM51N,ERR51 ;IT WAS, ALL'S WELL THAT END'S WELL (I THINK!?)
;IT WASN'T! SOMETHING WENT WRONG! REPORT IT
; 'DEVICE FATAL' ERROR # 20
4341          TRAP C$ERDF
4342 024052 104455          .WORD 20
4343 024054 000024          .WORD EM51N
4344 024056 021337          .WORD

```

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TEST 1 -- VIA TIMER 2 ONE SHOT MODE

4345	024060	011176				.WORD	ERR51
4346							
4347	024062	004537	004630	348:	JSR	R5,INITT2	:RE-INITIALIZE TIMER # 1 W/ NORMAL RESET VALUES
4348	024066	000000			0		
4349	024070	000000			0		
4350	024072	103001			BCC	.+4	:IF NO ERROR, EXIT
4351	024074				ERROR		:ELSE, REPORT IT
4352	024074	104460					TRAP C\$ERROR
4353							
4354	024076				ENDSUB		
4355	024076						L10030:
4356	024076	104403					TRAP C\$ESUB
4357							
4358	024100				ENDTST		
4359	024100						L10027:
4360	024100	104401					TRAP C\$ETST
4361							
4362							
4363							
4364							
4365							
4366							
4367							
4368							
4369							
4370							
4371							
4372							
4373							
4374	024102	112537	002433	LODT2C:	MOVB	(R5)+,TMP8+1	:SETUP TO LOAD T2LL
4375	024106	112537	002435		MOVB	(R5)+,TMP9+1	: AND T2CH
4376	024112	004537	004042		JSR	R5,WRITE	:LOAD T2L-L (ADDR 08) WITH PASSED PARAMETER
4377	024116	120010			T2LL		
4378	024120	002433			TMP8+1		
4379	024122	004537	004042		JSR	R5,WRITE	:LOAD T2C-H (ADDR 09) WITH PASSED PARAMETER
4380	024126	120011			T2CH		: (THIS WILL ALSO RESET 'T2' & THE COUNTER)
4381	024130	002435			TMP9+1		
4382	024132	000205			RTS	R5	
4383							
4384							
4385							
4386							
4387							
4388							
4389							
4390	024134	004537	003616	GETT2:	JSR	R5,READ	:GET VIA'S IFR REG.
4391	024140	120015			IFR		
4392	024142	002444			TMPD		
4393	024144	103003			BCC	1\$:IF NO ERROR, PROCEED
4394	024146				ERROR		:ELSE, REPORT IT
4395	024146	104460					TRAP C\$ERROR
4396	024150	000262			SEV		:FLAG AN ERROR TO MAINLINE ROUTINE
4397	024152	000207			RTS	PC	: AND TAKE AN ABNORMAL RETURN
4398							
4399	024154	010046		1\$:	MOV	R0,-(SP)	:PRESERVE R0
4400	024156	113700	002444		MOVB	TMPD,R0	:PUT VALUE HERE TO PRESERVE TMPD

 LODT2C -- LOAD TIMER TWO AT ADDRESSES 08 & 09

CALLING SEQUENCE:

```

JSR    R5,LODT2C
.BYTE  <VALUE FOR T2L-L (ADDRESS 08)>
.BYTE  <VALUE FOR T2C-H (ADDRESS 09)>
<NEXT SEQUENTIAL INSTRUCTION

```

```

LODT2C: MOVB  (R5)+,TMP8+1    ;SETUP TO LOAD T2LL
        MOVB  (R5)+,TMP9+1    ; AND T2CH
        JSR   R5,WRITE        ;LOAD T2L-L (ADDR 08) WITH PASSED PARAMETER
        T2LL
        TMP8+1
        JSR   R5,WRITE        ;LOAD T2C-H (ADDR 09) WITH PASSED PARAMETER
        T2CH                  ; (THIS WILL ALSO RESET 'T2' & THE COUNTER)
        TMP9+1
        RTS   R5

```

 GETT2 -- GET THE 'T2' FLAG FROM THE VIA'S IFR REGISTER AND PUT IT INTO THE 'CARRY' BIT

```

GETT2: JSR   R5,READ          ;GET VIA'S IFR REG.
        IFR
        TMPD
        BCC  1$              ;IF NO ERROR, PROCEED
        ERROR                ;ELSE, REPORT IT
                                TRAP C$ERROR
        SEV
        RTS   PC            ;FLAG AN ERROR TO MAINLINE ROUTINE
                                ; AND TAKE AN ABNORMAL RETURN
1$:    MOV   R0,-(SP)        ;PRESERVE R0
        MOVB TMPD,R0        ;PUT VALUE HERE TO PRESERVE TMPD

```

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TEST 1 -- VIA TIMER 2 ONE SHOT MODE

4401	024162	106100
4402	024164	106100
4403	024166	106100
4404	024170	012600
4405	024172	000207
4406		

ROLB	R0	;'IRQ' GOES INTO CARRY BIT
ROLB	R0	;'T1' GOES INTO CARRY BIT
ROLB	R0	;'T2' GOES INTO CARRY BIT
MOV	(SP)+,R0	;RESTORE R0
RTS	PC	

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TEST 2 -- VIA'S SR INPUT (MODE 2) - SYSTEM CLOCK MODE

.SBTTL TEST 2 -- VIA'S SR INPUT (MODE 2) - SYSTEM CLOCK MODE

4407
4408
4409
4410
4411
4412
4413
4414
4415
4416
4417
4418
4419
4420
4421
4422
4423 024174
4424 024174 004737 003514
4425 024200 103003
4426 024202
4427 024202 104460
4428 024204
4429 024204 104410
4430 024206 000612
4431 024210 005037 002437
4432 024214 005037 002252
4433
4434 024220 004537 003616
4435 024224 120013
4436 024226 002440
4437 024230 103003
4438 024232
4439 024232 104460
4440 024234
4441 024234 104410
4442 024236 000562
4443 024240 113737 002440 002441
4444 024246 142737 000034 002441
4445 024254 004537 004042
4446 024260 120013
4447 024262 002441
4448 024264 103003
4449 024266
4450 024266 104460
4451 024270
4452 024270 104410
4453 024272 000526
4454 024274 004537 003616
4455 024300 120016
4456 024302 002446
4457 024304 103003
4458 024306
4459 024306 104460
4460 024310
4461 024310 104410
4462 024312 000506

```

*****
*
* TEST 2 -- VIA'S SR INPUT (MODE 2) - SYSTEM CLOCK MODE
*
* A MASTER CLEAR IS DONE. THEN THE SHIFT REG IS PLACED IN INPUT MODE
* UNDER CONTROL OF VIA CLK, BY SETTING ACR BIT 4 TO 0, BIT 3 TO 1, AND BIT 2
* TO 0. THE PROGRAM CHECKS FOR THE SR FLAG (BIT 2) IN THE IFR TO BE INITIALLY
* CLEARED. THEN, THE SR IS LOADED TO INITIALIZE THE SR OPERATION, AND THE
* PROGRAM CHECKS FOR SR FLAG = 1 AFTER ABOUT 8 US. AND READS SR REGISTER TO
* VERIFY THAT SHIFTING OCCURRED.
*
*****

```

```

:
: BGNSTST
:
: T2::
: JSR PC,MSTCLR ;INIT DMV & ENTER M-LOOP
: BCC 1$ ;IF NO ERROR, PROCEED WITH TESTING
: ERROR ;ELSE, REPORT ERROR
: TRAP C$ERROR
: ESCAPE TST ; & EXIT TEST
: TRAP C$ESCAPE
: .WORD L10031-.
1$: CLR TMPA+1 ;CLEAR THE 'WRITE' DATA FOR ERROR MESSAGES
: CLR TDATA ;THIS IS A FLAG TO INDICATE THAT 'SR' HASN'T
: ;BEEN LOADED YET.
: JSR R5,READ ;GET CURRENT 'ACR' CONTENTS (SHOULD BE 000)
: ACR
: TMPB
: BCC .+10 ;IF NO ERROR, PROCEED
: ERROR ;ELSE, REPORT IT
: TRAP C$ERROR
: ESCAPE TST ; AND EXIT THIS TEST
: TRAP C$ESCAPE
: .WORD L10031-.
: MOVB TMPB,TMPB+1 ;MOVE IT FROM I/P BUFFER TO O/P BUFFER
: BICB #<BIT2+BIT3+BIT4>,TMPB+1 ;MAKE SURE CURRENT MODE IS 0
: JSR R5,WRITE ;FORCE IT TO THAT MODE (MODE 0)
: ACR
: TMPB+1
: BCC .+10 ;IF NO ERROR, PROCEED
: ERROR ;ELSE, REPORT IT
: TRAP C$ERROR
: ESCAPE TST ; AND EXIT THIS TEST
: TRAP C$ESCAPE
: .WORD L10031-.
: JSR R5,READ ;READ IER INCASE IT'S NEEDED FOR ERROR MESSAGES
: IENR
: TMPE
: BCC .+10 ;IF NO ERROR, PROCEED
: ERROR ;ELSE, REPORT IT
: TRAP C$ERROR
: ESCAPE TST ; AND EXIT THIS TEST
: TRAP C$ESCAPE
: .WORD L10031-.

```

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TEST 2 -- VIA'S SR INPUT (MODE 2) - SYSTEM CLOCK MODE

```

4463 024314 004737 025022      JSR   PC,GETSR      ;SAMPLE SR INTERRUPT FLAG -- IT SHOULD BE 0
4464 024320 102002      BVC   .+6          ;IF NO ERROR, PROCEED
4465 024322 104410      ESCAPE TST         ;ELSE, IT'S ALREADY BEEN REPORTED -- EXIT
4466 024322 104410      TRAP  C$ESCAPE    ;
4467 024324 000474      .WORD L10031-.
4468 024326 103014      BCC   4$          ;IT IS, GOOD.
4469 024330 004537 003616      JSR   R5,READ     ;READ SR FOR ERROR MESSAGE
4470 024334 120012      SR
4471 024336 002436      TMPA
4472 024340 103003      BCC   .+10       ;IF NO ERROR, PROCEED
4473 024342 104460      ERROR          ;ELSE, REPORT IT
4474 024342 104460      ESCAPE TST       ;
4475 024344 104410      TRAP  C$ERROR    ;
4476 024344 104410      .WORD L10031-.  ;
4477 024346 000452      TRAP  C$ESCAPE    ;
4478 024350 000452      .WORD L10031-.  ;
4479 024350 104455      GEDF  EM52A,ERR52 ;IT ISN'T! REPORT SR NOT INITIALLY CLEARED
4480 024350 104455      ;
4481 024352 000025      TRAP  C$ERDF     ;
4482 024354 021457      .WORD 21         ;
4483 024356 011270      .WORD EM52A     ;
4484 024360 152737 000010 002441 4$:  BISB  #BIT3,TMPB+1 ;SET SHIFT REG. 10 MODE 2
4485 024366 004537 004042      JSR   R5,WRITE
4486 024372 120013      ACR
4487 024374 002441      TMPB+1
4488 024376 103003      BCC   .+10       ;IF NO ERROR, PROCEED
4489 024400 104460      ERROR          ;ELSE, REPORT IT
4490 024400 104460      ESCAPE TST       ;
4491 024402 104410      TRAP  C$ERROR    ;
4492 024402 104410      .WORD L10031-.  ;
4493 024404 000414      MOVB  #BIT7+BIT2,TMPE+1 ;ENABLE SR INTERRUPTS WITHIN DMV-11
4494 024406 112737 000204 002447      JSR   R5,WRITE    ;(WE WILL NOT BE ALLOWING THEM TO GIVE US
4495 024414 004537 004042      IENR          ;A Q-BUS INTERRUPT)
4496 024420 120016      TMPE+1
4497 024422 002447      BCC   .+10       ;IF NO ERROR, PROCEED
4498 024424 103003      ERROR          ;ELSE, REPORT IT
4499 024426 104460      ESCAPE TST       ;
4500 024426 104460      TRAP  C$ERROR    ;
4501 024430 104410      .WORD L10031-.  ;
4502 024430 104410      TRAP  C$ESCAPE    ;
4503 024432 000366      .WORD L10031-.  ;
4504 024434 004537 003616      JSR   R5,READ     ;READ IER INCASE IT'S NEEDED FOR ERROR MESSAGES
4505 024440 120016      IENR
4506 024442 002446      TMPE
4507 024444 103003      BCC   .+10       ;IF NO ERROR, PROCEED
4508 024446 104460      ERROR          ;ELSE, REPORT IT
4509 024446 104460      ESCAPE TST       ;
4510 024450 104410      TRAP  C$ERROR    ;
4511 024450 104410      .WORD L10031-.  ;
4512 024452 000346      TRAP  C$ESCAPE    ;
4513 024454 105037 002437      CLRB  TMPA+1     ;LOAD SR WITH PROPER VALUE....
4514 024460 005737 002366      TST   BRDTP      ;NOTE: THE INPUT LEAD (CB2) WILL EITHER BE
4515 024464 001403      BEQ   5$         ;TIED HI(M8064) OR LO(M8053).
4516 024466 112737 000377 002437      MOVB  #377,TMPA+1 ;IF M8064, THEN LOAD SR WITH 000.
4517 024474 004537 004042      JSR   R5,WRITE    ;IF M8053, THEN LOAD SR WITH 377.
4518 024500 120012      SR           ;THIS ALSO STARTS THE SHIFTING OPERATION.

```

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TEST 2 -- VIA'S SR INPUT (MODE 2) - SYSTEM CLOCK MODE

```

4519 024502 002437          TMPA+1          ;
4520
4521 024504 103003          BCC      .+10          ;IF NO ERROR, PROCEED
4522 024506          ERROR          ;ELSE, REPORT IT
4523 024506 104460          ESCAPE  TST          ;          AND EXIT THIS TEST          TRAP      C$ERROR
4524 024510          ;
4525 024510 104410          ;          AND EXIT THIS TEST          TRAP      C$ESCAPE
4526 024512 000306          ;          .WORD      L10031-.
4527
4528 024514 005337 002252          DEC      TDATA          ;INDICATE THAT 'SR' HAS BEEN LOADED NOW
4529 024520 012703 000100          MOV      #100,R3          ;GIVE THE INTERRUPT A CHANCE TO HAPPEN
4530 024524 077301          SOB      R3,
4531 024526 132777 000004 155572          BITB    #BIT2,@SEL3          ;DID AN SR INTERRUPT OCCUR WITHIN THE 6502?
4532 024534 001026          BNE      6$          ;YES, GOOD.
4533 024536 004537 003616          JSR      R5,READ          ;NO, SETUP TO REPORT THE ERROR:
4534 024542 120015          IFR          ; GET INTERRUPT FLAG REGISTER
4535 024544 002444          TMPD
4536 024546 103003          BCC      .+10          ;IF NO ERROR, PROCEED
4537 024550          ERROR          ;ELSE, REPORT IT
4538 024550 104460          ESCAPE  TST          ;          AND EXIT THIS TEST          TRAP      C$ERROR
4539 024552          ;
4540 024552 104410          ;          AND EXIT THIS TEST          TRAP      C$ESCAPE
4541 024554 000244          ;          .WORD      L10031-.
4542 024556 004537 003616          JSR      R5,READ          ; GET FINAL SR CONTENTS -- SHOULD BE 0
4543 024562 120012          SR
4544 024564 002436          TMPA
4545 024566 103003          BCC      .+10          ;IF NO ERROR, PROCEED
4546 024570          ERROR          ;ELSE, REPORT IT
4547 024570 104460          ESCAPE  TST          ;          AND EXIT THIS TEST          TRAP      C$ERROR
4548 024572          ;
4549 024572 104410          ;          AND EXIT THIS TEST          TRAP      C$ESCAPE
4550 024574 000224          ;          .WORD      L10031-.
4551 024576          GEDF    EM52B,ERR52          ;REPORT MISSING SR INTERRUPT WITHIN DMV-11
4552          ;          'DEVICE FATAL' ERROR # 22
4553 024576 104455          ;          TRAP      C$ERDF
4554 024600 000026          ;          .WORD      22
4555 024602 021535          ;          .WORD      EM52B
4556 024604 011270          ;          .WORD      ERR52
4557 024606          ESCAPE  TST          ;FURTHER TESTING INVALID
4558 024606 104410          ;
4559 024610 000210          ;          AND EXIT THIS TEST          TRAP      C$ESCAPE
4560 024612 004537 003616          JSR      R5,READ          ;GET FINAL SR CONTENTS:
4561 024616 120012          SR          ; IF M8064, THEN SR SHOULD=377
4562 024620 002436          TMPA          ; IF M8053, THEN SR SHOULD=000
4563 024622 103003          BCC      .+10          ;IF NO ERROR, PROCEED
4564 024624          ERROR          ;ELSE, REPORT IT
4565 024624 104460          ESCAPE  TST          ;          AND EXIT THIS TEST          TRAP      C$ERROR
4566 024626          ;
4567 024626 104410          ;          AND EXIT THIS TEST          TRAP      C$ESCAPE
4568 024630 000170          ;          .WORD      L10031-.
4569
4570 024632 005737 002366          TST      BRDYP          ;CHECK DMV-11 BOARD TYPE
4571 024636 001005          BNE      9$          ;
4572 024640 122737 000377 002436          CMPB    #377,TMPA          ;M8064::SEE IF CORRECT RESULT
4573 024646 001422          BEQ      8$          ;          YES:GOOD
4574 024650 000403          BR       7$          ;          NO: GO REPORT ERROR

```

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TEST 2 -- VIA'S SR INPUT (MODE 2) - SYSTEM CLOCK MODE

```

4575 024652 105737 002436
4576 024656 001416
4577 024660 004537 003616
4578 024664 120015
4579 024666 002444
4580 024670 103003
4581 024672
4582 024672 104460
4583 024674
4584 024674 104410
4585 024676 000122
4586 024700
4587
4588 024700 104455
4589 024702 000027
4590 024704 021567
4591 024706 011270
4592 024710
4593 024710 104410
4594 024712 000106
4595 024714 105077 155406
4596 024720 004537 003616
4597 024724 120012
4598 024726 002436
4599 024730 103003
4600 024732
4601 024732 104460
4602 024734
4603 024734 104410
4604 024736 000062
4605 024740 004737 005132
4606 024744 004737 005132
4607 024750 004737 005132
4608 024754 132777 000004 155344
4609 024762 001016
4610 024764 004537 003616
4611 024770 120015
4612 024772 002444
4613 024774 103003
4614 024776
4615 024776 104460
4616 025000
4617 025000 104410
4618 025002 000016
4619 025004
4620
4621 025004 104455
4622 025006 000030
4623 025010 021653
4624 025012 011270
4625 025014
4626 025014 104410
4627 025016 000002
4628 025020
4629 025020
4630 025020

```

```

9$:  TSTB  TMPA      ;M8053::SEE IF CORRECT RESULT
    BEQ   8$      ;      YES:GOOD.
7$:  JSR   R5,READ ;      NO: SETUP TO REPORT THE ERROR:
    IFR                      ;      GET INTERRUPT FLAG REGISTER
    TMPD
    BCC   .+10      ;IF NO ERROR, PROCEED
    ERROR                      ;ELSE, REPORT IT
                                TRAP   C$ERROR
                                .WORD  L10031-.
    ESCAPE TST      ;      AND EXIT THIS TEST
                                TRAP   C$ESCAPE
                                .WORD  L10031-.
    GEDF  EM52C,ERR52 ;REPORT INCOMPLETE OR BAD SHIFTING OPERATION
                                ;      'DEVICE FATAL' ERROR # 23
                                TRAP   C$ERDF
                                .WORD  23
                                .WORD  EM52C
                                .WORD  ERR52
    ESCAPE TST      ;FURTHER TESTING INVALID
                                TRAP   C$ESCAPE
                                .WORD  L10031-.
8$:  CLRB  @BSEL3   ;CLEAR THE INTERRUPT FLAGS
    JSR   R5,READ  ;HIT THE SHIFT REG. THIS TIME WITH A READ
    SR                      ; (WE DON'T REALLY CARE THIS TIME WHAT THE DATA
    TMPA                      ;      RETURNED IS. BUT, WE HAVE TO PUT IT SOMEWHERE
    BCC   .+10      ;IF NO ERROR, PROCEED
    ERROR                      ;ELSE, REPORT IT
                                TRAP   C$ERROR
                                .WORD  L10031-.
    ESCAPE TST      ;      AND EXIT THIS TEST
                                TRAP   C$ESCAPE
                                .WORD  L10031-.
    JSR   PC,STALL ;DELAY FOR A LITTLE WHILE TO LET THE INTERRUPT
    JSR   PC,STALL ;      GET THROUGH
    JSR   PC,STALL
    BITB  #BIT2,@BSEL3 ;DID WE GET AN INTERRUPT ON THE READ OPERATION?
    BNE   10$      ;YES, GOOD.
    JSR   R5,READ  ;NO, SETUP TO REPORT THE ERROR:
    IFR                      ;      GET INTERRUPT FLAG REGISTER
    TMPD
    BCC   .+10      ;IF NO ERROR, PROCEED
    ERROR                      ;ELSE, REPORT IT
                                TRAP   C$ERROR
                                .WORD  L10031-.
    ESCAPE TST      ;      AND EXIT THIS TEST
                                TRAP   C$ESCAPE
                                .WORD  L10031-.
    GEDF  EM52D,ERR52 ;REPORT THE FAILURE.
                                ;      'DEVICE FATAL' ERROR # 24
                                TRAP   C$ERDF
                                .WORD  24
                                .WORD  EM52D
                                .WORD  ERR52
    ESCAPE TST
                                TRAP   C$ESCAPE
                                .WORD  L10031-.
10$: ENDTST

```

L10031:

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TEST 2 -- VIA'S SR INPUT (MODE 2) - SYSTEM CLOCK MODE

4631	025020	104401				TRAP	C\$ETST
4632	025022	004537	003616	GETSR:	JSR	R5,READ	
4633	025026	120015			IFR		;GET CURRENT INTERRUPT FLAG REGISTER SETTINGS
4634	025030	002444			TMPD		
4635	025032	103003			BCC	1\$;IF NO ERROR, PROCEED
4636	025034				ERROR		;ELSE, REPORT IT
4637	025034	104460					
4638	025036	000262			SEV		;FLAG AN ERROR TO MAINLINE ROUTINE
4639	025040	000207			RTS	PC	; AND TAKE AN ABNORMAL RETURN
4640							
4641	025042	010046		1\$:	MOV	R0,-(SP)	;SAVE REGISTER FOR CALLER
4642	025044	113700	002444		MOVB	TMPD,R0	;PUT THEM WHERE WE CAN EASILY MASAGE THEM
4643	025050	106000			RORB	R0	;CA2 ==> CARRY BIT
4644	025052	106000			RORB	R0	;CA1 ==> CARRY BIT
4645	025054	106000			RORB	R0	;SR ==> CARRY BIT
4646	025056	012600			MOV	(SP)+,R0	;RESTORE REGISTER
4647	025060	000207			RTS	PC	;RETURN WITH SR INTERRUPT FLAG IN CARRY BIT
4648							

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TEST 3 -- NPR CONTROL REGISTER - MASTER CLEAR

.SBTTL TEST 3 -- NPR CONTROL REGISTER - MASTER CLEAR

```

:*****
:*
:*      TEST 3 -- NPR CONTROL REGISTER - MASTER CLEAR
:*
:* THE PROGRAM SETS THE FOLLOWING BITS IN THE NPR CONTROL REGISTER :
:* IN/OUT, BYTE OPER, AND DISABL INIT. THE REGISTER IS READ AND VERIFIED.
:* THEN, A MASTER CLEAR IS PERFORMED, AND THE REGISTER IS READ AND CHECKED FOR
:* 000.
:*
:-----*****

```

```

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4663 025062
4664 025062 004737 003514
4665 025066 103003
4666 025070
4667 025070 104460
4668 025072
4669 025072 104410
4670 025074 000352
4671
4672 025076 004737 005134
4673 025102 103002
4674 025104
4675 025104 104460
4676 025106 000557
4677
4678 025110 012702 002740
4679 025114 010237 002256
4680 025120 010237 025234
4681
4682 025124 004537 005004
4683 025130 002632
4684 025132 002654
4685 025134 000011
4686 025136 013701 025132
4687 025142 010137 002254
4688 025146 012703 000001
4689
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4693
4694
4695
4696
4697 025152 121112
4698 025154 001003
4699 025156 022122
4700 025160 077304
4701 025162 000412
4702
4703 025164 163701 025132
4704 025170 010137 002300

```

```

:      BGNTST
:
:      T3:
:      JSR    PC,MSTCLR  ;INIT DMV & START UP MAINT. LOOP
:      BCC    1$        ;IF NO ERROR, PROCEED WITH TESTING
:      ERROR  ;ELSE REPORT ERROR
:
:      ESCAPE TST      ; & EXIT TEST
:
:      TRAP   C$ERROR
:
:      TRAP   C$ESCAPE
:      .WORD  L10032-.
:
:      1$:  JSR    PC,NPREAD ;GET CONTENTS OF ALL NPR REGISTERS INTO BT2
:      BCC    30$        ;IF AN ERROR OCCURED,
:      ERROR  ;REPORT IT &
:
:      TRAP   C$ERROR
:
:      BR     24$        ; EXIT
:
:      30$:  MOV    #BT2,R2 ;POINT TO NPR REGISTER CONTENTS
:      MOV    R2,BDATA ;USE IT ALSO FOR ERROR HANDLING
:      MOV    R2,13$    ;SETUP ALSO FOR READ BACK
:
:      JSR    R5,MOVSW   ;GET THE 'EXPECTED' RESULTS TOO
:
:      12$:  BT1
:      11$:  9.
:      MOV    12$,R1    ;POINT TO TABLE OF EXPECTED REGISTER CONTENTS
:      MOV    R1,GDATA ;USE IT ALSO FOR ERROR HANDLING
:      MOV    #1,R3    ;COUNT OF # OF NPR REGISTERS BEING PROCESSED
:
:      ;FOR NOW, ONLY THE CONTROL REGISTER IS CHECKED!!

```

```

:=====
: PLEASE NOTE THAT 'GDATA' & 'BDATA' NOW CONTAIN POINTERS -- NOT DATA!
: THIS IS A DEVIANT AND THEREFORE SHOULD BE BORNE IN MIND WHEN TRYING TO
: FOLLOW THIS DEVIOUS LOGIC.
:=====

```

```

:      2$:  CMPB   (R1),(R2) ;CHECK ONE BYTE
:      BNE   3$        ;GO REPORT FAILURE IF ANY ERROR IS FOUND
:      CMP   (R1)+,(R2)+ ;BUMP POINTERS -- TABLES ARE ACTUALLY WORD TABLES
:      SOB   R3,2$    ;LOOP IF NOT DONE YET
:      BR    4$        ;ELSE, PROCEED WITH TESTING
:
:      3$:  SUB    12$,R1 ;CALCULATE THE REGISTER # CAUSING THE FAILURE
:      MOV   R1,REGNUM ;IDENTIFY FAULTY REGISTER

```

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TEST 3 -- NPR CONTROL REGISTER - MASTER CLEAR

```

4705 025174          GEDF  EM26,ERR8      ;NPR ERROR -- BAD INITIALIZATION
4706                ;                'DEVICE FATAL' ERROR # 25
4707 025174 104455          TRAP  C$ERDF
4708 025176 000031          .WORD 25
4709 025200 016152          .WORD EM26
4710 025202 006532          .WORD ERR8
4711 025204          ESCAPE TST          ;IF IT CAN'T BE INIT'ED, WE CAN'T TEST IT!
4712 025204 104410          TRAP  C$ESCAPE
4713 025206 000240          .WORD L10032-.
4714 025210 004537 004054  4$: JSR  R5,WRITEI      ;WRITE THE TEST PATTERN INTO THE CONTROL REG.
4715 025214 123004          NPRCTL
4716 025216 000355          5$: 355
4717 025220 103002          BCC  31$
4718 025222          ERROR          ;THIS SET SEVERAL BITS -- BUT NOT ALL!
4719 025222 104460          ;IF AN ERROR OCCURED,
4720 025224 000510          ;REPORT IT &
4721 025226 004537 003616  31$: BR  24$          TRAP  C$ERROR
4722 025232 123004          JSR  R5,READ      ; EXIT
4723 025234 002740          NPRCTL          ;READ IT BACK
4724 025236 103002          BT2  32$
4725 025240          BCC  32$          ;IF AN ERROR OCCURED,
4726 025240 104460          ERROR          ;REPORT IT &
4727 025242 000501          BR  24$          TRAP  C$ERROR
4728                ; EXIT
4729 025244 013777 025216 155002 32$: MOV  5$,@GDATA      ;BUILD THE EXPECTED RETURN VALUE
4730 025252 042777 000300 154774  BIC  #NPRABT!NPRGO,@GDATA ;WE CAN'T WRITE THESE BITS
4731                ;
4732 025260 004737 005134          JSR  PC,NPREAD   ;GET CONTENTS OF ALL NPR REGISTERS INTO BT2
4733 025264 103002          BCC  33$
4734 025266          ERROR          ;IF AN ERROR OCCURED,
4735 025266 104460          ;REPORT IT &
4736 025270 000466          BR  24$          TRAP  C$ERROR
4737 025272 013701 002254  33$: MOV  GDATA,R1      ; EXIT
4738 025276 013702 002256          MOV  BDATA,R2    ;POINT TO TABLE OF EXPECTED REGISTER CONTENTS
4739 025302 012703 000001          MOV  #1,R3       ;POINT TO NPR REGISTER CONTENTS
4740                ;COUNT OF # OF NPR REGISTERS BEING PROCESSED
4741                ;FOR NOW, ONLY THE CONTROL REGISTER IS CHECKED!!
4742 025306 121112          7$: CMPB  (R1),(R2) ;CHECK ONE BYTE
4743 025310 001003          BNE  8$          ;GO REPORT FAILURE IF ANY ERROR IS FOUND
4744 025312 022122          CMP  (R1)+,(R2)+ ;BUMP POINTERS -- TABLES ARE ACTUALLY WORD TABLES
4745 025314 077304          SOB  R3,7$      ;LOOP IF NOT DONE YET
4746 025316 000412          BR  9$          ;ELSE, PROCEED WITH TESTING
4747                ;
4748 025320 163701 025132  8$: SUB  12$,R1      ;CALCULATE THE REGISTER # CAUSING THE FAILURE
4749 025324 010137 002300          MOV  R1,REGNUM  ;IDENTIFY FAULTY REGISTER
4750 025330          GEDF  EM26A,ERR8 ;NPR ERROR -- DURING WRITE TO CONTROL REG.
4751                ;                'DEVICE FATAL' ERROR # 26
4752 025330 104455          TRAP  C$ERDF
4753 025332 000032          .WORD 26
4754 025334 016204          .WORD EM26A
4755 025336 006532          .WORD ERR8
4756 025340          ESCAPE TST          ;IF IT CAN'T BE INIT'ED, WE CAN'T TEST IT!
4757 025340 104410          TRAP  C$ESCAPE
4758 025342 000104          .WORD L10032-.
4759 025344 004737 003514  9$: JSR  PC,MSTCLR   ;RE-INITIALIZE DMV & ENTER M-LOOP
4760 025350 103003          BCC  20$        ;IF NO ERROR, PROCEED WITH TESTING

```

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TEST 3 -- NPR CONTROL REGISTER - MASTER CLEAR

```

4761 025352          ERROR          ;ELSE REPORT IT
4762 025352 104460          ;
4763 025354          ESCAPE TST      ;   & EXIT TEST          TRAP  C$ERROR
4764 025354 104410          ;
4765 025356 000070          ;
4766                ;
4767                ;   THE 'MASTER CLEAR' JUST PERFORMED SHOULD RESET THE NPR CONTROL
4768                ;   REGISTER. IT SHOULD NOW EQUAL 004 AGAIN.
4769                ;
4770 025360 013777 002632 154666 20$:  MOV    NPRMCR,@GDATA ;RESET THE EXPECTED DATA
4771                ;
4772                ;   ALSO, THE OTHER REGISTERS SHOULD STILL BE AT THEIR INITIAL VALUES
4773                ;
4774 025366 004737 005134    JSR    PC,NPREAD ;GET CONTENTS OF ALL NPR REGISTERS INTO BT2
4775 025372 103002          BCC    34$      ;IF AN ERROR OCCURED,
4776 025374          ERROR          ;REPORT IT &
4777 025374 104460          ;
4778 025376 000423          ;
4779 025400 013701 002254 34$:  BR     24$      ; EXIT
4780 025404 013702 002256    MOV    GDATA,R1 ;POINT TO TABLE OF EXPECTED REGISTER CONTENTS
4781 025410 012703 000001    MOV    BDATA,R2 ;POINT TO NPR REGISTER CONTENTS
4782                MOV    #1,R3 ;COUNT OF # OF NPR REGISTERS BEING PROCESSED
4783                ;FOR NOW, ONLY THE CONTROL REGISTER IS CHECKED!!
4784 025414 121112          21$:  CMPB   (R1),(R2) ;CHECK ONE BYTE
4785 025416 001003          BNE    22$      ;GO REPORT FAILURE IF ANY ERROR IS FOUND
4786 025420 022122          CMP    (R1)+,(R2)+ ;BUMP POINTERS — TABLES ARE ACTUALLY WORD TABLES
4787 025422 077304          SOB    R3,21$ ;LOOP IF NOT DONE YET
4788 025424 000410          BR     24$      ;ELSE, PROCEED WITH TESTING
4789                ;
4790 025426 163701 025132 22$:  SUB    12$,R1 ;CALCULATE THE REGISTER # CAUSING THE FAILURE
4791 025432 010137 002300    MOV    R1,REGNUM ;IDENTIFY FAULTY REGISTER
4792 025436          GEDF   EM26,ERR8 ;NPR ERROR — BAD INITIALIZATION
4793                ;   'DEVICE FATAL' ERROR # 27
4794 025436 104455          ;
4795 025440 000033          ;
4796 025442 016152          ;
4797 025444 006532          ;
4798 025446          ;
4799 025446          ;
4800 025446 104401          ;

```

L10032: TRAP C\$SETST

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TEST 4 -- NPR DATA-OUT

.SBTTL TEST 4 -- NPR DATA-OUT

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4851
4852
4853
4854
4855
4856

025450
000004
025450 004737 003514
025454 103003
025456
025456 104460
025460
025460 104410
025462 000474

025464
025464
025464 104402
025466 013737 002506 025504

025474 004537 005266
025500 002510
025502 002654
025504 000000
025506 000044
025510 103025

* TEST 4 -- NPR DATA-OUT
* FIRST SUBTEST :
* THE NPR OUTPUT ADDRESS REGISTER IS LOADED WITH THE ADDRESS OF A 2 BYTE
* BUFFER IN THE PROGRAM. THEN, EACH WORD OF DATA PATTERN F IS LOADED INTO THE
* NPR OUTPUT DATA REGISTER, A FULLWORD NPR OUTPUT REQUEST IS PERFORMED,
* AND THE PROGRAM CHECKS FOR THE CORRECT DATA IN THE PROGRAM BUFFER. ALSO,
* THE PROGRAM CHECKS THAT THE ABORT XFER BIT IN THE NPR CONTROL REGISTER
* NEVER GETS SET.
* DATA PATTERN F = 125252, 052525, 000000, 177777, 000001, 000002, 000004,
* 000010, 000020, 000040, 000100, 000200, 000400, 001000,
* 002000, 004000, 010000, 020000, 040000, 100000, 177776,
* 177775, 177773, 177767, 177757, 177737, 177677, 177577,
* 177377, 176777, 175777, 173777, 167777, 157777, 137777,
* 077777, 000000
* SECOND SUBTEST:
* THE ABOVE OPERATIONS ARE REPEATED IN BYTE NPR TRANSFER MODE, USING THE DATA
* BYTES IN DATA PATTERN B. THE LOW BYTE OF THE PROGRAM BUFFER IS USED, AND
* THE UPPER BYTE IS CLEARED AT THE START, AND IS CHECKED TO REMAIN UNCHANGED
* THROUGHOUT THE SUBTEST.
* DATA PATTERN B = 125, 252, 000, 377, 001, 002, 004, 010, 020, 040, 100,
* 200, 376, 375, 373, 367, 357, 337, 277, 177, 000

```
*****
: BGNST
:
: NPROTS = $T
: JSR PC,MSTCLR ;INIT DMV & START UP MAINT. LOOP
: BCC 1$ ;IF NO ERROR, PROCEED WITH TEST
: ERROR ;ELSE, REPORT IT
: ESCAPE TST ; & EXIT TEST
: TRAP C$ERROR
: TRAP C$ESCAPE
: .WORD L10033-

-----
1$: BGNSUB ;----- MAIN MEMORY WORD DATA-OUT TESTING -----
: T4.1: TRAP C$BSUB
: MOV PATF,4$ ;SETUP COUNT OF # OF WORDS IN TEST PATTERN
: JSR R5,NPRMOV ;MOVE DATA THROUGH THE NPR LOGIC
: 2$: PATF+2 ; ADDRESS OF DATA
: 3$: BUFAREA ; BUFFER AREA
: 4$: 0 ;*** MODIFIED FROM ABOVE *** -- WORD COUNT
: NPRDL ; OPERATION TO BE UTILIZED
: BCC 7$ ;IF ERROR, REPORT IT
```

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TEST 4 -- NPR DATA-OUT

```

4857 025512
4858 025512 104460
4859 025514 005737 002412
4860 025520 001421
4861 025522 022737 000006 002204
4862 025530 001415
4863 025532 012737 000002 002202
4864 025540 012737 000006 002204
4865 025546 012737 016316 002206
4866 025554 012737 007314 002210
4867 025562 000753
4868
4869 025564 013701 025500 7$: MOV 2$,R1 ;POINT TO GOOD DATA
4870 025570 013702 025502 MOV 3$,R2 ; & ACTUAL DATA
4871 025574 013703 025504 MOV 4$,R3 ;GET WORD COUNT
4872 025600 005037 002276 CLR ERRFLG ;RESET ERROR FLAG
4873
4874 025604 022122 5$: CMP (R1)+,(R2)+ ;CHECK RECEIVED DATA
4875 025606 001007 BNE 6$ ;ERROR, GO REPORT IT
4876 025610 077303 11$: SOB R3,5$ ;GOOD, IF MORE DO IT AGAIN
4877 025612 005737 002276 TST ERRFLG ;ELSE, SEE IF WE MUST FINISH AN ERROR MESSAGE
4878 025616 001440 BEQ 10$ ;NO, TEST IT AGAIN BUT WITH BYTE TRANSFERS
4879 025620 004737 013076 JSR PC,MULERR ;YES, USE COMMON ROUTINE TO END ERROR MESSAGE
4880 025624 000435 BR 10$ ;WE CAN TEST IT AGAIN BUT WITH BYTE TRANSFERS
4881
4882 025626 010146 6$: MOV R1,-(SP) ;SAVE THIS FOR FURTHER TESTING
4883 025630 014137 002254 MOV -(R1),GDATA ;SETUP FOR ERROR REPORT
4884 025634 014237 002256 MOV -(R2),BDATA
4885 025640 010237 002252 MOV R2,TDATA ;LSI-11'S MEMORY ADDRESS
4886 025644 163701 025500 SUB 2$,R1 ;CALCULATE THE OFFSET AT WHICH THE
4887 025650 006201 ASR R1 ; DATA COMPARISON ERROR OCCURED
4888 025652 010137 002300 MOV R1,REGNUM ;THE ERROR MESSAGE WILL REPORT THIS TOO
4889 025656 005737 002276 TST ERRFLG ;HAVE WE ALREADY REPORTED AN ERROR HERE?
4890 025662 001007 BNE 8$ ;YES, THEN WE ONLY PRINT DATA THIS TIME
4891 025664 005237 002276 INC ERRFLG ;NO, SET FLAG & REPORT THE WHOLE MESSAGE
4892 025670 GEDF EM26B,ERR9 ;WORD NPR TRANSFER DMY ==> LSI
4893 ; 'DEVICE FATAL' ERROR # 28
4894 025670 104455 TRAP C$ERDF
4895 025672 000034 .WORD 28
4896 025674 016226 .WORD EM26B
4897 025676 007300 .WORD ERR9
4898 025700 000402 BR 9$ ; RESUME TESTING
4899
4900 025702 004737 012574 8$: JSR PC,ERR9$ ;IDENTIFY THE FAILING DATA
4901 025706 012601 9$: MOV (SP)+,R1 ;RESTORE POINTERS
4902 025710 013702 002252 MOV TDATA,R2
4903 025714 005722 TST (R2)+
4904 025716 000734 BR 11$ ;AND RESUME TESTING
4905
4906 025720 10$: ENDSUB
4907 025720
4908 025720 104403 L10034: TRAP C$ESUB
4909
4910
4911
4912 025722 BGNSUB ;----- MAIN MEMORY BYTE DATA-OUT TESTING -----

```

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TEST 4 -- NPR DATA-OUT

```

4913 025722
4914 025722 104402
4915 025724 013737 002456 025742
4916
4917 025732 004537 005266
4918 025736 002460
4919 025740 002654
4920 025742 000000
4921 025744 000054
4922
4923 025746 103025
4924 025750
4925 025750 104460
4926 025752 005737 002412
4927 025756 001421
4928 025760 022737 000006 002204
4929 025766 001415
4930 025770 012737 000002 002202
4931 025776 012737 000006 002204
4932 026004 012737 016316 002206
4933 026012 012737 007314 002210
4934 026020 000753
4935
4936 026022 013701 025736
4937 026026 013702 025740
4938 026032 013703 025742
4939 026036 005037 002276
4940
4941 026042 122122
4942 026044 001007
4943 026046 077303
4944 026050 005737 002276
4945 026054 001437
4946 026056 004737 013076
4947 026062 000434
4948
4949 026064 010146
4950 026066 114137 002254
4951 026072 114237 002256
4952 026076 010237 002252
4953 026102 163701 025736
4954
4955 026106 010137 002300
4956 026112 005737 002276
4957 026116 001007
4958 026120 005237 002276
4959 026124
4960
4961 026124 104455
4962 026126 000035
4963 026130 016251
4964 026132 007306
4965 026134 000402
4966
4967 026136 004737 012732
4968 026142 012601

```

T4.2:

```

MOV PATB,4$ ;SETUP COUNT OF # OF WORDS IN TEST PATTERN
JSR R5,NPRMOV ;MOVE DATA THROUGH THE NPR LOGIC
PATB+2 ; ADDRESS OF DATA
BUFAREA ; BUFFER AREA
0 ;*** MODIFIED FROM ABOVE *** -- BYTE COUNT
NPRDLB ; OPERATION TO BE UTILIZED

BCC 7$ ;IF ERROR, REPORT IT
ERROR 13$:

TST TMP0 ;WE JUST REPORTED ONE ERROR BUT WAS IT A TIMEOUT
BEQ 7$ ;ERROR? IF SO, PROCEED WITH TESTING. ELSE,
CMP #NPRTOE,ERRNBR ;WE WILL HAVE TO REPORT IT HERE AND NOW.
BEQ 7$ ;THE TIMEOUT ERROR WAS ALREADY REPORTED.
MOV #T.EHRD,ERRTYP ;IT WASN'T REPORTED YET, SETUP FOR IT NOW:
MOV #NPRTOE,ERRNBR
MOV #EM26E,ERRMSG
MOV #ERR11,ERRBLK
BR 13$ ;LOOP BACK TO CAUSE REPORT @ PROPER PC LOCATION

MOV 2$,R1 ;POINT TO GOOD DATA
MOV 3$,R2 ; & ACTUAL DATA
MOV 4$,R3 ;GET BYTE COUNT
CLR ERRFLG ;RESET ERROR FLAG

CMPB (R1)+,(R2)+ ;CHECK RECEIVED DATA
BNE 6$ ;ERROR, GO REPORT IT
SOB R3,5$ ;GOOD, IF MORE DO IT AGAIN
TST ERRFLG ;ELSE, SEE IF WE MUST FINISH AN ERROR MESSAGE
BEQ 10$ ;NO, THEN WE CAN EXIT THE TEST
JSR PC,MULERR ;YES, OUTPUT THE REQUIRED BLANK LINES. NOW
BR 10$ ;THEN WE CAN EXIT THE TEST

MOV R1,-(SP) ;SAVE THIS FOR FURTHER TESTING
MOVB -(R1),GDATA ;SETUP FOR ERROR REPORT
MOVB -(R2),BDATA
MOV R2,TDATA ;LSI-11'S MEMORY ADDRESS
SUB 2$,R1 ;CALCULATE THE OFFSET AT WHICH THE
; DATA COMPARISON ERROR OCCURED
MOV R1,REGNUM ;THE ERROR MESSAGE WILL REPORT THIS TOO
TST ERRFLG ;HAVE WE ALREADY REPORTED AN ERROR HERE?
BNE 8$ ;YES, THEN WE ONLY PRINT DATA THIS TIME
INC ERRFLG ;NO, SET FLAG & REPORT THE WHOLE MESSAGE
GEDF EM26C,ERR10 ;BYTE NPR TRANSFER DMY ==> LSI
; 'DEVICE FATAL' ERROR # 29
TRAP CSERDF
WORD 29
WORD EM26C
WORD ERR10

BR 9$ ; RESUME TESTING

JSR PC,ERR10$ ;IDENTIFY THE FAILING DATA
MOV (SP)+,R1 ;RESTORE POINTERS

```

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TEST 4 -- NPR DATA-OUT

4969	026144	013702	002252
4970	026150	005202	
4971	026152	000735	
4972			
4973	026154		
4974	026154		
4975	026154	104403	
4976	026156		
4977	026156		
4978	026156	104401	

```

MOV   TDATA,R2
INC   R2
BR    11$

```

;AND RESUME TESTING

10\$: ENDSUB

ENDTST

L10035:

TRAP C\$ESUB

L10033:

TRAP C\$ETST

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TEST 5 -- NPR DATA-IN

.SBTTL TEST 5 -- NPR DATA-IN

```

*****
*
* TEST 5 -- NPR DATA-IN
*
* THE NPR INPUT ADDRESS REGISTER IS LOADED WITH THE ADDRESS OF A 2 BYTE
* BUFFER IN THE PROGRAM. THEN, EACH WORD OF DATA PATTERN F IS LOADED INTO THE
* PROGRAM BUFFER, A FULLWORD NPR INPUT REQUEST IS ISSUED AND PERFORMED,
* AND THE PROGRAM CHECKS FOR THE CORRECT DATA IN THE NPR INPUT DATA REG.
* ALSO, THE PROGRAM CHECKS THAT THE ABORT XFER BIT IN THE NPR CONTROL
* REGISTER NEVER GETS SET.
* DATA PATTERN F = 125252, 052525, 000000, 177777, 000001, 000002, 000004,
*                   000010, 000020, 000040, 000100, 000200, 000400, 001000,
*                   002000, 004000, 010000, 020000, 040000, 100000, 177776,
*                   177775, 177773, 177767, 177757, 177737, 177677, 177577,
*                   177377, 176777, 175777, 173777, 167777, 157777, 137777,
*                   077777, 000000
*
*****

```

```

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4991
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4997
4998
4999
5000
5001 026160
5002 026160 004737 003514
5003 026164 103003
5004 026166
5005 026166 104460
5006 026170
5007 026170 104410
5008 026172 000234
5009
5010 026174
5011 026174 013737 002506 026212 1$:
5012
5013 026202 004537 005266
5014 026206 002510
5015 026210 002654
5016 026212 000000
5017 026214 000004
5018
5019 026216 103025
5020 026220
5021 026220 104460
5022 026222 005737 002412
5023 026226 001421
5024 026230 022737 000006 002204
5025 026236 001415
5026 026240 012737 000002 002202
5027 026246 012737 000006 002204
5028 026254 012737 016316 002206
5029 026262 012737 007314 002210
5030 026270 000753
5031
5032 026272 013701 026206 7$:
5033 026276 013702 026210
5034 026302 013703 026212

```

```

BGNTST
JSR PC,MSTCLR ;INIT DMV & START UP MAINT. LOOP
BCC 1$ ;IF NO ERROR, PROCEED WITH TEST
ERROR ;ELSE, REPORT IT
ESCAPE TST ; & EXIT TEST
TRAP C$ERROR
WORD C$ESCAPE L10036-.

1$: MOV PATF,4$ ;SETUP COUNT OF # OF WORDS IN TEST PATTERN

2$: JSR R5,NPRMOV ;MOVE DATA THROUGH THE NPR LOGIC
3$: PATF+2 ; ADDRESS OF DATA
4$: BUFAREA ; BUFFER AREA
0 ;*** MODIFIED FROM ABOVE *** -- WORD COUNT
NPRLD ; OPERATION TO BE UTILIZED

7$: BCC 7$ ;IF ERROR, REPORT IT
13$: ERROR

TRAP C$ERROR
TST TMP0 ;WE JUST REPORTED ONE ERROR BUT WAS IT A TIMEOUT
BEQ 7$ ;ERROR? IF SO, PROCEED WITH TESTING. ELSE,
CMP #NPRTOE,ERRNBR ;WE WILL HAVE TO REPORT IT HERE AND NOW.
BEQ 7$ ;THE TIMEOUT ERROR WAS ALREADY REPORTED.
MOV #T.EHRD,ERRTYP ;IT WASN'T REPORTED YET, SETUP FOR IT NOW:
MOV #NPRTOE,ERRNBR
MOV #EM26E,ERRMSG
MOV #ERR11,ERRBLK
BR 13$ ;LOOP BACK TO CAUSE REPORT @ PROPER PC LOCATION

7$: MOV 2$,R1 ;POINT TO GOOD DATA
MOV 3$,R2 ; & ACTUAL DATA
MOV 4$,R3 ;GET WORD COUNT

```

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TEST 5 -- NPR DATA-IN

```

5035 026306 005037 002276          CLR      ERRFLG          ;RESET ERROR FLAG
5036
5037 026312 022122          5$:     CMP      (R1)+,(R2)+ ;CHECK RECEIVED DATA
5038 026314 001007          BNE      6$              ;ERROR, GO REPORT IT
5039 026316 077303          11$:    SOB      R3,5$      ;GOOD, IF MORE DO IT AGAIN
5040 026320 005737 002276          TST      ERRFLG         ;ELSE, SEE IF WE MUST FINISH AN ERROR MESSAGE
5041 026324 001440          BEQ      10$            ;NO, THEN WE CAN EXIT THE TEST
5042 026326 004737 013076          JSR      PC,MULERR      ;YES, USE COMMON ROUTINE TO END ERROR MESSAGE
5043 026332 000435          BR       10$            ;THEN WE CAN EXIT THE TEST
5044
5045 026334 010246          6$:     MOV      R2,-(SP)   ;SAVE THIS FOR FURTHER TESTING
5046 026336 014137 002254          MOV      -(R1),GDATA    ;SETUP FOR ERROR REPORT
5047 026342 014237 002256          MOV      -(R2),BDATA
5048 026346 010137 002252          MOV      R1,TDATA      ;LSI-11'S MEMORY ADDRESS
5049 026352 163701 026206          SUB      2$,R1          ;CALCULATE THE OFFSET AT WHICH THE
5050 026356 006201          ASR      R1              ;DATA COMPARISON ERROR OCCURED
5051 026360 010137 002300          MOV      R1,REGNUM      ;THE ERROR MESSAGE WILL REPORT THIS TOO
5052 026364 005737 002276          TST      ERRFLG         ;HAVE WE ALREADY REPORTED AN ERROR HERE?
5053 026370 001007          BNE      8$              ;YES, THEN WE ONLY PRINT DATA THIS TIME
5054
5055 026372 005237 002276          INC      ERRFLG         ;NO, SET FLAG & REPORT THE WHOLE MESSAGE
5056 026376          GEDF     EM26D,ERR9     ;WORD NPR TRANSFER LSI ==> DMV
5057
5058 026376 104455          TRAP    C$ERDF          ;
5059 026400 000036          .WORD   30              ;
5060 026402 016274          .WORD   EM26D           ;
5061 026404 007300          .WORD   ERR9            ;
5062 026406 000402          BR       9$              ; RESUME TESTING
5063
5064 026410 004737 012574          8$:     JSR      PC,ERR9$ ;IDENTIFY THE FAILING DATA
5065 026414 012602          9$:     MOV      (SP)+,R2 ;RESTORE POINTERS
5066 026416 013701 002252          MOV      TDATA,R1
5067 026422 005721          TST      (R1)+
5068 026424 000734          BR       11$            ;AND RESUME TESTING
5069
5070 026426          10$:    ENDTST
5071 026426
5072 026426
5073 026426 104401          L10036: TRAP    C$ETST

```

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TEST 6 -- NPR XFER ABORT

.SBTTL TEST 6 -- NPR XFER ABORT

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5090
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5092 026430
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5095 026430
5096 026430
5097 026430 104402
5098 026432 004737 003514
5099 026436 103003
5100 026440
5101 026440 104460
5102 026442
5103 026442 104410
5104 026444 000164
5105 026446 012737 000001 002412
5106
5107 026454 012737 160000 002420
5108 026462 012737 000044 002414
5109 026470 004537 004042
5110 026474 000070
5111 026476 002420
5112 026500 103003
5113 026502
5114 026502 104460
5115 026504
5116 026504 104410
5117 026506 000122
5118 026510 004537 004042
5119 026514 000071
5120 026516 002421
5121 026520 103003
5122 026522
5123 026522 104460
5124 026524
5125 026524 104410
5126 026526 000102
5127 026530 004537 004054
5128 026534 000072
5129 026536 000200

```

*****
*
*   TEST 6 -- NPR XFER ABORT
*
* FIRST SUBTEST :
* THE PROGRAM PERFORMS AN OUTPUT NPR REQUEST TO A NON-EXISTENT MEMORY
* LOCATION, AND CHECKS FOR THE ASSERTION OF ABORT XFER BIT IN THE NPR CONTROL
* REGISTER. THEN, AN OUTPUT NPR IS DONE AND CHECKED, TO A LOCATION IN THE
* PROGRAM, USING 125252 FOR DATA, AND THE PROGRAM CHECKS FOR ABORT XFER TO
* BE CLEARED BY SETTING THE DONE BIT.
* SECOND SUBTEST :
* THE ABOVE SUBTEST IS REPEATED USING 4-UT NPR'S.
*****

```

```

:
:   BGNTST
:
:==== SUBTEST # 1 -- NPR OUTPUT TO NON-EXISTENT LOCATION FORCING NPR-ABORT ====
:
:   BGNSUB
:
:   T6.:
:
:   T6.1:
:
:   TRAP   C$BSUB
:   JSR    PC,MSTCLR   ;INIT DMV & ENTER M-LOOP
:   BCC    .+10        ;IF NO ERROR, PROCEED WITH TESTING
:   ERROR  ;ELSE, REPORT ERROR
:
:   TRAP   C$ERROR
:   ESCAPE SUB        ;   & EXIT TEST
:
:   TRAP   C$ESCAPE
:   .WORD  L10040-.
:   MOV    #1,TMP0    ;DISABLE PRINTOUT OF TIMEOUT-COUNT BY ERR11
:
:   MOV    #160000,TMP3 ;SETUP 11'S ADDRESS
:   MOV    #NPRDL,TMP1 ;CONTROL REG. VALUE FOR NPR-OUT COMMAND
:   JSR    R5,WRITE   ;SETUP ADDRESS OUT REGISTERS
:
:   BCC    .+10        ;IF NO ERROR, PROCEED
:   ERROR  ;ELSE, REPORT IT
:
:   TRAP   C$ERROR
:   ESCAPE SUB        ;   AND EXIT THIS TEST
:
:   TRAP   C$ESCAPE
:   .WORD  L10040-.
:   JSR    R5,WRITE
:   NPRAOH
:   TMP3+1
:   BCC    .+10        ;IF NO ERROR, PROCEED
:   ERROR  ;ELSE, REPORT IT
:
:   TRAP   C$ERROR
:   ESCAPE SUB        ;   AND EXIT THIS TEST
:
:   TRAP   C$ESCAPE
:   .WORD  L10040-.
:   JSR    R5,WRITEI
:   NPRAOX
:   NPRBS7
:
:   ; (THIS SETS BS7 & CLEARS EXTENDED ADDR. BITS)

```

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TEST 6 -- NPR XFER ABORT

```

5130 026540 103003          BCC      .+10          ;IF NO ERROR, PROCEED
5131 026542                ERROR          ;ELSE, REPORT IT
5132 026542 104460                ESCAPE  SUB          ;           AND EXIT THIS TEST          TRAP  C$ERROR
5133 026544                ESCAPE  SUB          ;           AND EXIT THIS TEST          TRAP  C$ESCAPE
5134 026544 104410                ESCAPE  SUB          ;           AND EXIT THIS TEST          .WORD L10040-.
5135 026546 000062                JSR     R5,WRITE      ;INITIATE THE NPR-OUT OPERATION
5136 026550 004537 004042          NPRCTL  R5,WRITE      ;INITIATE THE NPR-OUT OPERATION
5137 026554 123004          NPRCTL  R5,WRITE      ;INITIATE THE NPR-OUT OPERATION
5138 026556 002414          TMP1
5139 026560 103003          BCC      .+10          ;IF NO ERROR, PROCEED
5140 026562                ERROR          ;ELSE, REPORT IT
5141 026562 104460                ESCAPE  SUB          ;           AND EXIT THIS TEST          TRAP  C$ERROR
5142 026564                ESCAPE  SUB          ;           AND EXIT THIS TEST          TRAP  C$ESCAPE
5143 026564 104410                ESCAPE  SUB          ;           AND EXIT THIS TEST          .WORD L10040-.
5144 026566 000042                JSR     R5,READ       ;READ BACK THE CONTROL-STATUS REGISTER
5145 026570 004537 003616          NPRCTL  R5,READ       ;READ BACK THE CONTROL-STATUS REGISTER
5146 026574 123004          NPRCTL  R5,READ       ;READ BACK THE CONTROL-STATUS REGISTER
5147 026576 002416          TMP2
5148 026600 103003          BCC      .+10          ;IF NO ERROR, PROCEED
5149 026602                ERROR          ;ELSE, REPORT IT
5150 026602 104460                ESCAPE  SUB          ;           AND EXIT THIS TEST          TRAP  C$ERROR
5151 026604                ESCAPE  SUB          ;           AND EXIT THIS TEST          TRAP  C$ESCAPE
5152 026604 104410                ESCAPE  SUB          ;           AND EXIT THIS TEST          .WORD L10040-.
5153 026606 000022                BITB   #NPRABT,TMP2  ;THE ABORT BIT SHOULD BE SET
5154 026610 132737 000200 002416          BNE    20$           ;IT IS. EXIT SUBTEST
5155 026616 001004                GEDF   EM26G,ERR11   ;IT DIDN'T. REPORT MISSING NPR ABORT
5156 026620                GEDF   EM26G,ERR11   ;           'DEVICE FATAL' ERROR # 31
5157
5158 026620 104455                TRAP   C$ERDF
5159 026622 000037                .WORD 31
5160 026624 016402                .WORD EM26G
5161 026626 007314                .WORD ERR11
5162 026630
5163 026630
5164 026630 104403                L10040:
5165
5166
5167 026632                ;=== BTEST #2 -- NPR OUTPUT TO EXISTENT LOCATION YIELDING NO NPR-ABORT ===
5168 026632                BGNSUB
5169 026632 104402                T6.2:
5170 026634 004737 003514          JSR     PC,MSTCLR     ;INIT DMV & ENTER M-LOOP          TRAP  C$BSUB
5171 026640 103003          BCC      .+10          ;IF NO ERROR, PROCEED WITH TESTING
5172 026642                ERROR          ;ELSE, REPORT ERROR
5173 026642 104460                ESCAPE  SUB          ;           & EXIT TEST          TRAP  C$ERROR
5174 026644                ESCAPE  SUB          ;           & EXIT TEST          TRAP  C$ESCAPE
5175 026644 104410                ESCAPE  SUB          ;           & EXIT TEST          .WORD L10041-.
5176 026646 000164                MOV     #1,TMP0      ;DISABLE PRINTOUT OF TIMEOUT-COUNT BY ERR11
5177 026650 012737 000001 002412          MOV     #1,TMP0      ;DISABLE PRINTOUT OF TIMEOUT-COUNT BY ERR11
5178
5179 026656 012737 002654 002420          MOV     #BUFAREA,TMP3 ;SETUP 11'S ADDRESS
5180 026664 012737 000044 002414          MOV     #NPRDL,TMP1  ;CONTROL REG. VALUE FOR NPR-OUT COMMAND
5181 026672 004537 004042          JSR     R5,WRITE      ;SETUP ADDRESS OUT REGISTERS
5182 026676 000070          NPRACL  R5,WRITE      ;SETUP ADDRESS OUT REGISTERS
5183 026700 002420          TMP3
5184 026702 103003          BCC      .+10          ;IF NO ERROR, PROCEED
5185 026704                ERROR          ;ELSE, REPORT IT

```

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TEST 6 -- NPR XFER ABORT

```

5186 026704 104460
5187 026706          ESCAPE SUB          ;          AND EXIT THIS TEST          TRAP   C$ERROR
5188 026706 104410          ;          AND EXIT THIS TEST          TRAP   C$ESCAPE
5189 026710 000122          .WORD   L10041-.
5190 026712 004537 004042      JSR    R5,WRITE
5191 026716 000071      NPRAOH
5192 026720 002421      TMP3+1
5193 026722 103003      BCC    .+10          ;IF NO ERROR, PROCEED
5194 026724          ERROR          ;ELSE, REPORT IT
5195 026724 104460          ;          AND EXIT THIS TEST          TRAP   C$ERROR
5196 026726          ESCAPE SUB          ;          AND EXIT THIS TEST          TRAP   C$ESCAPE
5197 026726 104410          .WORD   L10041-.
5198 026730 000102          ;          AND EXIT THIS TEST          TRAP   C$ESCAPE
5199 026732 004537 004054      JSR    R5,WRITEI
5200 026736 000072      NPRAOX
5201 026740 000000      0          ; (THIS CLEARS BS7 & EXTENDED ADDR. BITS)
5202 026742 103003      BCC    .+10          ;IF NO ERROR, PROCEED
5203 026744          ERROR          ;ELSE, REPORT IT
5204 026744 104460          ;          AND EXIT THIS TEST          TRAP   C$ERROR
5205 026746          ESCAPE SUB          ;          AND EXIT THIS TEST          TRAP   C$ESCAPE
5206 026746 104410          .WORD   L10041-.
5207 026750 000062          ;          AND EXIT THIS TEST          TRAP   C$ESCAPE
5208 026752 004537 004042      JSR    R5,WRITE
5209 026756 123004      NPRCTL          ;INITIATE THE NPR-OUT OPERATION
5210 026760 002414      TMP1
5211 026762 103003      BCC    .+10          ;IF NO ERROR, PROCEED
5212 026764          ERROR          ;ELSE, REPORT IT
5213 026764 104460          ;          AND EXIT THIS TEST          TRAP   C$ERROR
5214 026766          ESCAPE SUB          ;          AND EXIT THIS TEST          TRAP   C$ESCAPE
5215 026766 104410          .WORD   L10041-.
5216 026770 000042          ;          AND EXIT THIS TEST          TRAP   C$ESCAPE
5217 026772 004537 003616      JSR    R5,READ
5218 026776 123004      NPRCTL          ;READ BACK THE CONTROL-STATUS REGISTER
5219 027000 002416      TMP2
5220 027002 103003      BCC    .+10          ;IF NO ERROR, PROCEED
5221 027004          ERROR          ;ELSE, REPORT IT
5222 027004 104460          ;          AND EXIT THIS TEST          TRAP   C$ERROR
5223 027006          ESCAPE SUB          ;          AND EXIT THIS TEST          TRAP   C$ESCAPE
5224 027006 104410          .WORD   L10041-.
5225 027010 000022          ;          AND EXIT THIS TEST          TRAP   C$ESCAPE
5226 027012 132737 000200 002416      BITB  #NPRABT,TMP2 ;THE ABORT BIT SHOULD BE SET
5227 027020 001404      BEQ   20$          ;IT IS. EXIT SUBTEST
5228 027022          GEDF  EM26E,ERR11 ;IT DIDN'T. REPORT MISSING NPR ABORT
5229          ;          'DEVICE FATAL' ERROR # 32
5230 027022 104455          ;          AND EXIT THIS TEST          TRAP   C$ERDF
5231 027024 000040          .WORD   32
5232 027026 016316          .WORD   EM26E
5233 027030 007314          .WORD   ERR11
5234 027032          20$:  ENDSUB
5235 027032          ;          AND EXIT THIS TEST          TRAP   C$ESUB
5236 027032 104403          L10041:
5237 027034          ENDTST
5238 027034          ;          AND EXIT THIS TEST          TRAP   C$SETST
5239 027034 104401          L10037:

```

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TEST 7 -- NPR EXTENDED ADDRESS BIT TEST

.SBTTL TEST 7 -- NPR EXTENDED ADDRESS BIT TEST

```

*****
*
*      TEST 7 -- NPR EXTENDED ADDRESS BIT TEST
*
* THIS TEST WILL ONLY BE RUN IF THERE IS AT LEAST 32K WORDS OF MEMORY ON THE
* SYSTEM. IF THERE IS, THE PROGRAM CHOOSES A LOCATION TO USE IN THE ADDRESS
* RANGE 200000-377776 (OCTAL). THEN, THE FOLLOWING 2 SUBTESTS ARE PERFORMED :
*
* FIRST SUBTEST :
* AN INPUT NPR IS PERFORMED AND CHECKED USING THE MEMORY LOCATION, WITH
* 125252 FOR DATA. THE PROGRAM CHECKS THAT THE ABORT XFER BIT REMAINS
* CLEARED.
* SECOND SUBTEST :
* AN OUTPUT NPR IS PERFORMED AND CHECKED USING THE MEMORY LOCATION, WITH
* 125252 FOR DATA. THE PROGRAM CHECKS THAT THE ABORT XFER BIT REMAINS
* CLEARED.
*
*****

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5262 027036
5263 027036 004737 003514
5264 027042 103003
5265 027044
5266 027044 104460
5267 027046
5268 027046 104432
5269 027050 001506
5270 027052 013700 002120
5271 027056 042700 001777
5272
5273 027062 001002
5274 027064
5275 027064 104432
5276 027066 001470
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5280 027070 010037 002436
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5282
5283
5284 027074
5285 027074
5286 027074 104402
5287 027076 012737 177777 002434
5288 027104 012737 002000 002442
5289 027112 112737 000001 002451
5290 027120 004737 030560
5291
5292
5293 027124 004537 004054
5294 027130 000074
5295 027132 000000

```

```

:      BGNTST
:
:      T7::
:      JSR      PC,MSTCLR      ;INIT DMV & ENTER M-LOOP
:      BCC      1$            ;IF NO ERROR, PROCEED WITH TESTING
:      ERROR
:      ELSE, REPORT ERROR
:
:      EXIT      TST          ; & EXIT TEST
:
:      TRAP     C$ERROR
:
:      TRAP     C$EXIT
:      .WORD    L10042-.
1$:  MOV      L$HIMEM,R0      ;GET LAST VALID 'PAR' VALUE FROM SUPERVISOR
:      BIC      #1777,R0      ;THESE BITS CORESPOND TO BITS 6 --> 15 OF THE
:      BNE      2$            ;ACTUAL ADDRESS AND AREN'T OUR CONCERN HERE.
:      EXIT      TST          ;IF THE RESULT IS ZERO,
:      THERE IS NOTHING TO TEST
:
:      TRAP     C$EXIT
:      .WORD    L10042-.
:      ELSE, PROCEED TO SETUP MPU'S PAR AND DMV'S NPR
:      REGISTER MAXIMUM VALUES
:
2$:  MOV      R0,TMPA          ;THIS IS FOR THE MPU
:
:=====
12$: BGNSUB                  ;TEST THE NPR-IN USING EXTENDED ADDR. BITS
:
:      T7.1:
:      TRAP     C$BSUB
:      MOV      #177777,TMP9   ;INITIALIZE TEMP 9
:      MOV      #BIT10,TMPC    ;INITIALIZE PAGE ADDRESS REG. VARIABLE
:      MOV      #BIT0,TMPF+1   ;INITIALIZE NPR EXTENDED ADDRESS REG. VARIABLE
:      JSR      PC,XMINIT      ;INITIALIZE THE MPU
:
:***** WRITE/READ/VERIFY NPRAIH,NPRAIL *****
:      JSR      R5,WRITEI      ;POINT NPR REGISTERS TO 0
:      NPRAIL
:      0

```

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TEST 7 -- NPR EXTENDED ADDRESS BIT TEST

```

5296 027134 004537 004054      JSR    R5,WRITEI
5297 027140 000075      NPRAIH
5298 027142 000000      0
5299 027144 004537 003616      JSR    R5,READ      ;READ BACK THE ADDRESS & VERIFY IT
5300 027150 000074      NPRAIL
5301 027152 002434      TMP9
5302 027154 004537 003616      JSR    R5,READ
5303 027160 000075      NPRAIH
5304 027162 002435      TMP9+1
5305 027164 023727 002434 000000      CMP    TMP9,#000000 ;IS IT CORRECT?
5306 027172 001424      BEQ    2$           ;YES, PROCEED.
5307 027174 013737 002434 002256      MOV    TMP9,BDATA  ;NO, SETUP FOR & REPORT LOADING FAILURE
5308 027202 012737 000000 002254      MOV    #000000,GDATA
5309 027210 012737 000007 002300      MOV    #7,REGNUM   ;IDENTIFY NPRAIH AS THE CULPRIT
5310 027216 105737 002434      TSTB  TMP9         ;IS THAT REALLY TRUE?
5311 027222 001002      BNE    1$         ;MAYBE. BUT, NPRAIL IS DEFINITELY AT FAULT
5312 027224 005237 002300      INC    REGNUM      ;SO IDENTIFY IT AS SUCH
5313 027230      1$: GEDF    EM26A,ERR14 ;REPORT THE FAILURE
5314      ; 'DEVICE FATAL' ERROR # 33
5315 027230 104455      TRAP   C$ERDF
5316 027232 000041      .WORD 33
5317 027234 016204      .WORD EM26A
5318 027236 011002      .WORD ERR14
5319 027240      ESCAPE SUB      ; AND EXIT THIS SUBTEST
5320 027240 104410      TRAP   C$ESCAPE
5321 027242 000506      .WORD L10043-.
5322      ;*****
5323      ;***** MAIN SUBTEST #1 LOOP STARTS HERE *****
5324      ;*****
5325      ;*****
5326      ;***** COMPLEMENT OF NPRDRH:NPRDRL => TMP3 *****
5327 027244 004537 003616      2$: JSR    R5,READ      ;GET THE CURRENT CONTENTS OF THE NPR DATA REG'S
5328 027250 123000      NPRDRL
5329 027252 002414      TMP1
5330 027254 103003      BCC    .+10       ;IF NO ERROR, PROCEED
5331 027256      ERROR           ;ELSE, REPORT IT
5332 027256 104460      ESCAPE SUB      ; AND EXIT THIS TEST
5333 027260      TRAP   C$ERROR
5334 027260 104410      TRAP   C$ESCAPE
5335 027262 000466      .WORD L10043-.
5336 027264 004537 003616      JSR    R5,READ
5337 027270 123001      NPRDRH
5338 027272 002415      TMP1+1
5339 027274 103003      BCC    .+10       ;IF NO ERROR, PROCEED
5340 027276      ERROR           ;ELSE, REPORT IT
5341 027276 104460      ESCAPE SUB      ; AND EXIT THIS TEST
5342 027300      TRAP   C$ERROR
5343 027300 104410      TRAP   C$ESCAPE
5344 027302 000466      .WORD L10043-.
5345 027304 013737 002414 002420      MOV    TMP1,TMP3  ;USE CURRENT DATA & BUILD BACKGROUND PATTERN
5346 027312 005137 002420      COM    TMP3       ;COMPLEMENT IT TO GENERATE A BACKGROUND PATTERN
5347
5348      ;***** TMP3 => 1ST LOCATION OF EACH EXTENDED MEMORY BLOCK *****
5349 027316 012737 002000 002446      4$: MOV    #BIT10,TMPE ;REFILL ALL TEST LOCATIONS STARTING HERE
5350 027324 004537 030700      JSR    R5,XMWRIT  ;WRITE BACKGROUND PATTERN GENERATED ABOVE
5351 027330 002446      TMPE           ; POINTER TO 'PAR' FORMAT ADDRESS

```


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TEST 7 -- NPR EXTENDED ADDRESS BIT TEST

```

5408 027522          ESCAPE SUB          :      AND EXIT THIS SUBTEST
5409 027522 104410          TRAP          C$ESCAPE
5410 027522 000224          .WORD          L10043-.
5411 027526 112737 000004 002417  MOVB  #NPRLD,TMP2+1 ;SETUP CONTROL VALUE TO DO NPR-IN
5412 027534 004537 004042  JSR   R5,WRITE     ;PERFORM THE 'EXTENDED' DATA-IN NPR
5413 027540 123004          NPRCTL
5414 027542 002417          TMP2+1
5415 027544 103003          BCC   .+10        ;IF NO ERROR, PROCEED
5416 027546          ERROR          ;ELSE, REPORT IT
5417 027546 104460          TRAP          C$ERROR
5418 027550          ESCAPE SUB          :      AND EXIT THIS SUBTEST
5419 027550 104410          TRAP          C$ESCAPE
5420 027552 000176          .WORD          L10043-.
5421
5422
5423 027554 004537 003616  ;***** CHECK THE NPR OPERATION (DATA/NPRCTL) *****
5424 027560 123004          JSR   R5,READ     ;CHECK THE NPR OPERATION
5425 027562 002416          NPRCTL
5426 027564 103003          TMP2
5427 027566          BCC   .+10        ;IF NO ERROR, PROCEED
5428 027566 104460          ERROR          ;ELSE, REPORT IT
5429 027570          ESCAPE SUB          :      AND EXIT THIS TEST
5430 027570 104410          TRAP          C$ERROR
5431 027572 000156          TRAP          C$ESCAPE
5432 027574 004537 003616  .WORD          L10043-.
5433 027600 123001          JSR   R5,READ     ;GET THE DATA WE SHOULD HAVE JUST LOADED INTO
5434 027602 002257          NPRDRH          ; THE NPR DATA REGISTERS FROM THE
5435 027604 103003          BDATA+1        ; EXTENDED MEMORY AREA
5436 027606          BCC   .+10        ;IF NO ERROR, PROCEED
5437 027606 104460          ERROR          ;ELSE, REPORT IT
5438 027610          ESCAPE SUB          :      AND EXIT THIS TEST
5439 027610 104410          TRAP          C$ERROR
5440 027612 000136          TRAP          C$ESCAPE
5441 027614 004537 003616  .WORD          L10043-.
5442 027620 123000          JSR   R5,READ     ;GET THE DATA WE SHOULD HAVE JUST LOADED INTO
5443 027622 002256          NPRDRL          ; THE NPR DATA REGISTERS FROM THE
5444 027624 103003          BDATA          ; EXTENDED MEMORY AREA
5445 027626          BCC   .+10        ;IF NO ERROR, PROCEED
5446 027626 104460          ERROR          ;ELSE, REPORT IT
5447 027630          ESCAPE SUB          :      AND EXIT THIS TEST
5448 027630 104410          TRAP          C$ERROR
5449 027632 000116          TRAP          C$ESCAPE
5450 027634 132737 000300 002416  .WORD          L10043-.
5451 027642 001414          BITB  #300,TMP2  ;DID IT ABORT OR HANG?
5452 027644 100005          BEQ   14$        ;NO, GOOD. PROCEED WITH SUBTEST
5453 027646          BPL   10$        ;YES, WHICH ONE?
5454          GEDF  EM27A,ERR12 ;ABORT, REPORT IT AS SUCH.
5455          ;          'DEVICE FATAL' ERROR # 35
5456 027646 104455          TRAP          C$ERDF
5457 027650 000043          .WORD          35
5458 027652 016437          .WORD          EM27A
5459 027654 007456          .WORD          ERR12
5460 027656 000404          BR    12$
5461          GEDF  EM27B,ERR12 ;AND EXIT
5462          ;          'DEVICE FATAL' ERROR # 36
5463 027660 104455          TRAP          C$ERDF
5463 027662 000044          .WORD          36

```

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TEST 7 -- NPR EXTENDED ADDRESS BIT TEST

```

5464 027664 016461                                .WORD  EM27B
5465 027666 007456                                .WORD  ERR12
5466 027670                                     12$:  ESCAPE  SUB          ; AND EXIT SUBTEST
5467 027670 104410                                TRAP   C$ESCAPE
5468 027672 000056                                .WORD  L10043-.
5469 027674 023737 002256 002254 14$:  CMP      BDATA,GDATA  ;DID WE READ THE TEST DATA USING THE NPR?
5470 027702 001406                                BEQ    20$
5471 027704                                GEDF   EM27C,ERR12      ;YES, WELL THIS ONE WORKED.
5472                                ;NO!  REPORT THE ERROR.
5473 027704 104455                                ; 'DEVICE FATAL' ERROR # 37
5474 027706 000045                                TRAP   C$ERDF
5475 027710 016502                                .WORD  37
5476 027712 007456                                .WORD  EM27C
5477 027714                                .WORD  ERR12
5478 027714 104410                                ESCAPE  SUB          ; EXIT FROM SUBTEST AFTER PRINTING ERROR MSG.
5479 027716 000032                                TRAP   C$ESCAPE
5480 027720 062737 002000 002442 20$:  ADD      #BIT10,TPMC   ;POINT TO NEXT PAGE ADDRESS REG. VALUE
5481 027726 001410                                BEQ    63$
5482 027730 023737 002442 002436  CMP      TMP9,TPMA     ;IF 0, WE'RE DONE
5483 027736 101004                                BHI    63$
5484 027740 105237 002451                                INCB   TMP9+1         ;IF GREATER THEN MAXIMUM VALUE,
5485 027744 000137 027244                                JMP     2$             ; WE'RE DONE TOO.
5486                                ;ELSE, INCREMENT NPR'S EXTENDED ADDR. REG.
5487 027750                                     63$:  ENDSUB          ;AND GO BACK TO DO THIS ADDRESS
5488 027750
5489 027750 104403                                L10043:  TRAP   C$ESUB
5490                                ;=====
5491                                ;
5492 027752                                     BGNSUB          ;TEST THE NPR-OUT USING EXTENDED ADDR. BITS
5493 027752                                     ;T7.2:
5494 027752 104402                                TRAP   C$BSUB
5495 027754 012737 002000 002442  MOV      #BIT10,TPMC   ;INITIALIZE PAGE ADDRESS REG. VARIABLE
5496 027762 112737 000001 002451  MOV     #BIT0,TMP9+1  ;INITIALIZE NPR EXTENDED ADDRESS REG. VARIABLE
5497 027770 004737 030560  JSR     PC,XMINIT     ;INITIALIZE THE MPU
5498
5499 ;***** WRITE/READ/VERIFY NPRAOH,NPRAOL *****
5500 027774 004537 004054  JSR     R5,WRITEI    ;POINT NPR REGISTERS TO 0
5501 030000 000070  NPRAOL
5502 030002 000000  0
5503 030004 004537 004054  JSR     R5,WRITEI
5504 030010 000071  NPRAOH
5505 030012 000000  0
5506 030014 004537 003616  JSR     R5,READ      ;READ BACK THE ADDRESS & VERIFY IT
5507 030020 000070  NPRAOL
5508 030022 002434  TMP9
5509 030024 004537 003616  JSR     R5,READ
5510 030030 000071  NPRAOH
5511 030032 002435  TMP9+1
5512 030034 023727 002434 000000  CMP     TMP9,#000000  ;IS IT CORRECT?
5513 030042 001427  BEQ    2$            ;YES, PROCEED.
5514 030044 013737 002434 002256  MOV     TMP9,BDATA   ;NO, SETUP FOR & REPORT LOADING FAILURE
5515 030052 012737 000000 002254  MOV     #000000,GDATA
5516 030060 012737 000004 002300  MOV     #4,REGNUM
5517 030066 105737 002434  TSTB   TMP9
5518 030072 001002  BNE    1$
5519 030074 005237 002300  INC     REGNUM
;
; SO IDENTIFY IT AS SUCH

```

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TEST 7 -- NPR EXTENDED ADDRESS BIT TEST

```

5520 030100
5521
5522 030100 104455
5523 030102 000046
5524 030104 016204
5525 030106 011002
5526 030110
5527 030110 104410
5528 030112 000442
5529 030114 012737 123456 002420
5530
5531
5532
5533
5534
5535
5536 030122 062737 021475 002420
5537 030130 013737 002420 002414
5538 030136 005137 002414
5539
5540 030142 012737 002000 002446
5541 030150 004537 030700
5542 030154 002446
5543 030156 002420
5544 030160 103003
5545 030162
5546 030162 104460
5547 030164
5548 030164 104410
5549 030166 000366
5550 030170 062737 002000 002446
5551 030176 001404
5552 030200 023737 002446 002436
5553 030206 101760
5554
5555
5556
5557 030210 004537 004042
5558 030214 000072
5559 030216 002451
5560 030220 103003
5561 030222
5562 030222 104460
5563 030224
5564 030224 104410
5565 030226 000326
5566 030230 004537 003616
5567 030234 000072
5568 030236 002450
5569 030240 103003
5570 030242
5571 030242 104460
5572 030244
5573 030244 104410
5574 030246 000306
5575 030250 123737 002450 002451

```

```

1$:  GEDF  EM26A,ERR14  ;REPORT THE FAILURE
;      'DEVICE FATAL' ERROR # 38
;      TRAP  C$ERDF
;      .WORD 38
;      .WORD EM26A
;      .WORD ERR14
ESCAPE SUB  ; AND EXIT THIS SUBTEST
;      TRAP  C$ESCAPE
;      .WORD L10044-.
MOV  #123456,TMP3  ;USE THIS AS INITIAL BACKGROUND PATTERN
;*****
;***** MAIN SUBTEST #2 LOOP STARTS HERE *****
;*****
;***** TMP3 => 1ST LOCATION OF EACH EXTENDED MEMORY BLOCK *****
2$:  ADD  #21475,TMP3  ;GENERATE THE PATTERN WE'LL USE THIS TIME
MOV  TMP3,TMP1  ;PUT HERE FOR ERROR HANDLER
COM  TMP1
4$:  MOV  #BIT10,TMPE  ;REFILL ALL TEST LOCATIONS STARTING HERE
JSR  R5,XMWRIT  ;WRITE BACKGROUND PATTERN GENERATED ABOVE
TMPE ; POINTER TO ADDRESS (IN 'PAR' FORMAT)
TMP3 ; POINTER TO DATA (TO BE WRITTEN)
BCC  .+10 ;IF NO ERROR, PROCEED
ERROR ;ELSE, REPORT IT
ESCAPE SUB  ; AND EXIT THIS TEST
;      TRAP  C$ERROR
;      .WORD L10044-.
ADD  #BIT10,TMPE  ;INCREMENT THE PAGE ADDR. REG. VALUE
BEQ  6$ ;DONE IF IT GOES TO ZERO
CMP  TMPE,TMPA  ;IS THE NEW VALUE WITHIN CURRENT MEMORY?
BLOS 4$ ;YES, THE WRITE IT TOO.
;NO, DONE.
;***** WRITE/READ/VERIFY NPRAIX *****
6$:  JSR  R5,WRITE  ;SETUP NPR EXTENDED ADDR. REG BITS
NPRAOX
TMPE+1
BCC  .+10 ;IF NO ERROR, PROCEED
ERROR ;ELSE, REPORT IT
ESCAPE SUB  ; AND EXIT THIS TEST
;      TRAP  C$ERROR
;      .WORD L10044-.
JSR  R5,READ  ;READ IT BACK & VERIFY THAT IT'S CORRECT
NPRAOX
TMPE
BCC  .+10 ;IF NO ERROR, PROCEED
ERROR ;ELSE, REPORT IT
ESCAPE SUB  ; AND EXIT THIS TEST
;      TRAP  C$ERROR
;      .WORD L10044-.
CMPB TMPF,TMPF+1 ;DID IT LOAD CORRECTLY?

```

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TEST 7 -- NPR EXTENDED ADDRESS BIT TEST

```

5576 030256 001417          BEQ      8$          ;YES, PROCEED
5577 030260 113737 002451 002254  MOVE    TMPF+1,GDATA ;NO, SETUP FOR ERROR HANDLER
5578 030266 113737 002450 002256  MOV     TMPF,BDATA
5579 030274 012737 000003 002300  MOV     #3,REGNUM    ; IDENTIFY NPRAIX AS FAILING REG.
5580 030302          GEDF    EM26A,ERR14 ;REPORT THE FAILURE
5581          ;          ; 'DEVICE FATAL' ERROR # 39
5582 030302 104455          TRAP    C$ERDF
5583 030304 000047          .WORD  39
5584 030306 016204          .WORD  EM26A
5585 030310 011002          .WORD  ERR14
5586 030312          ESCAPE  SUB          ; AND EXIT THIS SUBTEST
5587 030314 104410          TRAP    C$ESCAPE
5588 030314 000240          .WORD  L10044-.
5589
5590          ;***** WRITE(LSI-11) TEST LOCATION BACKGROUND PATTERN *****
5591 030316 004537 030700 8$: JSR    R5,XMWRIT ;SETUP TEST LOCATION'S BACKGROUND PATTERN
5592 030322 002442          TMP    TMP1
5593 030324 002414          MOV     TMP3,GDATA ;GENERATE A TEST DATA PATTERN FROM BACKGROUND
5594 030326 013737 002420 002254  ADD     #52525,GDATA ;PATTERN BY ADDING THIS TO IT.
5595 030334 062737 052525 002254
5596
5597          ;***** LOAD(DMV) TEST PATTERN *****
5598 030342 004537 004042          JSR    R5,WRITE ;LOAD UP THE TEST PATTERN
5599 030346 123001          NPRDRH GDATA+1
5600 030350 002255          JSR    R5,WRITE
5601 030352 004537 004042          NPRDRL GDATA
5602 030356 123000          MOV     #NPRDL,TMP2+1 ;SETUP CONTROL VALUE TO DO NPR-OUT
5603 030360 002254
5604 030362 112737 000044 002417
5605
5606          ;***** PERFORM/CHECK EXTENDED NPR OPERATION *****
5607 030370 004537 004042          JSR    R5,WRITE ;PERFORM THE 'EXTENDED' DATA-OUT NPR
5608 030374 123004          NPRCTL TMP2+1
5609 030376 002417          BCC    .+10 ;IF NO ERROR, PROCEED
5610 030400 103003          ERROR ;ELSE, REPORT IT
5611 030402          ESCAPE  SUB          ; AND EXIT THIS SUBTEST TRAP    C$ERROR
5612 030402 104460          ; TRAP    C$ESCAPE
5613 030404          ; .WORD  L10044-.
5614 030404 104410          JSR    R5,READ ;CHECK THE NPR OPERATION
5615 030406 000146          NPRCTL TMP2
5616 030410 004537 003616          BCC    .+10 ;IF NO ERROR, PROCEED
5617 030414 123004          ERROR ;ELSE, REPORT IT
5618 030416 002416          ESCAPE  SUB          ; AND EXIT THIS SUBTEST TRAP    C$ERROR
5619 030420 103003          ; TRAP    C$ESCAPE
5620 030422 104460          ; .WORD  L10044-.
5621 030422 000126          BITB   #300,TMP2 ;DID IT ABORT OR HANG?
5622 030424 104410          BEQ    14$ ;NO, GOOD. PROCEED WITH SUBTEST
5623 030426 000126          BPL    10$ ;YES, WHICH ONE?
5624 030430 132737 000300 002416          GEDF   EM27A,ERR12 ;ABORT, REPORT IT AS SUCH.
5625 030430 001414          ; 'DEVICE FATAL' ERROR # 40
5626 030436 001414          TRAP    C$ERDF
5627 030440 100005          .WORD  40
5628 030442
5629
5630 030442 104455          TRAP    C$ERDF
5631 030444 000050          .WORD  40

```

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TEST 7 -- NPR EXTENDED ADDRESS BIT TEST

```

5632 030446 016437 .WORD EM27A
5633 030450 007456 .WORD ERR12
5634 030452 000404
5635 030454 10$: BR 12$ ;AND EXIT
GEDF EM27B,ERR12 ;HANG, REPORT IT AS SUCH.
; 'DEVICE FATAL' ERROR # 41
5636
5637 030454 104455 TRAP C$ERDF
5638 030456 000051 .WORD 41
5639 030460 016461 .WORD EM27B
5640 030462 007456 .WORD ERR12
5641 030464 12$: ESCAPE SUB ; AND EXIT SUBTEST
5642 030464 104410 TRAP C$ESCAPE
5643 030466 000066 .WORD L10044-.
5644 030470 004537 030776 14$: JSR R5.XMREAD ;GET THE DATA WE SHOULD HAVE JUST LOADED INTO
TMPD
BDATA
5645 030474 002442
5646 030476 002256
5647 030500 023737 002256 002254 CMP BDATA,GDATA ;DID WE READ THE TEST DATA USING THE NPR?
5648 030506 001406 BEQ 20$ ;YES, WELL THIS ONE WORKED.
5649 030510 GEDF EM27C,ERR12 ;NO! REPORT THE ERROR.
5650 ; 'DEVICE FATAL' ERROR # 42
5651 030510 104455 TRAP C$ERDF
5652 030512 000052 .WORD 42
5653 030514 016502 .WORD EM27C
5654 030516 007456 .WORD ERR12
5655 030520 ESCAPE SUB ; EXIT FROM SUBTEST AFTER PRINTING ERROR MSG.
5656 030520 104410 TRAP C$ESCAPE
5657 030522 000032 .WORD L10044-.
5658 030524 062737 002000 002442 20$: ADD #BIT10,TMPC ;POINT TO NEXT PAGE ADDRESS REG. VALUE
5659 030532 001410 BEQ 63$ ;IF 0, WE'RE DONE
5660 030534 023737 002442 002436 CMP TMPC,TMPA ;IF GREATER THEN MAXIMUM VALUE,
5661 030542 101004 BHI 63$ WE'RE DONE TOO.
5662 030544 105237 002451 INCB TMPF+1 ;ELSE, INCREMENT NPR'S EXTENDED ADDR. REG.
5663 030550 000137 030122 JMP 2$ ;AND GO BACK TO DO THIS ADDRESS
5664
5665
5666 030554 63$: ENDSLW
5667 030554 L10044: TRAP C$ESUB
5668 030554 104403
5669 030556 ENDTST
5670 030556 L10042: TRAP C$SETST
5671 030556 104401
5672
5673 ;*****
5674 ; XMINIT -- SUBROUTINE TO INITIALIZE EXTENDED MEMORY (ALIAS: MEMORY MANAGEMENT
5675 ; UNIT) HARDWARE REGISTERS.
5676 ;*****
5677
5678
5679 030560 010046 XMINIT: MOV R0,-(SP) ;SAVE WORKING REGISTERS
5680 030562 010146 MOV R1,-(SP)
5681 030564 010346 MOV R3,-(SP)
5682 030566 013737 000004 031122 MOV #4,XM4HOL ;SETUP #4 TRAP VALUE (JUST IN CASE)
5683
5684 030574 012700 077406 MOV #77406,R0 ;'PDR' INITIALIZATION VALUE
5685 ; 774 = FULL PAGE ACCESS
5686 ; 0 = UPWARD EXPANSION
5687 ; 6 = RESIDENT READ/WRITE

```

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TEST 7 -- NPR EXTENDED ADDRESS BIT TEST

5688 030600 012701 172300
5689 030604 012703 000010
5690 030610 010021
5691 030612 077302
5692
5693
5694 030614 005000
5695 030616 012701 172340
5696 030622 012703 000007
5697 030626 010021
5698 030630 062700 000200
5699 030634 077304
5700 030636 012721 177600
5701
5702 030642
5703 030642 012746 000007
5704 030646 012746 031124
5705 030652 012746 000250
5706 030656 012746 000003
5707 030662 104437
5708 030664 062706 000010
5709 030670 012603
5710 030672 012601
5711 030674 012600
5712 030676 000207
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5717
5718
5719
5720
5721
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5723
5724
5725 030700 010146
5726
5727 030702 012701 172354
5728 030706 011146
5729 030710 013511
5730 030712 011137 002426
5731 030716 012737 000060 172516
5732 030724 000241
5733 030726 013737 000004 031122
5734 030734 012737 031074 000004
5735 030742 052737 000001 177572
5736 030750 013537 140000
5737 030754 042737 000001 177572
5738 030762 013737 031122 000004
5739 030770 012611
5740
5741 030772 012601
5742 030774 000205
5743

```

MOV #172300,R1 ;ADDRESS OF KPDR0
MOV #8,R3 ;LOOP VALUE -- # OF PDR'S
1$: MOV R0,(R1)+ ;SETUP 1 PDR
SOB R3,1$ ;IF ANOTHER PDR, DO IT TOO
;ELSE, FALL THROUGH & INITIALIZE PAR'S

CLR R0 ;INITIALIZATION VALUE FOR KPAR0
MOV #172340,R1 ;ADDRESS OF KPAR0
MOV #7,R3 ;LOOP VALUE -- ONLY FIRST 7 PAR'S DONE BY LOOP
2$: MOV R0,(R1)+ ;SETUP 1 PAR
ADD #200,R0 ;CALCULATE NEXT PAR'S INITIALIZATION VALUE
SOB R3,2$ ;IF ANOTHEER PAR, DO IT TOO
MOV #177600,(R1)+ ;ELSE, SETUP KPAR7 FOR I/O PAGE ACCESSING

SETVEC #250,#XMINTH,#7 ;SETUP OUR OWN TRAP CATCHER FOR ABORT HANDLING
MOV #7,-(SP)
MOV #XMINTH,-(SP)
MOV #250,-(SP)
MOV #3,-(SP)
TRAP C$SVEC
ADD #10,SP

MOV (SP)+,R3 ;RESTORE CALLER'S REGISTERS
MOV (SP)+,R1
MOV (SP)+,R0
RTS PC ;RETURN

```

```

*****
: XMWRT -- SUBROUTINE TO WRITE ONE WORD INTO AN EXTENDED MEMORY LOCATION.
:
: CALLING SEQUENCE:
:
: JSR R5,XMWRT
: <PRINTER TO HIGH ORDER BITS OF ADDRESS IN 'PAR' FORMAT>
: <POINTER TO DATA TO BE WRITTEN>
:
*****

```

```

XMWRT: MOV R1,-(SP) ;SAVE REGISTER(S)

MOV #172354,R1 ;ADDRESS OF KPAR6
MOV (R1)-,(SP) ;SAVE CURRENT KPAR6 VALUE
MOV @R5+,(R1) ;SETUP 'PAR' FOR THIS WRITE
MOV (R1),TMP6 ;SAVE ADDRESS FOR ERROR MESSAGE
MOV #BIT4+BITS,@#172516 ;ENABLE 22 BIT & I/O PAGE ADDRESSING IN SR3
CLC ;CLEAR OUR ERROR FLAG
MOV @#4,XM4HOL ;* SETUP TRAP CATCHER @4 (BECAUSE OF MAPPING)
; *
MOV #XM4INT,@#4
BIS #1,@#177572 ;ENABLE MEMORY MANAGEMENT
MOV @R5+,@#140000 ;WRITE ONE WORD IN THE SPECIFIED PAGE
BIC #1,@#177572 ;TURN OFF MEMORY MANAGEMENT
MOV XM4HOL,@#4 ;* RESTORE SUPERVISOR TRAP VECTOR @4
MOV (SP)+,(R1) ;RESTORE KPAR6

MOV (SP)+,R1 ;RESTORE CALLER'S REGISTER(S)
RTS R5 ;RETURN

```

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TEST 7 -- NPR EXTENDED ADDRESS BIT TEST

```

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5756 030776 010146
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5758 031000 012701 172354
5759 031004 011146
5760 031006 013511
5761 031010 011137 002426
5762 031014 012737 000060 172516
5763 031022 000241
5764 031024 013737 000004 031122
5765 031032 012737 031074 000004
5766 031040 052737 000001 177572
5767 031046 013735 140000
5768 031052 042737 000001 177572
5769 031060 013737 031122 000004
5770 031066 012611
5771
5772 031070 012601
5773 031072 000205
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5779 031074 042737 000001 177572
5780 031102 013737 031122 000004
5781 031110 000240
5782 031112 000240
5783 031114 000240
5784 031116 000177 146662
5785 031122 000000
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5791 031124
5792 031124
5793 031124 010037 002372
5794 031130 010137 002374
5795 031134 010237 002376
5796 031140 010337 002400
5797 031144 010437 002402
5798 031150 010537 002404
5799 031154 016637 000002 002406

```

```

*****
: XMRDAD -- SUBROUTINE TO READ FROM AN EXTENDED MEMORY LOCATION.
:
: CALLING SEQUENCE:
:
: JSR    R5,XMRDAD
: <PRINTER TO HIGH ORDER BITS OF ADDRESS IN 'PAR' FORMAT>
: <POINTER TO DATA RECEIVING LOCATION>
*****

```

```

XMRDAD: MOV    R1,-(SP)          ;SAVE REGISTER(S)
        MOV    #172354,R1      ;ADDRESS OF KPAR6
        MOV    (R1),-(SP)      ;SAVE CURRENT KPAR6 VALUE
        MOV    @R5+,(R1)       ;SETUP 'PAR' FOR THIS READ
        MOV    (R1),TMP6       ;SAVE ADDRESS FOR ERROR MESSAGE
        MOV    #BIT4+BITS,@#172516 ;ENABLE 22 BIT & I/O PAGE ADDRESSING IN SR3
        CLC                    ;CLEAR OUR ERROR FLAG
        MOV    @#4,XM4HOL      ;* SETUP TRAP CATCHER @4 (BECAUSE OF MAPPING)
        MOV    #XM4INT,@#4
        BIS    #1,@#177572     ;ENABLE MEMORY MANAGEMENT
        MOV    @#140000,@(R5)+ ;READ ONE WORD IN THE SPECIFIED PAGE
        BIC    #1,@#177572     ;TURN OFF MEMORY MANAGEMENT
        MOV    XM4HOL,@#4      ;* RESTORE SUPERVISOR TRAP VECTOR @4
        MOV    (SP)+,(R1)      ;RESTORE KPAR6
        MOV    (SP)+,R1        ;RESTORE CALLER'S REGISTER(S)
        RTS    R5              ;RETURN

```

```

*****
: HANDLER FOR @LOC 4 TRAP PROCESSING (FOR TESTS 7 & 8)
*****
XM4INT: BIC    #1,@#177572     ;TURN OFF MEMORY MANAGEMENT
        MOV    XM4HOL,@#4      ;* RESTORE SUPERVISOR TRAP VECTOR @4
        NOP
        NOP
        NOP
        JMP    @4              ;NOW JUMP THRU IT !
XM4HOL: 0

```

```

*****
: INTERRUPT HANDLER FOR MEMORY MANAGEMENT ABORT PROCESSING
*****

```

```

BGNSRV  XMINTH
        MOV    R0,REG0          ;SAVE GENERAL REGISTERS
        MOV    R1,REG1
        MOV    R2,REG2
        MOV    R3,REG3
        MOV    R4,REG4
        MOV    R5,REG5
        MOV    2(SP),REG6

```

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TEST 7 -- NPR EXTENDED ADDRESS BIT TEST

```

5800 031162 011637 002410      MOV      (SP),REG7      ;LIKEWISE FOR PC
5801
5802 031166 013737 177572 002412  MOV      @#177572,TMP0  ;SAVE THE MMU'S STATUS/CONTROL REGISTERS
5803 031174 013737 177574 002414  MOV      @#177574,TMP1
5804 031202 013737 177576 002416  MOV      @#177576,TMP2
5805 031210 013737 172516 002420  MOV      @#172516,TMP3
5806
5807 031216 013737 172354 002422  MOV      @#172354,TMP4  ;SAVE KERNEL PAR WE'RE SUPPOSE TO BE USING
5808
5809 031224 013737 172314 002424  MOV      @#172314,TMP5  ;SAVE KERNEL PDR WE'RE SUPPOSE TO BE USING
5810
5811 031232 011537 002430      MOV      (R5),TMP7     ;SAVE DATA READ OR WRITTEN
5812
5813 031236      GTDF      EM27,ERR13  ;QUEUE UP THE MMU ERROR
5814      ;          QUEUE 'DEVICE FATAL' ERROR # 43
5815 031236 012737 000001 002202  MOV      #T.EDF,ERRTYP
5816 031244 012737 000053 002204  MOV      #43,ERRNBR
5817 031252 012737 016424 002206  MOV      #EM27,ERRMSG
5818 031260 012737 010240 002210  MOV      #ERR13,ERRBLK
5819
5820 031266 052766 000001 000002  BIS      #BIT0,2(SP)   ;SET CARRY BIT (AS ERROR FLAG) IN PSW ON STACK
5821
5822 031274      ENDSRV
5823 031274
5824 031274 000002      L10045:
5825      RTI

```


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TEST 8 -- SPECIAL MFG EXTENDED BIT TEST

.SBTTL TEST 8 -- SPECIAL MFG EXTENDED BIT TEST

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031276
031276 032737 000002 002370
031304 001002
031306
031306 104432
031310 001242
031312 004737 003514
031316 103003
031320
031320 104460
031322
031322 104432
031324 001226
031326
031326
031326 104402
031330 004737 030560
031334 005004
031336 005002
031340 016262 032554 032572
031346 005722
031350 020227 000016
031354 001371

```
*****
*
* TEST 8 -- SPECIAL MFG EXTENDED BIT TEST
* THIS TEST WAS DESIGNED SPECIFICALLY TO ALLOW MANUFACTURING TO CHECK THE
* NPRAIX/NPRAOX BITS WITHOUT A FULL 4 M. OF MEMORY.
* IT WILL CHECK THE 12 DMV EXTENDED ADDRESS BITS (6:NPRAIX/6:NPRAOX) ON
* A Q22 SYSTEM IF MEMORY IS PRESENT AT THE FOLLOWING PHYSICAL ADDRESSES:
*
*      17600000      17400000      17200000
*      16600000      15600000      13600000
*      7600000
*
* FIRST SUBTEST :      TEST 'NPRAIX' EXTENDED ADDRESS BITS
* SECOND SUBTEST :      TEST 'NPRAOX' EXTENDED ADDRESS BITS
*
*****
:      BGNTST
:
:      T8::
:
:      BIT      #2,PT.CTL      ;IS THIS A MFG SPECIAL Q22 SYSTEM?
:      BNE      .+6            ;YES: GO START TEST
:      EXIT     TST            ; NO: SKIP THIS TEST
:
:
:      JSR      PC,MSTCLR      ;INIT DMV & ENTER M-LOOP
:      BCC     1$             ;IF NO ERROR, PROCEED WITH TESTING
:      ERROR    ;ELSE, REPORT ERROR
:
:      EXIT     TST            ; & EXIT TEST
:
:      TRAP    C$EXIT         TRAP    C$EXIT
:      .WORD   L10046-        .WORD   L10046-
:
:=====
:== SUBTEST #1 : TEST THE NPR-IN EXTENDED ADDRESS BITS
:=====
1$:      BGNSUB
:
:      T8.1:
:
:      JSR      PC,XMINIT      ;INITIALIZE THE MMU
:      CLR     R4              ;CLEAR INDEX
:
:
:      CLR     R2              ;SETUP EXTENDED MEM BACKGROUND PATTERN
:      MOV     XLOC0(R2),XVAL0(R2) ;(IN XVAL0 => XVAL6)
:
:      TST     (R2)+
:      CMP     R2,#14.
:      BNE     2$
:
:===== MAIN LOOP STARTS HERE =====
:
:*      WRITE XVAL0, XVAL1, XVAL2, ... XVAL6 INTO THE SEVEN SPECIFIC
```

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TEST 8 -- SPECIAL MFG EXTENDED BIT TEST

```

5882
5883
5884
5885 031356 005002
5886 031360 016237 032572 002440 11$: CLR R2 ;CLEAR LOCAL INDEX
5887 031366 016237 032554 002436 MOV XVAL0(R2),TMPB ;SETUP DATA POINTER
5888 031374 006337 002436 MOV XLOC0(R2),TMPA ;SETUP/ADJUST PAR VALUE
5889 031400 006337 002436 ASL TMPA
5890 031404 004537 030700 ASL TMPA
5891 031410 002436 JSR R5,XMWRIT ;WRITE BACKGROUND PATTERN
5892 031412 002440 TMPA ; POINTER TO PAR VALUE
5893 031414 103003 TMPB ; POINTER TO DATA
5894 031416 BCC .+10 ;IF NO ERROR, PROCEED
5895 031416 104460 ERROR ;ELSE, REPORT ERROR
5896 031420 ESCAPE SUB ; & EXIT SUBTEST TRAP C$ERROR
5897 031420 104410 TRAP C$ESCAPE
5898 031422 000352 .WORD L10047-.
5899
5900 031424 005722 TST (R2)+ ;BUMP INDEX
5901 031426 020227 000016 CMP R2,#14. ;ALL 'XLOC' EXTENDED ADDRESSES WRITTEN?
5902 031432 001352 BNE 11$ ; NO: WRITE ANOTHER
5903
5904 ;***** SETUP DMV'S NPR ADDRESSING REGISTERS *****
5905 ;***** (WRITE/READ/VERIFY NPRAIH,NPRAIL,NPRAIX) *****
5906 031434 116437 032555 031470 3$: MOVB XLOC0+1(R4),4$ ;SETUP NPRAIX VALUE.
5907 031442 004537 004054 JSR R5,WRITEI ;POINT NPR REGISTERS TO EXTENDED ADDRESS
5908 031446 000074 NPRAIL
5909 031450 000000 0
5910 031452 004537 004054 JSR R5,WRITEI
5911 031456 000075 NPRAIH
5912 031460 000000 0
5913 031462 004537 004054 JSR R5,WRITEI
5914 031466 000076 NPRAIX
5915 031470 000000 00
5916 031472 004537 003616 4$: JSR R5,READ ;READ BACK THE ADDRESS
5917 031476 000074 NPRAIL
5918 031500 002256 BDATA
5919 031502 004537 003616 JSR R5,READ
5920 031506 000075 NPRAIH
5921 031510 002257 BDATA+1
5922 031512 004537 003616 JSR R5,READ
5923 031516 000076 NPRAIX
5924 031520 002434 TMP9
5925
5926 031522 005737 002256 TST BDATA ;***** NOW CHECK THEM *****
5927 031526 001413 BEQ 6$ ;NPRAIL,NPRAIH=0 ?
5928 031530 005037 002254 CLR GDATA ; YES: TRY CHECKING NPRAIX
5929 031534 012737 000007 002300 MOV #7,REGNUM ; NO: REPORT ERROR...
5930 031542 105737 002256 TSTB BDATA
5931 031546 001020 BNE 7$
5932 031550 005237 002300 INC REGNUM
5933 031554 000415 BR 7$
5934 031556 013737 031470 002254 6$: MOV 4$,GDATA ;SET UP NPRAIX EXPECTED
5935 031564 013737 002434 002256 MOV TMP9,BDATA ;SET UP NPRAIX READ...
5936 031572 023737 002254 002256 CMP GDATA,BDATA ;DOES NPRAIX=EXPECTED ?
5937 031600 001411 BEQ 9$ ; YES: CONTINUE

```

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TEST 8 -- SPECIAL MFG EXTENDED BIT TEST

```

5938 031602 012737 000006 002300      MOV    #6,REGNUM      ; NO: REPORT ERROR
5939 031610      GEDF    EM26A,ERR14
5940      ;          'DEVICE FATAL' ERROR # 44
5941 031610 104455      TRAP    C$ERDF
5942 031612 000054      .WORD   44
5943 031614 016204      .WORD   EM26A
5944 031616 011002      .WORD   ERR14
5945 031620      ESCAPE  SUB
5946 031620 104410      TRAP    C$ESCAPE
5947 031622 000152      .WORD   L10047-.
5948
5949      ;***** SETUP/START/CHECK THE NPR OPERATION (DATA/NPRCTL) *****
5950 031624 112737 000004 002417 9$:  MOVB   #NPRLD,TMP2+1 ;SETUP CONTROL VALUE TO DO NPR-IN
5951
5952 031632 004537 004042      JSR    R5,WRITE      ;PERFORM THE 'EXTENDED' DATA-IN NPR
5953 031636 123004      NPRCTL
5954 031640 002417      TMP2+1
5955 031642 004537 003616      JSR    R5,READ       ;CHECK THE NPR OPERATION
5956 031646 123004      NPRCTL
5957 031650 002416      TMP2
5958 031652 004537 003616      JSR    R5,READ       ;GET THE DATA WE SHOULD HAVE JUST LOADED INTO
5959 031656 123001      NPRDRH              ; THE NPR DATA REGISTERS FROM THE
5960 031660 002257      BDATA+1             ; EXTENDED MEMORY AREA
5961 031662 004537 003616      JSR    R5,READ
5962 031666 123000      NPRDRL
5963 031670 002256      BDATA
5964 031672 132737 000300 002416      BITB   #300,TMP2    ;DID IT ABORT OR HANG?
5965 031700 001414      BEQ    14$          ; NO: GOOD. PROCEED WITH SUBTEST
5966 031702 100005      BPL    10$          ; YES: WHICH ONE?
5967 031704      GEDF    EM27A,ERR12 ;ABORT, REPORT IT AS SUCH.
5968      ;          'DEVICE FATAL' ERROR # 45
5969 031704 104455      TRAP    C$ERDF
5970 031706 000055      .WORD   45
5971 031710 016437      .WORD   EM27A
5972 031712 007456      .WORD   ERR12
5973 031714 000404
5974 031716      BR     12$
5975      10$:  GEDF    EM27B,ERR12 ;AND EXIT
5976      ;HANG, REPORT IT AS SUCH.
5977      ;          'DEVICE FATAL' ERROR # 46
5978      TRAP    C$ERDF
5979      .WORD   46
5980      .WORD   EM27B
5981      .WORD   ERR12
5982
5983
5984      ;***** NOW CHECK DATA READ AGAINST EXPECTED VALUE *****
5985 031732 016437 032554 002254 14$:  MOV    XLOC0(R4),GDATA ;SET UP EXPECTED READ VALUE
5986
5987 031740 023737 002256 002254      CMP    BDATA,GDATA  ;DID WE READ THE TEST DATA USING THE NPR?
5988 031746 001406      BEQ    15$
5989 031750      GEDF    EM27C,ERR12 ; NO: REPORT THE ERROR.
5990      ;          'DEVICE FATAL' ERROR # 47
5991      TRAP    C$ERDF
5992      .WORD   47
5993      .WORD   EM27C

```

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TEST 8 -- SPECIAL MFG EXTENDED BIT TEST

```

5994 031756 007456
5995 031760
5996 031760 104410
5997 031762 000012
5998
5999 031764 005724
6000 031766 020427 000016
6001 031772 001220
6002
6003 031774
6004 031774
6005 031774 104403
6006
6007
6008
6009
6010
6011
6012 031776
6013 031776
6014 031776 104402
6015 032000 004737 030560
6016 032004 005004
6017
6018
6019 032006 005002
6020 032010 012762 125252 032572
6021 032016 005722
6022 032020 020227 000016
6023 032024 001371
6024
6025
6026
6027
6028
6029 032026 005002
6030 032030 016237 032572 002440
6031 032036 016237 032554 002436
6032 032044 006337 002436
6033 032050 006337 002436
6034 032054 004537 030700
6035 032060 002436
6036 032062 002440
6037 032064 103003
6038 032066
6039 032066 104460
6040 032070
6041 032070 104410
6042 032072 000456
6043
6044 032074 005722
6045 032076 020227 000016
6046 032102 001352
6047
6048
6049

ESCAPE SUB ; AND EXIT FROM SUBTEST .WORD ERR12
TRAP C$ESCAPE
.L10047-.

15$: TST (R4)+ ; YES: BUMP INDEX
CMP R4,#14. ; ARE WE DONE W/ALL EXTENDED LOCATIONS
BNE 3$ ; NO: DO NEXT EXTENDED LOCATION

63$: ENDSUB ; YES: END SUBROUTINE
L10047: TRAP C$ESUB

=====
== SUBTEST #2 : TEST THE NPR-OUT EXTENDED ADDRESS BITS
=====
BGNSUB ;TEST THE NPR-OUT USING EXTENDED ADDR. BITS
T8.2: TRAP C$BSUB

=====
MAIN LOOP STARTS HERE
=====
18LP: CLR R2 ;SETUP EXTENDED MEM BACKGROUND PATTERN
3$: MOV #125252,XVAL0(R2) ;(125252 IN XVAL0 => XVAL6)
TST (R2)+ ; THIS IS DONE FOR ERROR REPORTING
CMP R2,#14. ; PURPOSES.
BNE 3$

;* WRITE (USING MMU) XVAL0, XVAL1, XVAL2, ... XVAL6 (IE: 125252) INTO
;* THE SEVEN SPECIFIC EXTENDED ADDRESSES SPECIFIED BY XLOC0 THRU XLOC6
;* (XLOC'S SPECIFY THE UPPER TWO BYTES OF THE 3 BYTE EXTENDED ADDR)

11$: CLR R2 ;CLEAR LOCAL INDEX
MOV XVAL0(R2),TMPB ;SETUP DATA POINTER
MOV XLOC0(R2),TMPA ;SETUP/ADJUST PAR VALUE
ASL TMPA
ASL TMPA
JSR R5,XMWRIT ;WRITE BACKGROUND PATTERN INTO EXTENDED MEMORY
TMPA ; POINTER TO PAR VALUE
TMPB ; POINTER TO DATA (@DATA = 125252)
BCC .+10 ;IF NO ERROR, PROCEED
ERROR ;ELSE, REPORT ERROR
TRAP C$ERROR

ESCAPE SUB ; & EXIT SUBTEST
TRAP C$ESCAPE
.L10050-.

TST (R2)+ ;BUMP INDEX
CMP R2,#14. ;ALL 'XLOC' EXTENDED ADDRESSES WRITTEN?
BNE 11$ ; NO: WRITE ANOTHER

;* WE NOW CHANGE ONE LOCATION IN THE 'XVAL' BACKGROUND TABLE.
;* AFTER OUR DMV NPR OUT, THIS TABLE WILL REPRESENT THE

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TEST 8 -- SPECIAL MFG EXTENDED BIT TEST

```

6050
6051 032104 012764 052525 032572 ;* EXPECTED VALUES OF OUR EXTENDED MEMORY.
6052                                MOV #052525,XVAL0(R4) ;SETUP EXPECTED PATTERN AFTER NPR-OUT
6053                                ;XVAL0 => XVAL6 NOW = EXPECTED PATTERN
6054
6055 ;***** SETUP DMV NPR ADDRESSING REGISTERS *****
6056 ;***** (WRITE/READ/VERIFY NPRAOH,NPRAOL,NPRAOX) *****
6057 032112 116437 032555 032146 MOVB XLOC0+1(R4),5$ ;INIT NPRAOX VALUE
6058 032120 004537 004054 JSR R5,WRITEI ;POINT NPR REGISTERS TO EXTENDED ADDRESS
6059 032124 000070 NPRAOL
6060 032126 000000 0
6061 032130 004537 004054 JSR R5,WRITEI
6062 032134 000071 NPRAOH
6063 032136 000000 0
6064 032140 004537 004054 JSR R5,WRITEI
6065 032144 000072 NPRAOX
6066 032146 000000 5$: 00 ;READ BACK THE ADDRESS
6067 032150 004537 003616 JSR R5,READ
6068 032154 000070 NPRAOL
6069 032156 002256 BDATA
6070 032160 004537 003616 JSR R5,READ
6071 032164 000071 NPRAOH
6072 032166 002257 BDATA+1
6073 032170 004537 003616 JSR R5,READ
6074 032174 000072 NPRAOX
6075 032176 002434 TMP9
6076 032200 005737 002256 TST BDATA ;***** NOW CHECK THEM *****
6077 032204 001413 BEQ 6$ ;NPRAOL,NPRAOH=0 ?
6078 032206 005037 002254 CLR GDATA ; YES: TRY CHECKING NPRAOX
6079 032212 012737 000004 002300 MOV #4,REGNUM ; NO: REPORT ERROR...
6080 032220 105737 002256 TSTB BDATA
6081 032224 001020 BNE 7$
6082 032226 005237 002300 INC REGNUM
6083 032232 000415 BR 7$
6084 032234 013737 032146 002254 6$: MOV 5$,GDATA ;SET UP NPRAOX EXPECTED
6085 032242 013737 002434 002256 MOV TMP9,BDATA ;SET UP NPRAOX READ...
6086 032250 023737 002254 002256 CMP GDATA,BDATA ;DOES NPRAOX=EXPECTED ?
6087 032256 001411 BEQ 9$ ; YES: CONTINUE
6088 032260 012737 000003 002300 MOV #3,REGNUM ; NO: REPORT ERROR
6089 032266 7$: GEDF EM26A,ERR14
6090 ; 'DEVICE FATAL' ERROR # 48
6091 032266 104455 TRAP CSERDF
6092 032270 000060 .WORD 48
6093 032272 016204 .WORD EM26A
6094 032274 011002 .WORD ERR14
6095 032276 ESCAPE SUB
6096 032276 104410 TRAP CSERDF
6097 032300 000250 .WORD L10050-
6098
6099 ;***** SETUP/START/CHECK THE NPR OPERATION (DATA/NPRCTL) *****
6100 032302 012737 052525 002254 9$: MOV #052525,GDATA ;DATA DMV WILL NPR TO TOP LOC
6101
6102 ;***** LOAD (DMV) TEST PATTERN *****
6103 032310 004537 004042 JSR R5,WRITE ;LOAD UP THE TEST PATTERN TO BE
6104 032314 123001 NPRDRH ;WRITTEN INTO EXTENDED MEMORY BY
6105 032316 002255 GDATA+1 ;THE DMV

```

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TEST 8 -- SPECIAL MFG EXTENDED BIT TEST

```

6106 032320 004537 004042      JSR    R5,WRITE
6107 032324 123000              NPRDRL
6108 032326 002254              GDATA
6109
6110                               ;***** PERFORM/CHECK NPR OPERATION (BUT NOT DATA) *****
6111 032330 112737 000044 002417  MOVB   #NPRDL,TMP2+1 ;SETUP CONTROL VALUE TO DO NPR-OUT
6112
6113 032336 004537 004042      JSR    R5,WRITE ;PERFORM THE 'EXTENDED' DATA-OUT NPR
6114 032342 123004              NPRCTL
6115 032344 002417              TMP2+1
6116 032346 004537 003616      JSR    R5,READ ;CHECK THE NPR OPERATION
6117 032352 123004              NPRCTL
6118 032354 002416              TMP2
6119 032356 132737 000300 002416  BITB   #300,TMP2 ;DID IT ABORT OR HANG?
6120 032364 001414              BEQ    14$ ;NO, GOOD. PROCEED WITH SUBTEST
6121 032366 100005              BPL    10$ ;YES, WHICH ONE?
6122 032370                      GEDF   EM27A,ERR12 ;ABORT, REPORT IT AS SUCH.
6123                               ; 'DEVICE FATAL' ERROR # 49
6124 032370 104455                      TRAP   C$ERDF
6125 032372 000061                      .WORD 49
6126 032374 016437                      .WORD EM27A
6127 032376 007456                      .WORD ERR12
6128 032400 000404
6129 032402                      BR     12$ ;AND EXIT
6130                               GEDF   EM27B,ERR12 ;HANG, REPORT IT AS SUCH.
6131                               ; 'DEVICE FATAL' ERROR # 50
6132 032402 104455                      TRAP   C$ERDF
6133 032404 000062                      .WORD 50
6134 032406 016461                      .WORD EM27B
6135 032410 007456                      .WORD ERR12
6136 032412 104410
6137 032414 000134                      ESCAPE SUB ; AND EXIT SUBTEST
6138                               TRAP   C$ESCAPE
6139                               .WORD L10050-.
6140                               ;***** READ EXTENDED MEM INTO LOCAL RAM (RXVAL0-6) *****
6141 032416 005002              14$: CLR    R2 ;CLEAR LOCAL INDEX
6142 032420 016237 032610 002440 15$: MOV   RXVAL0(R2),TMPB ;SETUP DATA POINTER
6143 032426 016237 032554 002436  MOV   XLOC0(R2),TMPA ;SETUP/ADJUST PAR VALUE
6144 032434 006337 002436      ASL   TMPA
6145 032440 006337 002436      ASL   TMPA
6146 032444 004537 030776      JSR    R5,XMREAD ;READ EXTENDED MEM BACKGROUND PATTERN
6147 032450 002436              TMPA ; POINTER TO PAR VALUE
6148 032452 002440              TMPB ; POINTER TO DATA STORAGE
6149 032454 103003              BCC   .+10 ;IF NO ERROR, PROCEED
6150 032456 104460              ERROR ;ELSE, REPORT IT
6151 032460                      ESCAPE TST ; AND EXIT ENTIRE TEST!
6152 032460 104410                      TRAP   C$ERROR
6153 032462 000070                      .WORD C$ESCAPE
6154 032464 062702 000002              ADD   #2,R2 ;BUMP INDEX
6155 032470 020227 000014              CMP   R2,#12. ;ALL 'XLOC' EXTENDED ADDRESSES READ?
6156 032474 001351              BNE   15$ ; NO: READ ANOTHER
6157
6158                               ;***** NOW CHECK EXPECTED VS. ACTUAL EXT. MEM VALUES *****
6159 032476 005002              16$: CLR   R2
6160 032500 026262 032610 032572  CMP   RXVAL0(R2),XVAL0(R2)
6161 032506 001406              BEQ   17$

```

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TEST 8 -- SPECIAL MFG EXTENDED BIT TEST

```

6162 032510          GEDF   EM60N,ERR60
6163
6164 032510 104455          ;      'DEVICE FATAL' ERROR # 51
6165 032512 000063          TRAP   C$ERDF
6166 032514 021711          .WORD 51
6167 032516 011720          .WORD EM60N
6168 032520          ESCAPE SUB          .WORD ERR60
6169 032520 104410          TRAP   C$ESCAPE
6170 032522 000026          .WORD L10050-.
6171 032524 005722          17$:  TST   (R2)+      ;BUMP LOCAL INDEX
6172 032526 020227 000016  CMP   R2,#14.      ;ALL VALUES CHECKED ?
6173 032532 001362          BNE   16$
6174
6175 032534 005724          20$:  TST   (R4)+      ; YES: BUMP INDEX
6176 032536 020427 000016  CMP   R4,#14.      ;ARE WE DONE W/ALL EXTENDED LOCATIONS
6177 032542 001402          BEQ   63$          ; YES: END
6178 032544 000137 032006  JMP   T8LP          ; NO: GO DO SOME MORE
6179 032550          63$:  ENDSUB
6180 032550          L10050:
6181 032550 104403          TRAP   C$ESUB
6182 032552          MFEND:  ENDTST
6183 032552          L10046:
6184 032552 104401          TRAP   C$ETST
6185
6186
6187 032554 037400          XLOC0: 37400      ;ADDRESS 17600000 POINTER
6188 032556 037000          XLOC1: 37000      ;ADDRESS 17400000 POINTER
6189 032560 036400          XLOC2: 36400      ;ADDRESS 17200000 POINTER
6190 032562 035400          XLOC3: 35400      ;ADDRESS 16600000 POINTER
6191 032564 033400          XLOC4: 33400      ;ADDRESS 15600000 POINTER
6192 032566 027400          XLOC5: 27400      ;ADDRESS 13600000 POINTER
6193 032570 017400          XLOC6: 17400      ;ADDRESS 07600000 POINTER
6194
6195 032572 000000          XVAL0: 0
6196 032574 000000          XVAL1: 0
6197 032576 000000          XVAL2: 0
6198 032600 000000          XVAL3: 0
6199 032602 000000          XVAL4: 0
6200 032604 000000          XVAL5: 0
6201 032606 000000          XVAL6: 0
6202
6203 032610 000000          RXVAL0: 0
6204 032612 000000          RXVAL1: 0
6205 032614 000000          RXVAL2: 0
6206 032616 000000          RXVAL3: 0
6207 032620 000000          RXVAL4: 0
6208 032622 000000          RXVAL5: 0
6209 032624 000000          RXVAL6: 0

```

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TEST 9 -- Q-BUS INTERRUPT 'A' & 'B' SELECTION

.SBTTL TEST 9 -- Q-BUS INTERRUPT 'A' & 'B' SELECTION

TEST 9 -- Q-BUS INTERRUPT 'A' & 'B' SELECTION

THIS TEST CONTAINS SUBTESTS IN WHICH A SEQUENCE OF STEPS IS PERFORMED. IN GENERAL, EACH SUBTEST PERFORMS THE FOLLOWING:

1. INTERRUPTS ARE DISABLED FOR BOTH 'A' & 'B'
2. THE INTERRUPT REQUEST REGISTER IS WRITTEN INTO
3. A TEST IS MADE TO BE SURE THAT NEITHER INTERRUPT OCCURS
4. BOTH INTERRUPTS ARE ENABLES
5. A TEST IS MADE TO BE SURE THAT IF AN INTERRUPT IS EXPECTED, IT IS RECEIVED AND IF IT ISN'T EXPECTED IT DOESN'T HAPPEN.

ALL TESTING IS DONE HERE WITH THE PROCESSOR'S PRIORITY SET AT 0.

BGNTST

```

JSR    PC,MSTCLR    ;ISSUE MASTER CLEAR & ENTER MAINT. LOOP
BCC    1$           ;IF NO ERROR, CONTINUE
ERROR  ;ELSE, REPORT IT AND
EXIT   TST          ;EXIT THIS TEST
TRAP   C$ERROR
TRAP   C$EXIT
WORD  L10051-

```

TEST FOR NO INTERRUPT WHEN ENABLED

1. DISABLE BOTH INTERRUPTS
2. ASSERT BOTH REQUEST BITS TO 1
3. CHECK FOR NO 'A' INTERRUPT
4. CHECK FOR NO 'B' INTERRUPT
5. ENABLE BOTH INTERRUPTS
6. CHECK FOR NO 'A' INTERRUPT
7. CHECK FOR NO 'B' INTERRUPT

1\$: BGNSUB

```

MOV    #-1,INTWCH  ;TELL BOTH HANDLERS TO 'WATCH' FOR INTERRUPTS
CLR    INTFLG      ;CLEAR BOTH INTERRUPT FLAGS
TRAP   C$BSUB

```

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032626
032626 004737 003514
032632 103003
032634 104460
032636 104432
032640 001272

032642
032642 104402
032644 012737 177777 002274
032652 005037 002272

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TEST 9 -- Q-BUS INTERRUPT 'A' & 'B' SELECTION

```

6266 032656 112777 000000 147434      MOVB    #0, @BSELO      ;DISABLE BOTH INTERRUPTS
6267 032664 004537 004054      JSR     R5,WRITEI      ;LOAD THE INTERRUPT CONTROL REGISTER WITH
6268 032670 123005                    IRQREG                ; BOTH BITS SET. THIS SHOULD NOT CAUSE
6269 032672 000006                    !RQA!IRQB            ; AN INTERRUPT AT EITHER LEVEL
6270 032674 103003                    BCC     30$           ;IF AN ERROR OCCURED,
6271 032676                    ERROR                ;REPORT IT &
6272 032676 104460                    ESCAPE  TST          ; QUIT                      TRAP    C$ERROR
6273 032700                    .WORD L10051-        .WORD  C$ESCAPE
6274 032700 104410                    .WORD L10051-        .WORD  C$ESCAPE
6275 032702 001230                    .WORD L10051-        .WORD  L10051-
6276
6277 032704 105737 002272      30$:  TSTB    INTFLG      ;DID AN 'A' INTERRUPT OCCUR?
6278 032710 001407                    BEQ     5$           ;NO, GOOD. GO TEST THE 'B' INTERRUPT
6279 032712 012737 000001 002254      MOV     #1,GDATA      ;YES, TELL ERROR HANDLER WHAT WE HAD DC
6280 032720                    GEDF   EM34,ERR1     ;REPORT THE UNEXPECTED INTERRUPT
6281                    ; 'DEVICE FATAL' ERROR # 52
6282 032720 104455                    .WORD 52            TRAP    C$ERDF
6283 032722 000064                    .WORD EM34          .WORD  52
6284 032724 016526                    .WORD ERR1         .WORD  EM34
6285 032726 006212                    .WORD ERR1         .WORD  ERR1
6286
6287 032730 105737 002273      5$:   TSTB    INTFLG+1    ;DID A 'B' INTERRUPT OCCUR?
6288 032734 001407                    BEQ     6$           ;NO, GOOD. NOW TRY LETTING ONE THROUGH
6289 032736 012737 000002 002254      MOV     #2,GDATA      ;YES, TELL ERROR HANDLER WHAT WE HAD DONE
6290 032744                    GEDF   EM34B,ERR1   ;REPORT THE UNEXPECTED INTERRUPT
6291                    ; 'DEVICE FATAL' ERROR # 53
6292 032744 104455                    .WORD 53            TRAP    C$ERDF
6293 032746 000065                    .WORD EM34B        .WORD  53
6294 032750 016540                    .WORD ERR1         .WORD  EM34B
6295 032752 006212                    .WORD ERR1         .WORD  ERR1
6296
6297 032754 005037 002272      6$:   CLR     INTFLG      ;CLEAR BOTH INTERRUPT FLAGS
6298 032760 112777 000021 147332      MOVB   #IENBA:IENBB,@BSELO ;ENABLE BOTH INTERRUPTS
6299 032766 012703 001000      MOV     #1000,R3      ;GIVE THE INTERRUPT SOME TIME TO HAPPEN
6300 032772 077301                    SOB     R3            ; BY SITTING HERE FOR A WHILE
6301 032774 105737 002272      TSTB   INTFLG        ;DID AN 'A' INTERRUPT OCCUR?
6302 033000 001407                    BEQ     7$           ;NO, GOOD. GO TEST THE 'B' INTERRUPT
6303 033002 012737 000003 002254      MOV     #3,GDATA      ;YES, TELL ERROR HANDLER WHAT WE HAD DONE
6304 033010                    GEDF   EM34,ERR1   ;REPORT THE UNEXPECTED INTERRUPT
6305                    ; 'DEVICE FATAL' ERROR # 54
6306 033010 104455                    .WORD 54            TRAP    C$ERDF
6307 033012 000066                    .WORD EM34          .WORD  54
6308 033014 016526                    .WORD ERR1         .WORD  EM34
6309 033016 006212                    .WORD ERR1         .WORD  ERR1
6310
6311 033020 105737 002273      7$:   TSTB    INTFLG+1    ;DID A 'B' INTERRUPT OCCUR?
6312 033024 001407                    BEQ     8$           ;NO, GOOD. NOW TRY LETTING ONE THROUGH
6313 033026 012737 000004 002254      MOV     #4,GDATA      ;YES, TELL ERROR HANDLER WHAT WE HAD DONE
6314 033034                    GEDF   EM34B,ERR1   ;REPORT THE UNEXPECTED INTERRUPT
6315                    ; 'DEVICE FATAL' ERROR # 55
6316 033034 104455                    .WORD 55            TRAP    C$ERDF
6317 033036 000067                    .WORD EM34B        .WORD  55
6318 033040 016540                    .WORD ERR1         .WORD  EM34B
6319 033042 006212                    .WORD ERR1         .WORD  ERR1
6320
6321 033044      8$:   ENDSUB

```

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TEST 9 -- Q-BUS INTERRUPT 'A' & 'B' SELECTION

L10052: TRAP C\$ESUB

6322 033044
6323 033044 104403
6324
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6342

```

-----
TEST FOR 'A' INTERRUPT WHEN ENABLED
1. DISABLE BOTH INTERRUPTS
2. ASSERT 'B' REQUEST BIT TO 1:
   DISABLING 'B' & FORCING 'A'
3. CHECK FOR NO 'A' INTERRUPT
4. CHECK FOR NO 'B' INTERRUPT
5. ENABLE BOTH INTERRUPTS
6. CHECK FOR 'A' INTERRUPT
7. CHECK FOR NO 'B' INTERRUPT

```

6343 033046
6344 033046
6345 033046 104402
6346 033050 012737 177777 002274
6347 033056 005037 002272
6348 033062 112777 000000 147230
6349 033070 004537 004054
6350 033074 123005
6351 033076 000002
6352 033100 103003
6353 033102
6354 033102 104460
6355 033104
6356 033104 104410
6357 033106 001024
6358

```

BGNSUB
T9.2:
MOV # -1,INTWCH ; TELL BOTH HANDLERS TO 'WATCH' FOR INTERRUPTS
CLR INTFLG ; CLEAR BOTH INTERRUPT FLAGS
MOVB #0,@BSELO ; DISABLE INTERRUPTS AGAIN
JSR R5,WRITEI ; CAUSE AN INTERRUPT PENDING ON 'A'
                ; BUT NOT ON 'B'
IRQREG
IRQB
BCC 31$ ; IF AN ERROR OCCURED,
ERROR ; REPORT IT &
ESCAPE TST ; QUIT
                TRAP C$ERROR
                .WORD C$ESCAPE
                .WORD L10051-

```

6359 033110 105737 002272 31\$: TSTB INTFLG
6360 033114 001407 BEQ 10\$
6361 033116 012737 000005 002254 MOV #5,GDATA
6362 033124 GEDF EM34,ERR1
6363
6364 033124 104455
6365 033126 000070
6366 033130 016526
6367 033132 006212
6368

```

31$: TSTB INTFLG ; DID AN 'A' INTERRUPT OCCUR?
BEQ 10$ ; NO, GOOD. GO TEST THE 'B' INTERRUPT
MOV #5,GDATA ; YES, TELL ERROR HANDLER WHAT WE HAD DONE
GEDF EM34,ERR1 ; REPORT THE UNEXPECTED INTERRUPT
                ; 'DEVICE FATAL' ERROR # 56
                TRAP C$ERDF
                .WORD 56
                .WORD EM34
                .WORD ERR1

```

6369 033134 105737 002273 10\$: TSTB INTFLG+1
6370 033140 001407 BEQ 11\$
6371 033142 012737 000006 002254 MOV #6,GDATA
6372 033150 GEDF EM34B,ERR1
6373
6374 033150 104455
6375 033152 000071
6376 033154 016540
6377 033156 006212

```

10$: TSTB INTFLG+1 ; DID A 'B' INTERRUPT OCCUR?
BEQ 11$ ; NO, GOOD. NOW TRY LETTING ONE THROUGH
MOV #6,GDATA ; YES, TELL ERROR HANDLER WHAT WE HAD DONE
GEDF EM34B,ERR1 ; REPORT THE UNEXPECTED INTERRUPT
                ; 'DEVICE FATAL' ERROR # 57
                TRAP C$ERDF
                .WORD 57
                .WORD EM34B
                .WORD ERR1

```

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TEST 9 -- Q-BUS INTERRUPT 'A' & 'B' SELECTION

```

6378
6379 033160 005037 002272 11$: CLR INTFLG ;CLEAR BOTH INTERRUPT FLAGS
6380 033164 112777 000021 147126 MOVB #IENBA,IENBB,IBSELO ;ENABLE BOTH INTERRUPTS
6381 033172 012703 001000 MOV #1000,R3 ;GIVE THE INTERRUPT SOME TIME TO HAPPEN
6382 033176 077301 SOB R3 ; BY SITTING HERE FOR A WHILE
6383 033200 105737 002272 TSTB INTFLG ;DID AN 'A' INTERRUPT OCCUR?
6384 033204 001007 BNE 12$ ;YES, GOOD. GO TEST THE 'B' INTERRUPT
6385 033206 012737 000007 002254 MOV #7,GDATA ;NO, TELL ERROR HANDLER WHAT WE HAD DONE
6386 033214 GEDF EM35,ERR1 ;REPORT MISSING INTERRUPT ON 'ENABLE'
6387 ; 'DEVICE FATAL' ERROR # 58
6388 033214 104455 TRAP CSERDF
6389 033216 000072 .WORD 58
6390 033220 016552 .WORD EM35
6391 033222 006212 .WORD ERR1
6392
6393 033224 105737 002273 12$: TSTB INTFLG+1 ;DID A 'B' INTERRUPT OCCUR?
6394 033230 001407 BEQ 13$ ;NO, GOOD. NOW TRY HITTING THE 'B' INTERRUPT
6395 033232 012737 000010 002254 MOV #8,GDATA ;YES, TELL ERROR HANDLER WHAT WE HAD DONE
6396 033240 GEDF EM34B,ERR1 ;REPORT THE UNEXPECTED INTERRUPT
6397 ; 'DEVICE FATAL' ERROR # 59
6398 033240 104455 TRAP CSERDF
6399 033242 000073 .WORD 59
6400 033244 016540 .WORD EM34B
6401 033246 006212 .WORD ERR1
6402
6403 033250 13$: ENDSUB
6404 033250
6405 033250 104403 L10053: TRAP CS$SUB
6406
6407 -----
6408 TEST FOR 'B' INTERRUPT WHEN ENABLED
6409
6410 1. DISABLE BOTH INTERRUPTS
6411
6412 2. ASSERT 'A' REQUEST BIT TO 1:
6413 DISABLING 'A' & FORCING 'B'
6414
6415 3. CHECK FOR NO 'A' INTERRUPT
6416
6417 4. CHECK FOR NO 'B' INTERRUPT
6418
6419 5. ENABLE BOTH INTERRUPTS
6420
6421 6. CHECK FOR NO 'A' INTERRUPT
6422
6423 7. CHECK FOR 'B' INTERRUPT
6424
6425 BGNSUB
6426
6427 T9.3: TRAP CS$SUB
6428 033252 104402 MOV #-1,INTWCH ;TELL BOTH HANDLERS TO 'WATCH' FOR INTERRUPTS
6429 033254 012737 177777 002274 CLR INTFLG ;CLEAR BOTH INTERRUPT FLAGS
6430 033262 005037 002272 MOVB #0,IBSELO ;DISABLE INTERRUPTS AGAIN
6431 033274 004537 004054 JSR R5,WRITEI ;CAUSE AN INTERRUPT PENDING ON 'B'
6432 033300 123005 ; BUT NOT ON 'A'
6433 033302 000004 IRQREG
IRQA
    
```

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TEST 9 -- Q-BUS INTERRUPT 'A' & 'B' SELECTION

```

6434 033304 103003          BCC 32$          ;IF AN ERROR OCCURED,
6435 033306                ERROR                ;REPORT IT &
6436 033306 104460                ESCAPE TST          ; EXIT
6437 033310                TRAP C$ERROR
6438 033310 104410                TRAP C$ESCAPE
6439 033312 000620                .WORD L10051-
6440
6441 033314 105737 002272 32$:  TSTB INTFLG      ;DID AN 'A' INTERRUPT OCCUR?
6442 033320 001407          BEQ 14$          ;NO, GOOD. GO TEST THE 'B' INTERRUPT
6443 033322 012737 000011 002254  MOV #9.,GDATA ;YES, TELL ERROR HANDLER WHAT WE HAD DONE
6444 033330                GEDF EM34,ERR1 ;REPORT THE UNEXPECTED INTERRUPT
6445                                ; 'DEVICE FATAL' ERROR # 60
6446 033330 104455                TRAP C$ERDF
6447 033332 000074                .WORD 60
6448 033334 016526                .WORD EM34
6449 033336 006212                .WORD ERR1
6450
6451 033340 105737 002273 14$:  TSTB INTFLG+1    ;DID A 'B' INTERRUPT OCCUR?
6452 033344 001407          BEQ 15$          ;NO, GOOD. NOW TRY LETTING ONE THROUGH
6453 033346 012737 000012 002254  MOV #10.,GDATA ;YES, TELL ERROR HANDLER WHAT WE HAD DONE
6454 033354                GEDF EM34B,ERR1 ;REPORT THE UNEXPECTED INTERRUPT
6455                                ; 'DEVICE FATAL' ERROR # 61
6456 033354 104455                TRAP C$ERDF
6457 033356 000075                .WORD 61
6458 033360 016540                .WORD EM34B
6459 033362 006212                .WORD ERR1
6460
6461 033364 005037 002272 15$:  CLR INTFLG      ;CLEAR BOTH INTERRUPT FLAGS
6462 033370 112777 000021 146722  MOVB #IENBA!IENBB, @BSSEL0 ;ENABLE BOTH INTERRUPTS
6463 033376 012703 001000          MOV #1000,R3 ;GIVE THE INTERRUPT SOME TIME TO HAPPEN
6464 033402 077301          SOB R3, ; BY SITTING HERE FOR A WHILE
6465 033404 105737 002272          TSTB INTFLG    ;DID AN 'A' INTERRUPT OCCUR?
6466 033410 001407          BEQ 16$          ;NO, GOOD. GO TEST THE 'B' INTERRUPT
6467 033412 012737 000013 002254  MOV #11.,GDATA ;YES, TELL ERROR HANDLER WHAT WE HAD DONE
6468 033420                GEDF EM34,ERR1 ;REPORT THE UNEXPECTED INTERRUPT
6469                                ; 'DEVICE FATAL' ERROR # 62
6470 033420 104455                TRAP C$ERDF
6471 033422 000076                .WORD 62
6472 033424 016526                .WORD EM34
6473 033426 006212                .WORD ERR1
6474
6475 033430 105737 002273 16$:  TSTB INTFLG+1    ;DID A 'B' INTERRUPT OCCUR?
6476 033434 001007          BNE 17$          ;YES, GOOD. NOW TRY HITTING THE 'B' INTERRUPT
6477 033436 012737 000014 002254  MOV #12.,GDATA ;NO, TELL ERROR HANDLER WHAT WE HAD DONE
6478 033444                GEDF EM35B,ERR1 ;REPORT MISSING INTERRUPT ON 'ENABLE'
6479                                ; 'DEVICE FATAL' ERROR # 63
6480 033444 104455                TRAP C$ERDF
6481 033446 000077                .WORD 63
6482 033450 016573                .WORD EM35B
6483 033452 006212                .WORD ERR1
6484
6485 033454 17$:  ENDSUB
6486 033454                L10054:
6487 033454 104403                TRAP C$ESUB
6488
6489

```

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TEST 9 -- Q-BUS INTERRUPT 'A' & 'B' SELECTION

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033456
033456
033456 104402
033460 012737 177777 002274
033466 005037 002272
033472 112777 000000 146620
033500 004537 004054
033504 123005
033506 000000
033510 103003
033512
033512 104460
033514
033514 104410
033516 000414
033520 105737 002272
033524 001407
033526 012737 000015 002254
033534
033534 104455
033536 000100
033540 016526
033542 006212
033544 105737 002273
033550 001407
033552 012737 000016 002254
033560
033560 104455
033562 000101
033564 016540
033566 006212
033570 005037 002272
033574 112777 000021 146516
033602 012703 001000

- ```

: TEST FOR 'A' INTERRUPT BUT NO 'B' WHEN BOTH ENABLED & FORCED
:
: 1. DISABLE BOTH INTERRUPTS
:
: 2. ASSERT BOTH 'A' & 'B' REQUEST BITS TO 0:
: FORCING BOTH 'A' & 'B' (BUT ONLY GETTING 'A')
:
: 3. CHECK FOR NO 'A' INTERRUPT
:
: 4. CHECK FOR NO 'B' INTERRUPT
:
: 5. ENABLE BOTH INTERRUPTS
:
: 6. CHECK FOR 'A' INTERRUPT
:
: 7. CHECK FOR NO 'B' INTERRUPT

```

BGNSUB

T9.4:

```

 TRAP CSBSUB
MOV #-1,INTWCH ;TELL BOTH HANDLERS TO 'WATCH' FOR INTERRUPTS
CLR INTFLG ;CLEAR BOTH INTERRUPT FLAGS
MOVB #0,@BSILO ;DISABLE INTERRUPTS AGAIN
JSR R5,WRITEI ;CAUSE AN INTERRUPT PENDING ON BOTH 'A' & 'B'
IRQREG
0
BCC 31$;IF AN ERROR OCCURED,
ERRCP ;REPORT IT &
 TRAP CSERROR
ESCAPE TST ; QUIT
 TRAP CS$ESCAPE
 .WORD L10051-
31$: TSTB INTFLG ;DID AN 'A' INTERRUPT OCCUR?
 BEQ 10$;NO, GOOD. GO TEST THE 'B' INTERRUPT
 MOV #13,,GDATA ;YES, TELL ERROR HANDLER WHAT WE HAD DONE
 GEDF EM34,ERR1 ;REPORT THE UNEXPECTED INTERRUPT
 ; 'DEVICE FATAL' ERROR # 64
 TRAP CSERDF
 .WORD 64
 .WORD EM34
 .WORD ERR1
10$: TSTB INTFLG+1 ;DID A 'B' INTERRUPT OCCUR?
 BEQ 11$;NO, GOOD. NOW TRY LETTING ONE THROUGH
 MOV #14,,GDATA ;YES, TELL ERROR HANDLER WHAT WE HAD DONE
 GEDF EM34B,ERR1 ;REPORT THE UNEXPECTED INTERRUPT
 ; 'DEVICE FATAL' ERROR # 65
 TRAP CSERDF
 .WORD 65
 .WORD EM34B
 .WORD ERR1
11$: CLR INTFLG ;CLEAR BOTH INTERRUPT FLAGS
 MOVB #IENBA!IENBB,@BSILO ;ENABLE BOTH INTERRUPTS
 MOV #1000,R3 ;GIVE THE INTERRUPT SOME TIME TO HAPPEN

```

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TEST 9 -- Q-BUS INTERRUPT 'A' & 'B' SELECTION

```

6546 033606 077301 SOB R3 ; BY SITTING HERE FOR A WHILE
6547 033610 105737 002272 TSTB INTFLG ; DID AN 'A' INTERRUPT OCCUR?
6548 033614 001007 BNE 12$; YES, GOOD. GO TEST THE 'B' INTERRUPT
6549 033616 012737 000017 002254 MOV #15.,GDATA ; NO, TELL ERROR HANDLER WHAT WE HAD DONE
6550 033624 GEDF EM35,ERR1 ; REPORT MISSING INTERRUPT ON 'ENABLE'
6551 ; 'DEVICE FATAL' ERROR # 66
6552 033624 104455 TRAP C$ERDF
6553 033626 000102 .WORD 66
6554 033630 016552 .WORD EM35
6555 033632 006212 .WORD ERR1
6556
6557 033634 105737 002273 12$: TSTB INTFLG+1 ; DID A 'B' INTERRUPT OCCUR?
6558 033640 001407 BEQ 13$; NO, GOOD. NOW TRY HITTING THE 'B' INTERRUPT
6559 033642 012737 000020 002254 MOV #16.,GDATA ; YES, TELL ERROR HANDLER WHAT WE HAD DONE
6560 033650 GEDF EM34B,ERR1 ; REPORT THE UNEXPECTED INTERRUPT
6561 ; 'DEVICE FATAL' ERROR # 67
6562 033650 104455 TRAP C$ERDF
6563 033652 000103 .WORD 67
6564 033654 016540 .WORD EM34B
6565 033656 006212 .WORD ERR1
6566
6567 033660 13$: ENDSUB
6568 033660
6569 033660 104403 L10055: TRAP C$ESUB
6570
6571
6572 -----
6573 TEST FOR 'A' INTERRUPT WHILE ENABLED
6574 1. ENABLE BOTH INTERRUPTS
6575 2. ASSERT 'B' REQUEST BIT TO 1:
6576 DISABLING 'B' & FORCING 'A'
6577 3. CHECK FOR 'A' INTERRUPT
6578 4. CHECK FOR NO 'B' INTERRUPT
6579 :
6580 :
6581 :
6582 :
6583 BGNSUB
6584 :
6585 :
6586 :
6587 :
6588 :
6589 :
6590 :
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6600 :
6601 :

```

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TEST 9 -- Q-BUS INTERRUPT 'A' &amp; 'B' SELECTION

```

6602 033736 001007 BNE 12$;YES, GOOD. GO TEST THE 'B' INTERRUPT
6603 033740 012737 000023 002254 MOV #19.,GDATA ;NO, TELL ERROR HANDLER WHAT WE HAD DONE
6604 033746 GEDF EM35,ERR1 ;REPORT MISSING INTERRUPT ON 'ENABLE'
6605 ; 'DEVICE FATAL' ERROR # 68
6606 033746 104455 TRAP C$ERDF
6607 033750 000104 .WORD 68
6608 033752 016552 .WORD EM35
6609 033754 006212 .WORD ERR1
6610
6611 033756 105737 002273 12$: TSTB INTFLG+1 ;DID A 'B' INTERRUPT OCCUR?
6612 033762 001407 BEQ 13$;NO, GOOD. NOW TRY HITTING THE 'B' INTERRUPT
6613 033764 012737 000024 002254 MOV #20.,GDATA ;YES, TELL ERROR HANDLER WHAT WE HAD DONE
6614 033772 GEDF EM34B,ERR1 ;REPORT THE UNEXPECTED INTERRUPT
6615 ; 'DEVICE FATAL' ERROR # 69
6616 033772 104455 TRAP C$ERDF
6617 033774 000105 .WORD 69
6618 033776 016540 .WORD EM34B
6619 034000 006212 .WORD ERR1
6620
6621 034002 13$: ENDSUB
6622 034002
6623 034002 104403 L10056: TRAP C$ESUB
6624
6625 -----
6626 TEST FOR 'B' INTERRUPT WHILE ENABLED
6627 :
6628 1. ENABLE BOTH INTERRUPTS
6629 :
6630 2. ASSERT 'A' REQUEST BIT TO 1:
6631 DISABLING 'A' & FORCING 'B'
6632 :
6633 3. CHECK FOR NO 'A' INTERRUPT
6634 :
6635 4. CHECK FOR 'B' INTERRUPT
6636 :
6637 BGNSUB
6638
6639 T9.6:
6640 034004 104402 TRAP C$BSUB
6641 034006 012737 177777 002274 MOV #-1,INTWCH ;TELL BOTH HANDLERS TO 'WATCH' FOR INTERRUPTS
6642 034014 005037 002272 CLR INTFLG ;CLEAR BOTH INTERRUPT FLAGS
6643 034020 112777 000021 146272 MOV #IENBA!IENBB, @BSEL0 ;ENABLE BOTH INTERRUPTS
6644 034026 004537 004054 JSR R5,WRITEI ;CAUSE AN INTERRUPT PENDING ON 'B'
6645 034032 123005 ; BUT NOT ON 'A'
6646 034034 000004 IRQREG
6647 034036 103003 IRQA
6648 034040 104460 BCC 32$;IF AN ERROR OCCURED,
6649 034042 ERROR ;REPORT IT &
6650 034042 104410 ESCAPE TST ; QUIT
6651 034044 000066 TRAP C$ERROR
6652 .WORD L10051-
6653 034046 012703 001000 32$: MOV #1000,R3 ;GIVE THE INTERRUPT SOME TIME TO HAPPEN
6654 034052 077301 SOB R3 ; BY SITTING HERE FOR A WHILE
6655 034054 105737 002272 TSTB INTFLG ;DID AN 'A' INTERRUPT OCCUR?
6656 034060 001407 BEQ 16$;NO, GOOD. GO TEST THE 'B' INTERRUPT
6657 034062 012737 000025 002254 MOV #21.,GDATA ;YES, TELL ERROR HANDLER WHAT WE HAD DONE

```

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TEST 9 -- Q-BUS INTERRUPT 'A' & 'B' SELECTION

```

6658 034070 GEDF EM34,ERR1 ;REPORT THE UNEXPECTED INTERRUPT
6659 ; 'DEVICE FATAL' ERROR # 70
6660 034070 104455 TRAP C$ERDF
6661 034072 000106 .WORD 70
6662 034074 016526 .WORD EM34
6663 034076 006212 .WORD ERR1
6664
6665 034100 105737 002273 16$: TSTB INTFLG+1 ;DID A 'B' INTERRUPT OCCUR?
6666 034104 001007 BNE 17$;YES, GOOD. NOW TRY HITTING THE 'B' INTERRUPT
6667 034106 012737 000026 002254 MOV #22,,GDATA ;NO, TELL ERROR HANDLER WHAT WE HAD DONE
6668 034114 GEDF EM35B,ERR1 ;REPORT MISSING INTERRUPT ON 'ENABLE'
6669 ; 'DEVICE FATAL' ERROR # 71
6670 034114 104455 TRAP C$ERDF
6671 034116 000107 .WORD 71
6672 034120 016573 .WORD EM35B
6673 034122 006212 .WORD ERR1
6674
6675 034124 17$: ENDSUB
6676 034124
6677 034124 104403 L10057: TRAP C$ESUB
6678
6679 034126 005037 002274 CLR INTWCH ;TELL HANDLERS TO STOP WATCHING FOR INTERRUPTS
6680 034132 ENDTST
6681 034132
6682 034132 104401 L10051: TRAP C$ETST

```



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TEST 10 -- BUS RESET WITH DISABLE INIT SET

.SBTTL TEST 10 -- BUS RESET WITH DISABLE INIT SET

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034134  
034134 032737 000001 002316  
034142 001031  
034144 004737 003514  
034150 103003  
034152  
034152 104460  
034154  
034154 104410  
034156 000050  
034160 004537 004054  
034164 123004  
034166 000105  
034170 112777 000377 146130  
034176  
034176 104433  
034200 012703 001000  
034204 077301  
034206 122777 000377 146112  
034214 001404  
034216  
034216 104455  
034220 000110  
034222 016614  
034224 006212

\*\*\*\*\*  
\*  
\* TEST 10 -- BUS RESET WITH DISABLE INIT SET  
\*  
\* A BYTE SELECT REGISTER (BSEL3) IS LOADED WITH 377, DISABLE INIT BIT IS SET  
\* IN THE NPR CONTROL REGISTER, AND A BUS RESET INSTRUCTION IS EXECUTED. THE  
\* PROGRAM THEN CHECKS THAT THE DMV-11 WAS NOT CLEARED, BY CHECKING FOR 377  
\* STILL IN BSEL3  
\*  
\*\*\*\*\*

BGNTST  
T10::  
BIT #BIT0,PFLAG ;IF BUS RESETS ARE NOT ALLOWED,  
BNE 10\$ ; BYPASS THIS TEST  
;ELSE,  
JSR PC,MSTCLR ;INIT DMV & START UP THE MAINT. LOOP  
BCC 1\$ ;IF AN ERROR OCCURED,  
ERROR ;REPORT IT &  
ESCAPE TST ; EXIT TRAP C\$ERROR  
; TRAP C\$ESCAPE  
; .WORD L10060-.  
1\$: JSR R5,WRITEI ;NOW SET 'DISABLE INIT'  
NPRCTL  
DMVDAI!DMVPU!NPRGO  
;THE 'NPRGO' BIT IS SET BECAUSE ASSERTING IT  
;TO A ZERO WOULD KICK OFF AN NPR OPERATION!  
;THE 'DMVPU' BIT MUST ALWAYS BE SET WHENEVER  
;THE NPR-CONTROL REGISTER IS LOADED.  
MOVB #377,@BSEL3 ;THIS REGISTER WILL ONLY GET ALTERED IF THE  
;DMV-11 IS SUCCESSFULLY RESET: THE 'DMVPU'  
;BIT WILL BE CLEARED, THE MICRO-DIAGNOSTIC  
;WILL BE STARTED, AND FINDING 'DMVPU' CLEARED,  
;IT WILL CLEAR ALL BSEL REGISTERS (INCLUDING  
;BSEL3) AND PERFORM THE 17 TESTS IS CONTAINS.  
;OF COURSE, IF THIS ALL HAPPENS, THAN THIS  
;TEST WILL HAVE FAILED!  
BRESET ;THE 'SUPERVISOR' WILL DO A BUS RESET FOR US  
TRAP C\$RESET  
MOV #1000,R3 ;DELAY FOR A BIT SO THE MICRO-DIAG. CAN DO  
SOB R3 ;ITS THING IF IT'S GOING TO  
CMPB #377,@BSEL3 ;IF A FAILURE OCCURED, THIS SHOULD HAVE BEEN  
BEQ 10\$ ;ALTERED BY NOW. IF NOT, ALL'S WELL -- EXIT  
GEDF EM40,ERR1 ;ELSE, 'DISABL INIT' DIDN'T STOP 'BUS RESET'  
; 'DEVICE FATAL' ERROR # 72  
TRAP C\$ERDF  
; .WORD 72  
; .WORD EM40  
; .WORD ERR1

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TEST 10 -- BUS RESET WITH DISABLE INIT SET

6739 034226  
6740 034226  
6741 034226 104401

10\$: ENDTST

L10060: TRAP CSETST

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TEST 11 -- MASTER CLEAR WITH DISABLE INIT SET

.SBTTL TEST 11 -- MASTER CLEAR WITH DISABLE INIT SET

```

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6757 034230
6758 034230 004737 003514
6759 034234 103003
6760 034236
6761 034236 104460
6762 034240
6763 034240 104410
6764 034242 000030
6765 034244 004537 004054
6766 034250 123004
6767 034252 000105
6768
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6770
6771
6772
6773 034254 004737 003514
6774 034260 103004
6775 034262
6776
6777 034262 104455
6778 034264 000111
6779 034266 016671
6780 034270 006212
6781 034272
6782 034272
6783 034272 104401

```

```

:*****
:*
:* TEST 11 -- MASTER CLEAR WITH DISABLE INIT SET
:* THE 'DISABL INIT' BIT IN THE NPR CONTROL REGISTER IS SET AND A MASTER CLEAR
:* IS ISSUED. IF THE MASTER CLEAR SUBROUTINE DETECTS AN ERROR, THE MASTER
:* CLEAR WILL NOT HAVE FUNCTIONED PROPERLY. WHERE THE NORMAL ERROR MESSAGE
:* (QUEUED UP BY 'MASCLR') IS NORMALLY PRINTED, THIS TEST WILL PRINT ITS OWN
:* INSTEAD.
:*****

```

```

: BGNTST
:
: JSR PC,MSTCLR ;INIT DMV & START UP THE MAINT. LOOP
: BCC 1$;IF AN ERROR OCCURED,
: ERROR ;REPORT IT &
:
: ESCAPE TST ; EXIT
:
: JSR R5,WRITEI ;NOW SET 'DISABLE INIT'
: NPRCTL DMVDAI!DMVPU!NPRGO
:
: ;THE 'NPRGO' BIT IS SET BECAUSE ASSERTING IT
: ;TO A ZERO WOULD KICK OFF AN NPR OPERATION!
: ;THE 'DMVPU' BIT MUST ALWAYS BE SET WHENEVER
: ;THE NPR-CONTROL REGISTER IS LOADED.
:
: JSR PC,MSTCLR ;INIT THE DMV & RESTART THE M-LOOP
: BCC 2$;IF AN ERROR OCCURED, IGNORE QUEUED ERROR AND
: GEDF EM41,ERR1 ;REPORT THAT 'DISABL INIT' STOPPED MASTER CLEAR
:
:
: ; 'DEVICE FATAL' ERROR # 73
:
: TRAP C$ERROR
: .WORD 73
: .WORD EM41
: .WORD ERR1
:
: L10061:
: TRAP C$SETST

```

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TEST 12 -- DCOK H LO BIT

.SBTTL TEST 12 -- DCOK H LO BIT

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034364  
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034366  
034366  
034370

000014  
032737 000001 002370  
001002  
104432  
000476  
032737 000001 002316  
001402  
104432  
000462  
012727 000050  
000000  
013727 002116  
000000  
005367 177772  
001375  
005367 177756  
001367

DCOKTS = \$T  
BIT #PU24,PT.CTL  
BNE 1\$  
EXIT TST  
1\$: BIT #BIT0,PFLAG  
BEQ 2\$  
EXIT TST  
2\$: DELAY 40.

\*\*\*\*\*  
\* TEST 12 -- DCOK H LO BIT  
\* DCOK H LO IS SET IN THE NPR CONTROL REGISTER WHICH SHOULD CAUSE A VECTOR TO  
\* THE FIRST INTERRUPT HANDLER WHERE THE VECTOR IS CHANGED TO POINT TO THE  
\* SECOND HANDLER. THIS SECOND HANDLER WILL THEN STALL FOR A WHILE WAITING FOR  
\* THE POWER-UP INTERRUPT WHICH SHOULD KICK US INTO THE SECOND HANDLER. IN  
\* BOTH HANDLERS FLAGS ARE SET TO SAY THAT WE GOT THERE. WHEN WE FINALLY  
\* RETURN TO OUR MAINLINE CODE, WE WILL RESUME THE DELAY FUNCTION WE WERE IN  
\* AND THEN CHECK THE FLAGS.  
\* IN SUBTEST # 1, WE EXPECT THE DMV TO BE RESET.  
\*\*\*\*\*

-----  
: BGNTST  
: T12: :  
: DCOKTS = \$T ;DEFINE TEST # FOR 'INIT' SECTION  
: BIT #PU24,PT.CTL ;IS POWER-UP STRAPPED FOR OPTION 0?  
: BNE 1\$ ;YES, THEN WE CAN DO THIS TEST  
: EXIT TST ;NO, WE CAN'T DO THIS TEST UNLESS IT IS!  
: TRAP C\$EXIT  
: .WORD L10062-.  
: 1\$: BIT #BIT0,PFLAG ;IF BUS RESETS ARE ALLOWED,  
: BEQ 2\$ ; PERFORM THIS TEST  
: EXIT TST ;ELSE, BYPASS IT  
: TRAP C\$EXIT  
: .WORD L10062-.  
: 2\$: DELAY 40. ;DELAY TO PREVENT TST # ROACH  
: MOV #40.,(PC)+  
: .WORD 0  
: MOV LSDLY,(PC)+  
: .WORD 0  
: DEC -6(PC)  
: BNE -4  
: DEC -22(PC)  
: BNE -20

-----  
: SUBTEST #1: DCOK H LO (RESET DMV)  
-----

: BGNSUB ; <====> TEST FOR POWER-DOWN/UP & DMV-11 RESET  
: T12.1: :  
: JSR PC,MSTCLR ;INIT DMV & START UP THE MAINT. LOOP TRAP C\$SUB  
: BCC 3\$ ;IF AN ERROR OCCURED, TRAP C\$SUB  
: ERROR ;REPORT IT &  
: ESCAPE SUB ; EXIT TRAP C\$ERROR  
: TRAP C\$ESCAPE  
: .WORD L10063-.

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TEST 12 -- DCOK H LO BIT

```

6840 034372
6841 034372 012746 000007
6842 034376 012746 034500
6843 034402 012746 000024
6844 034406 012746 000003
6845 034412 104437
6846 034414 062706 000010
6847 034420 112737 177777 002274
6848 034426 105037 002272
6849 034432 152777 000001 145660
6850
6851 034440 010637 002454
6852
6853
6854
6855
6856 034444 012777 177506 145662
6857 034452 012777 123004 145650
6858 034460 112777 000042 145636
6859
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6866
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6869 034466 000001
6870
6871
6872
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6874
6875
6876
6877 034470
6878
6879 034470 104455
6880 034472 000112
6881 034474 016746
6882 034476 006212
6883
6884 034500
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```

```

3$: SETVEC #24,#58,#7 ;SETUP VECTOR FOR POWER FAIL INTERRUPT HANDLER
MOV #7,-(SP)
MOV #58,-(SP)
MOV #24,-(SP)
MOV #3,-(SP)
TRAP C$SVEC
ADD #10,SP

MOVB #-1,INTWCH ;EXPECT AN 'A' INTERRUPT (IF 'DCOK' FAILS!)
CLRB INTFLG ;CLEAR THE FLAG IN CASE WE WANT TO DETECT IT
BISB #IENBA,@BSEL0 ;NOW ENABLE 'A' INTERRUPTS

MOV SP,OLDSP ;SAVE THE STACK POINTER

; SETUP 'DISABL INIT' TO ALLOW BINIT TO RESET THE DMV AND THEN CAUSE
; A POWER-FAIL CONDITION TO BE SIMULATED.

MOV #177400!LSIDCL!DMVPU!NPRGO,@SEL6 ;VALUE TO BE LOADED INTO
MOV #NPRCTL,@SEL4 ;THE NPR CONTROL REGISTER
MOVB #WRILOC!<IRQA*8.>,@SEL2 ;TELL M-LOOP TO WRITE IT & INTERRUPT
;US JUST BEFORE REQUESTING ANOTHER
;M-LOOP COMMAND.

; SETTING 'NPRGO' PREVENTS AN 'NPR' OPERATION FROM OCCURRING. THE HIGH
; ORDER BYTE IS SET TO -1 AND IS USED AS A FLAG -- WHEN THE RESET
; OCCURS, THE SELECT REGISTERS WILL ALL BE CLEARED: WE WILL BE
; LOOKING AT THE RUN BIT TO BE SET AND AT BSEL7 TO BE CLEARED AS POSITIVE
; PROOF THAT THE DMV GOT RESET.

4$: WAIT ;HANG HERE UNTIL INTERRUPTED

; IF THE DCOK WORKS AS IT SHOULD, THE NEXT INSTRUCTION TO BE EXECUTED
; IS AT THE LABEL '5$'. IF I DOESN'T WORK, THE DMV SHOULD FINISH THE
; 'WRITE' COMMAND AND GENERAT A Q-BUS INTERRUPT. ON RETURNING FROM
; THAT INTERRUPT, WE FALL INTO THE ERROR CALL BELOW:

GEDF EM42A,ERR1 ;REPORT MISSING POWER-UP
; 'DEVICE FATAL' ERROR # 74
TRAP C$ERDF
.WORD 74
.WORD EM42A
.WORD ERR1

5$:

; IN EITHER CASE: RESTORE THE POWER FAIL VECTOR, STACK POINTER, AND
; THE FLAGS USED BY THE Q-BUS INTERRUPT SERVICE ROUTINE.

; BUT F I R S !

; IN SOME CASES THE Q-BUS GETS CONFUSED WHEN WE PERFORM THE ABOVE
; (HIGHLY NON-STANDARD) 'DCOK' MANIPULATION. EXPERIENCE HAS SHOWN
; THAT THE INSTRUCTION BEING EXECUTED CAN BE CORRUPTED -- USUALLY (BUT
; NOT ALWAYS) BEING CLEARED TO ZERO (A HALT INSTRUCTION). THIS IS NOT
; A FAILURE OF THE DMV AND THEREFORE SHOULD NOT BE OUR CONCERN HERE.

```

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TEST 12 -- DCOK H LO BIT

```

6896 : THE FAILURE SHOULD NOT AFFECT THE TEST WITHIN THE PASS IN WHICH IT
6897 : OCCURS BUT IN A SUBSEQUENT PASS. IN AN EFFORT TO ELIMINATE ANY
6898 : PROBLEMS FROM THIS GLITCH, WE RESTORE THE INSTRUCTION:
6899
6900 034500 012737 000001 034466 MOV #1,4$;RESTORE THE 'WAIT' INSTRUCTION -- JUST IN
6901 : CASE IT GOT MODIFIED!
6902 034506 013706 002454 MOV OLDSP,SP ;RESTORE THE STACK
6903 034512 142777 000001 145600 BICB #IENBA,@BSELO ;DISABLE THE 'A' INTERRUPT
6904 034520 105037 002274 CLRB INTWCH ;STOP EXPECTING Q-BUS INTERRUPTS
6905 034524 CLRVEC #24 ;RETURN THE VECTOR TO THE SUPERVISOR
6906 034524 012700 000024 MOV #24,R0 ;
6907 034530 104436 TRAP C$CVEC ;
6908 034532 SETPRI #0 ;MAKE SURE WE'RE BACK RUNNING AT 0 AGAIN!
6909 034532 012700 000000 MOV #0,R0 ;
6910 034536 104441 TRAP C$SPRI ;
6911
6912 034540 013701 002262 MOV DELAY1,R1 ;INITIALIZE THE LOOP COUNTER FOR DELAY LOOP
6913 034544 001402 10$: BEQ 11$; EXIT DELAY LOOP IF TIME HAS EXPIRED
6914 034546 005301 DEC R1 ; ELSE, DECREMENT THE LOOP COUNTER AND
6915 034550 000775 BR 10$; CONTINUE TO LOOP
6916 034552 11$: ; TIME UP !
6917 034552 132777 000200 145542 BITB #RUN,@BSEL1 ;CHECK RUN BIT
6918 034560 001403 BEQ 12$; NOT SET... REPORT ERROR.
6919 034562 105777 145550 TSTB @BSEL7 ;THIS REGISTER SHOULD HAVE BEEN CLEARED
6920 034566 001404 BEQ 13$;IT IS, EVERYTHING HERE IS OK -- EXIT SUBTEST
6921 034570 12$: GEDF EM42B,ERR1 ;NO, THEN REPORT THE FAILURE
6922 : 'DEVICE FATAL' ERROR # 75
6923 034570 104455 TRAP C$ERDF ;
6924 034572 000113 .WORD 75 ;
6925 034574 017020 .WORD EM42B ;
6926 034576 006212 .WORD ERR1 ;
6927 034600 004737 003346 13$: JSR PC,MASCLR ;RESTORE DMV-11 TO A NORMAL STATE!
6928 034604 103001 BCC 14$;NO ERRORS, EXIT SUBTEST
6929 034606 ERROR ;REPORT MSTCLR ERROR
6930 034606 104460 TRAP C$ERROR ;
6931 034610 14$: ;
6932 034610 ENDSUB
6933 034610 ;
6934 034610 104403 L10063: TRAP C$ESUB ;
6935
6936 :-----
6937 : SUBTEST #2: DCOK H LO (DMV-11 SHOULDN'T BE RESET)
6938 :-----
6939 : SINCE HITTING 'DCOK H LO' WITHOUT 'HALT' OCCASIONALLY CORRUPTS
6940 : PROGRAM MEMORY: WE SET BOTH 'HALT' AND 'DCOK H LO' IN THIS
6941 : SUB-TEST (IF HALT FAILS, THIS TEST MAY BLOW UP).
6942 034612 BGNSUB ; <==> TEST FOR POWER-DOWN/UP & NO DMV-11 RESET
6943 034612 T12.2:
6944 034612 104402 TRAP C$BSUB ;
6945
6946 034614 004737 003514 JSR PC,MSTCLR ;INIT DMV & START UP THE MAINT. LOOP
6947 034620 103003 BCC 2$;IF AN ERROR OCCURED,
6948 034622 ERROR ;REPORT IT &
6949 034622 104460 TRAP C$ERROR ;
6950 034624 ESCAPE SUB ; EXIT
6951 034624 104410 TRAP C$ESCAPE ;

```

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TEST 12 -- DCOK H LO BIT

```

6952 034626 000154
6953 034630
6954 034630 012746 000007
6955 034634 012746 034732
6956 034640 012746 000024
6957 034644 012746 000003
6958 034650 104437
6959 034652 062706 000010
6960 034656 010637 002454
6961
6962 034662 004537 004166
6963 034666 035006
6964 034670 000040
6965
6966 034672 112777 000377 145434
6967
6968 034700 012777 000077 145422
6969 034706 012777 000005 145410
6970
6971
6972
6973 034714 012703 001000
6974 034720 077301
6975
6976
6977
6978 034722
6979
6980 034722 104455
6981 034724 000114
6982 034726 016746
6983 034730 006212
6984
6985
6986
6987 034732 013706 002454
6988 034736
6989 034736 012700 000024
6990 034742 104436
6991 034744
6992 034744 012700 000000
6993 034750 104441
6994 034752 122777 000377 145354
6995 034760 001404
6996 034762
6997
6998 034762 104455
6999 034764 000115
7000 034766 017074
7001 034770 006212
7002
7003 034772 004737 003346
7004 034776 103001
7005 035000
7006 035000 104460
7007 035002

```

```

2$: SETVEC #24,#5$,#7 ;SETUP VECTOR FOR POWER FAIL INTERRUPT HANDLER
;MOV #7,-(SP)
;MOV #5$,-(SP)
;MOV #24,-(SP)
;MOV #3,-(SP)
;TRAP C$SVEC
;ADD #10,SP

MOV SP,OLDSP ;SAVE THE STACK POINTER

JSR R5,MOVLTD ;MOVE THE MICRO CODE INTO THE DMV
;SMCODE ; THIS IS WHERE IT STARTS
;EMCODE-SMCODE ; THIS IS ITS SIZE IN BYTES

MOVB #377,@BSEL6 ;WRITE ALL 1'S TO BSEL6

MOV #77,@SEL4 ;START ADDRESS OF MICROCODE
MOV #EXECUT,@SEL2 ;INITIATE M-CODE

; *** IF THE RESET GETS THROUGH, THE MICRO-DIAGNOSTIC WILL CLEAR BSEL4 ***

MOV #1000,R3 ;STALL FOR A BIT (UCODE SHOULD HALT US HERE)
SOB R3,,

; IF WE GET HERE, WE NEVER GOT THE EXPECTED POWER-UP SEQUENCE!

GEDF EM42A,ERR1 ;REPORT MISSING POWER-UP
; 'DEVICE FATAL' ERROR # 76
;TRAP C$SERDF
;.WORD 76
;.WORD EM42A
;.WORD ERR1

;IN EITHER CASE, RESTORE THE VECTOR & STACK AND SEE IF THE DMV GOT RESET

5$: MOV OLDSP,SP ;RESTORE THE STACK
CLRVEC #24 ;RETURN THE VECTOR TO THE SUPERVISOR
;MOV #24,R0
;TRAP C$CVEC

SETPRI #0 ;MAKE SURE WE'RE BACK RUNNING AT 0 AGAIN!
;MOV #0,R0
;TRAP C$SPRI

CMPB #377,@BSEL6 ;THIS REGISTER SHOULD NOT HAVE BEEN CLEARED
BEQ 10$;IT ISN'T, ALL IS OK -- EXIT SUBTEST
GEDF EM42C,ERR1 ;IT IS, THEN REPORT THE FAILURE
; 'DEVICE FATAL' ERROR # 77
;TRAP C$SERDF
;.WORD 77
;.WORD EM42C
;.WORD ERR1

; * AT THIS POINT 'DINIT' IS STILL SET *
10$: JSR PC,MASCLR ;MAKE SURE THE DMV IS PROPERLY RESET!
BCC 11$;EVERYTHING OK, EXIT SUBR AND TEST.
ERROR ;REPORT MASCLR ERROR
;TRAP C$ERROR

11$:

```

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TEST 12 -- DCOK H LO BIT

|      |        |        |
|------|--------|--------|
| 7008 | 035002 |        |
| 7009 | 035002 |        |
| 7010 | 035002 | 104403 |
| 7011 | 035004 |        |
| 7012 | 035004 |        |
| 7013 | 035004 | 104401 |

ENDSUB

ENDTST

L10064: TRAP C\$ESUB

L10062: TRAP C\$ETST



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SUBTEST 2'S M-CODE -- COMPLETE

```

7014
7015
7016
7017
7018 035006
7019 035006 251 125
7020 035010 215 004 246
7021 035013 251 105
7022 035015 215 004 246
7023 035020 352
7024 035021 352
7025 035022 352
7026 035023 251 107
7027 035025 215 004 246
7028 035030 251 105
7029 035032 215 004 246
7030 035035 251 003
7031 035037 205 000
7032 035041 306 000
7033 035043 320 374
7034 035045 140
7035
7036 035046
7037

```

```

.SBTTL SUBTEST 2'S M-CODE -- COMPLETE
:-----
: 6502 MICROCODE FOR TEST #11/ SUBTEST #2
:-----
SMCODE:
.BYTE 251,125 :A9 55 LDA #GOBSY1!HALT!PWRUP!DINIT
.BYTE 215,4,246 :8D 04 A6 STA NPRCTL :SET DISABLE INIT/HALT
.BYTE 251,105 :A9 45 LDA #GOBSY1!PWRUP!DINIT
.BYTE 215,4,246 :8D 04 A6 STA NPRCTL :CLEAR HALT
.BYTE 352 :EA NOP :WAIT A WHILE
.BYTE 352 :EA NOP
.BYTE 352 :EA NOP
.BYTE 251,107 :A9 47 LDA #GOBSY1!PWRUP!SDCOK!DINIT
.BYTE 215,4,246 :8D 04 A6 STA NPRCTL :SET DCOK
.BYTE 251,105 :A9 45 LDA #GOBSY1!PWRUP!DINIT
.BYTE 215,4,246 :8D 04 A6 STA NPRCTL :CLEAR DCOK
.BYTE 251,3 :A9 03 LDA #S03 :DELAY FOR 16.8 USEC
.BYTE 205,0 :85 00 STA SPO :BUS INIT IS 10 USEC
.BYTE 306,0 :C6 00 5$:DEC SPO :
.BYTE 320,374 :D0 FC BNE 5$:
.BYTE 140 :60 RTS :RETURN TO M-LOOP
.EVEN
EMCODE:
:-----

```





TEST 13 -- HALT MODE VERIFICATION

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7204  
7205

\*\*\*\*\*

THE ENTRY INTO THE CONSOLE 'ODT' WILL BE ABORTED!

\*\*\*\*\*

THE ROUTINE NPR'D INTO LOC. 0 WILL BE EXECUTED -- EVENTUALLY HANGING AT THE 'BR .' INSTRUCTION @ LOC. 4.

\*\*\*\*\*

THE ROUTINE JUST LOADED BY TH' DMV MICROCODE WILL NOW BE EXECUTE' (WE HOPE). WHEN THE SUBROUTINE IS RE-ENTERED (@ HLTST2),

- 1 THE INTERRUPT VECTORS WILL BE RESTORED;
- 2 THE PRIORITY LEVEL WILL BE LOWERED BACK TO 0;
- 3 RO, R1, & R6 WILL ALL BE CHECKED FOR THE PROPER CONTENTS; AND
- 4 TMPO WILL BE CHECKED FOR THE PROPER CONTENTS;

\*\*\*\*\*

4 000777 BR .

---

SET 'DCOK H LO' & 'DISABL INIT' AND CLEAR 'HALT'

DELAY FOR A SHORT TIME (ABOUT 1 MICROSECOND

CLEAR 'DCOK H LO' AND SET 'DISABL INIT'

NPR-OUT THE FOLLOWING:

| LOC: | CONTENTS |     |           |  |  |
|------|----------|-----|-----------|--|--|
| 6    | 005001   | CLR | R1        |  |  |
| 10   | 062701   | ADD | #(+2),R1  |  |  |
| 12   | 062701   | ADD | #(+2),R1  |  |  |
| :    | :        | :   |           |  |  |
| :    | :        | :   |           |  |  |
| 360  | 062701   | ADD | #(+2),R1  |  |  |
| 362  | 062701   | ADD | #(+2),R1  |  |  |
| 364  | 010037   | MOV | RO,@TMPO  |  |  |
| 366  | [TMPO]   |     |           |  |  |
| 370  | 013706   | MOV | @OLDSP,R6 |  |  |
| 372  | [OLDSP]  |     |           |  |  |
| 374  | 000137   | JMP | HLTST2    |  |  |
| 376  | [HLTST2] |     |           |  |  |

THIS IS SYNONYMOUS TO THE DMV-11 LOADING A 'MESSAGE' STARTING AT MEM. LOC. 000006.

NPR-OUT THE FOLLOWING:

| LOC: | CONTENTS   |
|------|------------|
| 4    | 000240 NOP |

THIS IS HOW THE DMV-11 WILL TAKE THE 11 CPU OUT OF THE 'BR .' CONDITION.

DROP 'DISABL INIT'

AN EXIT IS TAKEN TO THE M-LOOP

\*\*\*\*\*

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TEST 13 -- HALT MODE VERIFICATION

```

7206 : BGNTST
7207 035046
7208 000015
7209 035046 032737 000001 002370 HLTEST = $T ;DEFINE TEST # FOR 'INIT' SECTION
7210 035054 001002 BIT #PU24,PT.CTL ;IS POWER-UP STRAPPED FOR OPTION 0?
7211 035056 BNE 5$;YES, THEN WE CAN DO THIS TEST
7212 035056 104432 EXIT TST ;NO, WE CAN'T DO THIS TEST UNLESS IT IS!
7213 035060 000772 TRAP C$EXIT
7214 .WORD L10065-.
7215 035062 032737 000001 002316 5$: BIT #BIT0,PFLAG ;IF BUS RESETS ARE ALLOWED,
7216 035070 001402 BEQ 2$;PERFORM THIS TEST
7217 035072 EXIT TST ;ELSE, BYPASS IT
7218 035072 104432 TRAP C$EXIT
7219 035074 000756 .WORD L10065-.
7220
7221 035076 2$: DELAY 40. ;DELAY TO PREVENT TST # ROACHING
7222 035076 012727 000050 MOV #40.,(PC)+
7223 035102 000000 .WORD 0
7224 035104 013727 002116 MOV LSDLY,(PC)+
7225 035110 000000 .WORD 0
7226 035112 005367 177772 DEC -6(PC)
7227 035116 001375 BNE -4
7228 035120 005367 177756 DEC -22(PC)
7229 035124 001367 BNE -20
7230 -----
7231 035126 1$: BGNSUB
7232 035126
7233 035126 104402 T13.1: TRAP C$BSUB
7234 035130 004737 003514 JSR PC,MSTCLR ;RESET DMV & ENTER M-LOOP
7235 035134 103003 BCC 2$;IF NO ERROR HERE, CONTINUE
7236 035136 ERROR ;ELSE, REPORT THE ERROR
7237 035136 104460 TRAP C$ERROR
7238 035140 ESCAPE TST ; & EXIT THE TEST
7239 035140 104410 TRAP C$ESCAPE
7240 035142 000710 .WORD L10065-.
7241 035144
7242 035144 004537 004166 2$: JSR R5,MOVLTD ;MOVE THE MICRO CODE INTO THE DMV
7243 035150 036054 MC1 ; THIS IS WHERE IT STARTS
7244 035152 000114 MC2-MC1 ; THIS IS ITS SIZE IN BYTES
7245
7246 035154 005037 002412 CLR TMO
7247 035160 SETVEC #24,#24$,#7 ;INITIALIZE THE COUNTER
7248 035160 012746 000007 ;SETUP POWER-UP VECTOR
7249 035164 012746 035334 MOV #7,-(SP)
7250 035170 012746 000024 MOV #24,-(SP)
7251 035174 012746 000003 MOV #24,-(SP)
7252 035200 104437 MOV #3,-(SP)
7253 035202 062706 000010 TRAP C$SVEC
7254 035206 012777 000077 145114 ADD #10,SP
7255 035214 112777 177777 145114 MOV #77,@SEL4 ;START ADDRESS OF MICROCODE
7256 035222 012777 000005 145074 MOV #-1,@SEL7 ;SET FLAG (BSEL7)
7257 MOV #EXECUT,@SEL2 ;INITIATE M-CODE
7258 035230 005002
7259 035232 105777 145100 3$: CLR R2
7260 035236 001405 TSTB @SEL7 ;WAIT FOR FLAG TO BE CLEARED
7261 035240 077204 BEQ 5$
7261 SOB R2,3$

```

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TEST 13 -- HALT MODE VERIFICATION

```

7262 035242 GEDF EM43A,ERR1 ;TIMEOUT...M-CODE IS HUNG!
7263 ; 'DEVICE FATAL' ERROR # 78
7264 035242 104455 TRAP C$ERDF
7265 035244 000116 .WORD 78
7266 035246 017153 .WORD EM43A
7267 035250 006212 .WORD ERR1
7268
7269 035252 010637 002454 5$: MOV SP,OLDSP ;SAVE STACK POINTER FOR LATER
7270 035256 012777 002412 145044 MOV #TMP0,@SEL4 ;PASS ADDRESS OF TMP0 TO M-CODE
7271 035264 112777 177777 145044 MOVB #-1,@SEL7 ;TELL M-CODE TO PROCEED
7272
7273 035272 005237 002412 4$: INC TMP0 ;LOOP HERE UNTIL TMP0 GOES TO 0 AGAIN
7274 035276 001375 BNE 4$
7275 035300 000240 NOP
7276 035302 000240 NOP
7277 035304 000240 NOP
7278 035306 GEDF EM43A,ERR1 ;DMV SEEMS TO HAVE HUNG!
7279 ; 'DEVICE FATAL' ERROR # 79
7280 035306 104455 TRAP C$ERDF
7281 035310 000117 .WORD 79
7282 035312 017153 .WORD EM43A
7283 035314 006212 .WORD ERR1
7284 035316 000240 NOP
7285 035320 004737 003346 JSR PC,MASCLR ; (FOR PATCHING)
7286 035324 103001 BCC 9$;RESET THE DMV
7287 035326 ERROR ;RESET SUCCEEDED, ESCAPE TEST
7288 035326 104460 ; REPORT RESET ERROR
7289 035330 9$: ESCAPE TST ; & GET OUT
7290 035330 104410 TRAP C$ESCAPE
7291 035332 000520 .WORD L10065-.
7292
7293 035334 013706 002454 24$: MOV OLDSP,SP ;RESTORE R6 FIRST!
7294 035340 CLAVEC #24 ;RETURN THE VECTORE TO THE SUPERVISOR
7295 035340 012700 000024 MOV #24,R0
7296 035344 104436 TRAP C$CVEC
7297 035346 SETPRI #0 ;RESTORE PRIORITY LEVEL TO 0
7298 035346 012700 000000 MOV #0,R0
7299 035352 104441 TRAP C$SPRI
7300 035354 027737 144750 002412 CMP @SEL4,TMP0 ;THESE SHOULD BE EQUAL
7301 035362 001405 BEQ 30$;THEY ARE -- TEST PASSED
7302 035364 GEDF EM43B,ERR1 ;THEY AREN'T -- HALT DIDN'T WORK
7303 ; 'DEVICE FATAL' ERROR # 80
7304 035364 104455 TRAP C$ERDF
7305 035366 000120 .WORD 80
7306 035370 017177 .WORD EM43B
7307 035372 006212 .WORD ERR1
7308 035374 000240 NOP
7309 035376 105077 144734 30$: CLRB @SEL7 ; (FOR PATCHING)
7310 035402 ENDSUB ;TELL M-CODE TO CLEAR DINIT
7311 035402 L10066:
7312 035402 104403 TRAP C$ESUB
7313 ;-----
7314 035404 BGNSUB
7315 035404 T13.2:
7316 035404 104402 TRAP C$BSUB
7317 035406 004737 003514 JSR PC,MSTCLR ;RESET DMV & ENTER M-LOOP

```

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TEST 13 -- HALT MODE VERIFICATION

|      |        |        |        |        |                    |  |                                            |       |                                    |
|------|--------|--------|--------|--------|--------------------|--|--------------------------------------------|-------|------------------------------------|
| 7318 | 035412 | 103003 |        |        | BCC 1\$            |  | :IF NO ERROR HERE, CONTINUE                |       |                                    |
| 7319 | 035414 |        |        |        | ERROR              |  | :ELSE, REPORT THE ERROR                    |       |                                    |
| 7320 | 035414 | 104460 |        |        |                    |  |                                            | TRAP  | C\$ERROR                           |
| 7321 | 035416 |        |        |        | ESCAPE TST         |  | : & EXIT THE TEST                          |       |                                    |
| 7322 | 035416 | 104410 |        |        |                    |  |                                            | TRAP  | C\$ESCAPE                          |
| 7323 | 035420 | 000432 |        |        |                    |  |                                            | .WORD | L10065-                            |
| 7324 | 035422 |        |        |        |                    |  |                                            |       |                                    |
| 7325 | 035422 | 004537 | 004166 |        | 1\$: JSR R5,MOVLTD |  | :MOVE THE MICRO CODE INTO THE DMV          |       |                                    |
| 7326 | 035426 | 036170 |        |        | MC2                |  | : THIS IS WHERE IT STARTS                  |       |                                    |
| 7327 | 035430 | 000243 |        |        | MC2END-MC2         |  | : THIS IS ITS SIZE IN BYTES                |       |                                    |
| 7328 |        |        |        |        |                    |  |                                            |       |                                    |
| 7329 | 035432 | 004537 | 005004 |        | JSR R5,MOVSW       |  | :SAVE THE INTERRUPT VECTORS                |       |                                    |
| 7330 | 035436 | 000000 |        |        | 0                  |  |                                            |       |                                    |
| 7331 | 035440 | 002654 |        |        | BUFAREA            |  | : IN THE BUFFER AREA                       |       |                                    |
| 7332 | 035442 | 000200 |        |        | 400/2              |  | : LOC'S 0 ==> 377 WILL BE SAVED            |       |                                    |
| 7333 |        |        |        |        |                    |  |                                            |       |                                    |
| 7334 | 035444 |        |        |        | SETVEC #376,#0,#7  |  | :FAKE OUT THE SUPERVISOR, WE'RE JUST       |       |                                    |
| 7335 | 035444 | 012746 | 000007 |        |                    |  |                                            | MOV   | #7,-(SP)                           |
| 7336 | 035450 | 012746 | 000000 |        |                    |  |                                            | MOV   | #0,-(SP)                           |
| 7337 | 035454 | 012746 | 000376 |        |                    |  |                                            | MOV   | #376,-(SP)                         |
| 7338 | 035460 | 012746 | 000003 |        |                    |  |                                            | MOV   | #3,-(SP)                           |
| 7339 | 035464 | 104437 |        |        |                    |  |                                            | TRAP  | C\$SVEC                            |
| 7340 | 035466 | 062706 | 000010 |        |                    |  |                                            | ADD   | #10,SP                             |
| 7341 |        |        |        |        |                    |  |                                            |       |                                    |
| 7342 |        |        |        |        |                    |  |                                            |       | : SETTING LOCATION 376 TO ZERO (0) |
| 7343 | 035472 | 012777 | 000077 | 144630 | MOV #77,@SEL4      |  | :START ADDRESS OF MICROCODE                |       |                                    |
| 7344 | 035500 | 112777 | 177777 | 144630 | MOV #1,@SEL7       |  | :SET FLAG (SEL7)                           |       |                                    |
| 7345 | 035506 | 012777 | 000005 | 144610 | MOV #EXECUT,@SEL2  |  | :INITIATE M-CODE                           |       |                                    |
| 7346 |        |        |        |        |                    |  |                                            |       |                                    |
| 7347 | 035514 | 005002 |        |        | 3\$: CLR R2        |  |                                            |       |                                    |
| 7348 | 035516 | 105777 | 144614 |        | TSTB @SEL7         |  | :WAIT FOR FLAG TO BE CLEARED               |       |                                    |
| 7349 | 035522 | 001406 |        |        | BEQ 5\$            |  |                                            |       |                                    |
| 7350 | 035524 | 077204 |        |        | SOB R2,3\$         |  |                                            |       |                                    |
| 7351 | 035526 |        |        |        | GEDF EM43A,ERR1    |  | :TIMEOUT...M-CODE IS HUNG!                 |       |                                    |
| 7352 |        |        |        |        |                    |  | : 'DEVICE FATAL' ERROR # 81                |       |                                    |
| 7353 | 035526 | 104455 |        |        |                    |  |                                            | TRAP  | C\$ERDF                            |
| 7354 | 035530 | 000121 |        |        |                    |  |                                            | .WORD | 81                                 |
| 7355 | 035532 | 017153 |        |        |                    |  |                                            | .WORD | EM43A                              |
| 7356 | 035534 | 006212 |        |        |                    |  |                                            | .WORD | ERR1                               |
| 7357 | 035536 | 000447 |        |        | BR 22\$            |  | :EXIT                                      |       |                                    |
| 7358 |        |        |        |        |                    |  |                                            |       |                                    |
| 7359 | 035540 | 010637 | 002454 |        | 5\$: MOV SP,OLDSP  |  | :SAVE STACK POINTER FOR LATER              |       |                                    |
| 7360 | 035544 | 005037 | 002412 |        | CLR TMP0           |  | :RESET EXECUTION INDICATOR (TMP0)          |       |                                    |
| 7361 | 035550 | 112777 | 177777 | 144560 | MOV #1,@SEL7       |  | :TELL M-CODE TO PROCEED                    |       |                                    |
| 7362 |        |        |        |        |                    |  |                                            |       |                                    |
| 7363 | 035556 | 005003 |        |        | CLR R3             |  | :WE'LL WAIT THIS LONG FOR THE M-CODE TO    |       |                                    |
| 7364 |        |        |        |        |                    |  | : INTERRUPT OUR SEQUENCE OF OPERATION      |       |                                    |
| 7365 | 035560 | 005737 | 000376 |        | 10\$: TST @#376    |  | :LOOK FOR THE M-CODE TO LOAD THIS LOCATION |       |                                    |
| 7366 | 035564 | 001017 |        |        | BNE 20\$           |  | :WE SHOULD NEVER SEE THIS HAPPEN!!!        |       |                                    |
| 7367 | 035566 | 077304 |        |        | SOB R3,10\$        |  | :LOOP UNTIL WE'RE INTERRUPTED              |       |                                    |
| 7368 |        |        |        |        |                    |  | :IF WE AREN'T, WE HAVE A REAL PROBLEM      |       |                                    |
| 7369 |        |        |        |        |                    |  |                                            |       |                                    |
| 7370 | 035570 | 004537 | 005004 |        | JSR R5,MOVSW       |  | :RESTORE THE INTERRUPT VECTORS             |       |                                    |
| 7371 | 035574 | 002654 |        |        | BUFAREA            |  | : FROM THE BUFFER AREA                     |       |                                    |
| 7372 | 035576 | 000000 |        |        | 0                  |  |                                            |       |                                    |
| 7373 | 035600 | 000200 |        |        | 400/2              |  | : TO LOC'S 0 ==> 377                       |       |                                    |

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TEST 13 -- HALT MODE VERIFICATION

```

7374
7375 035602 SETPRI #0 ;RESTORE PRIORITY LEVEL TO 0
7376 035602 012700 000000
7377 035606 104441 MOV #0,R0
7378 TRAP C$SPRI
7379 035610 GEDF EM43A,ERR1 ;BUT WE AREN'T SURE WHAT IT IS!!!
7380 ; 'DEVICE FATAL' ERROR # 82
7381 035610 104455 TRAP C$ERDF
7382 035612 000122 .WORD 82
7383 035614 017153 .WORD EM43A
7384 035616 006212 .WORD ERR1
7385 035620 000240 NOP
7386 035622 000415 BR 22$; (FOR PATCHING)
7387 ; GO RESET DMV & EXIT
7388 035624 004537 005004 20$: JSR R5,MOVSW ;RESTORE THE INTERRUPT VECTORS
7389 035630 002654 BUFAREA ; FROM THE BUFFER AREA
7390 035632 000000 0
7391 035634 000200 400/2 ; TO LOC'S 0 ==> 377
7392
7393 035636 SETPRI #0 ;RESTORE PRIORITY LEVEL TO 0
7394 035636 012700 000000
7395 035642 104441 MOV #0,R0
7396 TRAP C$SPRI
7397 035644 GEDF EM43C,ERR1 ;POWER-UP INTERRUPT DIDN'T OCCUR
7398 ; 'DEVICE FATAL' ERROR # 83
7399 035644 104455 TRAP C$ERDF
7400 035646 000123 .WORD 83
7401 035650 017215 .WORD EM43C
7402 035652 006212 .WORD ERR1
7403 035654 000240 NOP
7404 035656 004737 003346 22$: JSR PC,MASCLR ;RESET THE DMV
7405 035662 103001 BCC 23$; RESET OK, PROCEED
7406 035664 ERROR ; RESET FAILED, REPORT IT
7407 035664 104460 TRAP C$ERROR
7408 035666 23$:
7409 035666 ESCAPE SUB ; & GET THE HECK OUT OF HERE!!!
7410 035666 104410 TRAP C$ESCAPE
7411 035670 000160 .WORD L10067-.
7412 035672 HLTST2:
7413
7414 035672 004537 005004 JSR R5,MOVSW ;RESTORE THE INTERRUPT VECTORS
7415 035676 002654 BUFAREA ; FROM THE BUFFER AREA
7416 035700 000000 0
7417 035702 000200 400/2 ; TO LOC'S 0 ==> 377
7418
7419 035704 010002 MOV R0,R2 ;R0 WILL BE CORRUPTED BY SUPERVISOR CALLS!
7420
7421 035706 SETPRI #0 ;RESTORE PRIORITY LEVEL TO 0
7422 035706 012700 000000
7423 035712 104441 MOV #0,R0
7424 035714 022701 071573 TRAP C$SPRI
7425 035720 001412 CMP #071573,R1 ;CHECK RESULTS OF THE ADDITIONS
7426 035722 010137 002256 BEQ 5$;OK -->
7427 035726 012737 177777 002254 MOV R1,BDATA ;WHAT!!! SETUP & REPORT AN
7428 035734 MOV #-1,GDATA ; ADDITION ERROR
7429 GEDF EM43D,ERR1 ;ADDITION DIDN'T WORK PROPERLY
; 'DEVICE FATAL' ERROR # 84

```



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TEST 13 -- HALT MODE VERIFICATION

```

7430 035734 104455
7431 035736 000124
7432 035740 017242
7433 035742 006212
7434 035744 000240
7435 035746 020227 177777 5$: NOP
7436 035752 001003 BNE 8$
7437 035754 023702 002412 CMP TMP0,R2
7438 035760 001412 BEQ 10$
7439 035762 010237 002256 8$: MOV R2,BDATA
7440 035766 012737 177777 002254 MOV #-1,GDATA
7441 035774 GEDF EM43D,ERR1
7442
7443 035774 104455
7444 035776 000125
7445 036000 017242
7446 036002 006212
7447 036004 000240
7448 036006 020637 002454 10$: NOP
7449 036012 001412 BEQ 15$
7450 036014 010637 002256 MOV SP,BDATA
7451 036020 013737 002454 002254 MOV OLDSP,GDATA
7452 036026 GEDF EM43D,ERR1
7453
7454 036026 104455
7455 036030 000126
7456 036032 017242
7457 036034 006212
7458 036036 000240
7459 036040 004737 003346 15$: NOP
7460 036044 103001 JSR PC,MASCLR
7461 036046 BCC 16$
7462 036046 104460 ERROR
7463 036050 16$: ENDSUB
7464 036050
7465 036050
7466 036050 104403 L10067: TRAP C$ESUB
7467 036052
7468 036052 L10065:
7469 036052 104401 TRAP C$ETST

```

```

TRAP C$ERDF
.WORD 84
.WORD EM43D
.WORD ERR1
: (FOR PATCHING)
:THE LOADED ROUTINE SHOULD HAVE DONE THIS
:IT DIDN'T -- IT FAILED
:THESE SHOULD BE EQUAL TOO
:OK -->
:WHAT!!! SETUP & REPORT AN ERROR
:LOADED ROUTINE FAILED
:'DEVICE FATAL' ERROR # 85
TRAP C$ERDF
.WORD 85
.WORD EM43D
.WORD ERR1
: (FOR PATCHING)
:THESE SHOULD ALSO BE EQUAL
:OK -->
:WHAT!!! SETUP & REPORT AN ERROR
:LOADED ROUTINE FAILED
:'DEVICE FATAL' ERROR # 86
TRAP C$ERDF
.WORD 86
.WORD EM43D
.WORD ERR1
: (FOR PATCHING)
:MAKE SURE THE DMV IS RESET
: RESET OK, CONTINUE
:RESET FAILED, REPORT ERROR
TRAP C$ERROR
L10067: TRAP C$ESUB
L10065: TRAP C$ETST

```

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SUBTEST 1'S M-CODE -- ASSIGNMENTS

.SBTTL SUBTEST 1'S M-CODE -- ASSIGNMENTS

\*\*\*\*\*  
: MICRO-CODE FOR SUBROUTINE # 1  
\*\*\*\*\*

MC1:

ASSEMBLED BY: COMPAS MICROSYSTEMS MINMIC (V6A)  
(WITH CHANGES EDITED IN)

:LINE# LOC CODE  
:0002 0000  
:0003 0000  
:0004 0000  
:0005 0000  
:0006 0000  
:0007 0000  
:0008 0000  
:0009 0000  
:0010 0000  
:0011 0000  
:0012 0000  
:0013 0000  
:0014 0000  
:0015 0000  
:0016 0000  
:0017 0000  
:0018 0000  
:0019 0000  
:0020 0000  
:0021 0000  
:0022 0000  
:0023 0000  
:0024 0000  
:0025 0000  
:0026 0000  
:0027 0000  
:0028 0000  
:0029 0000  
:0030 0000  
:0031 0000  
:0032 0000  
:0033 0000  
:0034 0000  
:0035 0000  
:0036 0000  
:0037 0000  
:0038 0000  
:0039 0000  
:0040 0000  
:0041 0000  
:0042 0000  
:0043 0000  
:0044 0000  
:0045 0000  
:0046 0000

LINE  
\*=\$0000

:EQUATES FOR BIT DEFINITIONS

BIT0 =a1  
BIT1 =a2  
BIT2 =a4  
BIT4 =a20  
BIT5 =a40  
BIT6 =a100

:ADDRESS EQUATES FOR CSR REGISTERS

BSEL4 =\$14  
BSEL5 =BSEL4+1  
BSEL7 =BSEL4+3

:NPR ADDRESS REGISTER EQUATES

NPRAIL = \$003C ;IN NPR ADRS LO REG  
NPRAIH = NPRAIL+1 ;IN NPR ADRS HI REG  
NPRAIX = NPRAIL+2 ;IN NPR EXTENDED ADRS REG

:NPR DATA REG EQUATES

NPRDIL = \$A600 ;IN NPR DATA LO REG  
NPRDIH = NPRDIL+1 ;IN NPR DATA HI REG

:NPR CONTROL REG EQUATES

NPRCTL = \$A604 ;NPR CONTROL REGISTER  
NONPR = BIT6 ;USED TO PREVENT AN NPR  
INOUT = BIT5 ;SET TO 1 FOR INPUT, SET TO 0 FOR OUTPUT NPR  
HALT = BIT4 ;SET DURING MOP MODE ONLY  
PWRUP = BIT2 ;CLEARED BY BUS INIT TO INDICATE PWR UP  
SDCLOW = BIT1 ;SET TO 1 TO RESET LSI-11 FOR MOP BOOT  
DISINI = BIT0 ;SET TO 1 TO DISABLE BUS INIT TO 6502

:NPR REQUEST FUNCTIONS

NPRRED = PWRUP ;IN/OUT BIT = 0 FOR READ TO DMV-11

:MISCELLANEOUS EQUATES

STARAM = \$003F ;STARTING ADRS OF GEN'L PURPOSE RAM TO TEST

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SUBTEST 1'S M-CODE -- ASSIGNMENTS

```

7526 :0047 0000
7527 :SBTTL SUBTEST 1'S M-CODE -- ROUTINE
7528 :0048 0000 *=STARAM ;START OF MICROCODE IN RAM
7529
7530 :LINE# LOC CODE LINE
7531
7532 036054 251 000 .BYTE 251,00
7533 :0050 003F A9 00 LDA #0 ;CLEAR BSEL7
7534 036056 205 027 .BYTE 205,27
7535 :0051 0041 85 17 STA BSEL7
7536 036060 245 027 :0052 0043
7537 :0053 0043 A5 17 WAIT1 LDA BSEL7 ;WAIT FOR IT TO GO <> 0
7538 036062 360 374 .BYTE 360,374
7539 :0054 0045 F0 FC BEQ WAIT1
7540 :0055 0047
7541 :0056 0047 ; WE SHOULD NOW BE IN SYNC WITH THE 11 PROCESSOR
7542 :0057 0047
7543 036064 245 024 .BYTE 245,24
7544 :0058 0047 A5 14 LDA BSEL4 ;GET & SAVE THE ADDRESS
7545 036066 205 074 .BYTE 205,74
7546 :0059 0049 85 3C STA NPRAIL ;OF 'TMPO' AND USE IT TO
7547 036070 245 025 .BYTE 245,25
7548 :0060 004B A5 15 LDA BSEL5 ;SETUP FOR AN NPR-IN
7549 036072 205 075 .BYTE 205,75
7550 :0061 004D 85 3D STA NPRAIH ;OPERATION LATER
7551 :0062 004F
7552 036074 251 124 .BYTE 251,124
7553 :0063 004F A9 54 LDA #NONPR!HALT!PWRUP
7554 036076 215 004 246 .BYTE 215,4,246
7555 :0064 0051 8D 04 A6 STA NPRCTL ;HALT THE 11 CPU
7556 :0064 0054
7557 :0064 0054 ; DELAY TO ALLOW 'HALT' TO TAKE EFFECT (ABOUT
7558 :0064 0054 ; 100 MICROSECONDS).
7559 036101 240 041 .BYTE 240,41
7560 :0064 0054 A0 21 LDY #S21 ;INITIAL VALUE OF COUNTER
7561 036103 210 .BYTE 210
7562 :0064 0056 83 DELAY DEY ;(33. FOR .6 US CYCLE)
7563 036104 320 375 .BYTE 320,375
7564 :0064 0057 D0 FD BNE DELAY
7565 :0065 0059
7566 :0066 0059 ; WE NOW HAVE TO READ THE 11 CPU'S LOCATION WHO'S
7567 :0067 0059 ; ADDRESS WE PREVIOUSLY READ FROM SEL6
7568 :0068 0059
7569 036106 251 000 .BYTE 251,00
7570 :0069 0059 A9 00 LDA #0 ;CLEAR THE EXTENDED-ADDRESS-IN
7571 036110 205 076 .BYTE 205,76
7572 :0070 005B 85 3E STA NPRAIX
7573 036112 251 024 .BYTE 251,24
7574 :0071 005D A9 14 LDA #NPRRED!HALT
7575 036114 215 004 246 .BYTE 215,4,246
7576 :0072 005F 8D 04 A6 STA NPRCTL ;READ ONE WORD FROM THE 11 CPU
7577 :0073 0062
7578 036117 054 004 246 .BYTE 54,4,246
7579 :0074 0062 2C 04 A6 NPRWAT BIT NPRCTL ;WAIT FOR IT TO 'ALMOST' COMPLETE
7580 :.BYTE 160,373
7581 036122 160 373

```

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SUBTEST 1'S M-CODE -- ROUTINE

```

7582 :0075 0065 70 FB BVS NPRWAT
7583 036124 352 .BYTE 352
7584 :0075 0067 EA NOP ; SHOULD COMPLETE HERE
7585 :0076 0068
7586 036125 255 000 246 .BYTE 255,0,246
7587 :0077 0068 AD 00 A6 LDA NPRDIL ; MOVE THE WORD JUST READ INTO
7588 036130 205 024 .BYTE 205,24
7589 :0078 0068 85 14 STA BSEL4 ; SEL4
7590 036132 255 001 246 .BYTE 255,1,246
7591 :0079 006D AD 01 A6 LDA NPRDIH
7592 036135 205 025 .BYTE 205,25
7593 :0080 0070 85 15 STA BSEL5
7594 :0088 0072
7595 :0089 0072 ; DROP 'HALT' AND SET 'DCK H LO' & 'DISABL INIT'
7596 :0090 0072
7597 036137 251 125 .BYTE 251,125
7598 :0091 0072 A9 55 LDA #NONPR!PWRUP!DISINI!HALT
7599 036141 215 004 246 .BYTE 215,4,246
7600 :0092 0074 8D 04 A6 STA NPRCTL
7601 036144 251 107 .BYTE 251,107
7602 :0093 0077 A9 47 LDA #NONPR!PWRUP!SDCLOW!DISINI
7603 036146 215 004 246 .BYTE 215,4,246
7604 :0094 0079 8D 04 A6 STA NPRCTL
7605 :0095 007C
7606 :0096 007C ; NOW LET THE 11 CPU GO THROUGH THE POWER-UP SEQUENCE
7607 :0097 007C
7608 036151 251 105 .BYTE 251,105
7609 :0098 007C A9 45 LDA #NONPR!PWRUP!DISINI
7610 036153 215 004 246 .BYTE 215,4,246
7611 :0099 007E 8D 04 A6 STA NPRCTL
7612 :0100 0081
7613 :0101 0081 ; WHEN BSEL7 IS CLEARED, CLEAR 'DISABL INIT'
7614 :0102 0081
7615 036156 245 027 .BYTE 245,27
7616 :0103 0081 A5 17 WAIT2 LDA BSEL7
7617 036160 320 374 .BYTE 320,374
7618 :0104 0083 D0 FC BNE WAIT2
7619 :0105 0085
7620 036162 251 104 .BYTE 251,104
7621 :0106 0085 A9 44 LDA #NONPR!PWRUP
7622 036164 215 004 246 .BYTE 215,4,246
7623 :0107 0087 8D 04 A6 STA NPRCTL
7624 :0108 008A
7625 :0109 008A ; USE A STANDARD SUBROUTINE RETURN TO GET BACK INTO
7626 :0110 008A ; THE MAINTENANCE LOOP
7627 :0111 008A
7628 036167 140 .BYTE 140
7629 :0112 008A 60 RTS
7630 :0113 008B
7631
7632 ; ERRORS = 0000
7633 .SBTTL SUBTEST 1'S M-CODE -- SYMBOL TABLE
7634
7635 ; BIT0 0001 BIT1 0002 BIT2 0004 BIT4 0010
7636
7637

```

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SUBTEST 1'S M-CODE -- SYMBOL TABLE

|      |   |                                 |      |        |      |        |      |        |      |
|------|---|---------------------------------|------|--------|------|--------|------|--------|------|
| 7638 | : | BIT5                            | 0020 | BIT6   | 0040 | BSEL4  | 0014 | BSEL5  | 0015 |
| 7639 | : | BSEL7                           | 0017 | DELAY  | 006E | DISINI | 0001 | HALT   | 0010 |
| 7640 | : | INOUT                           | 0020 | NONPR  | 0040 | NPRAIH | 003D | NPRAIL | 003C |
| 7641 | : | NPRAIX                          | 003E | NPRCTL | A604 | NPRDIH | A601 | NPRDIL | A600 |
| 7642 | : | NPRRED                          | 0004 | NPRWAT | 005D | PWRUP  | 0004 | SDCLOW | 0002 |
| 7643 | : | STARAM                          | 003F | WAIT1  | 0043 | WAIT2  | 0078 |        |      |
| 7644 | : | END OF ASSEMBLY(V6A)            |      |        |      |        |      |        |      |
| 7645 | : | SYMBOLS LEFT = 1473 OUT OF 1500 |      |        |      |        |      |        |      |

.SBTTL SUBTEST 1'S M-CODE -- CROSS REFERENCE TABLE (CREF V01-05 )

|      |   |        |      |     |    |    |     |     |  |
|------|---|--------|------|-----|----|----|-----|-----|--|
| 7650 | : | BIT0   |      | 5#  | 37 |    |     |     |  |
| 7651 | : | BIT1   | 6#   | 36  |    |    |     |     |  |
| 7652 | : | BIT2   | 7#   | 35  |    |    |     |     |  |
| 7653 | : | BIT4   | 8#   | 34  |    |    |     |     |  |
| 7654 | : | BIT5   | 9#   | 33  |    |    |     |     |  |
| 7655 | : | BIT6   | 10#  | 32  |    |    |     |     |  |
| 7656 | : | BSEL4  | 14#  | 58  | 78 |    |     |     |  |
| 7657 | : | BSEL5  | 15#  | 60  | 80 |    |     |     |  |
| 7658 | : | BSEL7  | 16#  | 15  | 16 | 51 | 53  | 100 |  |
| 7659 | : | DELAY  | 86#  | 87  |    |    |     |     |  |
| 7660 | : | DISINI | 37#  | 91  | 96 |    |     |     |  |
| 7661 | : | HALT   |      | 34# | 63 | 71 |     |     |  |
| 7662 | : | INOUT  | 33#  |     |    |    |     |     |  |
| 7663 | : | NONPR  | 32#  | 63  | 91 | 96 | 103 |     |  |
| 7664 | : | NPRAIH | 21#  | 61  |    |    |     |     |  |
| 7665 | : | NPRAIL | 20#  | 21  | 22 | 59 |     |     |  |
| 7666 | : | NPRAIX | 22#  | 70  |    |    |     |     |  |
| 7667 | : | NPRCTL | 31#  | 64  | 72 | 74 | 92  | 104 |  |
| 7668 | : | NPRDIH | 27#  | 79  |    |    |     |     |  |
| 7669 | : | NPRDIL | 26#  | 27  | 77 |    |     |     |  |
| 7670 | : | NPRRED | 41#  | 71  |    |    |     |     |  |
| 7671 | : | NPRWAT | 74#  | 75  |    |    |     |     |  |
| 7672 | : | PWRUP  | 35#  | 41  | 63 | 91 | 96  | 103 |  |
| 7673 | : | SDCLOW | 36#  | 91  |    |    |     |     |  |
| 7674 | : | STARAM | 45#  | 48  |    |    |     |     |  |
| 7675 | : | WAIT1  | 53#  | 54  |    |    |     |     |  |
| 7676 | : | WAIT2  | 100# | 101 |    |    |     |     |  |

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SUBTEST 1'S M-CODE -- CROSS REFERENCE TABLE (CREF V01-05 )

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```

.EVEN
.SBTTL SUBTEST 2'S M-CODE -- ASSIGNMENTS
:*****
: MICRO-CODE FOR SUBROUTINE # 2
:*****
.RADIX 8.
MC2:
:LINE# LOC CODE LINE
:0002 0000 *=$0000
:0003 0000
:0004 0000 ;EQUATES FOR BIT DEFINITIONS
:0005 0000 BIT0 =a1
:0006 0000 BIT1 =a2
:0007 0000 BIT2 =a4
:0008 0000 BIT4 =a20
:0009 0000 BIT6 =a100
:0010 0000
:0011 0000
:0012 0000 ;ADDRESS EQUATES FOR CSR REGISTERS
:0013 0000 BSEL7 =$17
:0014 0000
:0015 0000
:0016 0000 ;NPR ADDRESS REGISTER EQUATES
:0017 0000 NPRAOL = $0038 ;OUT NPR ADRS LO REG
:0018 0000 NPRAOH = NPRAOL+1 ;OUT NPR ADRS HI REG
:0019 0000 NPRAOX = NPRAOL+2 ;OUT NPR EXTENDED ADRS REG
:0020 0000
:0021 0000
:0022 0000 ;NPR DATA REG EQUATES
:0023 0000 NPRDOL = $A600 ;OUT NPR DATA LO REG
:0024 0000 NPRDOH = NPRDOL+1 ;OUT NPR DATA HI REG
:0025 0000
:0026 0000
:0027 0000 ;NPR CONTROL REG EQUATES
:0028 0000 NPRCTL = $A604 ;NPR CONTROL REGISTER
:0029 0000 NONPR = BIT6 ;USED TO PREVENT AN NPR
:0030 0000 HALT = BIT4 ;SET DURING MOP MODE ONLY
:0031 0000 PWUP = BIT2 ;CLEARED BY BUS INIT
:0032 0000 SDCLOW = BIT1 ;SET TO 1 TO RESET LSI-11 FOR MOP BOOT
:0033 0000 DISINI = BIT0 ;SET TO 1 TO DISABLE BUS INIT TO 6502
:0034 0000
:0035 0000
:0036 0000 ;MISCELLANEOUS EQUATES
:0037 0000 STARAM = $003F ;STARTING ADRS OF GEN'L PURPOSE RAM
:0038 0000

```

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## SUBTEST 2'S M-CODE -- ROUTINE

```

7723 .SBTTL SUBTEST 2'S M-CODE -- ROUTINE
7724 :0039 0000
7725 :0040 0000 *=STARAM ;START OF MICROCODE IN RAM
7726 036170 251 000 .BYTE 251,000
7727 :0042 003F A9 00 LDA #0 ;CLEAR BSEL7
7728 036172 205 027 .BYTE 205,027
7729 :0043 0041 85 17 STA BSEL7
7730 :0044 0043
7731 036174 245 027 .BYTE 245,027
7732 :0045 0043 A5 17 WAIT1 LDA BSEL7 ;WAIT FOR IT TO GO <> 0
7733 036176 360 374 .BYTE 360,374
7734 :0046 0045 F0 FC BEQ WAIT1
7735 :0047 0047
7736 :0048 0047 ; WE SHOULD NOW BE IN SYNC WITH THE 11 PROCESSOR
7737 :0049 0047
7738 :0050 0047 ; THE NEXT SEQUENCE WILL SEND THE 11 CPU THROUGH
7739 :0051 0047 ; A POWER-UP SEQUENCE AND RESET EVERYTHING ELSE ON THE
7740 :0052 0047 ; Q-BUS. WE HAVE PREVENTED OURSELVES FROM BEING RESET
7741 :0053 0047 ; BY SETTING 'DISABL INIT'.
7742 :0054 0047
7743 036200 251 125 .BYTE 251,125
7744 :0054 0047 A9 55 LDA #NONPR!HALT!PWRUP!DISINI
7745 036202 215 004 246 .BYTE 215,004,246
7746 :0054 0049 8D 04 A6 STA NPRCTL
7747 :0054 004C
7748 :0054 004C ; DELAY TO ALLOW 'HALT' TO TAKE EFFECT (ABOUT
7749 :0054 004C ; 100 MICROSECONDS).
7750 036205 240 041 .BYTE 240,41
7751 :0054 004C A0 21 LDY #$21 ;INITIAL VALUE OF COUNTER
7752 036207 210 .BYTE 210
7753 :0054 004E 88 DELAY DEY ;(33. FOR .6 US CYCLE)
7754 036210 320 375 .BYTE 320,375
7755 :0054 004F D0 FD BNE DELAY
7756 036212 251 127 .BYTE 251,127
7757 :0055 0051 A9 57 LDA #NONPR!HALT!PWRUP!SDCLOW!DISINI
7758 036214 215 004 246 .BYTE 215,004,246
7759 :0056 0053 8D 04 A6 STA NPRCTL ;HANG THE 11 CPU ETC.
7760 036217 251 165 .BYTE 251,165
7761 :0057 0056 A9 75 LDA #NONPR!HALT!PWRUP!DISINI!NPROUT
7762 036221 215 004 246 .BYTE 215,004,246
7763 :0058 0058 8D 04 A6 STA NPRCTL ;NOW LET IT 'POWER-UP'
7764 :0059 005B
7765 036224 251 000 .BYTE 251,000
7766 :0060 005B A9 00 LDA #0 ;SETUP NPR ADDR OUT HIGH FOR
7767 036226 205 071 .BYTE 205,071
7768 :0061 005D 85 39 STA NPRACH ; ALL NPR'S
7769 036230 205 072 .BYTE 205,072
7770 :0062 005F 85 3A STA NPRAOX ;THE EXTENDED BYTE TOO
7771 036232 252 .BYTE 252
7772 :0063 0061 AA TAX ;INITALIZE DATA TABLE INDEX
7773 :0064 0062
7774 :0065 0062 ; WE ARE NOW SETUP TO MOVE THE 'MOV' INSTRUCTION INTO
7775 :0066 0062 ; LOCATION 0 OF THE 11'S MEMORY
7776 :0067 0062
7777 036233 251 024 .BYTE 251,024
7778 :0068 0062 A9 14 LDA #24 ;POINT TO 11'S POWER-UP VEC.

```

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SUBTEST 2'S M-CODE -- ROUTINE

```

7779 036235 205 070 .BYTE 205,070
7780 :0069 0064 85 38 STA NPRAOL
7781 036237 040 305 000 .BYTE 040,305,000 JSR NPR ;MOVE TO 24
7782 :0070 0066 20 C5 00 JSR NPR ;MOVE TO 26
7783 036242 040 305 000 .BYTE 040,305,000
7784 :0071 0069 20 C5 00
7785 :0072 006C
7786 036245 251 000 .BYTE 251,000
7787 :0073 006C A9 00 LDA #0
7788 036247 205 070 .BYTE 205,070
7789 :0074 006E 85 38 STA NPRAOL ;POINT TO THE 11'S LOC. 0
7790 036251 040 305 000 .BYTE 040,305,000 JSR NPR ;MOVE TO LOC 0
7791 :0075 0070 20 C5 00 JSR NPR ;MOVE TO LOC 2
7792 036254 040 305 000 .BYTE 040,305,000 JSR NPR ;MOVE TO 4
7793 :0076 0073 20 C5 00
7794 036257 040 305 000 .BYTE 040,305,000
7795 :0077 0076 20 C5 00
7796 :0078 0079
7797 :0079 0079 ; NOW THAT WE'VE TAKEN CONTROL OF THE 11'S POWER-
7798 :0080 0079 ; UP VECTOR, FORCE HIM THROUGH IT. BUT DON'T LET
7799 :0081 0079 ; HIS BINIT RESET US (BY KEEPING 'DISABL INIT'
7800 :0082 0079 ; SET)
7801 :0083 0079
7802 036262 251 107 .BYTE 251,107
7803 :0084 0079 A9 47 LDA #NONPR!PWRUP!SDCLOW!DISINI
7804 036264 215 004 246 .BYTE 215,004,246 STA NPRCTL
7805 :0085 007B 8D 04 A6
7806 036267 251 145 .BYTE 251,145 LDA #NONPR!PWRUP!DISINI!NPROUT
7807 :0086 007E A9 65
7808 036271 15 004 246 .BYTE 215,004,246 STA NPRCTL
7809 :0087 0080 8D 04 A6
7810 036274 352 .BYTE 352
7811 :0087 0083 EA NOP
7812 036275 352 .BYTE 352
7813 :0087 0084 EA NOP
7814 :0088 0085
7815 :0089 0085 ; NOW WE CAN NPR-OUT THE REST OF THE DATA WE HAVE
7816 :0090 0085
7817 036276 040 305 000 .BYTE 040,305,000 JSR NPR ;MOVE TO LOC 6
7818 :0091 0085 20 C5 00
7819 036301 240 166 .BYTE 240,166 LDY #@166 ;THIS IS HOW MANY WE'LL DO
7820 :0092 0088 A0 76
7821 036303 040 305 000 .BYTE 040,305,000
7822 :0093 008A 20 C5 00 FILOOP JSR NPR ;WRITE 1 WORD
7823 036306 312 .BYTE 312
7824 :0094 008D CA DEX ;BACK UP THE DATA POINTER
7825 036307 312 .BYTE 312
7826 :0095 008E CA DEX ; -- WE WANT THE SAME WORD
7827 036310 210 .BYTE 210
7828 :0096 008F 88 DEY ;IF NOT DONE,
7829 036311 320 370 .BYTE 320,370
7830 :0097 0090 D0 F8 BNE FILOOP ; DO IT AGAIN
7831 036313 240 006 .BYTE 240,006
7832 :0098 0092 A0 06 LDY #@6 ;ELSE, SETUP TO DO 6 MORE
7833 036315 350 .BYTE 350
7834 :0099 0094 E8 INX ; WORDS

```



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SUBTEST 2'S M-CODE -- ROUTINE

|      |        |        |        |     |                     |        |              |                     |                              |
|------|--------|--------|--------|-----|---------------------|--------|--------------|---------------------|------------------------------|
| 7835 | 036316 | 350    |        |     | .BYTE 350           |        |              |                     |                              |
| 7836 |        |        |        |     | :0100 0095 E8       | INX    |              | :POINT TO NEXT WORD |                              |
| 7837 | 036317 | 040    | 305    | 000 | .BYTE 040,305,000   |        |              |                     |                              |
| 7838 |        |        |        |     | :0101 0096 20 C5 00 | ENDLDP | JSR          | NPR                 | :MOVE THE LAST 6 WORDS       |
| 7839 | 036322 | 210    |        |     | .BYTE 210           |        |              |                     |                              |
| 7840 |        |        |        |     | :0102 0099 88       | DEY    |              |                     | :IF NOT DONE,                |
| 7841 | 036323 | 320    | 372    |     | .BYTE 320,372       |        |              |                     |                              |
| 7842 |        |        |        |     | :0103 009A D0 FA    | BNE    | ENDLDP       |                     | : DO IT AGAIN                |
| 7843 |        |        |        |     | :0104 009C          |        |              |                     | :ELSE, THE SIMULATED 'DOWN-  |
| 7844 |        |        |        |     | :0105 009C          |        |              |                     | : LINE LOAD' IS COMPLETE     |
| 7845 |        |        |        |     | :0106 009C          |        |              |                     |                              |
| 7846 | 036325 | 251    | 004    |     | .BYTE 251,004       |        |              |                     |                              |
| 7847 |        |        |        |     | :0107 009C A9 04    | LDA    | #4           |                     | : AND THE 11'S LOC. 4        |
| 7848 | 036327 | 205    | 070    |     | .BYTE 205,070       |        |              |                     |                              |
| 7849 |        |        |        |     | :0108 009E 85 38    | STA    | NPRAOL       |                     |                              |
| 7850 | 036331 | 040    | 305    | 000 | .BYTE 040,305,000   |        |              |                     |                              |
| 7851 |        |        |        |     | :0109 00A0 20 C5 00 | JSR    | NPR          |                     | :OVER-WRITE THE 'BR .''      |
| 7852 |        |        |        |     | :0110 00A3          |        |              |                     | :INSTRUCTION TO LET THE JUST |
| 7853 |        |        |        |     | :0111 00A3          |        |              |                     | :LOADED ROUTINE BE EXECUTED  |
| 7854 |        |        |        |     | :0112 00A3          |        |              |                     |                              |
| 7855 | 036334 | 251    | 104    |     | .BYTE 251,104       |        |              |                     |                              |
| 7856 |        |        |        |     | :0113 00A3 A9 44    | LDA    | #NONPR!PWRUP |                     |                              |
| 7857 | 036336 | 215    | 004    | 246 | .BYTE 215,004,246   |        |              |                     |                              |
| 7858 |        |        |        |     | :0114 00A5 8D 04 A6 | STA    | NPRCTL       |                     | :LET BINIT RESET US AGAIN    |
| 7859 |        |        |        |     | :0115 00A8          |        |              |                     |                              |
| 7860 | 036341 | 140    |        |     | .BYTE 140           |        |              |                     |                              |
| 7861 |        |        |        |     | :0116 00A8 60       | RTS    |              |                     | :RETURN TO MAINTENANCE LOOP  |
| 7862 |        |        |        |     | :0117 00A9          |        |              |                     |                              |
| 7863 |        |        |        |     | :0118 00A9          |        |              |                     |                              |
| 7864 |        |        |        |     | :0119 00A9          |        |              |                     |                              |
| 7865 |        |        |        |     | :0120 00A9          |        |              |                     |                              |
| 7866 |        |        |        |     | :0121 00A9          |        |              |                     |                              |
| 7867 |        |        |        |     | :0122 00A9          |        |              |                     |                              |
| 7868 |        |        |        |     | :0123 00A9          |        |              |                     |                              |
| 7869 |        |        |        |     | :0124 00A9          |        |              |                     |                              |
| 7870 |        |        |        |     | :0125 00A9          |        |              |                     |                              |
| 7871 |        |        |        |     | :0126 00A9          |        |              |                     |                              |
| 7872 |        |        |        |     | :0127 00A9          |        |              |                     |                              |
| 7873 | 036342 | 000000 |        |     | .WORD 000000        |        |              |                     |                              |
| 7874 |        |        |        |     | :0128 00A9 00 00    |        | .DBYTE       | 0,a340              | :LOC'S 24 & 26               |
| 7875 | 036344 | 000340 |        |     | .WORD 000340        |        |              |                     |                              |
| 7876 |        |        |        |     | :0128 00AB 00 E0    |        |              |                     |                              |
| 7877 | 036346 | 012700 | 177777 |     | MOV #-1,R0          |        |              |                     |                              |
| 7878 |        |        |        |     | :0129 00AD 15 C0    |        | .DBYTE       | a012700,-1,a777     | :LOC'S 0 --> 4               |
| 7879 |        |        |        |     | :0129 00AF FF FF    |        |              |                     |                              |
| 7880 | 036352 | 000777 |        |     | BR                  |        |              |                     |                              |
| 7881 |        |        |        |     | :0129 00B1 01 FF    |        |              |                     |                              |
| 7882 | 036354 | 005001 |        |     | CLR R1              |        |              |                     |                              |
| 7883 |        |        |        |     | :0130 00B3 0A 01    |        | .DBYTE       | a005001             | :LOC. 6                      |
| 7884 | 036356 | 062701 |        |     | .WORD 062701        |        |              |                     |                              |
| 7885 |        |        |        |     | :0131 00B5 65 C1    |        | .DBYTE       | a062701             | :LOC'S 10 --> 362            |
| 7886 | 036360 | 010037 | 002412 |     | MOV R0,aTMP0        |        |              |                     |                              |
| 7887 |        |        |        |     | :0132 00B7 10 1F    |        | .DBYTE       | a010037             | :LOC 364 'MOV'               |
| 7888 |        |        |        |     | :0133 00B9 00 00    |        | .DBYTE       | 0                   | :LOC 366                     |
| 7889 | 036364 | 013706 | 002454 |     | MOV aWOLDSP,SP      |        |              |                     |                              |
| 7890 |        |        |        |     | :0134 00BB 17 C6    |        | .DBYTE       | a013706             | :LOC 370 'MOV'               |

```

:*****
:
: DATABL -- DATA TABLE CONTAINING THE DATA THAT
: IS TO BE NPR'D INTO THE 11'S MEMORY
:
:*****

```

```

DATABL
.DBYTE 0,a340 ;LOC'S 24 & 26

.DBYTE a012700,-1,a777 ;LOC'S 0 --> 4

.DBYTE a005001 ;LOC. 6

.DBYTE a062701 ;LOC'S 10 --> 362

.DBYTE a010037 ;LOC 364 'MOV'
.DBYTE 0 ;LOC 366

.DBYTE a013706 ;LOC 370 'MOV'

```

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SUBTEST 2'S M-CODE -- ROUTINE

```

7891 :0135 00BD 00 00
7892 036370 000137 035672 JMP @MHLTST2
7893 :0136 00BF 00 5F
7894 :0137 00C1 00 00
7895 036374 000240 NOP
7896 :0138 00C3 00 A0
7897 :0139 00C5
7898 :0140 00C5
7899 :0141 00C5
7900 :0142 00C5
7901 :0143 00C5
7902 :0144 00C5
7903 :0145 00C5
7904 :0146 00C5
7905 :0147 00C5
7906 :0148 00C5
7907 :0149 00C5
7908 :0150 00C5
7909 :0151 00C5
7910 :0152 00C5
7911 :0153 00C5
7912 :0154 00C5
7913 :0155 00C5
7914 :0156 00C5
7915 :0157 00C5
7916 :0158 00C5
7917 :0159 00C5
7918 :0160 00C5
7919 :0161 00C5
7920 036376 265 251 .BYTE 265,251
7921 :0162 00C5 B5 A9
7922 036400 215 000 246 .BYTE 215,000,246
7923 :0163 00C7 8D 00 A6
7924 036403 265 252 .BYTE 265,252
7925 :0164 00CA B5 AA
7926 036405 215 001 246 .BYTE 215,001,246
7927 :0165 00CC 8D 01 A6
7928 :0166 00CF
7929 036410 255 004 246 .BYTE 255,004,246
7930 :0167 00CF AD 04 A6
7931 036413 215 004 246 .BYTE 215,004,246
7932 :0168 00D2 8D 04 A6
7933 :0169 00D5
7934 036416 350 .BYTE 350
7935 :0170 00D5 E8
7936 036417 350 .BYTE 350
7937 :0171 00D6 E8
7938 :0172 00D7
7939 :0173 00D7
7940 :0174 00D7
7941 036420 054 004 246 .BYTE 054,004,246
7942 :0175 00D7 2C 04 A6
7943 036423 160 373 .BYTE 160,373
7944 :0176 00DA 70 FB
7945 036425 352 .BYTE 352
7946 :0177 00DC EA

```

```

.DBYTE 0 ;LOC 372
.DBYTE @000137 ;LOC 374 'JMP'
.DBYTE 0 ;LOC 376
.DBYTE @000240 ;'NOP' FOR LOC 4

```

```

: THE THREE WORDS FOR LOCATIONS 366, 372, & 376 ARE
: ASSEMBLED IN WHEN THIS CODE IS INCLUDED INTO THE
: DIAGNOSTIC

```

```

: 'NPR' SUBROUTINE:
: 1 TAKE THE DATA FROM THE DATA TABLE AS INDEXED BY
: 'X' AND PUT IT INTO THE NPR DATA OUT REGISTERS
: 2 GET THE CURRENT SETTING OF THE NPR CONTROL REG.
: AND WRITE IT BACK TO CAUSE A WORD NPR-OUT
: 3 INCREMENT THE NPR-OUT-ADDRESS-LOW REGISTER
: 4 WAIT FOR 'GOBUSY' TO GO LOW
: 5 RETURN TO CALLER

```

```

NPR LDA DATABL,X ;LOAD THE DATA-OUT REG'S
 STA NPRDOL
 LDA DATABL+1,X
 STA NPRDOH
 LDA NPRCTL
 STA NPRCTL ;KICK OFF A WORD NPR-OUT
 INX
 INX ;POINT TO THE NEXT DATA
 ; WORD
NPRWAT
 BIT NPRCTL ;WAIT FOR THE NPR TO
 BVS NPRWAT ; COMPLETE
 NOP

```

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SUBTEST 2'S M-CODE -- ROUTINE

7947 036426 346 070  
7948  
7949 036430 346 070  
7950  
7951  
7952 036432 140  
7953  
7954  
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7992  
7993  
7994  
7995  
7996  
7997  
7998  
7999  
8000  
8001  
8002 036433

```

.BYTE 346,070
:0178 00DD E6 38 INC NPRAOL ;POINT TO THE NEXT WORD
.BYTE 346,070
:0179 00DF E6 38 INC NPRAOL ;OF THE 11'S MEMORY
:0180 00E1
.BYTE 140
:0181 00E1 60 RTS ;RETURN TO CALLER
:0182 00E2
:
:
:ERRORS = 0000
:
:SBTTL SUBTEST 2'S M-CODE -- SYMBOL TABLE
:
:BIT0 0001 BIT1 0002 BIT2 0004 BIT4 0010
:BIT6 0040 BSEL7 0017 DATABL 009D DISINI 0001
:ENDLOP 008A FILOOP 007E HALT 0010 NONPR 0040
:NPR 00B9 NPRACH 0039 NPRAOL 0038 NPRAOX 003A
:NPRCTL A604 NPRDOH A601 NPRDOL A600 NPRWAT 00CB
:PWUP 0004 SDCLOW 0002 STARAM 003F WAIT1 0043
:
:END OF ASSEMBLY(V6A)
:SYMBOLS LEFT = 1476 OUT OF 1500
:
:COMPAS MICROSYSTEMS MINMIC CROSS ASSEMBLER PAGE C-1
:SBTTL SUBTEST 2'S M-CODE -- CROSS REFERENCE TABLE (CREF V01-05)
:
:
:BIT0 5# 33
:BIT1 6# 32
:BIT2 7# 31
:BIT4 8# 30
:BIT6 9# 29
:BSEL7 13# 43 45
:DATABL 127# 162 164
:DISINI 33# 55 57 84 86
:ENDLOP 101# 103
:FILOOP 93# 97
:HALT 30# 55 57
:NONPR 29# 55 57 84 86 113
:NPR 70 71 75 76 77 91 93
: 101 109 162#
:NPRACH 18# 61
:NPRAL 17# 18 19 69 74 108 178
: 179
:NPRAOX 19# 62
:NPRCTL 28# 56 58 85 87 114 167
: 168 175
:NPRDOH 24# 165
:NPRDOL 23# 24 163
:NPRWAT 174# 176
:PWUP 31# 55 57 84 86 113
:SDCLOW 32# 55 84
:STARAM 37# 40
:WAIT1 45# 46
MC2END:

```

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8003

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036434

SUBTEST 2'S M-CODE -- CROSS REFERENCE TABLE (CREF V01-05 )  
.EVEN

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HARDWARE PARAMETER CODING SECTION

.SBTTL HARDWARE PARAMETER CODING SECTION

```

://////
:/ THE HARDWARE PARAMETER CODING SECTION CONTAINS MACROS
:/ THAT ARE USED BY THE SUPERVISOR TO BUILD P-TABLES. THE
:/ MACROS ARE NOT EXECUTED AS MACHINE INSTRUCTIONS BUT ARE
:/ INTERPRETED BY THE SUPERVISOR AS DATA STRUCTURES. THE
:/ MACROS ALLOW THE SUPERVISOR TO ESTABLISH COMMUNICATIONS
:/ WITH THE OPERATOR.
://////

```

8004  
8005  
8006  
8007  
8008  
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036434  
036434 000030  
036436  
036436  
036440 036516  
036442 160020  
036444 177776  
036446  
036446 001031  
036450 036544  
036452 000000  
036454 000674  
036456  
036456 002032  
036460 036575  
036462 007000  
036464 000000  
036466 000007  
036470  
036470 007130  
036472 036711  
036474 000100  
036476  
036476 005032  
036500 036626  
036502 000007  
036504 000000  
036506 000002  
036510  
036510 007130  
036512 036773  
036514 000200  
036516  
036516  
036516 042504 044526 042503  
036544 042504 044526 042503  
036575 104 053105 041511

BGNHRD  
GPRMA ADRES,0,0,160020,177776,YES  
GPRMA VECTOR,2,0,0,674,YES  
GPRMD PRIRTY,4,0,7000,0,7,YES  
GPRML PU24.M,16,100,YES  
GPRMD BDTY.M,12,0,7,0,2,YES  
GPRML XMF G.M,16,200,YES  
ENDHRD  
.NLIST BEX  
ADDRES: .ASCIZ /DEVICE CSR ADDRESS : /  
VECTOR: .ASCIZ /DEVICE VECTOR ADDRESS : /  
PRIRTY: .ASCIZ /DEVICE PRIORITY LEVEL : /

.WORD L10070-LSHARD/2  
LSHARD::  
.WORD TSCODE  
.WORD ADDRES  
.WORD TSLOLIM  
.WORD TSHILIM  
.WORD TSCODE  
.WORD VECTOR  
.WORD TSLOLIM  
.WORD TSHILIM  
.WORD TSCODE  
.WORD PRIRTY  
.WORD 7000  
.WORD TSLOLIM  
.WORD TSHILIM  
.WORD TSCODE  
.WORD PU24.M  
.WORD 100  
.WORD TSCODE  
.WORD BDTY.M  
.WORD 7  
.WORD TSLOLIM  
.WORD TSHILIM  
.WORD TSCODE  
.WORD XMF G.M  
.WORD 200  
.EVEN  
L10070:

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HARDWARE PARAMETER CODING SECTION

|        |        |        |        |
|--------|--------|--------|--------|
| 036626 | 047502 | 051101 | 020104 |
| 036711 | 111    | 020123 | 044124 |
| 036773 | 111    | 020123 | 044124 |

```
BDTY.M: .ASCIZ /BOARD TYPE (0=M8064, 1=M8053-v.35, 2=M8053-EIA) : /
PU24.M: .ASCIZ /IS THE PROCESSOR STRAPPED TO MODE 0 ON POWER-UP? /
XMG.M: .ASCIZ /IS THIS A MANUFACTURING TEST STAND?/
```

|      |        |
|------|--------|
| 8057 | 037040 |
|------|--------|

```
.LIST BEX
.EVEN
```

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SOFTWARE PARAMETER CODING SECTION

.SBTTL SOFTWARE PARAMETER CODING SECTION

8058  
8059  
8060  
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8067  
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8071  
8072  
8073  
8074  
8075  
8076

037040  
037040 000000  
037042  
037042  
037042

:///////////////////////////////////////////////////////////////////  
:// THE SOFTWARE PARAMETER CODING SECTION CONTAINS MACROS  
:// THAT ARE USED BY THE SUPERVISOR TO BUILD P-TABLES. THE  
:// MACROS ARE NOT EXECUTED AS MACHINE INSTRUCTIONS BUT ARE  
:// INTERPRETED BY THE SUPERVISOR AS DATA STRUCTURES. THE  
:// MACROS ALLOW THE SUPERVISOR TO ESTABLISH COMMUNICATIONS  
:// WITH THE OPERATOR.  
:///////////////////////////////////////////////////////////////////

BGNSFT  
  
ENDSFT

.WORD L10071-LSSOFT/2  
LSSOFT::  
  
.EVEN  
L10071:

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PATCH AREA FOR DEBUG

```

8077
8078 037042
8079 037142 037142
8080 037142 000240
8081 037144 000240
8082 037146 000240
8083
8084
8085
8086
8087 037150
8088 037150
8089
8090 037150 000000
8091 037152 000000
8092 037154
8093 000001

```

.SBTTL PATCH AREA FOR DEBUG  
PATCH:

```

.=.+100
NOP
NOP
NOP

```

\*\*\*\*\*

```

.SBTTL 'ENDMOD' & 'LASTAD'
 ENDMOD
 LASTAD

```

```

.EVEN
.WORD 0
.WORD 0

```

```

L$LAST::
.END

```





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CROSS REFERENCE TABLE -- USER SYMBOLS

|         |        |       |       |      |       |       |       |       |       |      |       |      |      |      |  |  |  |  |  |
|---------|--------|-------|-------|------|-------|-------|-------|-------|-------|------|-------|------|------|------|--|--|--|--|--|
| BSEL6   | 002334 | 1719# | 1982  | 2081 | 2243  | 6966* | 6994  |       |       |      |       |      |      |      |  |  |  |  |  |
| BSEL7   | 002336 | 1721# | 2244  | 6919 | 7255* | 7259  | 7271* | 7309* | 7344* | 7348 | 7361* |      |      |      |  |  |  |  |  |
| BSLT0 = | 000020 | 1546# |       |      |       |       |       |       |       |      |       |      |      |      |  |  |  |  |  |
| BSLT1 = | 000021 | 1547# |       |      |       |       |       |       |       |      |       |      |      |      |  |  |  |  |  |
| BSLT2 = | 000022 | 1549# |       |      |       |       |       |       |       |      |       |      |      |      |  |  |  |  |  |
| BSLT3 = | 000023 | 1550# |       |      |       |       |       |       |       |      |       |      |      |      |  |  |  |  |  |
| BSLT4 = | 000024 | 1552# |       |      |       |       |       |       |       |      |       |      |      |      |  |  |  |  |  |
| BSLT5 = | 000025 | 1553# |       |      |       |       |       |       |       |      |       |      |      |      |  |  |  |  |  |
| BSLT6 = | 000026 | 1555# |       |      |       |       |       |       |       |      |       |      |      |      |  |  |  |  |  |
| BSLT7 = | 000027 | 1556# |       |      |       |       |       |       |       |      |       |      |      |      |  |  |  |  |  |
| BSR0    | 002212 | 1614# | 2237* | 3533 |       |       |       |       |       |      |       |      |      |      |  |  |  |  |  |
| BSR1    | 002214 | 1616# | 2238* | 3531 |       |       |       |       |       |      |       |      |      |      |  |  |  |  |  |
| BSR10   | 002232 | 1629# | 2245* | 3575 |       |       |       |       |       |      |       |      |      |      |  |  |  |  |  |
| BSR11   | 002234 | 1630# | 2246* | 3573 |       |       |       |       |       |      |       |      |      |      |  |  |  |  |  |
| BSR12   | 002236 | 1631# | 2247* | 3571 |       |       |       |       |       |      |       |      |      |      |  |  |  |  |  |
| BSR13   | 002240 | 1632# | 2248* | 3569 |       |       |       |       |       |      |       |      |      |      |  |  |  |  |  |
| BSR14   | 002242 | 1633# | 2249* | 3596 |       |       |       |       |       |      |       |      |      |      |  |  |  |  |  |
| BSR15   | 002244 | 1634# | 2250* | 3594 |       |       |       |       |       |      |       |      |      |      |  |  |  |  |  |
| BSR16   | 002246 | 1635# | 2251* | 3592 |       |       |       |       |       |      |       |      |      |      |  |  |  |  |  |
| BSR17   | 002250 | 1636# | 2252* | 3590 |       |       |       |       |       |      |       |      |      |      |  |  |  |  |  |
| BSR2    | 002216 | 1618# | 2239* | 3529 |       |       |       |       |       |      |       |      |      |      |  |  |  |  |  |
| BSR3    | 002220 | 1620# | 2240* | 3527 |       |       |       |       |       |      |       |      |      |      |  |  |  |  |  |
| BSR4    | 002222 | 1622# | 2241* | 3554 |       |       |       |       |       |      |       |      |      |      |  |  |  |  |  |
| BSR5    | 002224 | 1624# | 2242* | 3552 |       |       |       |       |       |      |       |      |      |      |  |  |  |  |  |
| BSR6    | 002226 | 1626# | 2243* | 3550 |       |       |       |       |       |      |       |      |      |      |  |  |  |  |  |
| BSR7    | 002230 | 1628# | 2244* | 3548 |       |       |       |       |       |      |       |      |      |      |  |  |  |  |  |
| BT1 =   | 002654 | 1896# | 4684  |      |       |       |       |       |       |      |       |      |      |      |  |  |  |  |  |
| BT2 =   | 002740 | 1897# | 2548  | 2552 | 2556  | 2560  | 2564  | 2568  | 2572  | 2576 | 2580  | 4678 | 4723 |      |  |  |  |  |  |
| BUFARE  | 002654 | 1873# | 1879  | 1896 | 1897  | 4852  | 4919  | 5015  | 5179  | 7331 | 7371  | 7389 | 7415 |      |  |  |  |  |  |
| CFMT0   | 022566 | 3779  | 3902# |      |       |       |       |       |       |      |       |      |      |      |  |  |  |  |  |
| CFMT2   | 022655 | 3856  | 3902# |      |       |       |       |       |       |      |       |      |      |      |  |  |  |  |  |
| CFMT3   | 022735 | 3862  | 3902# |      |       |       |       |       |       |      |       |      |      |      |  |  |  |  |  |
| CONSOL  | 002314 | 1660# | 3758* | 3775 | 3898* |       |       |       |       |      |       |      |      |      |  |  |  |  |  |
| CONTIN  | 022456 | 3750  | 3869# |      |       |       |       |       |       |      |       |      |      |      |  |  |  |  |  |
| CONTST  | 022556 | 3761  | 3898# |      |       |       |       |       |       |      |       |      |      |      |  |  |  |  |  |
| CSREGS= | 000010 | 1442# |       |      |       |       |       |       |       |      |       |      |      |      |  |  |  |  |  |
| CSAU =  | 000052 | 1173# | 4008  |      |       |       |       |       |       |      |       |      |      |      |  |  |  |  |  |
| CSAUTO= | 000061 | 1173# | 3958  |      |       |       |       |       |       |      |       |      |      |      |  |  |  |  |  |
| CSBRK = | 000022 | 1173# |       |      |       |       |       |       |       |      |       |      |      |      |  |  |  |  |  |
| CSBSEG= | 000004 | 1173# |       |      |       |       |       |       |       |      |       |      |      |      |  |  |  |  |  |
| CSBSUB= | 000002 | 1173# | 4056  | 4847 | 4914  | 5097  | 5169  | 5286  | 5494  | 5869 | 6014  | 6263 | 6345 | 6427 |  |  |  |  |  |
|         |        | 6509  | 6585  | 6639 | 6832  | 6944  | 7233  | 7316  |       |      |       |      |      |      |  |  |  |  |  |
| CSCEFG= | 000045 | 1173# |       |      |       |       |       |       |       |      |       |      |      |      |  |  |  |  |  |
| CSCLCK= | 000062 | 1173# |       |      |       |       |       |       |       |      |       |      |      |      |  |  |  |  |  |
| CSCLEA= | 000012 | 1173# | 3980  |      |       |       |       |       |       |      |       |      |      |      |  |  |  |  |  |
| CSCLOS= | 000035 | 1173# |       |      |       |       |       |       |       |      |       |      |      |      |  |  |  |  |  |
| CSCLP1= | 000006 | 1173# |       |      |       |       |       |       |       |      |       |      |      |      |  |  |  |  |  |
| CSVEC=  | 000036 | 1173# | 3769  | 3947 | 3974  | 3977  | 6907  | 6990  | 7296  |      |       |      |      |      |  |  |  |  |  |
| CSDECL= | 000044 | 1173# |       |      |       |       |       |       |       |      |       |      |      |      |  |  |  |  |  |
| CSDODU= | 000051 | 1173# | 3952  |      |       |       |       |       |       |      |       |      |      |      |  |  |  |  |  |
| CSDRPT= | 000024 | 1173# |       |      |       |       |       |       |       |      |       |      |      |      |  |  |  |  |  |
| CSDU =  | 000053 | 1173# | 3995  |      |       |       |       |       |       |      |       |      |      |      |  |  |  |  |  |
| CSEDIT= | 000003 | 1173# | 1248  |      |       |       |       |       |       |      |       |      |      |      |  |  |  |  |  |
| CSERDF= | 000055 | 1173# | 2808  | 2842 | 4083  | 4108  | 4137  | 4156  | 4175  | 4200 | 4225  | 4245 | 4269 | 4295 |  |  |  |  |  |
|         |        | 4319  | 4342  | 4480 | 4553  | 4588  | 4621  | 4707  | 4752  | 4794 | 4894  | 4961 | 5058 | 5158 |  |  |  |  |  |
|         |        | 5230  | 5315  | 5390 | 5455  | 5462  | 5473  | 5522  | 5582  | 5630 | 5637  | 5651 | 5941 | 5969 |  |  |  |  |  |







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CROSS REFERENCE TABLE -- USER SYMBOLS

|          |          |       |       |       |       |       |      |      |      |      |      |      |      |      |  |  |  |  |  |
|----------|----------|-------|-------|-------|-------|-------|------|------|------|------|------|------|------|------|--|--|--|--|--|
| EVL =    | 000004 G | 1426# |       |       |       |       |      |      |      |      |      |      |      |      |  |  |  |  |  |
| EXECUT=  | 000005   | 1531# | 6969  | 7256  | 7345  |       |      |      |      |      |      |      |      |      |  |  |  |  |  |
| ESEND =  | 002100   | 1173# |       |       |       |       |      |      |      |      |      |      |      |      |  |  |  |  |  |
| ESLOAD=  | 000035   | 1173# | 1271  |       |       |       |      |      |      |      |      |      |      |      |  |  |  |  |  |
| FMT02A   | 013127   | 3343  | 3669  | 3699# |       |       |      |      |      |      |      |      |      |      |  |  |  |  |  |
| FMT06    | 013530   | 2984  | 3041  | 3699# |       |       |      |      |      |      |      |      |      |      |  |  |  |  |  |
| FMT06A   | 013542   | 2974  | 3699# |       |       |       |      |      |      |      |      |      |      |      |  |  |  |  |  |
| FMT07    | 013476   | 2968  | 3313  | 3699# |       |       |      |      |      |      |      |      |      |      |  |  |  |  |  |
| FMT09    | 013621   | 3618  | 3656  | 3699# |       |       |      |      |      |      |      |      |      |      |  |  |  |  |  |
| FMT09A   | 013673   | 3635  | 3676  | 3699# |       |       |      |      |      |      |      |      |      |      |  |  |  |  |  |
| FMT10    | 013714   | 3329  | 3628  | 3699# |       |       |      |      |      |      |      |      |      |      |  |  |  |  |  |
| FMT11    | 013770   | 2933  | 2950  | 3261  | 3281  | 3699# |      |      |      |      |      |      |      |      |  |  |  |  |  |
| FMT12    | 007672   | 3175  | 3225# |       |       |       |      |      |      |      |      |      |      |      |  |  |  |  |  |
| FMT12A   | 007732   | 3181  | 3225# |       |       |       |      |      |      |      |      |      |      |      |  |  |  |  |  |
| FMT12D   | 010020   | 3198  | 3225# |       |       |       |      |      |      |      |      |      |      |      |  |  |  |  |  |
| FMT12E   | 010063   | 3206  | 3225# |       |       |       |      |      |      |      |      |      |      |      |  |  |  |  |  |
| FMT12F   | 010145   | 3216  | 3225# |       |       |       |      |      |      |      |      |      |      |      |  |  |  |  |  |
| FMT13A   | 010546   | 3233  | 3300# |       |       |       |      |      |      |      |      |      |      |      |  |  |  |  |  |
| FMT13B   | 010611   | 3239  | 3300# |       |       |       |      |      |      |      |      |      |      |      |  |  |  |  |  |
| FMT13C   | 010656   | 3245  | 3300# |       |       |       |      |      |      |      |      |      |      |      |  |  |  |  |  |
| FMT13D   | 010721   | 3251  | 3300# |       |       |       |      |      |      |      |      |      |      |      |  |  |  |  |  |
| FMT13E   | 010764   | 3271  | 3291  | 3300# |       |       |      |      |      |      |      |      |      |      |  |  |  |  |  |
| FMT16    | 014007   | 2999  | 3013  | 3031  | 3699# |       |      |      |      |      |      |      |      |      |  |  |  |  |  |
| FMT16A   | 014032   | 3061  | 3081  | 3101  | 3699# |       |      |      |      |      |      |      |      |      |  |  |  |  |  |
| FMT17    | 014074   | 3145  | 3699# |       |       |       |      |      |      |      |      |      |      |      |  |  |  |  |  |
| FMT17A   | 014153   | 3153  | 3699# |       |       |       |      |      |      |      |      |      |      |      |  |  |  |  |  |
| FMT17B   | 014234   | 3160  | 3699# |       |       |       |      |      |      |      |      |      |      |      |  |  |  |  |  |
| FMT17C   | 014307   | 3137  | 3699# |       |       |       |      |      |      |      |      |      |      |      |  |  |  |  |  |
| FMT4     | 013213   | 2923  | 3520  | 3699# |       |       |      |      |      |      |      |      |      |      |  |  |  |  |  |
| FMT4A    | 013253   | 3534  | 3576  | 3699# |       |       |      |      |      |      |      |      |      |      |  |  |  |  |  |
| FMT4B    | 013306   | 2940  | 3541  | 3562  | 3583  | 3699# |      |      |      |      |      |      |      |      |  |  |  |  |  |
| FMT4C    | 013313   | 3555  | 3597  | 3699# |       |       |      |      |      |      |      |      |      |      |  |  |  |  |  |
| FMT5     | 013346   | 2896  | 3699# |       |       |       |      |      |      |      |      |      |      |      |  |  |  |  |  |
| FMT5A    | 013411   | 2913  | 3699# |       |       |       |      |      |      |      |      |      |      |      |  |  |  |  |  |
| FMT50A   | 014367   | 3699# |       |       |       |       |      |      |      |      |      |      |      |      |  |  |  |  |  |
| FMT50B   | 014441   | 3699# |       |       |       |       |      |      |      |      |      |      |      |      |  |  |  |  |  |
| FMT50C   | 014522   | 3699# |       |       |       |       |      |      |      |      |      |      |      |      |  |  |  |  |  |
| FMT50D   | 014562   | 3699# |       |       |       |       |      |      |      |      |      |      |      |      |  |  |  |  |  |
| FMT50E   | 014577   | 3699# |       |       |       |       |      |      |      |      |      |      |      |      |  |  |  |  |  |
| FMT50M   | 014614   | 3699# |       |       |       |       |      |      |      |      |      |      |      |      |  |  |  |  |  |
| FMT51A   | 014673   | 3378  | 3699# |       |       |       |      |      |      |      |      |      |      |      |  |  |  |  |  |
| FMT52A   | 011557   | 3413  | 3448# |       |       |       |      |      |      |      |      |      |      |      |  |  |  |  |  |
| FMT52B   | 011607   | 3430  | 3448# |       |       |       |      |      |      |      |      |      |      |      |  |  |  |  |  |
| FMT52C   | 011652   | 3438  | 3448# |       |       |       |      |      |      |      |      |      |      |      |  |  |  |  |  |
| FMT52H   | 011514   | 3400  | 3448# |       |       |       |      |      |      |      |      |      |      |      |  |  |  |  |  |
| FMT60    | 012034   | 3464  | 3492# |       |       |       |      |      |      |      |      |      |      |      |  |  |  |  |  |
| FMT61    | 012073   | 3470  | 3492# |       |       |       |      |      |      |      |      |      |      |      |  |  |  |  |  |
| FMT62    | 012134   | 3480  | 3492# |       |       |       |      |      |      |      |      |      |      |      |  |  |  |  |  |
| FRSPAS   | 002304   | 1656# | 3794* |       |       |       |      |      |      |      |      |      |      |      |  |  |  |  |  |
| FRSTIM   | 002302   | 1655# | 3889* |       |       |       |      |      |      |      |      |      |      |      |  |  |  |  |  |
| FSAU =   | 000015   | 1173# | 4005  | 4007  |       |       |      |      |      |      |      |      |      |      |  |  |  |  |  |
| FSAUTO=  | 000020   | 1173# | 3921  | 3957  |       |       |      |      |      |      |      |      |      |      |  |  |  |  |  |
| F\$BGN = | 000040   | 1173# | 1175  | 2801  | 2835  | 2869  | 2878 | 2888 | 2963 | 3114 | 3123 | 3132 | 3173 | 3231 |  |  |  |  |  |
|          |          | 3306  | 3358  | 3392  | 3462  | 3708  | 3721 | 3921 | 3971 | 3989 | 4005 | 4053 | 4055 | 4064 |  |  |  |  |  |
|          |          | 4073  | 4078  | 4098  | 4103  | 4113  | 4131 | 4149 | 4161 | 4169 | 4190 | 4195 | 4215 | 4220 |  |  |  |  |  |
|          |          | 4240  | 4250  | 4259  | 4264  | 4285  | 4290 | 4309 | 4314 | 4324 | 4337 | 4355 | 4359 | 4423 |  |  |  |  |  |

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CROSS REFERENCE TABLE -- USER SYMBOLS

|  |       |       |       |       |       |       |      |       |      |       |       |       |       |
|--|-------|-------|-------|-------|-------|-------|------|-------|------|-------|-------|-------|-------|
|  | 4429  | 4441  | 4452  | 4461  | 4466  | 4476  | 4492 | 4502  | 4511 | 4525  | 4540  | 4549  | 45 8  |
|  | 4567  | 4584  | 4593  | 4603  | 4617  | 4626  | 4630 | 4663  | 4669 | 4712  | 4757  | 4764  | 4 7   |
|  | 4833  | 4840  | 4846  | 4907  | 4913  | 4974  | 4977 | 5001  | 5007 | 5072  | 5092  | 5096  | 5103  |
|  | 5116  | 5125  | 5134  | 5143  | 5152  | 5163  | 5168 | 5175  | 5188 | 5197  | 5206  | 5215  | 5224  |
|  | 5235  | 5238  | 5262  | 5268  | 5275  | 5285  | 5320 | 5334  | 5343 | 5357  | 5372  | 5381  | 5395  |
|  | 5409  | 5419  | 5430  | 5439  | 5448  | 5467  | 5478 | 5488  | 5493 | 5527  | 5548  | 5564  | 5573  |
|  | 5587  | 5614  | 5623  | 5642  | 5656  | 5667  | 5670 | 5792  | 5849 | 5854  | 5862  | 5868  | 5897  |
|  | 5946  | 5981  | 5996  | 6004  | 6013  | 6041  | 6096 | 6136  | 6152 | 6169  | 6180  | 6183  | 6235  |
|  | 6241  | 6262  | 6274  | 6322  | 6344  | 6356  | 6404 | 6426  | 6438 | 6486  | 6508  | 6520  | 6568  |
|  | 6584  | 6596  | 6622  | 6638  | 6650  | 6676  | 6681 | 6697  | 6706 | 6740  | 6757  | 6763  | 6782  |
|  | 6804  | 6809  | 6815  | 6831  | 6838  | 6933  | 6943 | 6951  | 7009 | 7012  | 7207  | 7212  | 7218  |
|  | 7232  | 7239  | 7290  | 7311  | 7315  | 7322  | 7410 | 7465  | 7468 | 8018  | 8072  | 8088  |       |
|  | 1173# | 3971  | 3979  |       |       |       |      |       |      |       |       |       |       |
|  | 1173# | 3989  | 3994  |       |       |       |      |       |      |       |       |       |       |
|  | 1173# | 1175  | 2822  | 2856  | 2874  | 2884  | 2959 | 3110  | 3119 | 3128  | 3169  | 3225  | 3300  |
|  | 3353  | 3388  | 3447  | 3492  | 3893  | 3959  | 3981 | 3996  | 4009 | 4053  | 4055  | 4064  | 4073  |
|  | 4078  | 4098  | 4103  | 4113  | 4131  | 4149  | 4161 | 4169  | 4190 | 4195  | 4215  | 4220  | 4240  |
|  | 4250  | 4259  | 4264  | 4285  | 4290  | 4309  | 4314 | 4324  | 4337 | 4355  | 4357  | 4359  | 4361  |
|  | 4423  | 4429  | 4441  | 4452  | 4461  | 4466  | 4476 | 4492  | 4502 | 4511  | 4525  | 4540  | 4549  |
|  | 4558  | 4567  | 4584  | 4593  | 4603  | 4617  | 4626 | 4630  | 4632 | 4663  | 4669  | 4712  | 4757  |
|  | 4764  | 4779  | 4801  | 4833  | 4840  | 4846  | 4907 | 4909  | 4913 | 4974  | 4976  | 4977  | 4979  |
|  | 5001  | 5007  | 5072  | 5074  | 5092  | 5096  | 5103 | 5116  | 5125 | 5134  | 5143  | 5152  | 5163  |
|  | 5165  | 5168  | 5175  | 5188  | 5197  | 5206  | 5215 | 5224  | 5235 | 5237  | 5238  | 5240  | 5262  |
|  | 5268  | 5275  | 5285  | 5320  | 5334  | 5343  | 5357 | 5372  | 5381 | 5395  | 5409  | 5419  | 5430  |
|  | 5439  | 5448  | 5467  | 5478  | 5488  | 5490  | 5493 | 5527  | 5548 | 5564  | 5573  | 5587  | 5614  |
|  | 5623  | 5642  | 5656  | 5667  | 5669  | 5670  | 5672 | 5825  | 5849 | 5854  | 5862  | 5868  | 5897  |
|  | 5946  | 5981  | 5996  | 6004  | 6006  | 6013  | 6041 | 6096  | 6136 | 6152  | 6169  | 6180  | 6182  |
|  | 6183  | 6185  | 6235  | 6241  | 6262  | 6274  | 6322 | 6324  | 6344 | 6356  | 6404  | 6406  | 6426  |
|  | 6438  | 6486  | 6488  | 6508  | 6520  | 6568  | 6570 | 6584  | 6596 | 6622  | 6624  | 6638  | 6650  |
|  | 6676  | 6678  | 6681  | 6683  | 6697  | 6706  | 6740 | 6742  | 6757 | 6763  | 6782  | 6784  | 6804  |
|  | 6809  | 6815  | 6831  | 6838  | 6933  | 6935  | 6943 | 6951  | 7009 | 7011  | 7012  | 7014  | 7207  |
|  | 7212  | 7218  | 7232  | 7239  | 7290  | 7311  | 7313 | 7315  | 7322 | 7410  | 7465  | 7467  | 7468  |
|  | 7470  | 8055  | 8077  | 8088  |       |       |      |       |      |       |       |       |       |
|  | 1173# | 8018  | 8053  |       |       |       |      |       |      |       |       |       |       |
|  | 1173# | 1323  | 1349  |       |       |       |      |       |      |       |       |       |       |
|  | 1173# | 3721  | 3891  |       |       |       |      |       |      |       |       |       |       |
|  | 1173# | 5268  | 5275  | 5854  | 5862  | 6241  | 6809 | 6815  | 7212 | 7218  |       |       |       |
|  | 1173# | 1175  | 8088  |       |       |       |      |       |      |       |       |       |       |
|  | 1173# | 2869  | 2872  | 2878  | 2882  | 2888  | 2957 | 2963  | 3108 | 3114  | 3117  | 3123  | 3126  |
|  | 3132  | 3167  | 3173  | 3223  | 3231  | 3298  | 3306 | 3351  | 3358 | 3386  | 3392  | 3445  | 3462  |
|  | 3490  |       |       |       |       |       |      |       |      |       |       |       |       |
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|  | 1173# |       |       |       |       |       |      |       |      |       |       |       |       |
|  | 1173# |       |       |       |       |       |      |       |      |       |       |       |       |
|  | 1173# |       |       |       |       |       |      |       |      |       |       |       |       |
|  | 1173# | 8072  | 8075  |       |       |       |      |       |      |       |       |       |       |
|  | 1173# | 2801  | 2820  | 2835  | 2854  | 5792  | 5823 |       |      |       |       |       |       |
|  | 1173# | 4056  | 4355  | 4847  | 4907  | 4914  | 4974 | 5097  | 5163 | 5169  | 5235  | 5286  | 5488  |
|  | 5494  | 5667  | 5869  | 6004  | 6014  | 6180  | 6263 | 6322  | 6345 | 6404  | 6427  | 6486  | 6509  |
|  | 6568  | 6585  | 6622  | 6639  | 6676  | 6832  | 6933 | 6944  | 7009 | 7233  | 7311  | 7316  | 7465  |
|  | 1173# | 1358  | 1362  |       |       |       |      |       |      |       |       |       |       |
|  | 1173# | 4054  | 4359  | 4424  | 4630  | 4664  | 4799 | 4834  | 4977 | 5002  | 5072  | 5093  | 5238  |
|  | 5263  | 5670  | 5850  | 6183  | 6236  | 6681  | 6698 | 6740  | 6758 | 6782  | 6805  | 7012  | 7208  |
|  | 7468  |       |       |       |       |       |      |       |      |       |       |       |       |
|  | 1642# | 2030* | 2070* | 2113* | 2176* | 2890  | 2912 | 2990  | 3019 | 3046  | 3191  | 3328  | 3342  |
|  | 3506  | 3627  | 3668  | 4687* | 4729* | 4730* | 4737 | 4770* | 4779 | 4883* | 4950* | 5046* | 5308* |

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FSJMP = 000050  
FSMOD = 000000  
FSMSG = 000011

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FSPWR = 000017  
FSRPT = 000012  
FSSEG = 000003  
FSSOFT= 000005  
FSSRV = 000010  
FSSUB = 000002

FSSW = 000014  
FSTEST= 000001

GDATA 002254







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## CROSS REFERENCE TABLE -- USER SYMBOLS

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|--------|--------|---|-------|-------|-------|
| LSAUTO | 023014 | G | 1279  | 3921# |       |
| LSCCP  | 002106 | G | 1276# |       |       |
| LSCLEA | 023140 | G | 1277  | 3971# |       |
| LSCO   | 002032 | G | 1232# |       |       |
| LSDEPO | 002011 | G | 1214# |       |       |
| LSDESC | 003274 | G | 1269  | 1922# |       |
| LSDESP | 002076 | G | 1268# |       |       |
| LSDEVP | 002060 | G | 1254# |       |       |
| LSDISP | 002124 | G | 1239  | 1299# |       |
| LSDLY  | 002116 | G | 1284# | 6821  | 7224  |
| LSDTP  | 002040 | G | 1238# |       |       |
| LSDTYP | 002034 | G | 1234# |       |       |
| LSDU   | 023156 | G | 1265  | 3989# |       |
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| LSDVTY | 003254 | G | 1255  | 1910# |       |
| LSEF   | 002052 | G | 1249# |       |       |
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| LSEVRT | 002202 | G | 1273  | 1604# |       |
| LSETP  | 002102 | G | 1272# |       |       |
| LSEXP1 | 002046 | G | 1244# |       |       |
| LSEXP4 | 002064 | G | 1258# |       |       |
| LSEXP5 | 002066 | G | 1260# |       |       |
| LSHARD | 036436 | G | 1221  | 8018  | 8019# |
| LSHIME | 002120 | G | 1286# | 5270  |       |
| LSHPCP | 002016 | G | 1220# |       |       |
| LSHPTP | 002022 | G | 1224# |       |       |
| LSHW   | 002160 | G | 1225  | 1323  | 1324# |
| LSICP  | 002104 | G | 1274# |       |       |
| LSINIT | 022020 | G | 1275  | 3721# |       |
| LSLADP | 002026 | G | 1228# |       |       |
| LSLAST | 037154 | G | 1229  | 8092# |       |
| LSLOAD | 002100 | G | 1270# |       |       |
| LSLUN  | 002074 | G | 1266# |       |       |
| LSPREV | 002050 | G | 1246# |       |       |
| LSNAME | 002000 | G | 1203# |       |       |
| LSPRIO | 002042 | G | 1240# |       |       |
| LSPROT | 022012 | G | 1281  | 3708# |       |
| LSPRT  | 002112 | G | 1280# |       |       |
| LSREPP | 002062 | G | 1256# |       |       |
| LSREV  | 002010 | G | 1212# |       |       |
| LSOFT  | 037042 | G | 8072  | 8073# |       |
| LSSPC  | 002056 | G | 1252# |       |       |
| LSSPCP | 002020 | G | 1222# |       |       |
| LSSTP  | 002024 | G | 1226# |       |       |
| LSSTA  | 002030 | G | 1230# |       |       |
| LSW    | 002202 | G | 1358  | 1359# |       |
| LSTEST | 002114 | G | 1282# |       |       |
| LSTIML | 002014 | G | 1218# |       |       |
| LSUNIT | 002012 | G | 1216# |       |       |
| L10000 | 002200 |   | 1323  | 1349# |       |
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| L10003 | 006206 |   | 2854# |       |       |
| L10004 | 006216 |   | 2872# |       |       |
| L10005 | 006230 |   | 2882# |       |       |
| L10006 | 006530 |   | 2957# |       |       |







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CROSS REFERENCE TABLE -- USER SYMBOLS

|         |        |         |       |       |       |       |       |       |       |       |       |      |       |      |
|---------|--------|---------|-------|-------|-------|-------|-------|-------|-------|-------|-------|------|-------|------|
| SEL16   | 002354 | 1732#   | 2265  |       |       |       |       |       |       |       |       |      |       |      |
| SEL2    | 002324 | 1714#   | 2259  | 6969* | 7256* | 7345* |       |       |       |       |       |      |       |      |
| SEL4    | 002330 | 1717#   | 2058* | 2101* | 2143* | 2164* | 2260  | 6857* | 6968* | 7254* | 7270* | 7300 | 7343* |      |
| SEL6    | 002334 | 1720#   | 2124  | 2144* | 2165* | 2261  | 6856* |       |       |       |       |      |       |      |
| SFPTBL  | 002202 | G 1360# |       |       |       |       |       |       |       |       |       |      |       |      |
| SLT0 =  | 000020 | 1545#   | 1546  | 1547  | 1548  | 1549  | 1550  | 1551  | 1552  | 1553  | 1554  | 1555 | 1556  |      |
| SLT2 =  | 000022 | 1548#   |       |       |       |       |       |       |       |       |       |      |       |      |
| SLT4 =  | 000024 | 1551#   |       |       |       |       |       |       |       |       |       |      |       |      |
| SLT6 =  | 000026 | 1554#   |       |       |       |       |       |       |       |       |       |      |       |      |
| SMCODE  | 035006 | 6963    | 6964  | 7018# |       |       |       |       |       |       |       |      |       |      |
| SR =    | 120012 | 1572#   | 4470  | 4518  | 4543  | 4561  | 4597  |       |       |       |       |      |       |      |
| STALL   | 005132 | 2537#   | 4605  | 4606  | 4607  |       |       |       |       |       |       |      |       |      |
| STARES  | 002306 | 1657#   | 3754* | 3787* | 3793* | 3948  |       |       |       |       |       |      |       |      |
| STARST  | 022072 | 3729    | 3754# |       |       |       |       |       |       |       |       |      |       |      |
| SVCGBL= | 000000 | 1173#   | 1175  | 1182# | 1203  | 1212  | 1214  | 1216  | 1218  | 1220  | 1222  | 1224 | 1226  | 1228 |
|         |        | 1230    | 1232  | 1234  | 1236  | 1238  | 1240  | 1242  | 1244  | 1246  | 1249  | 1252 | 1254  | 1256 |
|         |        | 1258    | 1260  | 1262  | 1264  | 1266  | 1268  | 1270  | 1272  | 1274  | 1276  | 1278 | 1280  | 1282 |
|         |        | 1284    | 1286  | 1299  | 1324  | 1325  | 1359  | 1360  | 1504  | 1910  | 1922  | 2801 | 2835  | 2869 |
|         |        | 2878    | 2888  | 2963  | 3114  | 3123  | 3132  | 3173  | 3231  | 3306  | 3358  | 3392 | 3462  | 3708 |
|         |        | 3721    | 3921  | 3971  | 3989  | 4005  | 5792  | 8019  | 8073  | 8092# | 8093  |      |       |      |
| SVCINS= | 000001 | 1173#   | 1179# | 1204  | 1205  | 1206  | 1207  | 1208  | 1209  | 1210  | 1211  | 1213 | 1215  | 1217 |
|         |        | 1219    | 1221  | 1223  | 1225  | 1227  | 1229  | 1231  | 1233  | 1235  | 1237  | 1239 | 1241  | 1243 |
|         |        | 1245    | 1247  | 1248  | 1250  | 1251  | 1253  | 1255  | 1257  | 1259  | 1261  | 1263 | 1265  | 1267 |
|         |        | 1269    | 1271  | 1273  | 1275  | 1277  | 1279  | 1281  | 1283  | 1285  | 1287  | 1298 | 1300  | 1301 |
|         |        | 1302    | 1303  | 1304  | 1305  | 1306  | 1307  | 1308  | 1309  | 1310  | 1311  | 1312 | 1323  | 1358 |
|         |        | 1911    | 1914  | 1923  | 1930  | 1993  | 1994  | 1995  | 1996  | 2033  | 2034  | 2035 | 2036  | 2073 |
|         |        | 2074    | 2075  | 2076  | 2116  | 2117  | 2118  | 2119  | 2179  | 2180  | 2181  | 2182 | 2732  | 2733 |
|         |        | 2734    | 2735  | 2808  | 2809  | 2810  | 2811  | 2821  | 2842  | 2843  | 2844  | 2845 | 2855  | 2873 |
|         |        | 2883    | 2894  | 2895  | 2896  | 2897  | 2898  | 2899  | 2900  | 2910  | 2911  | 2912 | 2913  | 2914 |
|         |        | 2915    | 2916  | 2917  | 2921  | 2922  | 2923  | 2924  | 2925  | 2926  | 2927  | 2929 | 2930  | 2931 |
|         |        | 2932    | 2933  | 2934  | 2935  | 2936  | 2937  | 2939  | 2940  | 2941  | 2942  | 2943 | 2944  | 2946 |
|         |        | 2947    | 2948  | 2949  | 2950  | 2951  | 2952  | 2953  | 2954  | 2958  | 2966  | 2967 | 2968  | 2969 |
|         |        | 2970    | 2971  | 2972  | 2974  | 2975  | 2976  | 2977  | 2978  | 2983  | 2984  | 2985 | 2986  | 2987 |
|         |        | 2988    | 2992  | 2993  | 2994  | 2995  | 2996  | 2997  | 2998  | 2999  | 3000  | 3001 | 3002  | 3003 |
|         |        | 3006    | 3007  | 3008  | 3009  | 3010  | 3011  | 3012  | 3013  | 3014  | 3015  | 3016 | 3017  | 3024 |
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|         |        | 3072    | 3073  | 3074  | 3075  | 3076  | 3077  | 3078  | 3079  | 3080  | 3081  | 3082 | 3083  | 3084 |
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|         |        | 3257    | 3258  | 3259  | 3260  | 3261  | 3262  | 3263  | 3264  | 3265  | 3267  | 3268 | 3269  | 3270 |
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|         |        | 3337    | 3338  | 3339  | 3340  | 3341  | 3342  | 3343  | 3344  | 3345  | 3346  | 3347 | 3352  | 3373 |
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## CROSS REFERENCE TABLE -- USER SYMBOLS

|      |      |      |      |      |      |      |      |      |      |      |      |      |
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| 3467 | 3468 | 3470 | 3471 | 3472 | 3473 | 3474 | 3477 | 3478 | 3479 | 3480 | 3481 | 3482 |
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| 3529 | 3530 | 3531 | 3532 | 3533 | 3534 | 3535 | 3536 | 3537 | 3538 | 3540 | 3541 | 3542 |
| 3543 | 3544 | 3545 | 3547 | 3548 | 3549 | 3550 | 3551 | 3552 | 3553 | 3554 | 3555 | 3556 |
| 3557 | 3558 | 3559 | 3561 | 3562 | 3563 | 3564 | 3565 | 3566 | 3568 | 3569 | 3570 | 3571 |
| 3572 | 3573 | 3574 | 3575 | 3576 | 3577 | 3578 | 3579 | 3580 | 3582 | 3583 | 3584 | 3585 |
| 3586 | 3587 | 3589 | 3590 | 3591 | 3592 | 3593 | 3594 | 3595 | 3596 | 3597 | 3598 | 3599 |
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| 3664 | 3665 | 3666 | 3667 | 3668 | 3669 | 3670 | 3671 | 3672 | 3673 | 3675 | 3676 | 3677 |
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| 3768 | 3769 | 3778 | 3779 | 3780 | 3781 | 3782 | 3783 | 3801 | 3802 | 3803 | 3805 | 3854 |
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| 4511 | 4512 | 4523 | 4525 | 4526 | 4538 | 4540 | 4541 | 4547 | 4549 | 4550 | 4553 | 4554 |
| 4555 | 4556 | 4558 | 4559 | 4565 | 4567 | 4568 | 4582 | 4584 | 4585 | 4588 | 4589 | 4590 |
| 4591 | 4593 | 4594 | 4601 | 4603 | 4604 | 4615 | 4617 | 4618 | 4621 | 4622 | 4623 | 4624 |
| 4626 | 4627 | 4631 | 4637 | 4667 | 4669 | 4670 | 4675 | 4707 | 4708 | 4709 | 4710 | 4712 |
| 4713 | 4719 | 4726 | 4735 | 4752 | 4753 | 4754 | 4755 | 4757 | 4758 | 4762 | 4764 | 4765 |
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| 5640 | 5642 | 5643 | 5651 | 5652 | 5653 | 5654 | 5656 | 5657 | 5668 | 5671 | 5703 | 5704 |
| 5705 | 5706 | 5707 | 5708 | 5815 | 5816 | 5817 | 5818 | 5824 | 5854 | 5855 | 5860 | 5862 |
| 5863 | 5869 | 5895 | 5897 | 5898 | 5941 | 5942 | 5943 | 5944 | 5946 | 5947 | 5969 | 5970 |
| 5971 | 5972 | 5976 | 5977 | 5978 | 5979 | 5981 | 5982 | 5991 | 5992 | 5993 | 5994 | 5996 |
| 5997 | 6005 | 6014 | 6039 | 6041 | 6042 | 6091 | 6092 | 6093 | 6094 | 6096 | 6097 | 6124 |
| 6125 | 6126 | 6127 | 6131 | 6132 | 6133 | 6134 | 6136 | 6137 | 6150 | 6152 | 6153 | 6164 |
| 6165 | 6166 | 6167 | 6169 | 6170 | 6181 | 6184 | 6239 | 6241 | 6242 | 6263 | 6272 | 6274 |
| 6275 | 6282 | 6283 | 6284 | 6285 | 6292 | 6293 | 6294 | 6295 | 6306 | 6307 | 6308 | 6309 |
| 6316 | 6317 | 6318 | 6319 | 6323 | 6345 | 6354 | 6356 | 6357 | 6364 | 6365 | 6366 | 6367 |

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CROSS REFERENCE TABLE -- USER SYMBOLS

|          |        |       |       |       |       |       |       |       |       |       |       |       |       |       |
|----------|--------|-------|-------|-------|-------|-------|-------|-------|-------|-------|-------|-------|-------|-------|
|          |        | 6374  | 6375  | 6376  | 6377  | 6388  | 6389  | 6390  | 6391  | 6398  | 6399  | 6400  | 6401  | 6405  |
|          |        | 6427  | 6436  | 6438  | 6439  | 6446  | 6447  | 6448  | 6449  | 6456  | 6457  | 6458  | 6459  | 6470  |
|          |        | 6471  | 6472  | 6473  | 6480  | 6481  | 6482  | 6483  | 6487  | 6509  | 6518  | 6520  | 6521  | 6528  |
|          |        | 6529  | 6530  | 6531  | 6538  | 6539  | 6540  | 6541  | 6552  | 6553  | 6554  | 6555  | 6562  | 6563  |
|          |        | 6564  | 6565  | 6569  | 6585  | 6594  | 6596  | 6597  | 6606  | 6607  | 6608  | 6609  | 6616  | 6617  |
|          |        | 6618  | 6619  | 6623  | 6639  | 6648  | 6650  | 6651  | 6660  | 6661  | 6662  | 6663  | 6670  | 6671  |
|          |        | 6672  | 6673  | 6677  | 6682  | 6704  | 6706  | 6707  | 6727  | 6734  | 6735  | 6736  | 6737  | 6741  |
|          |        | 6761  | 6763  | 6764  | 6777  | 6778  | 6779  | 6780  | 6783  | 6809  | 6810  | 6815  | 6816  | 6819  |
|          |        | 6820  | 6821  | 6822  | 6823  | 6824  | 6825  | 6826  | 6832  | 6836  | 6838  | 6839  | 6841  | 6842  |
|          |        | 6843  | 6844  | 6845  | 6846  | 6879  | 6880  | 6881  | 6882  | 6906  | 6907  | 6909  | 6910  | 6923  |
|          |        | 6924  | 6925  | 6926  | 6930  | 6934  | 6944  | 6949  | 6951  | 6952  | 6954  | 6955  | 6956  | 6957  |
|          |        | 6958  | 6959  | 6980  | 6981  | 6982  | 6983  | 6989  | 6990  | 6992  | 6993  | 6998  | 6999  | 7000  |
|          |        | 7001  | 7006  | 7010  | 7013  | 7212  | 7213  | 7218  | 7219  | 7222  | 7223  | 7224  | 7225  | 7226  |
|          |        | 7227  | 7228  | 7229  | 7233  | 7237  | 7239  | 7240  | 7248  | 7249  | 7250  | 7251  | 7252  | 7253  |
|          |        | 7264  | 7265  | 7266  | 7267  | 7280  | 7281  | 7282  | 7283  | 7288  | 7290  | 7291  | 7295  | 7296  |
|          |        | 7298  | 7299  | 7304  | 7305  | 7306  | 7307  | 7312  | 7316  | 7320  | 7322  | 7323  | 7335  | 7336  |
|          |        | 7337  | 7338  | 7339  | 7340  | 7353  | 7354  | 7355  | 7356  | 7376  | 7377  | 7381  | 7382  | 7383  |
|          |        | 7384  | 7394  | 7395  | 7399  | 7400  | 7401  | 7402  | 7407  | 7410  | 7411  | 7422  | 7423  | 7430  |
|          |        | 7431  | 7432  | 7433  | 7443  | 7444  | 7445  | 7446  | 7454  | 7455  | 7456  | 7457  | 7462  | 7466  |
|          |        | 7469  | 8018  | 8022  | 8023  | 8024  | 8025  | 8027  | 8028  | 8029  | 8030  | 8032  | 8033  | 8034  |
|          |        | 8035  | 8036  | 8038  | 8039  | 8040  | 8042  | 8043  | 8044  | 8045  | 8046  | 8048  | 8049  | 8050  |
|          |        | 8053  | 8072  | 8075  | 8089  | 8090  | 8091  | 8091  | 8091  | 8091  | 8091  | 8091  | 8091  | 8091  |
| SVCSUB=  | 000001 | 1173# | 1181# | 4055  | 4846  | 4913  | 5096  | 5168  | 5285  | 5493  | 5868  | 6013  | 6262  | 6344  |
| SVCTAG=  | 000001 | 6426  | 6508  | 6584  | 6638  | 6831  | 6943  | 7232  | 7315  |       |       |       |       |       |
|          |        | 1173# | 1183# | 1349  | 1362  | 2820  | 2854  | 2872  | 2882  | 2957  | 3108  | 3117  | 3126  | 3167  |
|          |        | 3223  | 3298  | 3351  | 3386  | 3445  | 3490  | 3891  | 3957  | 3979  | 3994  | 4007  | 4355  | 4359  |
|          |        | 4630  | 4799  | 4907  | 4974  | 4977  | 5072  | 5163  | 5235  | 5238  | 5488  | 5667  | 5670  | 5823  |
|          |        | 6004  | 6180  | 6183  | 6322  | 6404  | 6486  | 6568  | 6622  | 6676  | 6681  | 6740  | 6782  | 6933  |
|          |        | 7009  | 7012  | 7311  | 7465  | 7468  | 8054  | 8076  |       |       |       |       |       |       |
| SVCTST=  | 000001 | 1173# | 1180# | 4053  | 4423  | 4663  | 4833  | 5001  | 5092  | 5262  | 5849  | 6235  | 6697  | 6757  |
|          |        | 6804  | 7207  |       |       |       |       |       |       |       |       |       |       |       |
| SWPBOT=  | 121000 | 1498# |       |       |       |       |       |       |       |       |       |       |       |       |
| SWPDDC=  | 121400 | 1499# |       |       |       |       |       |       |       |       |       |       |       |       |
| SSL SYM= | 010000 | 1173# | 1350# | 1363# | 2821# | 2855# | 2873# | 2883# | 2958# | 3109# | 3118# | 3127# | 3168# | 3224# |
|          |        | 3299# | 3352# | 3387# | 3446# | 3491# | 3872# | 3958# | 3980# | 3995# | 4008# | 4356# | 4360# | 4631# |
|          |        | 4800# | 4908# | 4975# | 4978# | 5073# | 5164# | 5236# | 5239# | 5489# | 5668# | 5671# | 5824# | 6005# |
|          |        | 6181# | 6184# | 6323# | 6405# | 6487# | 6569# | 6623# | 6677# | 6682# | 6741# | 6783# | 6934# | 7010# |
|          |        | 7013# | 7312# | 7466# | 7469# | 8055# | 8077# |       |       |       |       |       |       |       |
| TDATA    | 002252 | 1641# | 3435  | 3634  | 3675  | 4432* | 4528* | 4885* | 4902  | 4952* | 4969  | 5048* | 5066  |       |
| TMPA     | 002436 | 1763# | 3411  | 3428  | 4431* | 4471  | 4513* | 4516* | 4519  | 4544  | 4562  | 4572  | 4575  | 4598  |
|          |        | 5280* | 5361  | 5482  | 5552  | 5660  | 5887* | 5888* | 5889* | 5891  | 6031* | 6032* | 6033* | 6035  |
|          |        | 6142* | 6143* | 6144* | 6146  |       |       |       |       |       |       |       |       |       |
| TMPB     | 002440 | 1764# | 2298* | 2299* | 2322  | 2325  | 2327* | 2331  | 2379* | 2380* | 2404  | 2407  | 2409* | 2413  |
|          |        | 3360  | 3395  | 3409  | 3426  | 4436  | 4443* | 4444* | 4447  | 4484* | 4487  | 5886* | 5892  | 6030* |
|          |        | 6036  | 6141* | 6147  |       |       |       |       |       |       |       |       |       |       |
| TMPC     | 002442 | 1765# | 3398  | 3424  | 5288* | 5403  | 5480* | 5482  | 5495* | 5592  | 5645  | 5658* | 5660  |       |
| TMPD     | 002444 | 1766# | 3422  | 4392  | 4400  | 4535  | 4579  | 4612  | 4634  | 4642  |       |       |       |       |
| TMPE     | 002446 | 1767# | 2313* | 2317  | 2395* | 2399  | 3407  | 3420  | 4456  | 4494* | 4497  | 4506  | 5349* | 5351  |
|          |        | 5359* | 5361  | 5540* | 5542  | 5550* | 5552  |       |       |       |       |       |       |       |
| TMPF     | 002450 | 1768# | 3197  | 5289* | 5367  | 5376  | 5383  | 5385  | 5386  | 5484* | 5496* | 5559  | 5568  | 5575  |
|          |        | 5577  | 5578  | 5662* |       |       |       |       |       |       |       |       |       |       |
| TMPO     | 002412 | 1753# | 2721* | 2722  | 3133  | 3136  | 3260  | 3930* | 3948  | 3960* | 4859  | 4926  | 5022  | 5105* |
|          |        | 5177* | 5802* | 7246* | 7270  | 7273* | 7300  | 7360* | 7437  | 7886* |       |       |       |       |
| TMP1     | 002414 | 1754# | 2726* | 3144  | 3213  | 3259  | 5108* | 5138  | 5180* | 5210  | 5329  | 5338  | 5345  | 5537* |
|          |        | 5538* | 5593  | 5803* |       |       |       |       |       |       |       |       |       |       |
| TMP2     | 002416 | 1755# | 2727* | 3152  | 3193  | 3258  | 5147  | 5154  | 5219  | 5226  | 5411* | 5414  | 5425  | 5450  |



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CROSS REFERENCE TABLE -- USER SYMBOLS

|         |        |       |       |       |       |       |       |       |       |       |       |       |       |       |
|---------|--------|-------|-------|-------|-------|-------|-------|-------|-------|-------|-------|-------|-------|-------|
| TMP3    | 002420 | 5604* | 5609  | 5618  | 5625  | 5804* | 5950* | 5954  | 5957  | 5964  | 6111* | 6115  | 6118  | 6119  |
|         |        | 1756# | 2728* | 2729* | 3159  | 3203  | 3257  | 5107* | 5111  | 5120  | 5179* | 5183  | 5192  | 5345* |
|         |        | 5346* | 5352  | 5399  | 5529* | 5536* | 5537  | 5543  | 5594  | 5805* |       |       |       |       |
| TMP4    | 002422 | 1757# | 3270  | 5807* |       |       |       |       |       |       |       |       |       |       |
| TMP5    | 002424 | 1758# | 3269  | 5809* |       |       |       |       |       |       |       |       |       |       |
| TMP6    | 002426 | 1759# | 2296* | 2336  | 3268  | 5730* | 5761* |       |       |       |       |       |       |       |
| TMP7    | 002430 | 1760# | 2297* | 2341  | 3267  | 5811* |       |       |       |       |       |       |       |       |
| TMP8    | 002432 | 1761# | 2377* | 2418  | 3374  | 4126  | 4133  | 4210  | 4280  | 4374* | 4378  |       |       |       |
| TMP9    | 002434 | 1762# | 2378* | 2423  | 3376  | 4090* | 4093  | 4144  | 4151  | 4185  | 4254  | 4304  | 4375* | 4381  |
|         |        | 5287* | 5301  | 5304  | 5305  | 5307  | 5310  | 5508  | 5511  | 5512  | 5514  | 5517  | 5924  | 5935  |
|         |        | 6074  | 6085  |       |       |       |       |       |       |       |       |       |       |       |
| TXTMLT  | 021746 | 2910  | 3699# |       |       |       |       |       |       |       |       |       |       |       |
| TXTML0  | 015515 | 3699# |       |       |       |       |       |       |       |       |       |       |       |       |
| TXTML1  | 015521 | 3699# |       |       |       |       |       |       |       |       |       |       |       |       |
| TXTML2  | 015535 | 3699# |       |       |       |       |       |       |       |       |       |       |       |       |
| TXTML3  | 015552 | 3699# |       |       |       |       |       |       |       |       |       |       |       |       |
| TXTML4  | 015574 | 3699# |       |       |       |       |       |       |       |       |       |       |       |       |
| TXTML5  | 015615 | 3699# |       |       |       |       |       |       |       |       |       |       |       |       |
| TXTML6  | 015645 | 3699# |       |       |       |       |       |       |       |       |       |       |       |       |
| TXTML7  | 015657 | 3699# |       |       |       |       |       |       |       |       |       |       |       |       |
| TXTNP   | 015745 | 2967  | 3312  | 3699# |       |       |       |       |       |       |       |       |       |       |
| TXTNPT  | 021770 | 2966  | 3311  | 3699# |       |       |       |       |       |       |       |       |       |       |
| TXTNP0  | 015752 | 3699# |       |       |       |       |       |       |       |       |       |       |       |       |
| TXTNP1  | 015762 | 3699# |       |       |       |       |       |       |       |       |       |       |       |       |
| TXTNP2  | 015772 | 3699# |       |       |       |       |       |       |       |       |       |       |       |       |
| TXTNP3  | 016002 | 3699# |       |       |       |       |       |       |       |       |       |       |       |       |
| TXTNP4  | 016017 | 3699# |       |       |       |       |       |       |       |       |       |       |       |       |
| TXTNP5  | 016034 | 3699# |       |       |       |       |       |       |       |       |       |       |       |       |
| TXTNP6  | 016051 | 3699# |       |       |       |       |       |       |       |       |       |       |       |       |
| TXTNP7  | 016065 | 3699# |       |       |       |       |       |       |       |       |       |       |       |       |
| TXTNP8  | 016101 | 3699# |       |       |       |       |       |       |       |       |       |       |       |       |
| TXTNUL  | 015513 | 3699# |       |       |       |       |       |       |       |       |       |       |       |       |
| TXT1    | 014761 | 3518  | 3699# |       |       |       |       |       |       |       |       |       |       |       |
| TXT11A  | 015437 | 2983  | 3699# |       |       |       |       |       |       |       |       |       |       |       |
| TXT11B  | 015460 | 3040  | 3699# |       |       |       |       |       |       |       |       |       |       |       |
| TXT2    | 015017 | 3540  | 3699# |       |       |       |       |       |       |       |       |       |       |       |
| TXT2A   | 015061 | 3561  | 3699# |       |       |       |       |       |       |       |       |       |       |       |
| TXT2B   | 015120 | 3582  | 3699# |       |       |       |       |       |       |       |       |       |       |       |
| TXT3    | 015162 | 3519  | 3699# |       |       |       |       |       |       |       |       |       |       |       |
| TXT4    | 015212 | 2921  | 3699# |       |       |       |       |       |       |       |       |       |       |       |
| TXT4A   | 015252 | 2939  | 3699# |       |       |       |       |       |       |       |       |       |       |       |
| TXT6    | 015313 | 2922  | 3699# |       |       |       |       |       |       |       |       |       |       |       |
| TXT8A   | 015336 | 2998  | 3060  | 3699# |       |       |       |       |       |       |       |       |       |       |
| TXT8B   | 015353 | 3012  | 3080  | 3699# |       |       |       |       |       |       |       |       |       |       |
| TXT8C   | 015370 | 3030  | 3100  | 3699# |       |       |       |       |       |       |       |       |       |       |
| TXT8D   | 015405 | 3412  | 3699# |       |       |       |       |       |       |       |       |       |       |       |
| TXT8E   | 015422 | 3429  | 3699# |       |       |       |       |       |       |       |       |       |       |       |
| TSARGC= | 000001 | 1204# | 1205# | 1206# | 1207# | 1208# | 1209# | 2894# | 2900  | 2910# | 2917  | 2921# | 2927  | 2929# |
|         |        | 2937  | 2939# | 2944  | 2946# | 2954  | 2966# | 2972  | 2974# | 2978  | 2983# | 2988  | 2992# | 3003  |
|         |        | 3006# | 3017  | 3024# | 3035  | 3040# | 3045  | 3048# | 3065  | 3068# | 3085  | 3088# | 3105  | 3136# |
|         |        | 3141  | 3143# | 3149  | 3151# | 3157  | 3159# | 3164  | 3175# | 3179  | 3181# | 3185  | 3189# | 3202  |
|         |        | 3206# | 3210  | 3216# | 3220  | 3233# | 3237  | 3239# | 3243  | 3245# | 3249  | 3251# | 3255  | 3257# |
|         |        | 3265  | 3267# | 3275  | 3277# | 3285  | 3287# | 3295  | 3311# | 3317  | 3326# | 3333  | 3337# | 3347  |
|         |        | 3373# | 3382  | 3400# | 3404  | 3406# | 3417  | 3419# | 3434  | 3438# | 3442  | 3464# | 3468  | 3470# |
|         |        | 3474  | 3477# | 3484  | 3518# | 3524  | 3526# | 3538  | 3540# | 3545  | 3547# | 3559  | 3561# | 3566  |
|         |        | 3568# | 3580  | 3582# | 3587  | 3589# | 3601  | 3610# | 3614  | 3617# | 3622  | 3625# | 3632  | 3634# |

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CROSS REFERENCE TABLE -- USER SYMBOLS

|                 |       |       |       |       |       |       |       |       |       |       |       |       |       |
|-----------------|-------|-------|-------|-------|-------|-------|-------|-------|-------|-------|-------|-------|-------|
|                 | 3639  | 3648# | 3652  | 3655# | 3660  | 3663# | 3673  | 3675# | 3680  | 3688# | 3692  | 3778# | 3783  |
|                 | 3854# | 3860  | 3862# | 3866  |       |       |       |       |       |       |       |       |       |
| TSCODE= 007130  | 8022# | 8027# | 8032# | 8038# | 8042# | 8048# |       |       |       |       |       |       |       |
| TSERRN= 000126  | 1173# | 2809# | 2843# | 4084# | 4109# | 4138# | 4157# | 4176# | 4201# | 4226# | 4246# | 4270# | 4296# |
|                 | 4320# | 4343# | 4481# | 4554# | 4589# | 4622# | 4708# | 4753# | 4795# | 4895# | 4962# | 5059# | 5159# |
|                 | 5231# | 5316# | 5391# | 5456# | 5463# | 5474# | 5523# | 5583# | 5631# | 5638# | 5652# | 5942# | 5970# |
|                 | 5977# | 5992# | 6092# | 6125# | 6132# | 6165# | 6283# | 6293# | 6307# | 6317# | 6365# | 6375# | 6389# |
|                 | 6399# | 6447# | 6457# | 6471# | 6481# | 6529# | 6539# | 6553# | 6563# | 6607# | 6617# | 6661# | 6671# |
|                 | 6735# | 6778# | 6880# | 6924# | 6981# | 6999# | 7265# | 7281# | 7305# | 7354# | 7382# | 7400# | 7431# |
|                 | 7444# | 7455# |       |       |       |       |       |       |       |       |       |       |       |
| TSEXCP= 000000  | 8022# | 8026  | 8027# | 8031  | 8032# | 8037  | 8042# | 8047  |       |       |       |       |       |
| TSFLAG= 000040  | 4064# | 4073# | 4078# | 4098# | 4103# | 4113# | 4131# | 4149# | 4161# | 4169# | 4190# | 4195# | 4215# |
|                 | 4220# | 4240# | 4250# | 4259# | 4264# | 4285# | 4290# | 4309# | 4314# | 4324# | 4337# | 4429# | 4441# |
|                 | 4452# | 4461# | 4466# | 4476# | 4492# | 4502# | 4511# | 4525# | 4540# | 4549# | 4558# | 4567# | 4584# |
|                 | 4593# | 4603# | 4617# | 4626# | 4669# | 4712# | 4757# | 4764# | 4840# | 5007# | 5103# | 5116# | 5125# |
|                 | 5134# | 5143# | 5152# | 5175# | 5188# | 5197# | 5206# | 5215# | 5224# | 5268# | 5275# | 5320# | 5334# |
|                 | 5343# | 5357# | 5372# | 5381# | 5395# | 5409# | 5419# | 5430# | 5439# | 5448# | 5467# | 5478# | 5527# |
|                 | 5548# | 5564# | 5573# | 5587# | 5614# | 5623# | 5642# | 5656# | 5854# | 5862# | 5897# | 5946# | 5981# |
|                 | 5996# | 6041# | 6096# | 6136# | 6152# | 6169# | 6241# | 6274# | 6356# | 6438# | 6520# | 6596# | 6650# |
|                 | 6706# | 6763# | 6809# | 6815# | 6838# | 6951# | 7212# | 7218# | 7239# | 7290# | 7322# | 7410# |       |
| TSGMAN= 000000  | 1173# |       |       |       |       |       |       |       |       |       |       |       |       |
| TSHILI= 000002  | 8022# | 8025  | 8027# | 8030  | 8032# | 8036  | 8042# | 8046  |       |       |       |       |       |
| TSLAST= 000001  | 1173# | 8090# |       |       |       |       |       |       |       |       |       |       |       |
| TSLOLI= 000000  | 8022# | 8024  | 8027# | 8029  | 8032# | 8035  | 8042# | 8045  |       |       |       |       |       |
| TSLSYM= 010000  | 1173# | 1350  | 1363  | 2821  | 2855  | 2873  | 2883  | 2958  | 3109  | 3118  | 3127  | 3168  | 3224  |
|                 | 3299  | 3352  | 3387  | 3446  | 3491  | 3892  | 3958  | 3980  | 3995  | 4008  | 4356  | 4360  | 4631  |
|                 | 4800  | 4908  | 4975  | 4978  | 5073  | 5164  | 5236  | 5239  | 5489  | 5668  | 5671  | 5824  | 6005  |
|                 | 6181  | 6184  | 6323  | 6405  | 6487  | 6569  | 6623  | 6677  | 6682  | 6741  | 6783  | 6934  | 7010  |
|                 | 7013  | 7312  | 7466  | 7469  | 8055  | 8077  |       |       |       |       |       |       |       |
| TSLTND= 000015  | 8093# |       |       |       |       |       |       |       |       |       |       |       |       |
| TSNEST= 177777  | 1173# | 1175# | 1323# | 1349# | 1358# | 1362# | 2801# | 2820# | 2835# | 2854# | 2869# | 2872# | 2878# |
|                 | 2882# | 2888# | 2957# | 2963# | 3108# | 3114# | 3117# | 3123# | 3126# | 3132# | 3167# | 3173# | 3223# |
|                 | 3231# | 3298# | 3306# | 3351# | 3358# | 3386# | 3392# | 3445# | 3462# | 3490# | 3708# | 3713# | 3721# |
|                 | 3891# | 3921# | 3957# | 3971# | 3979# | 3989# | 3994# | 4005# | 4007# | 4054# | 4056# | 4355# | 4359# |
|                 | 4424# | 4630# | 4664# | 4799# | 4834# | 4847# | 4907# | 4914# | 4974# | 4977# | 5002# | 5072# | 5093# |
|                 | 5097# | 5163# | 5169# | 5235# | 5238# | 5263# | 5286# | 5488# | 5494# | 5667# | 5670# | 5792# | 5823# |
|                 | 5850# | 5869# | 6004# | 6014# | 6180# | 6183# | 6236# | 6263# | 6322# | 6345# | 6404# | 6427# | 6486# |
|                 | 6509# | 6568# | 6585# | 6622# | 6639# | 6676# | 6681# | 6698# | 6740# | 6758# | 6782# | 6805# | 6832# |
|                 | 6933# | 6944# | 7009# | 7012# | 7208# | 7233# | 7311# | 7316# | 7465# | 7468# | 8018# | 8053# | 8072# |
|                 | 8075# | 8088# |       |       |       |       |       |       |       |       |       |       |       |
| TSNS0 = 000000  | 1175# | 8088  |       |       |       |       |       |       |       |       |       |       |       |
| TSNS1 = 000005  | 1323# | 1349  | 1358# | 1362  | 2801# | 2820  | 2835# | 2854  | 2869# | 2872  | 2878# | 2882  | 2888# |
|                 | 2957  | 2963# | 3108  | 3114# | 3117  | 3123# | 3126  | 3132# | 3167  | 3173# | 3223  | 3231# | 3298  |
|                 | 3306# | 3351  | 3358# | 3386  | 3392# | 3445  | 3462# | 3490  | 3708# | 3713  | 3721# | 3891  | 3921# |
|                 | 3957  | 3971# | 3979  | 3989# | 3994  | 4005# | 4007  | 4054# | 4359  | 4424# | 4630  | 4664# | 4799  |
|                 | 4834# | 4977  | 5002# | 5072  | 5093# | 5238  | 5263# | 5670  | 5792# | 5823  | 5850# | 6183  | 6236# |
|                 | 6681  | 6698# | 6740  | 6758# | 6782  | 6805# | 7012  | 7208# | 7468  | 8018# | 8053  | 8072# | 8075  |
| TSNS2 = 000002  | 4056# | 4355  | 4847# | 4907  | 4914# | 4974  | 5097# | 5163  | 5169# | 5235  | 5286# | 5488  | 5494# |
|                 | 5667  | 5869# | 6004  | 6014# | 6180  | 6263# | 6322  | 6345# | 6404  | 6427# | 6486  | 6509# | 6568  |
|                 | 6585# | 6622  | 6639# | 6676  | 6832# | 6933  | 6944# | 7009  | 7233# | 7311  | 7316# | 7465  |       |
| TSPTNU= 000000  | 1173# |       |       |       |       |       |       |       |       |       |       |       |       |
| TSSAVL= 177777  | 1173# |       |       |       |       |       |       |       |       |       |       |       |       |
| TSS EGL= 177777 | 1173# |       |       |       |       |       |       |       |       |       |       |       |       |
| TSSUBN= 000002  | 1173# | 4053# | 4055# | 4423# | 4663# | 4833# | 4846# | 4913# | 5001# | 5092# | 5096# | 5168# | 5262# |
|                 | 5285# | 5493# | 5849# | 5868# | 6013# | 6235# | 6262# | 6344# | 6426# | 6508# | 6584# | 6638# | 6697# |
|                 | 6757# | 6804# | 6831# | 6943# | 7207# | 7232# | 7315# |       |       |       |       |       |       |

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## CROSS REFERENCE TABLE -- USER SYMBOLS

TSTAGL= 177777  
TSTAGN= 010072

TSTEMP= 000000

TSTEST= 000015

TSTSTM= 177777

|       |       |       |       |       |       |       |       |       |       |       |       |       |  |
|-------|-------|-------|-------|-------|-------|-------|-------|-------|-------|-------|-------|-------|--|
| 1173# |       |       |       |       |       |       |       |       |       |       |       |       |  |
| 1173# | 1323# | 1358# | 2801# | 2835# | 2869# | 2878# | 2888# | 2963# | 3114# | 3123# | 3132# | 3173# |  |
| 3231# | 3306# | 3358# | 3392# | 3462# | 3708# | 3721# | 3921# | 3971# | 3989# | 4005# | 4054# | 4056# |  |
| 4424# | 4664# | 4834# | 4847# | 4914# | 5002# | 5093# | 5097# | 5169# | 5263# | 5286# | 5494# | 5792# |  |
| 5850# | 5869# | 6014# | 6236# | 6263# | 6345# | 6427# | 6509# | 6585# | 6639# | 6698# | 6758# | 6805# |  |
| 6832# | 6944# | 7208# | 7233# | 7316# | 8018# | 8072# |       |       |       |       |       |       |  |
| 1300# | 1301# | 1302# | 1303# | 1304# | 1305# | 1306# | 1307# | 1308# | 1309# | 1310# | 1311# | 1312# |  |
| 1313# | 1349# | 1362# | 2820# | 2854# | 2872# | 2882# | 2957# | 3108# | 3117# | 3126# | 3167# | 3223# |  |
| 3298# | 3351# | 3386# | 3445# | 3490# | 3713# | 3891# | 3957# | 3979# | 3994# | 4007# | 4064# | 4065  |  |
| 4073# | 4074  | 4078# | 4079  | 4098# | 4099  | 4103# | 4104  | 4113# | 4114  | 4131# | 4132  | 4149# |  |
| 4150  | 4161# | 4162  | 4169# | 4170  | 4190# | 4191  | 4195# | 4196  | 4215# | 4216  | 4220# | 4221  |  |
| 4240# | 4241  | 4250# | 4251  | 4259# | 4260  | 4264# | 4265  | 4285# | 4286  | 4290# | 4291  | 4309# |  |
| 4310  | 4314# | 4315  | 4324# | 4325  | 4337# | 4338  | 4355# | 4359# | 4429# | 4430  | 4441# | 4442  |  |
| 4452# | 4453  | 4461# | 4462  | 4466# | 4467  | 4476# | 4477  | 4492# | 4493  | 4502# | 4503  | 4511# |  |
| 4512  | 4525# | 4526  | 4540# | 4541  | 4549# | 4550  | 4558# | 4559  | 4567# | 4568  | 4584# | 4585  |  |
| 4593# | 4594  | 4603# | 4604  | 4617# | 4618  | 4626# | 4627  | 4630# | 4669# | 4670  | 4712# | 4713  |  |
| 4757# | 4758  | 4764# | 4765  | 4799# | 4840# | 4841  | 4907# | 4974# | 4977# | 5007# | 5008  | 5072# |  |
| 5103# | 5104  | 5116# | 5117  | 5125# | 5126  | 5134# | 5135  | 5143# | 5144  | 5152# | 5153  | 5163# |  |
| 5175# | 5176  | 5188# | 5189  | 5197# | 5198  | 5206# | 5207  | 5215# | 5216  | 5224# | 5225  | 5235# |  |
| 5238# | 5268# | 5269  | 5275# | 5276  | 5320# | 5321  | 5334# | 5335  | 5343# | 5344  | 5357# | 5358  |  |
| 5372# | 5373  | 5381# | 5382  | 5395# | 5396  | 5409# | 5410  | 5419# | 5420  | 5430# | 5431  | 5439# |  |
| 5440  | 5448# | 5449  | 5467# | 5468  | 5478# | 5479  | 5488# | 5527# | 5578  | 5548# | 5549  | 5564# |  |
| 5565  | 5573# | 5574  | 5587# | 5588  | 5614# | 5615  | 5623# | 5624  | 5642# | 5643  | 5656# | 5657  |  |
| 5667# | 5670# | 5823# | 5854# | 5855  | 5862# | 5863  | 5897# | 5898  | 5946# | 5947  | 5981# | 5982  |  |
| 5996# | 5997  | 6004# | 6041# | 6042  | 6096# | 6097  | 6136# | 6137  | 6152# | 6153  | 6169# | 6170  |  |
| 6180# | 6183# | 6241# | 6242  | 6274# | 6275  | 6322# | 6356# | 6357  | 6404# | 6438# | 6439  | 6486# |  |
| 6520# | 6521  | 6568# | 6596# | 6597  | 6622# | 6650# | 6651  | 6676# | 6681# | 6706# | 6707  | 6740# |  |
| 6763# | 6764  | 6782# | 6809# | 6810  | 6815# | 6816  | 6838# | 6839  | 6933# | 6951# | 6952  | 7009# |  |
| 7012# | 7212# | 7213  | 7218# | 7219  | 7239# | 7240  | 7290# | 7291  | 7311# | 7322# | 7323  | 7410# |  |
| 7411  | 7465# | 7468# | 8022# | 8027# | 8032# | 8038# | 8042# | 8048# | 8053# | 8075# | 8088# |       |  |
| 1173# | 4053# | 4055  | 4423# | 4663# | 4833# | 4846  | 4913  | 5001# | 5092# | 5096  | 5168  | 5262# |  |
| 5285  | 5493  | 5849# | 5868  | 6013  | 6235# | 6262  | 6344  | 6426  | 6508  | 6584  | 6638  | 6697# |  |
| 6757# | 6804# | 6831  | 6943  | 7207# | 7232  | 7315  | 8093  |       |       |       |       |       |  |
| 1173# | 2808  | 2842  | 2873  | 2883  | 2899  | 2916  | 2926  | 2936  | 2943  | 2953  | 2958  | 2971  |  |
| 2977  | 2987  | 3002  | 3016  | 3034  | 3044  | 3064  | 3084  | 3104  | 3109  | 3118  | 3127  | 3140  |  |
| 3148  | 3156  | 3163  | 3168  | 3178  | 3184  | 3201  | 3209  | 3219  | 3224  | 3236  | 3242  | 3248  |  |
| 3254  | 3264  | 3274  | 3284  | 3294  | 3299  | 3316  | 3332  | 3346  | 3352  | 3381  | 3387  | 3403  |  |
| 3416  | 3433  | 3441  | 3446  | 347   | 3473  | 3483  | 3491  | 3523  | 3537  | 3544  | 3558  | 3565  |  |
| 3579  | 3586  | 3600  | 3613  | 3621  | 3631  | 3638  | 3651  | 3659  | 3672  | 3679  | 3691  | 3727  |  |
| 3733  | 3739  | 3745  | 3764  | 3769  | 3782  | 3802  | 3859  | 3865  | 3876  | 3884  | 3892  | 3928  |  |
| 3947  | 3952  | 3958  | 3974  | 3977  | 3980  | 3992  | 3995  | 4008  | 4056  | 4062  | 4064  | 4071  |  |
| 4073  | 4078  | 4083  | 4096  | 4098  | 4103  | 4108  | 4113  | 4129  | 4131  | 4137  | 4147  | 4149  |  |
| 4156  | 4161  | 4169  | 4175  | 4188  | 4190  | 4195  | 4200  | 4213  | 4215  | 4220  | 4225  | 4240  |  |
| 4245  | 4250  | 4257  | 4259  | 4264  | 4269  | 4283  | 4285  | 4290  | 4295  | 4307  | 4309  | 4314  |  |
| 4319  | 4324  | 4337  | 4342  | 4352  | 4356  | 4360  | 4395  | 4427  | 4429  | 4439  | 4441  | 4450  |  |
| 4452  | 4459  | 4461  | 4466  | 4474  | 4476  | 4480  | 4490  | 4492  | 4500  | 4502  | 4509  | 4511  |  |
| 4523  | 4525  | 4538  | 4540  | 4547  | 4549  | 4553  | 4558  | 4565  | 4567  | 4582  | 4584  | 4588  |  |
| 4593  | 4601  | 4603  | 4615  | 4617  | 4621  | 4626  | 4631  | 4637  | 4667  | 4669  | 4675  | 4707  |  |
| 4712  | 4719  | 4726  | 4735  | 4752  | 4757  | 4762  | 4764  | 4777  | 4794  | 4800  | 4838  | 4840  |  |
| 4847  | 4858  | 4894  | 4908  | 4914  | 4925  | 4961  | 4975  | 4978  | 5005  | 5007  | 5021  | 5058  |  |
| 5073  | 5097  | 5101  | 5103  | 5114  | 5116  | 5123  | 5125  | 5132  | 5134  | 5141  | 5143  | 5150  |  |
| 5152  | 5158  | 5164  | 5169  | 5173  | 5175  | 5186  | 5188  | 5195  | 5197  | 5204  | 5206  | 5213  |  |
| 5215  | 5222  | 5224  | 5230  | 5236  | 5239  | 5266  | 5268  | 5275  | 5286  | 5315  | 5320  | 5332  |  |
| 5334  | 5341  | 5343  | 5355  | 5357  | 5370  | 5372  | 5379  | 5381  | 5390  | 5395  | 5407  | 5409  |  |
| 5417  | 5419  | 5428  | 5430  | 5437  | 5439  | 5446  | 5448  | 5455  | 5462  | 5467  | 5473  | 5478  |  |
| 5489  | 5494  | 5522  | 5527  | 5546  | 5548  | 5562  | 5564  | 5571  | 5573  | 5582  | 5587  | 5612  |  |





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CROSS REFERENCE TABLE -- USER SYMBOLS

|          |   |        |       |       |       |       |       |       |       |       |       |       |       |       |       |
|----------|---|--------|-------|-------|-------|-------|-------|-------|-------|-------|-------|-------|-------|-------|-------|
| W1       | = | 003056 | 1880# | 1881  | 3027  |       |       |       |       |       |       |       |       |       |       |
| W2       | = | 003060 | 1881# | 1882  | 3025  |       |       |       |       |       |       |       |       |       |       |
| W3       | = | 003062 | 1882# | 1883  | 3086  |       |       |       |       |       |       |       |       |       |       |
| W4       | = | 003064 | 1883# | 1884  |       |       |       |       |       |       |       |       |       |       |       |
| W5       | = | 003066 | 1884# | 1885  |       |       |       |       |       |       |       |       |       |       |       |
| W6       | = | 003070 | 1885# | 1886  |       |       |       |       |       |       |       |       |       |       |       |
| W7       | = | 003072 | 1886# | 1887  |       |       |       |       |       |       |       |       |       |       |       |
| W8       | = | 003074 | 1887# | 1888  |       |       |       |       |       |       |       |       |       |       |       |
| W9       | = | 003076 | 1888# | 1889  |       |       |       |       |       |       |       |       |       |       |       |
| XDATA    |   | 002260 | 1644# | 3189  | 3326  | 3338  | 3507* | 3508* | 3625  | 3664  | 6187# |       |       |       |       |
| XLOC0    |   | 032554 | 3479  | 5874  | 5887  | 5906  | 5985  | 6031  | 6056  | 6142  |       |       |       |       |       |
| XLOC1    |   | 032556 | 6188# |       |       |       |       |       |       |       |       |       |       |       |       |
| XLOC2    |   | 032560 | 6189# |       |       |       |       |       |       |       |       |       |       |       |       |
| XLOC3    |   | 032562 | 6190# |       |       |       |       |       |       |       |       |       |       |       |       |
| XLOC4    |   | 032564 | 6191# |       |       |       |       |       |       |       |       |       |       |       |       |
| XLOC5    |   | 032566 | 6192# |       |       |       |       |       |       |       |       |       |       |       |       |
| XLOC6    |   | 032570 | 6193# |       |       |       |       |       |       |       |       |       |       |       |       |
| XPMFG.M  |   | 036773 | 8049  | 8056# |       |       |       |       |       |       |       |       |       |       |       |
| XPMINIT  |   | 030560 | 5290  | 5497  | 5679# | 5870  | 6015  |       |       |       |       |       |       |       |       |
| XPMINTH  |   | 031124 | 5704  | 5792# |       |       |       |       |       |       |       |       |       |       |       |
| XPMREAD  |   | 030776 | 5644  | 5756# | 6145  |       |       |       |       |       |       |       |       |       |       |
| XPMWRIT  |   | 030700 | 5350  | 5402  | 5541  | 5591  | 5725# | 5890  | 6034  |       |       |       |       |       |       |
| XPM4HOL  |   | 031122 | 5682* | 5733* | 5738  | 5764* | 5769  | 5780  | 5785# |       |       |       |       |       |       |
| XPM4INT  |   | 031074 | 5734  | 5765  | 5779# |       |       |       |       |       |       |       |       |       |       |
| XORGB    |   | 012162 | 3187  | 3318  | 3505# | 3623  | 3661  |       |       |       |       |       |       |       |       |
| XORSW    |   | 005064 | 2505# | 3018  |       |       |       |       |       |       |       |       |       |       |       |
| XVAL0    |   | 032572 | 3478  | 5874* | 5886  | 6020* | 6030  | 6051* | 6160  | 6195# |       |       |       |       |       |
| XVAL1    |   | 032574 | 6196# |       |       |       |       |       |       |       |       |       |       |       |       |
| XVAL2    |   | 032576 | 6197# |       |       |       |       |       |       |       |       |       |       |       |       |
| XVAL3    |   | 032600 | 6198# |       |       |       |       |       |       |       |       |       |       |       |       |
| XVAL4    |   | 032602 | 6199# |       |       |       |       |       |       |       |       |       |       |       |       |
| XVAL5    |   | 032604 | 6200# |       |       |       |       |       |       |       |       |       |       |       |       |
| XVAL6    |   | 032606 | 6201# |       |       |       |       |       |       |       |       |       |       |       |       |
| X\$ALWA= |   | 000000 | 1173# |       |       |       |       |       |       |       |       |       |       |       |       |
| X\$FALS= |   | 000040 | 1173# |       |       |       |       |       |       |       |       |       |       |       |       |
| X\$OFFS= |   | 000400 | 1173# |       |       |       |       |       |       |       |       |       |       |       |       |
| X\$TRUE= |   | 000020 | 1173# |       |       |       |       |       |       |       |       |       |       |       |       |
| SE       | = | 000126 | 1591# | 1992# | 2032# | 2072# | 2115# | 2178# | 2731# | 2736  | 2807# | 2841# | 4082# | 4107# | 4136# |
|          |   |        | 4155# | 4174# | 4199# | 4224# | 4244# | 4268# | 4294# | 4318# | 4341# | 4479# | 4552# | 4587# | 4620# |
|          |   |        | 4706# | 4751# | 4793# | 4893# | 4960# | 5057# | 5157# | 5229# | 5314# | 5389# | 5454# | 5461# | 5472# |
|          |   |        | 5521# | 5581# | 5629# | 5636# | 5650# | 5814# | 5940# | 5968# | 5975# | 5990# | 6090# | 6123# | 6130# |
|          |   |        | 6163# | 6281# | 6291# | 6305# | 6315# | 6363# | 6373# | 6387# | 6397# | 6445# | 6455# | 6469# | 6479# |
|          |   |        | 6527# | 6537# | 6551# | 6561# | 6605# | 6615# | 6659# | 6669# | 6733# | 6776# | 6878# | 6922# | 6979# |
|          |   |        | 6997# | 7263# | 7279# | 7303# | 7352# | 7380# | 7398# | 7429# | 7442# | 7453# |       |       |       |
| \$LSTIN= |   | 000001 | 1177# |       |       |       |       |       |       |       |       |       |       |       |       |
| \$LSTTA= |   | 000001 | 1178# |       |       |       |       |       |       |       |       |       |       |       |       |
| \$MPCSR= |   | 160000 | 1706# | 1711  | 1712  | 1714  | 1715  | 1717  | 1718  | 1720  | 1721  | 1723  | 1724  | 1726  | 1727  |
|          |   |        | 1729  | 1730  | 1732  | 1733  |       |       |       |       |       |       |       |       |       |
| \$T      | = | 000015 | 1591# | 4009# | 4407# | 4649# | 4801# | 4834  | 4979# | 5074# | 5240# | 5826# | 6210# | 6683# | 6742# |
|          |   |        | 6784# | 6805  | 7038# | 7208  |       |       |       |       |       |       |       |       |       |
| .        | = | 037154 | 1169# | 1776  | 1801# | 1802  | 1873# | 1914# | 1930# | 2004# | 3226# | 3301# | 3449# | 3493# | 3903# |
|          |   |        | 4065  | 4069  | 4074  | 4076  | 4079  | 4094  | 4099  | 4101  | 4104  | 4114  | 4127  | 4132  | 4145  |
|          |   |        | 4150  | 4162  | 4167  | 4170  | 4186  | 4191  | 4193  | 4196  | 4211  | 4216  | 4218  | 4221  | 4238  |
|          |   |        | 4241  | 4251  | 4255  | 4260  | 4262  | 4265  | 4281  | 4286  | 4288  | 4291  | 4305  | 4310  | 4312  |
|          |   |        | 4315  | 4325  | 4335  | 4338  | 4350  | 4430  | 4437  | 4442  | 4448  | 4453  | 4457  | 4462  | 4464  |
|          |   |        | 4467  | 4472  | 4477  | 4488  | 4493  | 4498  | 4503  | 4507  | 4512  | 4521  | 4526  | 4530  | 4536  |









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CROSS REFERENCE TABLE -- MACRO NAMES

|        |       |       |       |       |       |       |       |       |       |       |       |       |       |       |       |
|--------|-------|-------|-------|-------|-------|-------|-------|-------|-------|-------|-------|-------|-------|-------|-------|
| INLOOP | 1#    | 1173# |       |       |       |       |       |       |       |       |       |       |       |       |       |
| IOSETU | 1#    | 1173# |       |       |       |       |       |       |       |       |       |       |       |       |       |
| IOSTAR | 1#    | 1173# |       |       |       |       |       |       |       |       |       |       |       |       |       |
| KT11   | 1#    | 1173# |       |       |       |       |       |       |       |       |       |       |       |       |       |
| LASTAD | 1#    | 1173# | 8088  |       |       |       |       |       |       |       |       |       |       |       |       |
| MANUAL | 1#    | 1173# |       |       |       |       |       |       |       |       |       |       |       |       |       |
| MEMORY | 1#    | 1173# |       |       |       |       |       |       |       |       |       |       |       |       |       |
| MSG    | 4009# | 4015  | 4407# | 4413  | 4649# | 4655  | 4801# | 4807  | 4979# | 4985  | 5074# | 5080  | 5240# | 5246  | 5826# |
|        | 5832  | 6210# | 6216  | 6683# | 6689  | 6742# | 6748  | 6784# | 6790  | 7038# | 7044  |       |       |       |       |
| MSBYTE | 1#    | 1173# | 1203# | 1209  | 1210  | 1211  |       |       |       |       |       |       |       |       |       |
| MSCHEC | 1#    | 1173# | 5268# | 5275# | 5854# | 5862# | 6241# | 6809# | 6815# | 7212# | 7218# |       |       |       |       |
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CROSS REFERENCE TABLE -- MACRO NAMES

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## CROSS REFERENCE TABLE -- MACRO NAMES

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## CROSS REFERENCE TABLE -- MACRO NAMES

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CROSS REFERENCE TABLE -- MACRO NAMES

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CROSS REFERENCE TABLE -- MACRO NAMES

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|        | 4858# | 4894# | 4908# | 4914# | 4925# | 4961# | 4975# | 4978# | 5005# | 5007# | 5021# | 5058# | 5073# | 5097# | 5101# |
|        | 5103# | 5114# | 5116# | 5123# | 5125# | 5132# | 5134# | 5141# | 5143# | 5150# | 5152# | 5158# | 5164# | 5169# | 5173# |
|        | 5175# | 5186# | 5188# | 5195# | 5197# | 5204# | 5206# | 5213# | 5215# | 5222# | 5224# | 5230# | 5236# | 5239# | 5266# |
|        | 5268# | 5275# | 5286# | 5315# | 5320# | 5332# | 5334# | 5341# | 5343# | 5355# | 5357# | 5370# | 5372# | 5379# | 5381# |
|        | 5390# | 5395# | 5407# | 5409# | 5417# | 5419# | 5428# | 5430# | 5437# | 5439# | 5446# | 5448# | 5455# | 5462# | 5467# |



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CROSS REFERENCE TABLE -- MACRO NAMES

|        |       |       |       |       |       |       |       |       |       |       |       |       |       |       |       |
|--------|-------|-------|-------|-------|-------|-------|-------|-------|-------|-------|-------|-------|-------|-------|-------|
|        | 5473# | 5478# | 5489# | 5494# | 5522# | 5527# | 5546# | 5548# | 5562# | 5564# | 5571# | 5573# | 5582# | 5587# | 5612# |
|        | 5614# | 5621# | 5623# | 5630# | 5637# | 5642# | 5651# | 5656# | 5668# | 5671# | 5707# | 5854# | 5860# | 5862# | 5869# |
|        | 5895# | 5897# | 5941# | 5946# | 5969# | 5976# | 5981# | 5991# | 5996# | 6005# | 6014# | 6039# | 6041# | 6091# | 6096# |
|        | 6124# | 6131# | 6136# | 6150# | 6152# | 6164# | 6169# | 6181# | 6184# | 6239# | 6241# | 6263# | 6272# | 6274# | 6282# |
|        | 6292# | 6306# | 6316# | 6323# | 6345# | 6354# | 6356# | 6364# | 6374# | 6388# | 6398# | 6405# | 6427# | 6436# | 6438# |
|        | 6446# | 6456# | 6470# | 6480# | 6487# | 6509# | 6518# | 6520# | 6528# | 6538# | 6552# | 6562# | 6569# | 6585# | 6594# |
|        | 6596# | 6606# | 6616# | 6623# | 6639# | 6648# | 6650# | 6660# | 6670# | 6677# | 6682# | 6704# | 6706# | 6727# | 6734# |
|        | 6741# | 6761# | 6763# | 6777# | 6783# | 6809# | 6815# | 6832# | 6836# | 6838# | 6845# | 6879# | 6907# | 6910# | 6923# |
|        | 6930# | 6934# | 6944# | 6949# | 6951# | 6958# | 6980# | 6990# | 6993# | 6998# | 7006# | 7010# | 7013# | 7212# | 7218# |
|        | 7233# | 7237# | 7239# | 7252# | 7264# | 7280# | 7288# | 7290# | 7296# | 7299# | 7304# | 7312# | 7316# | 7320# | 7322# |
|        | 7339# | 7353# | 7377# | 7381# | 7395# | 7399# | 7407# | 7410# | 7423# | 7430# | 7443# | 7454# | 7462# | 7466# | 7469# |
| MSWORD | 1#    | 1173# | 1242# | 1251  | 1298# | 1300  | 1301  | 1302  | 1303  | 1304  | 1305  | 1306  | 1307  | 1308  | 1309  |
|        | 1310  | 1311  | 1312  | 2808# | 2809  | 2810  | 2811  | 2842# | 2843  | 2844  | 2845  | 4083# | 4084  | 4085  | 4086  |
|        | 4108# | 4109  | 4110  | 4111  | 4137# | 4138  | 4139  | 4140  | 4156# | 4157  | 4158  | 4159  | 4175# | 4176  | 4177  |
|        | 4178  | 4200# | 4201  | 4202  | 4203  | 4225# | 4226  | 4227  | 4228  | 4245# | 4246  | 4247  | 4248  | 4269# | 4270  |
|        | 4271  | 4272  | 4295# | 4296  | 4297  | 4298  | 4319# | 4320  | 4321  | 4322  | 4342# | 4343  | 4344  | 4345  | 4480# |
|        | 4481  | 4482  | 4483  | 4553# | 4554  | 4555  | 4556  | 4588# | 4589  | 4590  | 4591  | 4621# | 4622  | 4623  | 4624  |
|        | 4707# | 4708  | 4709  | 4710  | 4752# | 4753  | 4754  | 4755  | 4794# | 4795  | 4796  | 4797  | 4894# | 4895  | 4896  |
|        | 4897  | 4961# | 4962  | 4963  | 4964  | 5058# | 5059  | 5060  | 5061  | 5158# | 5159  | 5160  | 5161  | 5230# | 5231  |
|        | 5232  | 5233  | 5268# | 5275# | 5315# | 5316  | 5317  | 5318  | 5390# | 5391  | 5392  | 5393  | 5455# | 5456  | 5457  |
|        | 5458  | 5462# | 5463  | 5464  | 5465  | 5473# | 5474  | 5475  | 5476  | 5522# | 5523  | 5524  | 5525  | 5582# | 5583  |
|        | 5584  | 5585  | 5630# | 5631  | 5632  | 5633  | 5637# | 5638  | 5639  | 5640  | 5651# | 5652  | 5653  | 5654  | 5854# |
|        | 5862# | 5941# | 5942  | 5943  | 5944  | 5969# | 5970  | 5971  | 5972  | 5976# | 5977  | 5978  | 5979  | 5991# | 5992  |
|        | 5993  | 5994  | 6091# | 6092  | 6093  | 6094  | 6124# | 6125  | 6126  | 6127  | 6131# | 6132  | 6133  | 6134  | 6164# |
|        | 6165  | 6166  | 6167  | 6241# | 6282# | 6283  | 6284  | 6285  | 6292# | 6293  | 6294  | 6295  | 6306# | 6307  | 6308  |
|        | 6309  | 6316# | 6317  | 6318  | 6319  | 6364# | 6365  | 6366  | 6367  | 6374# | 6375  | 6376  | 6377  | 6388# | 6389  |
|        | 6390  | 6391  | 6398# | 6399  | 6400  | 6401  | 6446# | 6447  | 6448  | 6449  | 6456# | 6457  | 6458  | 6459  | 6470# |
|        | 6471  | 6472  | 6473  | 6480# | 6481  | 6482  | 6483  | 6528# | 6529  | 6530  | 6531  | 6538# | 6539  | 6540  | 6541  |
|        | 6552# | 6553  | 6554  | 6555  | 6562# | 6563  | 6564  | 6565  | 6606# | 6607  | 6608  | 6609  | 6616# | 6617  | 6618  |
|        | 6619  | 6660# | 6661  | 6662  | 6663  | 6670# | 6671  | 6672  | 6673  | 6734# | 6735  | 6736  | 6737  | 6777# | 6778  |
|        | 6779  | 6780  | 6809# | 6815# | 6879# | 6880  | 6881  | 6882  | 6923# | 6924  | 6925  | 6926  | 6980# | 6981  | 6982  |
|        | 6983  | 6998# | 6999  | 7000  | 7001  | 7212# | 7218# | 7264# | 7265  | 7266  | 7267  | 7280# | 7281  | 7282  | 7283  |
|        | 7304# | 7305  | 7306  | 7307  | 7353# | 7354  | 7355  | 7356  | 7381# | 7382  | 7383  | 7384  | 7399# | 7400  | 7401  |
|        | 7402  | 7430# | 7431  | 7432  | 7433  | 7443# | 7444  | 7445  | 7446  | 7454# | 7455  | 7456  | 7457  | 8022# | 8027# |
|        | 8032# | 8038# | 8042# | 8048# | 8090  | 8091  |       |       |       |       |       |       |       |       |       |
| MSXFER | 1#    | 1173# |       |       |       |       |       |       |       |       |       |       |       |       |       |
| NEWTST | 1591# | 4009  | 4407  | 4649  | 4801  | 4979  | 5074  | 5240  | 5826  | 6210  | 6683  | 6742  | 6784  | 7038  |       |
| NTST   | 1591# | 4009  | 4407  | 4649  | 4801  | 4979  | 5074  | 5240  | 5826  | 6210  | 6683  | 6742  | 6784  | 7038  |       |
| OPEN   | 1#    | 1173# |       |       |       |       |       |       |       |       |       |       |       |       |       |
| POINTE | 1#    | 1173# | 1190  |       |       |       |       |       |       |       |       |       |       |       |       |
| PRINTB | 1#    | 1173# | 2965  | 3310  | 3687  |       |       |       |       |       |       |       |       |       |       |
| PRINTF | 1#    | 1173# | 3777  | 3853  | 3861  |       |       |       |       |       |       |       |       |       |       |
| PRINTS | 1#    | 1173# |       |       |       |       |       |       |       |       |       |       |       |       |       |
| PRINTX | 1#    | 1173# | 2893  | 2909  | 2920  | 2928  | 2938  | 2945  | 2973  | 2982  | 2991  | 3005  | 3023  | 3039  | 3047  |
|        | 3067  | 3087  | 3135  | 3142  | 3150  | 3158  | 3174  | 3180  | 3188  | 3205  | 3215  | 3232  | 3238  | 3244  | 3250  |
|        | 3256  | 3266  | 3276  | 3286  | 3325  | 3336  | 3372  | 3399  | 3405  | 3418  | 3437  | 3463  | 3469  | 3476  | 3517  |
|        | 3525  | 3539  | 3546  | 3560  | 3567  | 3581  | 3588  | 3609  | 3616  | 3624  | 3633  | 3647  | 3654  | 3662  | 3674  |
| READBU | 1#    | 1173# |       |       |       |       |       |       |       |       |       |       |       |       |       |
| READEP | 1#    | 1173# | 3725  | 3731  | 3737  | 3743  |       |       |       |       |       |       |       |       |       |
| RFLAGS | 1#    | 1173# |       |       |       |       |       |       |       |       |       |       |       |       |       |
| SETDF  | 1591# | 1993  | 2033  | 2073  | 2116  | 2179  | 2732  | 5815  |       |       |       |       |       |       |       |
| SETHRD | 1591# |       |       |       |       |       |       |       |       |       |       |       |       |       |       |
| SETPRI | 1#    | 1173# | 6908  | 6991  | 7297  | 7375  | 7393  | 7421  |       |       |       |       |       |       |       |
| SETSF  | 1591# |       |       |       |       |       |       |       |       |       |       |       |       |       |       |
| SETSFT | 1591# |       |       |       |       |       |       |       |       |       |       |       |       |       |       |
| SETVEC | 1#    | 1173# | 3759  | 3871  | 3879  | 3923  | 5702  | 6840  | 6953  | 7247  | 7334  |       |       |       |       |

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CROSS REFERENCE TABLE -- MACRO NAMES

|        |       |       |       |       |       |       |       |       |       |       |       |      |      |      |      |
|--------|-------|-------|-------|-------|-------|-------|-------|-------|-------|-------|-------|------|------|------|------|
| SLASH  | 1#    | 1173# |       |       |       |       |       |       |       |       |       |      |      |      |      |
| STARS  | 1#    | 1173# |       |       |       |       |       |       |       |       |       |      |      |      |      |
| SVC    | 1#    | 1171# | 1172  |       |       |       |       |       |       |       |       |      |      |      |      |
| TSGEN  | 1591# | 1993  | 2033  | 2073  | 2116  | 2179  | 2732  | 5815  |       |       |       |      |      |      |      |
| XFER   | 1#    | 1173# | 5268# | 5275# | 5854# | 5862# | 6241# | 6809# | 6815# | 7212# | 7218# |      |      |      |      |
| XFERF  | 1#    | 1173# |       |       |       |       |       |       |       |       |       |      |      |      |      |
| XFERT  | 1#    | 1173# |       |       |       |       |       |       |       |       |       |      |      |      |      |
| SGEDF  | 1591# | 2807  | 2841  | 4082  | 4107  | 4136  | 4155  | 4174  | 4199  | 4224  | 4244  | 4268 | 4294 | 4318 | 4341 |
|        | 4479  | 4552  | 4587  | 4620  | 4706  | 4751  | 4793  | 4893  | 4960  | 5057  | 5157  | 5229 | 5314 | 5389 | 5454 |
|        | 5461  | 5472  | 5521  | 5581  | 5629  | 5636  | 5650  | 5940  | 5968  | 5975  | 5990  | 6090 | 6123 | 6130 | 6163 |
|        | 6281  | 6291  | 6305  | 6315  | 6363  | 6373  | 6387  | 6397  | 6445  | 6455  | 6469  | 6479 | 6527 | 6537 | 6551 |
|        | 6561  | 6605  | 6615  | 6659  | 6669  | 6733  | 6776  | 3878  | 6922  | 6979  | 6997  | 7263 | 7279 | 7303 | 7352 |
|        | 7380  | 7398  | 7429  | 7442  | 7453  |       |       |       |       |       |       |      |      |      |      |
| SGEHRD | 1591# |       |       |       |       |       |       |       |       |       |       |      |      |      |      |
| SGESF  | 1591# |       |       |       |       |       |       |       |       |       |       |      |      |      |      |
| SGESFT | 1591# |       |       |       |       |       |       |       |       |       |       |      |      |      |      |
| SGTDF  | 1591# | 1992  | 2032  | 2072  | 2115  | 2178  | 2731  | 5814  |       |       |       |      |      |      |      |
| SGTHRD | 1591# |       |       |       |       |       |       |       |       |       |       |      |      |      |      |
| SGTSF  | 1591# |       |       |       |       |       |       |       |       |       |       |      |      |      |      |
| SGTSFT | 1591# |       |       |       |       |       |       |       |       |       |       |      |      |      |      |

. ABS. 037154 000

ERRORS DETECTED: 0

CVDMBA.BIN, CVDMBA.SEQ/CRF/SOL=SVC34R.MAC, CVDMBA.P11  
 RUN-TIME: 40 49 5 SECONDS  
 RUN-TIME RATIO: 156/95=1.6  
 CORE USED: 21K (41 PAGES)