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IDENTIFICATION

PRODUCT CODE: AC-F264A-MC
PRODUCT NAME: CVD MBA0 DMV11 MICRO-CONTROLLER STATIC DIAGNOSTIC PART 2
PRODUCT DATE: JANUARY 1981
MAINTAINER: DIAGNOSTICS MERRIMACK CC:38P
AUTHORS: CHRIS BRIENEN
RAY MARSHALL
PURPOSE: THIS DIAGNOSTIC IS DESIGNED TO PERFORM STATIC LOGIC TESTS FOR
THE M8053 OR M8064 (HEREAFTER REFERRED TO AS THE DMV OR DMV-11)

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1.0 INTRODUCTION

THE M8053 AND M8064 ARE SINGLE-LINE SYNCHRONOUS, MICRO-PROCESSOR BASED COMMUNICATIONS INTERFACES WHICH CAN SUPPORT BOTH CHARACTER-ORIENTED (DDCMP, BSC, ETC.) AND BIT-ORIENTED (SDLC, HDLC, ETC.) PROTOCOLS. THE PURPOSE OF THIS PROGRAM IS TO PERFORM DIAGNOSTIC TESTING OF THE CSRS, RAM, AND BASIC MICRO-PROCESSOR LOGIC ON THESE BOARDS. THE FOLLOWING FUNCTIONS WILL BE PERFORMED: DMV RESIDENT U-DIAG EXECUTION CSR ADDRESSING, VIA REGISTER STATIC BIT INTERACTION AND READ/WRITE TESTING, AND ON-BOARD RAM TESTING.

THE STATIC LOGIC TESTS WILL PROVIDE EXTENSIVE TROUBLESHOOTING CAPABILITIES, SUCH AS TIGHT SCOPE LOOPS, SWITCH OPTIONS, AND ABILITY TO 'LOCK' ONTO INTERMITTENT ERRORS. IN ADDITION TESTS ARE DESIGNED AND STRUCTURED TO ACHIEVE MAXIMUM FAULT RESOLUTION AND FACILITATE REPLACEMENT OF THE SMALLEST FIELD REPLACEABLE UNIT.

THIS PROGRAM IS IMPLEMENTED USING THE DIAGNOSTIC SUPERVISOR AND A STRUCTURED PROGRAMMING APPROACH. BECAUSE THE DESIGN CONFORMS TO THE SUPERVISOR (STANDALONE VERSION) THE PROGRAM IS COMPATIBLE WITH ACT, APT, XXDP+, AND SLIDE.

THROUGH DIALOGUE WITH THE OPERATOR, THE PROGRAM ALLOWS MODIFICATION OF DEVICE PARAMETERS, SUCH AS LSI-BUS ADDRESS, VECTOR ADDRESSES AND DEVICE PRIORITY. IN ADDITION, THE OPERATOR CAN SPECIFY PARTICULAR TESTS TO BE RUN AND A VARIETY OF LOOPING, RUNNING, AND REPORTING MODES.

DEVICE ERRORS WILL BE REPORTED AS THEY OCCUR. THE REPORT WILL INCLUDE A TEST NUMBER AND DESCRIPTION OF THE ERROR, GOOD AND BAD TEST DATA, AND APPLICABLE DEVICE REGISTER CONTENTS.

2.0 HARDWARE REQUIREMENTS

THE FOLLOWING HARDWARE IS REQUIRED TO RUN THE M8053/8064 STATIC LOGIC TESTS:

PDP-11/03 OR PDP-11/23
16K WORDS OF MEMORY
CONSOLE TERMINAL
M8053 OR M8064 COMMUNICATIONS INTERFACE

3.0 PRELIMINARY PROGRAM REQUIREMENTS

THIS PROGRAM (CVDMB) SHOULD BE THE SECOND OF THE FIVE DMV-11 STATIC DIAGNOSTICS TO BE RUN (CVDMA SHOULD BE RUN FIRST). ERRORS FOUND IN THIS PROGRAM SHOULD BE CORRECTED BEFORE RUNNING ANY OF THE LINE UNIT DIAGNOSTICS (CVDMC, CVDMD, OR CVDME).

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4.0 GENERAL PROGRAM CONSIDERATIONS

4.1 DIAGNOSTIC SUPERVISOR

THIS PROGRAM IS COMPATIBLE WITH THE STANDALONE DIAGNOSTIC SUPERVISOR, AND MUST BE LOADED TO BE CO-RESIDENT WITH THE SUPERVISOR, OR BE PREVIOUSLY COMBINED WITH THE SUPERVISOR AND LOADED AS A SINGLE FILE. IN EITHER CASE, THE COMBINED PROGRAM WILL NOT EXCEED 16K OF MEMORY.

4.2 EXECUTION TIME

THE MAXIMUM TIME REQUIRED TO RUN THIS PROGRAM IS ABOUT ONE MINUTE PER PASS FOR EACH UNIT.

4.3 XXDP+

THIS PROGRAM MAY BE LOADED UNDER XXDP+, AND MAY BE RUN IN DUMP MODE OR CHAIN MODE.

4.4 ACT/SLIDE

THIS PROGRAM MAY BE LOADED UNDER ACT OR SLIDE AND MAY BE RUN IN DUMP MODE OR CHAIN MODE.

4.5 APT

THIS PROGRAM MAY BE LOADED BY THE APT SYSTEM (INCLUDING APT-RD) AND RUN IN PROGRAM MODE OR SCRIPT MODE.

4.6 MEMORY MANAGEMENT

MEMORY MANAGEMENT IS UTILIZED IN THIS PROGRAM TO VERIFY THE DMV-11'S ABILITY TO NPR INTO (AND OUT OF) EXTENDED MEMORY.

4.7 ERROR LOGGING

AT THE END OF EACH PASS ON ALL UNITS, THE PROGRAM PRINTS OUT THE CUMULATIVE TOTAL NUMBER OF ERRORS SINCE THE LAST START OR RESTART COMMAND.

5.0 PROGRAM LOAD MEDIA

THIS PROGRAM CAN BE LOADED FROM PAPER TAPE USING THE ABSOLUTE LOADER OR FROM ACT, SLIDE, OR APT SYSTEMS, OR FROM ANY MEDIA SUPPORTED BY XXDP+. WHEN USING THE PAPER TAPE ABSOLUTE LOADER, THE PROGRAM SHOULD BE LOADED FIRST,

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FOLLOWED BY THE DIAGNOSTIC SUPERVISOR. WHEN USING XXDP+, THE DIAGNOSTIC SUPERVISOR SHOULD BE LOADED FIRST, FOLLOWED BY THE DIAGNOSTIC PROGRAM.

6.0 OPERATING INSTRUCTIONS

6.1 LOADING AND STARTING PROCEDURES

6.1.1 LOADING PROCEDURES

THIS PROGRAM MAY BE LOADED FROM PAPER TAPE USING THE ABSOLUTE LOADER. IT MAY ALSO BE LOADED FROM ANY XXDP+ LOAD MEDIA. WHEN LOADED UNDER XXDP+, THE DIAGNOSTIC SUPERVISOR WILL BE LOADED AUTOMATICALLY.

6.1.2 STARTING PROCEDURES

THE PROGRAM STARTS AT LOCATION 200. USE STANDARD DEC PROCEDURES TO START THE PROGRAM.

6.1.3 STEPS FOR QUICK AND SIMPLE EXECUTION

THE DIAGNOSTIC CAN BE EXECUTED STANDALONE UNDER XXDP+, WITHOUT READING THE REMAINDER OF THIS DOCUMENT, AS FOLLOWS:

- A) LOAD AND START DIAGNOSTIC USING RUN COMMAND
- B) RECEIVE DIAGNOSTIC SUPERVISOR IDENTIFICATION AND PROMPT (DRS-C>)
- C) ENTER STA<CR>
- D) ANSWER HARDWARE AND SOFTWARE QUESTIONS
- E) GET END OF PASS MESSAGES OR ERROR MESSAGES
- F) TO END EXECUTION, ENTER CONTROL/C

6.2 INITIAL DIALOGUE

AFTER THE PROGRAM AND THE SUPERVISOR ARE LOADED AND THE PROGRAM IS STARTED, THE FOLLOWING IDENTIFICATION IS TYPED :

DRS LOADED
DIAG. RUN-TIME SERVICES
CVD MB-A-0
DMV-11 U-CONTRL LOGIC DIAG - PART 2 OF 2
UNIT IS M8053 OR M8064
DR>

THE OPERATOR THEN PROCEEDS BY TYPING ONE OR MORE OF THE COMMANDS DESCRIBED IN THE FOLLOWING SECTION 6.3. (FOR MORE DETAILED INFORMATION, REFER TO THE DIAGNOSTIC SUPERVISOR FUNCTIONAL SPECIFICATION).

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ISR INTERVENTION TESTS
IDU INHIBIT STATISTICAL REPORTS
LOT INHIBIT DROPPING OF UNITS BY DIAGNOSTIC LOOP ON TEST

THE FLAGS NAMED OR EQUATED TO 1 ARE SET, THOSE EQUATED TO 0 ARE CLEARED. A FLAG NOT SPECIFIED IS CLEARED. IF THE FLAGS SWITCH IS NOT GIVEN ALL FLAGS ARE CLEARED. SEE EXAMPLE AT END OF 6.3.1.5.

6.3.1.4 END OF PASS SWITCH (/EOP:<INCR>)

<INCR> IS A DECIMAL NUMBER INDICATING HOW OFTEN (IN TERMS OF PASSES) IT IS DESIRED THAT THE END OF PASS MESSAGE BE PRINTED. THE DEFAULT IS AT THE END OF EVERY PASS. SEE EXAMPLE AT END OF 6.3.1.5.

6.3.1.5 EFFECT OF START COMMAND

THE EFFECT OF THE START COMMAND IS TO INITIATE THE HARDWARE PARAMETER DIALOGUE, THE SOFTWARE PARAMETER DIALOGUE, AND THEN THE DIAGNOSTIC TESTS THEMSELVES.

THE HARDWARE PARAMETER DIALOGUE COMMENCES WITH THE QUESTION '# UNITS?' TO WHICH THE OPERATOR REPLIES WITH A DECIMAL NUMBER N FROM 1 TO 16. THE TERM 'UNIT' REFERS TO THE DEVICE TO WHICH THIS SERIES OF DIAGNOSTICS IS DEDICATED. FOLLOWING THIS ARE THE QUESTIONS WHEREBY THE P-TABLES THEMSELVES WILL BE BUILT. EACH P-TABLE IS A CORE-RESIDENT TABLE CONTAINING ALL THE HARDWARE INFORMATION FOR ONE UNIT. THE OPERATOR MUST SUPPLY N (NUMBER OF UNITS) VALUES FOR EACH QUESTION. HE MAY DO THIS BY GIVING ONE ANSWER TO EACH QUESTION (IN WHICH CASE THE SERIES OF QUESTIONS WILL BE POSED N TIMES) OR BY GIVING N VALUES, SEPARATED BY COMMAS, TO EACH QUESTION (SERIES WILL BE POSED ONCE). EACH QUESTION IS FOLLOWED BY THE RESPONSE RADIX (D FOR DECIMAL, B FOR BINARY, O FOR OCTAL, L FOR YES/NO) IN PARENTHESES AND THE DEFAULT VALUE AFTER THE PARENTHESES.

FOLLOWING THE HARDWARE QUESTIONS ARE THE SOFTWARE QUESTIONS TO BUILD THE SOFTWARE TABLES, WHICH DEFINE THE MODE (QUICK VERIFY ETC.) THAT THE DIAGNOSTIC WILL EXECUTE IN.

WHEN THE QUESTION '# UNITS?' IS ANSWERED, MEMORY STORAGE IS ALLOCATED FOR THE P-TABLES, AND IF THERE IS NOT ENOUGH TO ACCOMMODATE THEM THE MESSAGE 'TOO MANY UNITS' IS ISSUED. IN THIS CASE THE DIAGNOSTIC MUST BE EXECUTED MORE THAN ONCE TO TEST ALL UNITS.

EXAMPLE:

STA/TESTS:1:2-4:6:8-10/PASS:3/FLAGS:IER:HOE=1:UAM:LOE

THIS COMMAND WILL CAUSE THREE PASSES TO BE MADE, EACH PASS

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CONSISTING OF TESTS 1,2,3,4,6,8,9, AND 10 EXECUTED AGAINST ALL UNITS. THERE IS NO DIFFERENCE BETWEEN SAYING <FLAG> AND SAYING <FLAG=1>. THE NOTATION <FLAG=0> IS MEANINGFUL ONLY ON A COMMAND OTHER THAN START TO CLEAR A FLAG THAT WAS PREVIOUSLY SET. NOTE THAT ON ALL COMMANDS ONLY THE FIRST THREE LETTERS ARE SCANNED.

6.3.2 RESTART COMMAND

RES(TART)/TESTS:<TEST-LIST>/PASS:<PASS-CNT>/FLAGS:
 <FLAG-LIST>/UNITS:<UNIT-LIST>

6.3.2.1 TESTS, PASS, AND FLAGS SWITCHES

<TEST-LIST>, <PASS-CNT>, AND <FLAG-LIST> ARE AS IN THE START COMMAND.

6.3.2.2 UNITS SWITCH (/UNITS:<UNIT-LIST>)

<UNIT-LIST> IS A SEQUENCE OF DECIMAL NUMBERS (0,1 ETC.) OR RANGES OF DECIMAL NUMBERS (0-5, 8-10 ETC.) THAT SPECIFY THE UNITS TO BE TESTED. THE NUMBERS ARE SEPARATED BY COLONS. THE NUMBERS MAY RANGE FROM 0 THRU N-1 (N IS THE NUMBER OF UNITS SPECIFIED IN THE PREVIOUS START COMMAND). THE NUMBER INDICATES THE POSITION OF THE P-TABLE AS THE DATA WAS ENTERED DURING THE HARDWARE DIALOGUE. THE UNITS WHICH ARE SELECTED MUST NOT HAVE BEEN DROPPED BY THE DROP COMMAND. SEE THE DISCUSSION OF ADD AND DROP COMMANDS BELOW. DEFAULT IS TO TEST ALL UNITS WHICH HAVE NOT BEEN DROPPED BY A DROP COMMAND.

6.3.2.3 EFFECT OF RESTART COMMAND

THE RESTART COMMAND DIFFERS FROM THE START COMMAND IN THAT THE P-TABLES FROM THE PREVIOUS START COMMAND (THERE MUST HAVE BEEN ONE) ARE USED, INSTEAD OF NEW ONES BEING BUILT. THE UNITS SWITCH GIVES THE ABILITY TO SELECT A SUBSET OF THESE. THE SOFTWARE DIALOGUE MAY OPTIONALLY BE REEXECUTED (OPERATOR WILL BE ASKED). THE COMMAND CAN BE USED AFTER COMMAND MODE HAS BEEN REENTERED IN ANY OF THE THREE NORMAL WAYS: A) THE REQUESTED NUMBER OF PASSES HAVE BEEN MADE B) AN ERROR WAS ENCOUNTERED WITH THE HALT ON ERROR FLAG SET C) A CONTROL/C WAS ENTERED BY THE OPERATOR.

6.3.3 CONTINUE COMMAND

CON(TINUE)/PASS:<PASS-CNT>/FLAGS:<FLAG-LIST>

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6.3.3.1 PASS SWITCH (/PASS:<PASS-CNT>)

<PASS-CNT> IS SAME AS IN START COMMAND, BUT THE DEFAULT IS THE UNSATISFIED PASS-CNT FROM THE PREVIOUS START OR RESTART. IF NONE REMAINS, THE DEFAULT IS NON-ENDING EXECUTION.

6.3.3.2 FLAG SWITCH (/FLAGS:<FLAG-LIST>)

<FLAG-LIST> IS SAME AS IN START COMMAND, BUT UNSPECIFIED FLAGS RETAIN THEIR CURRENT VALUE.

6.3.3.3 EFFECT OF CONTINUE COMMAND

CONTINUE MUST FOLLOW A START OR RESTART, AND COMMAND MODE MUST HAVE BEEN ENTERED DUE TO A HALT ON ERROR OR A CONTROL/C. THE EFFECT OF THE COMMAND IS TO GO TO THE BEGINNING OF THE TEST THAT WAS BEING EXECUTED WHEN THE HALT OR CONTROL/C TOOK PLACE. SOFTWARE DIALOGUE MAY OPTIONALLY BE REEXECUTED. HARDWARE PARAMETERS MAY NOT BE CHANGED.

6.3.4 PROCEED COMMAND

PRO(CEED)/FLAGS:<FLAG-LIST>

6.3.4.1 FLAGS SWITCH (/FLAGS:<FLAG-LIST>)

<FLAG-LIST> IS AS IN THE START COMMAND, BUT UNSPECIFIED FLAGS RETAIN THEIR CURRENT VALUE.

6.3.4.2 EFFECT OF PROCEED COMMAND

PROCEED MUST FOLLOW A START, RESTART, OR CONTINUE. COMMAND MODE MUST HAVE BEEN ENTERED VIA A HALT ON ERROR. THE EFFECT OF THE COMMAND IS TO BEGIN EXECUTION AT THE LOCATION FOLLOWING THE ERROR CALL. NEITHER HARDWARE NOR SOFTWARE PARAMETERS MAY BE ALTERED.

6.3.5 ADD COMMAND

ADD/UNITS:<UNIT-LIST>

6.3.5.1 UNITS SWITCH (/UNITS:<UNIT-LIST>)

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<UNIT-LIST> IS AS IN THE RESTART COMMAND.

6.3.5.2 EFFECT OF ADD COMMAND

THE UNITS SPECIFIED ARE ADDED TO THE TEST SEQUENCE. EACH UNIT MUST HAVE A P-TABLE IN MEMORY DUE TO AN EARLIER HARDWARE DIALOGUE. THIS COMMAND MUST BE FOLLOWED BY A RESTART OR CONTINUE. THE UNITS SWITCH MUST BE SPECIFIED. THE ADD COMMAND IS MEANINGFUL ONLY FOR UNITS THAT WERE PREVIOUSLY DROPPED.

6.3.6 DROP COMMAND

DRO(P)/UNITS:<UNIT-LIST>

6.3.6.1 UNITS SWITCH (/UNITS:<UNIT-LIST>)

<UNIT-LIST> IS AS IN THE RESTART COMMAND.

6.3.6.2 EFFECT OF DROP COMMAND

THE UNITS SPECIFIED WILL BE DROPPED FROM TESTING. THE UNITS WILL BE RESELECTED ONLY BY THE EXECUTION OF AN ADD OR START COMMAND. THE UNITS SWITCH MUST BE ENTERED. THIS COMMAND MUST BE FOLLOWED BY A RESTART OR A CONTINUE COMMAND.

6.3.7 PRINT COMMAND

PRI(NT)

6.3.7.1 EFFECT OF PRINT COMMAND

THE TOTAL NUMBER OF ERRORS FOR EACH UNIT SINCE THE LAST START OR RESTART COMMAND ARE PRINTED. THE ISR (INHIBIT STATISTICAL REPORTING) FLAG IS CLEARED.

6.3.8 DISPLAY COMMAND

DIS(PLAY)/UNITS:<UNIT-LIST>

6.3.8.1 UNITS SWITCH (/UNITS:<UNIT-LIST>)

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<UNIT-LIST> IS AS IN THE RESTART COMMAND.

6.3.8.2 EFFECT OF DISPLAY COMMAND

THE HARDWARE P-TABLES FOR ALL UNITS UNDER TEST ARE PRINTED OUT IN THE FORMAT IN WHICH THEY WERE ENTERED. ANY UNITS THAT WERE DROPPED BY THE OPERATOR 'DROP' COMMAND ARE SO DESIGNATED.

6.3.9 FLAGS COMMAND

FLA(GS)

6.3.9.1 EFFECT OF FLAGS COMMAND

THE CURRENT SETTINGS OF ALL FLAGS ARE PRINTED.

6.3.10 ZFLAGS COMMAND

ZFL(AGS)

6.3.10.1 EFFECT OF ZFLAGS COMMAND

ALL FLAGS ARE CLEARED.

6.3.11 CONTROL CHARACTERS

A CONTROL C (C) ENTERED DURING THE EXECUTION OF A DIAGNOSTIC CAUSES A RETURN TO COMMAND MODE.

A CONTROL Z (Z) ENTERED DURING ONE OF THE THREE OPERATOR DIALOGUES- HARD CORE QUESTIONS (SEE 6.2), HARDWARE DIALOGUE (SEE 6.3.1.5), OR SOFTWARE DIALOGUE (SEE 6.3.1.5) CAUSES THE DEFAULTS TO BE TAKEN FOR THE REMAINDER OF THAT DIALOGUE.

A CONTROL O (O) ENTERED DURING THE EXECUTION OF A DIAGNOSTIC CAUSES ALL TELETYPE OUTPUT TO BE SUPPRESSED FOR THE REMAINDER OF THE DIAGNOSTIC OR UNTIL ANOTHER O IS TYPED, WHICH RESTORES NORMAL TELETYPE OUTPUT.

6.3.12 HARDWARE PARAMETERS

THE FOLLOWING 3 QUESTIONS WILL BE ASKED ON A START COMMAND. THE VALUE LOCATED TO THE LEFT OF THE QUESTION MARK IS THE DEFAULT VALUE THAT WILL BE TAKEN ON A CARRIAGE RETURN

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RESPONSE.

1. DEVICE CSR ADDRESS : (0) 160020?

THIS IS THE ADDRESS AT WHICH THE CSR REGISTERS (SELO) RESIDE ON THE LSI-BUS. THE ALLOWABLE RANGE IS 160020-177760 (OCTAL), AND THE DEFAULT VALUE IS 160020.

2. DEVICE VECTOR ADDRESS : (0) 300 ?

THIS IS THE ADDRESS OF THE INPUT INTERRUPT VECTOR FOR THIS DEVICE. THE ALLOWABLE RANGE IS 000-674 (OCTAL), AND THE DEFAULT VALUE IS 300.

3. DEVICE PRIORITY LEVEL : (0) 4 ?

THIS IS THE CPU PRIORITY AT WHICH THE INTERRUPT HANDLERS OF THIS DEVICE WILL BE EXECUTED. THE ALLOWABLE RANGE IS 0-7, AND THE DEFAULT VALUE IS 4.

4. IS THE PROCESSOR STRAPPED TO MODE 0 ON POWER UP : (L) Y ?

THIS IS THE CPU'S POWER UP STRAPPING. 'MODE 0' INDICATES THAT THE PROCESSOR WILL POWER UP USING LOCATIONS 24 AND 26. IF THE ANSWER TO THIS QUESTION IS NO, TESTS WHICH USE 'DCOK' WILL BE SKIPPED.

(NOTE: MODE 0 IS SELECTED WHEN JUMPERS W5 AND W6 ARE 'REMOVED' - SEE MICROCOMPUTER PROCESSOR HANDBOOK FOR MORE INFORMATION).

5. BOARD TYPE (0=M8064, 1=M8053-V35, 2=M8053-EIA) : (0) 0 ?

THIS IS THE TYPE OF DMV-11 CURRENTLY INSTALLED. NOTE THAT THE M8053 IS SWITCH SELECTABLE BETWEEN V.35 AND EIA.

6. IS THIS A MANUFACTURING TEST STAND : (L) N ?

THIS QUESTION REFERS TO A SPECIFIC MEMORY CONFIGURATION THAT IS REQUIRED TO RUN TEST #8 (SEE SEC. 7.0).

6.3.13 SOFTWARE PARAMETERS

NO SOFTWARE PARAMETER QUESTIONS ARE ASKED BY THIS PROGRAM.

6.3.14 EXTENDED DISCUSSION OF P-TABLE DIALOGUE

THE FULL CAPABILITY OF THE HARDWARE DIALOGUE IS REVEALED BY THE FOLLOWING DISCUSSION OF WHAT HAPPENS INTERNALLY.

AS SOON AS THE QUESTION '# UNITS?' IS ANSWERED (WITH THE NUMBER N, SAY) SPACE IN CORE IS ALLOCATED FOR N P-TABLES. ALL OF THE P-TABLES ARE OF THE SAME FORMAT, AND THERE IS A ONE-TO ONE CORRESPONDENCE BETWEEN THE HARDWARE PARAMETER QUESTIONS AND THE SLOTS IN THE P-TABLE FORMAT.

ON THE FIRST TRIP THRU THE QUESTIONS, ALL OF THE SLOTS IN

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THE SECOND TIME THRU THE SERIES, TABLES 7 THRU THE END ARE GOING TO BE AFFECTED (NOTE THAT THIS PIECE OF INFORMATION IS PRINTED OUT FOR THE THE OPERATOR IN THE FORM 'UNIT XX' AT THE BEGINNING OF EACH SERIES). QUESTION 1 IS RESPONDED TO BY A <CR>, SO SLOT ONE STAYS AT CONSTANT 75 IN TABLES 7 THRU 15, SINCE NO NEW EXPLICIT VALUES ARE TYPED IN. SLOT TWO GETS THE VALUES 7,8,9,10,11 IN TABLES 7 THRU 11, AND GETS AN 11 IN SLOT 12, AND GETS THE VALUES 13,14,15 IN TABLES 13 THRU 15. SLOT THREE GETS THE VALUE 77 IN TABLES 7 THRU 15.

THE DIALOGUE IS TERMINATED WHEN THE SOFTWARE RECOGNIZES THAT 16 EXPLICIT VALUES HAVE BEEN GIVEN FOR AT LEAST ONE QUESTION (NAMELY QUESTION 2).

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7.0 TEST DESCRIPTIONS

```

*****
* TEST 1 <VIA TIMER 2 ONE SHOT MODE>
*
* THIS TEST VERIFIES THAT THE TIMER 2 COUNTER IS OPERATIONAL IN
* INTERVAL-TIMER (ONE-SHOT) MODE.
*
* THE FOLLOWING IS PERFORMED :
*
* A MASTER CLEAR IS DONE & THE TIMER IS PLACED IN INTERVAL-TIMER MODE
* BY SETTING ACR5 = 0 AND THE PROGRAM CHECKS FOR 'T2' (BIT 5 IN IFR)
* TO BE INITIALLY CLEARED.
*
* T2L-L (ADR 08) & T2C-H (ADR 09) ARE BOTH LOADED WITH 252 (OCTAL).
* (THIS IS EQUIVALENT TO AAAA (HEX) OR 43,690 (DECIMAL).) LOADING
* T2C-H STARTS THE COUNTER.
*
* T2L-L IS LOADED WITH 001 AND T2C-H IS LOADED WITH 000 IN ORDER TO
* SET 'T2' WITH A QUICK UNDERFLOW. THE 'T2' FLAG BIT IN IFR IS READ
* AND CHECKED TO BE SET.
*
* T2C-H IS CHECKED TO = 0. CHECKING T2C-H SHOULD NOT HAVE CLEARED 'T2'
* -- THIS IS VERIFIED.
*
* T2C-L IS CHECKED TO = 0. CHECKING T2C-L SHOULD HAVE CLEARED 'T2' --
* THIS TOO IS VERIFIED.
*
* T2C-H IS LOADED WITH 0 AGAIN TO INITIATE A NEW COUNT DOWN (WHICH
* SHOULD UNDERFLOW ALMOST IMMEDIATELY) AND THE 'T2' BIT IN IFR IS
* CHECKED TO BE SET AGAIN.
*
* T2L-L IS LOADED WITH 125 (OCTAL) AND 'T2' BIT IS CHECKED TO BE STILL
* SET.
*
* T2C-H IS LOADED WITH 125, AND THE 'T2' BIT IS READ AND CHECKED TO BE
* CLEARED BY THE LOADING OF T2C-H.
*****

```

```

*****
* TEST 2 <VIA'S SR INPUT (MODE 2) - SYSTEM CLOCK MODE>
*
* A MASTER CLEAR IS DONE. THEN THE SHIFT REG IS PLACED IN INPUT MODE
* UNDER CONTROL OF VIA CLK, BY SETTING ACR BIT 4 TO 0, BIT 3 TO 1, AND BIT 2
* TO 0. THE PROGRAM CHECKS FOR THE SR FLAG (BIT 2) IN THE IFR TO BE INITIALLY
* CLEARED. THEN, THE SR IS LOADED TO INITIALIZE THE SR OPERATION, AND THE
* PROGRAM CHECKS FOR SR FLAG = 1 AFTER ABOUT 8 US. AND READS SR REGISTER TO
* VERIFY THAT SHIFTING OCCURRED.
*****

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```
*****
*      TEST 3 <NPR CONTROL REGISTER - MASTER CLEAR>
*
* THE PROGRAM SETS THE FOLLOWING BITS IN THE NPR CONTROL REGISTER :
* IN/OUT, BYTE OPER, AND DISABL INIT. THE REGISTER IS READ AND VERIFIED.
* THEN, A MASTER CLEAR IS PERFORMED, AND THE REGISTER IS READ AND CHECKED FOR
* 000.
*****
```

```
*****
*      TEST 4 <NPR DATA-OUT>
*
* FIRST SUBTEST :
* THE NPR OUTPUT ADDRESS REGISTER IS LOADED WITH THE ADDRESS OF A 2 BYTE
* BUFFER IN THE PROGRAM. THEN, EACH WORD OF DATA PATTERN F IS LOADED INTO THE
* NPR OUTPUT DATA REGISTER, A FULLWORD NPR OUTPUT REQUEST IS PERFORMED,
* AND THE PROGRAM CHECKS FOR THE CORRECT DATA IN THE PROGRAM BUFFER. ALSO,
* THE PROGRAM CHECKS THAT THE ABORT XFER BIT IN THE NPR CONTROL REGISTER
* NEVER GETS SET.
*   DATA PATTERN F = 125252, 052525, 000000, 177777, 000001, 000002, 000004,
*                   000010, 000020, 000040, 000100, 000200, 000400, 001000,
*                   002000, 004000, 010000, 020000, 040000, 100000, 177776,
*                   177775, 177773, 177767, 177757, 177737, 177677, 177577,
*                   177377, 176777, 175777, 173777, 167777, 157777, 137777,
*                   077777, 000000
*
* SECOND SUBTEST:
* THE ABOVE OPERATIONS ARE REPEATED IN BYTE NPR TRANSFER MODE, USING THE DATA
* BYTES IN DATA PATTERN B. THE LOW BYTE OF THE PROGRAM BUFFER IS USED, AND
* THE UPPER BYTE IS CLEARED AT THE START, AND IS CHECKED TO REMAIN UNCHANGED
* THROUGHOUT THE SUBTEST.
*   DATA PATTERN B = 125, 252, 000, 377, 001, 002, 004, 010, 020, 040, 100,
*                   200, 376, 375, 373, 367, 357, 337, 277, 177, 000
*****
```

```
*****
*      TEST 5 <NPR DATA-IN>
*
* THE NPR INPUT ADDRESS REGISTER IS LOADED WITH THE ADDRESS OF A 2 BYTE
* BUFFER IN THE PROGRAM. THEN, EACH WORD OF DATA PATTERN F IS LOADED INTO THE
* PROGRAM BUFFER, A FULLWORD NPR INPUT REQUEST IS ISSUED AND PERFORMED,
* AND THE PROGRAM CHECKS FOR THE CORRECT DATA IN THE NPR INPUT DATA REG.
* ALSO, THE PROGRAM CHECKS THAT THE ABORT XFER BIT IN THE NPR CONTROL
* REGISTER NEVER GETS SET.
*   DATA PATTERN F = 125252, 052525, 000000, 177777, 000001, 000002, 000004,
*                   000010, 000020, 000040, 000100, 000200, 000400, 001000,
*                   002000, 004000, 010000, 020000, 040000, 100000, 177776,
*                   177775, 177773, 177767, 177757, 177737, 177677, 177577,
*                   177377, 176777, 175777, 173777, 167777, 157777, 137777,
*                   077777, 000000
*****
```

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PROGRAM DOCUMENT

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```
*****
* TEST 6 <NPR XFER ABORT>
*
* FIRST SUBTEST :
* THE PROGRAM PERFORMS AN OUTPUT NPR REQUEST TO A NON-EXISTENT MEMORY
* LOCATION, AND CHECKS FOR THE ASSERTION OF ABORT XFER BIT IN THE NPR CONTROL
* REGISTER. THEN, AN OUTPUT NPR IS DONE AND CHECKED, TO A LOCATION IN THE
* PROGRAM, USING 125252 FOR DATA, AND THE PROGRAM CHECKS FOR ABORT XFER TO
* BE CLEARED BY SETTING THE DONE BIT.
* SECOND SUBTEST :
* THE ABOVE SUBTEST IS REPEATED USING INPUT NPR'S.
*****
```

```
*****
* TEST 7 <NPR EXTENDED ADDRESS BIT TEST>
*
* THIS TEST WILL ONLY BE RUN IF THERE IS AT LEAST 32K WORDS OF MEMORY ON THE
* SYSTEM. IF THERE IS, THE PROGRAM CHOOSES A LOCATION TO USE IN THE ADDRESS
* RANGE 200000-377776 (OCTAL). THEN, THE FOLLOWING 2 SUBTESTS ARE PERFORMED :
*
* FIRST SUBTEST :
* AN INPUT NPR IS PERFORMED AND CHECKED USING THE MEMORY LOCATION, WITH
* 125252 FOR DATA. THE PROGRAM CHECKS THAT THE ABORT XFER BIT REMAINS
* CLEARED.
* SECOND SUBTEST :
* AN OUTPUT NPR IS PERFORMED AND CHECKED USING THE MEMORY LOCATION, WITH
* 125252 FOR DATA. THE PROGRAM CHECKS THAT THE ABORT XFER BIT REMAINS
* CLEARED.
*****
```

```
*****
* TEST 8 <SPECIAL MFG EXTENDED BIT TEST>
*
* THIS TEST WAS DESIGNED SPECIFICALLY TO ALLOW MANUFACTURING TO CHECK THE
* NPRAIX/NPRAOX BITS WITHOUT A FULL 4 M. OF MEMORY.
*
* IT WILL CHECK THE 12 DMV EXTENDED ADDRESS BITS (6:NPRAIX/6:NPRAOX) ON
* A Q22 SYSTEM IF MEMORY IS PRESENT AT THE FOLLOWING PHYSICAL ADDRESSES:
*
*          17600000      17400000      17200000
*          16600000      15600000      13600000
*          7600000
*
* FIRST SUBTEST : TEST 'NPRAIX' EXTENDED ADDRESS BITS
* SECOND SUBTEST : TEST 'NPRAOX' EXTENDED ADDRESS BITS
*****
```

```
*****
* TEST 9 <Q-BUS INTERRUPT 'A' & 'B' SELECTION>
*
* THIS TEST CONTAINS SUBTESTS IN WHICH A SEQUENCE OF STEPS IS
```


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SOFTWARE P-TABLE

```

1350
1351
1352
1353
1354
1355
1356
1357 002200
1358 002200 000000
1359 002202
1360 002202
1361 002202
1362 002202

```

```

.SBTTL SOFTWARE P-TABLE
://////
:/ THE SOFTWARE P-TABLE CONTAINS THE VALUES OF THE PROGRAM
:/ PARAMETERS THAT CAN BE CHANGED BY THE OPERATOR.
://////

```

BGNSW SFPTBL

ENDSW

```

.LSSW:: .WORD L10001-LSSW/2
SFPTBL::
L10001:

```


CVDNBA.P11

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SWITCH PACKS

1492
1493
1494
1495
1496
1497
1498
1499
1500

.SBTTL SWITCH PACKS

::*****
:* SWITCH PACKS
:*****

121000
121400

SWPBOT = 121000
SWPDDCMP = 121400

:'BOOT ADDRESS'' SWITCH PACK [A200]
:'DDCMP ADDRESS'' SWITCH PACK [A300]

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MOVSB -- MOVE A STRING OF BYTES

2461
 2462
 2463
 2464
 2465
 2466
 2467
 2468
 2469
 2470
 2471
 2472
 2473
 2474 005034 010146
 2475 005036 010246
 2476 005040 010346
 2477
 2478 005042 012501
 2479 005044 012502
 2480 005046 012503
 2481
 2482 005050 112122
 2483 005052 077302
 2484
 2485
 2486 005054 012603
 2487 005056 012602
 2488 005060 012601
 2489
 2490 005062 000205

```

.SBTTL MOVSB -- MOVE A STRING OF BYTES
+*****
: MOVSB -- MOVE A STRING OF BYTES
:
:   CALLING SEQUENCE:
:
:   JSR      R5,MOVSB
:   .WORD   <ADDRESS OF SOURCE STRING>
:   .WORD   <ADDRESS OF DESTINATION STRING>
:   .WORD   <# OF BYTES TO MOVE>
:-----*****
MOVSB:  MOV     R1,-(SP)          ;SAVE THE REGISTERS WE'LL BE USING
        MOV     R2,-(SP)
        MOV     R3,-(SP)
:
        MOV     (R5)+,R1        ;INITIALIZE SOURCE POINTER
        MOV     (R5)+,R2        :       DESTINATION POINTER
        MOV     (R5)+,R3        :       COUNTER
:
1$:     MOVSB  (R1)+,(R2)+      ;MOVE IN 1 BYTE OF DATA
        SOB    R3,1$           ;IF MORE DATA, LOOP
:                               ;ELSE, RESTORE REGISTERS AND RETURN
:
        MOV     (SP)+,R3        ;RESTORE REGISTERS
        MOV     (SP)+,R2
        MOV     (SP)+,R1
:
        RTS    R5              ;RETURN TO CALLING ROUTINE

```


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TEXT ADDRESS TABLES FOR ERROR HANDLERS -- 'TXT_T'

.SBTTL TEXT ADDRESS TABLES FOR ERROR HANDLERS -- 'TXT_T'

----- TEXT ADDRESS TABLES USED BY ERROR HANDLERS -----

```
021746 015515 015521 015535 TXTMLT: .WORD TXTML0,TXTML1,TXTML2,TXTML3,TXTML4,TXTML5,TXTML6,TXTML7
021766 015745          .WORD TXTNP
021770 015752 015762 015772 TXTNPT: .WORD TXTNP0,TXTNP1,TXTNP2,TXTNP3,TXTNP4,TXTNP5,TXTNP6,TXTNP7,TXTNP8
.LIST BEX
```


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AUTO DROP UNIT SECTION

3960 023132 050237 002412
3961 023136 000002
3962

AD.HIT: BIS R2, TMPO
RTI

:FLAG THE HIT IF WE GET IT!
:RETURN

CVDMA.P11 18-DEC-80 15:53

CLEANUP CODING SECTION

.SBTTL CLEANUP CODING SECTION

```

://///////////////
:// THE CLEANUP CODING SECTION CONTAINS THE CODING THAT IS PERFORMED
:// AT THE END OF THE TEST SEQUENCE ON A PARTICULAR UNIT.
://///////////////
  
```

- 3963
- 3964
- 3965
- 3966
- 3967
- 3968
- 3969
- 3970 023140
- 3971 023140
- 3972 023140
- 3973 023140 013700 002360
- 3974 023144 104436
- 3975 023146
- 3976 023146 013700 002362
- 3977 023152 104436
- 3978 023154
- 3979 023154
- 3980 023154 104412

BGNCLN

CLRVEC @MPIPEC

CLRVEC @MPOVEC

ENDCLN

```

L$CLEAN::
;RETURN VECTORS TO SUPERVISOR
  
```

```

MOV @MPIPEC,RO
TRAP CSCVEC
  
```

```

MOV @MPOVEC,RO
TRAP CSCVEC
  
```

```

L10024:
TRAP C$CLEAN
  
```

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DROP UNIT SECTION

.SBTTL DROP UNIT SECTION

```

:////////////////////
:// THE DROP-UNIT SECTION CONTAINS THE CODING THAT CAUSES A DEVICE
:// TO NO LONGER BE TESTED.
:////////////////////

```

```

3981
3982
3983
3984
3985
3986
3987
3988 023156
3989 023156
3990
3991 023156
3992 023156 104433
3993 023160
3994 023160
3995 023160 104453

```

```

          BGNDU
:ISSUE UNIBUS RESET TO CLEAN UP
          BRESET
          ENDDU

```

```

LSDU::
          TRAP  CSRESET
L10025: TRAP  CSDU

```

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ADD UNIT SECTION

.SBTTL ADD UNIT SECTION

```

:////////////////////
:// THE ADD-UNIT SECTION CONTAINS THE CODING THAT CAUSES A DEVICE
:// TO BE (A) TESTED FOR THE FIRST TIME, OR (B) RESUMED IN TESTING. IF
:// 'EF.AUNIT' IS SET, THE UNIT WILL BE TESTED AS A NEW UNIT.
:////////////////////

```

```

3996
3997
3998
3999
4000
4001
4002
4003
4004 023162
4005 023162
4006 023162
4007 023162
4008 023162 104452

```

BGNAU

ENDAU

LSAU::

L10026: TRAP CSAU

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TEST 1 -- VIA TIMER 2 ONE SHOT MODE

.SBTTL TEST 1 -- VIA TIMER 2 ONE SHOT MODE

4009
4010
4011
4012
4013
4014
4015
4016
4017
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4064

023164
023164
023164
023164 104402

023166 004737 003514
023172 103003
023174
023174 104460
023176
023176 104410

*
* TEST 1 -- VIA TIMER 2 ONE SHOT MODE
*
* THIS TEST VERIFIES THAT THE TIMER 2 COUNTER IS OPERATIONAL IN
* INTERVAL-TIMER (ONE-SHOT) MODE.
*
* THE FOLLOWING IS PERFORMED :
*
* A MASTER CLEAR IS DONE & THE TIMER IS PLACED IN INTERVAL-TIMER MODE
* BY SETTING ACR5 = 0 AND THE PROGRAM CHECKS FOR 'T2' (BIT 5 IN IFR)
* TO BE INITIALLY CLEARED.
*
* T2L-L (ADR 08) & T2C-H (ADR 09) ARE BOTH LOADED WITH 252 (OCTAL).
* (THIS IS EQUIVALENT TO AAAA (HEX) OR 43,690 (DECIMAL).) LOADING
* T2C-H STARTS THE COUNTER.
*
* T2L-L IS LOADED WITH 001 AND T2C-H IS LOADED WITH 000 IN ORDER TO
* SET 'T2' WITH A QUICK UNDERFLOW. THE 'T2' FLAG BIT IN IFR IS READ
* AND CHECKED TO BE SET.
*
* T2C-H IS CHECKED TO = 0. CHECKING T2C-H SHOULD NOT HAVE CLEARED 'T2'
* -- THIS IS VERIFIED.
*
* T2C-L IS CHECKED TO = 0. CHECKING T2C-L SHOULD HAVE CLEARED 'T2' --
* THIS TOO IS VERIFIED.
*
* T2C-H IS LOADED WITH 0 AGAIN TO INITIATE A NEW COUNT DOWN (WHICH
* SHOULD UNDERFLOW ALMOST IMMEDIATELY) AND THE 'T2' BIT IN IFR IS
* CHECKED TO BE SET AGAIN.
*
* T2L-L IS LOADED WITH 125 (OCTAL) AND 'T2' BIT IS CHECKED TO BE STILL
* SET.
*
* T2C-H IS LOADED WITH 125, AND THE 'T2' BIT IS READ AND CHECKED TO BE
* CLEARED BY THE LOADING OF T2C-H.
*

```

: BGNTST
: BGNSUB
: T1::
: T1.1: TRAP CSBSUB
-----
: JSR PC,MSTCLR ;INIT DMV & ENTER M-LOOP
: BCC 1$ ;IF NO ERROR, PROCEED WITH TESTING
: ERROR ;ELSE, REPORT ERROR
: ESCAPE TST ; & EXIT TEST
: TRAP CSERROR
: TRAP CSESCAPE
```

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TEST 1 -- VIA TIMER 2 ONE SHOT MODE

```

4065 023200 000700
4066 023202 004537 004630
4067 023206 002000
4068 023210 000000
4069 023212 103003
4070 023214 104460
4071 023214 104460
4072 023216 104410
4073 023216 104410
4074 023220 000660
4075 023222 004737 024134
4076 023226 102002
4077 023230 104410
4078 023230 104410
4079 023232 000644
4080 023234 103033
4081 023236
4082
4083 023236 104455
4084 023240 000011
4085 023242 020734
4086 023244 011176
4087
4088
4089
4090 023246 112737 000002 002435
4091 023254 004537 004042
4092 023260 120011
4093 023262 002435
4094 023264 103003
4095 023266
4096 023266 104460
4097 023270
4098 023270 104410
4099 023272 000606
4100 023274 004737 024134
4101 023300 102002
4102 023302
4103 023302 104410
4104 023304 000572
4105 023306 103006
4106 023310
4107
4108 023310 104455
4109 023312 000012
4110 023314 020734
4111 023316 011176
4112 023320
4113 023320 104410
4114 023322 000554
4115
4116
4117
4118 023324 004537 024102
4119 023330 252
4120 023331 252

1$: JSR R5,INITT2 ;INITIALIZE TIMER # 2 .WORD L10027-.
    2000 ; 2000 ==> LATCHES (PREVENTS IMMED. TIMEOUT)
    0 ; MODE 0 & 'T2' INT. ENABLE FLAG CLEARED
    BCC .+10 ;IF NO ERROR, PROCEED
    ERROR ;ELSE, REPORT IT
    ESCAPE TST ; AND EXIT THIS TEST TRAP C$ERROR
                                     .WORD L10027-.
    JSR PC,GETT2 ;IS 'T2' SET?
    BVC .+6 ;IF NO ERROR, PROCEED
    ESCAPE SUB ;ELSE, IT'S ALREADY BEEN REPORTED -- EXIT TRAP C$ESCAPE
                                     .WORD L10030-.
    BCC 6$ ;NO, GOOD.
    GEDF EM51B,ERR51 ;YES, REPORT IT'S NOT BEING CLEARED @ INIT.
    ; 'DEVICE FATAL' ERROR # 9 TRAP C$ERDF
                                     .WORD 9
                                     .WORD EM51B
                                     .WORD ERR51

-----
    MOVB #2,TMP9+1
    JSR R5,WRITE ;INIT TIMER # 2 BY WRITING INTO
    T2CH ;T2C-H (ADDR 09)
    TMP9+1
    BCC .+10 ;IF NO ERROR, PROCEED
    ERROR ;ELSE, REPORT IT
    ESCAPE TST ; AND EXIT THIS TEST TRAP C$ERROR
                                     .WORD L10027-.
    JSR PC,GETT2 ;IS 'T2' SET?
    BVC .+6 ;IF NO ERROR, PROCEED
    ESCAPE SUB ;ELSE, IT'S ALREADY BEEN REPORTED -- EXIT TRAP C$ESCAPE
                                     .WORD L10030-.
    BCC 6$ ;NO, GOOD.
    GEDF EM51B,ERR51 ;YES, REPORT IT'S NOT BEING CLEARED @ INIT.
    ; 'DEVICE FATAL' ERROR # 10 TRAP C$ERDF
                                     .WORD 10
                                     .WORD EM51B
                                     .WORD ERR51
    ESCAPE SUB ;AND EXIT SUBTEST TRAP C$ESCAPE
                                     .WORD L10030-.

-----
6$: JSR R5,LODT2C ;LOAD TIMER # 2
7$: .BYTE 252
8$: .BYTE 252

```

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TEST 1 -- VIA TIMER 2 ONE SHOT MODE

```

4121
4122
4123
4124 023332 004537 003616      JSR    R5,READ      ;READ THE LOW COUNTER
4125 023336 120010                T2CL
4126 023340 002432                TMP8
4127 023342 103003                BCC    .+10         ;IF NO ERROR, PROCEED
4128 023344                ERROR                ;ELSE, REPORT IT
4129 023344 104460                ESCAPE TST         ;          AND EXIT THIS TEST          TRAP    CSERROR
4130 023346                ;                                         ;                                         TRAP    CSERROR
4131 023346 104410                ;                                         ;                                         .WORD  L10027-.
4132 023350 000530                ;MAKE SURE THE COUNTER IS DECREMENTING
4133 023352 123737 002432 023330  CMPB   TMP8,7$     ;IT IS, NOW SEE IF THE HIGH COUNTER IS TOO
4134 023360 001004                BNE    12$         ;IT WASN'T -- REPORT THE ERROR
4135 023362                GEDF   EM50D,ERR51 ;          'DEVICE FATAL' ERROR # 11
4136
4137 023362 104455                ;                                         ;                                         TRAP    CSERDF
4138 023364 000013                ;                                         ;                                         .WORD  11
4139 023366 017453                ;                                         ;                                         .WORD  EM50D
4140 023370 011176                ;                                         ;                                         .WORD  ERR51
4141 023372 012703 000100      12$:  MOV    #100,R3    ;INIT. TIMEOUT VALUE
4142 023376 004537 003616      13$:  JSR    R5,READ    ;READ THE HIGH COUNTER
4143 023402 120011                T2CH
4144 023404 002434                TMP9
4145 023406 103003                BCC    .+10         ;IF NO ERROR, PROCEED
4146 023410                ERROR                ;ELSE, REPORT IT
4147 023410 104460                ESCAPE TST         ;          AND EXIT THIS TEST          TRAP    CSERROR
4148 023412                ;                                         ;                                         TRAP    CSERROR
4149 023412 104410                ;                                         ;                                         .WORD  L10027-.
4150 023414 000464                ;DID IT CHANGE FROM THE LOADED VALUE?
4151 023416 123737 002434 023331  BNE    14$         ;YES, PROCEED WITH TESTING
4152 023424 001007                SOB    R3,13$      ;NO, IF NO TIMEOUT, TRY AGAIN
4153 023426 077315                GEDF   EM51E,ERR51 ;ELSE, REPORT THAT HIGH COUNTER ISN'T RUNNING
4154 023430                ;          'DEVICE FATAL' ERROR # 12
4155
4156 023430 104455                ;                                         ;                                         TRAP    CSERDF
4157 023432 000014                ;                                         ;                                         .WORD  12
4158 023434 021050                ;                                         ;                                         .WORD  EM51E
4159 023436 011176                ;                                         ;                                         .WORD  ERR51
4160 023440                ESCAPE SUB         ;          WE CAN'T PROCEED WITH TESTING EITHER
4161 023440 104410                ;                                         ;                                         TRAP    CSERROR
4162 023442 000434                ;                                         ;                                         .WORD  L10030-.
4163
4164
4165 023444 005003 024134      14$:  CLR    R3         ;INITIALIZE TIMEOUT COUNTER
4166 023446 004737                JSR    PC,GETT2    ;WAIT FOR TIMER TO COUNT DOWN
4167 023452 102002                BVC   .+6         ;IF NO ERROR, PROCEED
4168 023454                ESCAPE SUB         ;ELSE, IT'S ALREADY BEEN REPORTED -- EXIT
4169 023454 104410                ;                                         ;                                         TRAP    CSERROR
4170 023456 000420                ;                                         ;                                         .WORD  L10030-.
4171 023460 103406                BCS   16$         ;DONE.
4172 023462 077307                SOB    R3,15$      ;NOT YET, TIMEOUT?
4173 023464                GEDF   EM51P,ERR51 ;YES, REPORT NO 'T2' INT. FLAG
4174
4175 023464 104455                ;          'DEVICE FATAL' ERROR # 13
4176 023466 000015                ;                                         ;                                         TRAP    CSERDF
                                         ;                                         ;                                         .WORD  13

```

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TEST 1 -- VIA TIMER 2 ONE SHOT MODE

```

4177 023470 021405
4178 023472 011176
4179 023474 000445
4180
4181
4182
4183 023476 004537 003616
4184 023502 120011
4185 023504 002434
4186 023506 103003
4187 023510
4188 023510 104460
4189 023512
4190 023512 104410
4191 023514 000364
4192 023516 004737 024134
4193 023522 102002
4194 023524
4195 023524 104410
4196 023526 000350
4197 023530 103405
4198 023532
4199
4200 023532 104455
4201 023534 000016
4202 023536 021151
4203 023540 011176
4204 023542 000422
4205
4206
4207
4208 023544 004537 004042
4209 023550 120010
4210 023552 002433
4211 023554 103003
4212 023556
4213 023556 104460
4214 023560
4215 023560 104410
4216 023562 000316
4217 023564 004737 024134
4218 023570 102002
4219 023572
4220 023572 104410
4221 023574 000302
4222 023576 103404
4223 023600
4224
4225 023600 104455
4226 023602 000017
4227 023604 021275
4228 023606 011176
4229
4230
4231
4232 023610 004537 024102

```

```

                                .WORD  EM51P
                                .WORD  ERR51
BR      17$                      ;  & BYPASS 'T2'-RESET-ON-T2CH-READ CHECK
-----
16$:   JSR      R5,READ           ;READ T2C-H (ADDR 09)
        T2CH
        TMP9
        BCC     .+10             ;IF NO ERROR, PROCEED
        ERROR                    ;ELSE, REPORT IT
        ESCAPE TST              ;      AND EXIT THIS TEST          TRAP   C$ERROR
                                     .WORD  C$ESCAPE
                                     L10027-.
        JSR      PC,GETT2        ;IS 'T2' STILL SET?
        BVC     .+6             ;IF NO ERROR, PROCEED
        ESCAPE SUB              ;ELSE, IT'S ALREADY BEEN REPORTED -- EXIT
                                     TRAP   C$ESCAPE
                                     .WORD  L10030-.
        BCS     40$             ;YES, ALL'S OK
        GEDF    EM51G,ERR51     ;NO!  BAD VIA CHIP!
        ;      'DEVICE FATAL' ERROR # 14
                                     TRAP   C$ERDF
                                     .WORD  14
                                     .WORD  EM51G
                                     .WORD  ERR51
BR      17$                      ;  & BYPASS 'T2'-RESET-ON-T2LL-WRITE CHECK
-----
40$:   JSR      R5,WRITE         ;RE-LOAD T2L-L (ADDR 08)
        T2LL
        TMP8+1
        BCC     .+10             ;IF NO ERROR, PROCEED
        ERROR                    ;ELSE, REPORT IT
        ESCAPE TST              ;      AND EXIT THIS TEST          TRAP   C$ERROR
                                     .WORD  C$ESCAPE
                                     L10027-.
        JSR      PC,GETT2        ;IS 'T2' STILL SET?
        BVC     .+6             ;IF NO ERROR, PROCEED
        ESCAPE SUB              ;ELSE, IT'S ALREADY BEEN REPORTED -- EXIT
                                     TRAP   C$ESCAPE
                                     .WORD  L10030-.
        BCS     17$             ;YES, ALL'S STILL OK
        GEDF    EM51M,ERR51     ;NO!  SOMETHING WENT WRONG! REPORT IT
        ;      'DEVICE FATAL' ERROR # 15
                                     TRAP   C$ERDF
                                     .WORD  15
                                     .WORD  EM51M
                                     .WORD  ERR51
-----
17$:   JSR      R5,LODT2C        ;RE-LOAD TIMER # 2 WITH A VALUE WHICH CAUSE AN

```

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TEST 1 -- VIA TIMER 2 ONE SHOT MODE

```

4233 023614 001
4234 023615 000
4235
4236
4237 023616 004737 024134      JSR    PC,GETT2      ;WAS 'T2' SET BY THE ABOVE OPERATION?
4238 023622 102002      BVC    .+6           ;IF NO ERROR, PROCEED
4239 023624      ESCAPE SUB          ;ELSE, IT'S ALREADY BEEN REPORTED -- EXIT
4240 023624 104410      TRAP   C$ESCAPE
4241 023626 000250      .WORD L10030-.
4242 023630 103406      BCS    20$
4243 023632      GEDF   EM51F,ERR51 ;YES, OK -- CONTINUE ERROR CHECKING
4244      ;NO, BAD NEWS! REPORT THE FAILURE
4245      ; 'DEVICE FATAL' ERROR # 16
4245 023632 104455      TRAP   C$ERDF
4246 023634 000020      .WORD 16
4247 023636 021104      .WORD EM51F
4248 023640 011176      .WORD ERR51
4249 023642      ESCAPE SUB          ; AND GET OUT OF SUBTEST
4250 023642 104410      TRAP   C$ESCAPE
4251 023644 000232      .WORD L10030-.
4252 023646 004537 003616      20$: JSR    R5,READ    ;READ T2C-H (ADDR 09) TO SEE IF THIS CLEARS 'T2'
4253 023652 120011      ;(THIS VALUE ISN'T CHECKED BECAUSE IT CAN BE
4254 023654 002434      ; ALMOST ANYTHING)
4255 023656 103003      BCC    .+10         ;IF NO ERROR, PROCEED
4256 023660      ERROR             ;ELSE, REPORT IT
4257 023660 104460      ESCAPE TST         ; AND EXIT THIS TEST
4258 023662      TRAP   C$ERROR
4259 023662 104410      .WORD C$ESCAPE
4260 023664 000214      .WORD L10027-.
4261 023666 004737 024134      JSR    PC,GETT2    ;PUT THE CURRENT 'T2' VALUE INTO THE CARRY BIT
4262 023672 102002      BVC    .+6           ;IF NO ERROR, PROCEED
4263 023674      ESCAPE SUB          ;ELSE, IT'S ALREADY BEEN REPORTED -- EXIT
4264 023674 104410      TRAP   C$ESCAPE
4265 023676 000200      .WORD L10030-.
4266 023700 103405      BCS    21$
4267 023702      GEDF   EM50G,ERR51 ;IF SET, READING T2CH DIDN'T CLEAR IT -- OK!
4268      ;IF CLEARED! BAD VIA CHIP!
4269      ; 'DEVICE FATAL' ERROR # 17
4269 023702 104455      TRAP   C$ERDF
4270 023704 000021      .WORD 17
4271 023706 017610      .WORD EM50G
4272 023710 011176      .WORD ERR51
4273 023712 000400      BR     28$         ;BYPASS THE REST OF THIS SECTION OF TESTING
4274
4275 023714      21$:
4276
4277
4278 023714 004537 003616      28$: JSR    R5,READ    ;READ T2C-L (ADDR 08)
4279 023720 120010      ;(THIS VALUE ISN'T CHECKED BECAUSE IT CAN BE
4280 023722 002432      ; ALMOST ANYTHING)
4281 023724 103003      BCC    .+10         ;IF NO ERROR, PROCEED
4282 023726      ERROR             ;ELSE, REPORT IT
4283 023726 104460      ESCAPE TST         ; AND EXIT THIS TEST
4284 023730      TRAP   C$ERROR
4285 023730 104410      .WORD C$ESCAPE
4286 023732 000146      .WORD L10027-.
4287 023734 004737 024134      JSR    PC,GETT2    ;IS 'T2' CLEARED NOW
4288 023740 102002      BVC    .+6           ;IF NO ERROR, PROCEED

```

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TEST 1 -- VIA TIMER 2 ONE SHOT MODE

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4289 023742          ESCAPE SUB          ;ELSE, IT'S ALREADY BEEN REPORTED -- EXIT
4290 023742 104410          TRAP C$ESCAPE
4291 023744 000132          .WORD L10030-.
4292 023746 103004          BCC 29$
4293 023750          GEDF EM51C,ERR51 ;YES, ALL'S OK
;NO! BAD VIA CHIP!
; 'DEVICE FATAL' ERROR # 18
4295 023750 104455          TRAP C$ERDF
4296 023752 000022          .WORD 18
4297 023754 021002          .WORD EM51C
4298 023756 011176          .WORD ERR51
4299
4300
4301
-----
4302 023760 004537 004042 29$: JSR R5,WRITE ;RE-WRITE INTO T2C-H (ADDR 09) TO SET T2 AGAIN
4303 023764 120011          T2CH
4304 023766 002435          TMP9+1
4305 023770 103003          BCC .+10 ;IF NO ERROR, PROCEED
4306 023772          ERROR ;ELSE, REPORT IT
4307 023772 104460          ESCAPE TST ; AND EXIT THIS TEST TRAP C$ERROR
4308 023774          ; TRAP C$ESCAPE
4309 023774 104410          .WORD L10027-.
4310 023776 000102          JSR PC,GETT2 ;IS 'T2' SET AGAIN
4311 024000 004737 024134 BVC .+6 ;IF NO ERROR, PROCEED
4312 024004 102002          ESCAPE SUB ;ELSE, IT'S ALREADY BEEN REPORTED -- EXIT
4313 024006          TRAP C$ESCAPE
4314 024006 104410          .WORD L10030-.
4315 024010 000066          BCS 32$
4316 024012 103406          GEDF EM51L,ERR51 ;YES, ALL'S WELL (AGAIN?)
;NO! SOMETHING WENT WRONG! REPORT IT
; 'DEVICE FATAL' ERROR # 19
4319 024014 104455          TRAP C$ERDF
4320 024016 000023          .WORD 19
4321 024020 021213          .WORD EM51L
4322 024022 011176          .WORD ERR51
4323 024024          ESCAPE SUB ; AND EXIT FROM THIS SUBTEST TRAP C$ESCAPE
4324 024024 104410          .WORD L10030-.
4325 024026 000050
4326
4327
-----
4328
4329 024030 004537 024102 32$: JSR R5,LODT2C ;AGAIN RE-LOAD TIMER # 2. THIS TIME WITH
4330 024034 125 125          .BYTE 125,125 ; LARGER BUT DIFFERENT VALUES
4331
4332
-----
4333
4334 024036 004737 024134 JSR PC,GETT2 ;'T2' SHOULD NOW BE CLEARED
4335 024042 102002          BVC .+6 ;IF NO ERROR, PROCEED
4336 024044          ESCAPE SUB ;ELSE, IT'S ALREADY BEEN REPORTED -- EXIT
4337 024044 104410          TRAP C$ESCAPE
4338 024046 000030          .WORD L10030-.
4339 024050 103004          BCC 34$
4340 024052          GEDF EM51N,ERR51 ;IT WAS, ALL'S WELL THAT END'S WELL (I THINK!?)
;IT WASN'T! SOMETHING WENT WRONG! REPORT IT
; 'DEVICE FATAL' ERROR # 20
4342 024052 104455          TRAP C$ERDF
4343 024054 000024          .WORD 20
4344 024056 021337          .WORD EM51N

```


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TEST 1 -- VIA TIMER 2 ONE SHOT MODE

4401 024162 106100
4402 024164 106100
4403 024166 106100
4404 024170 012600
4405 024172 000207
4406

ROLB	R0	:'IRQ' GOES INTO CARRY BIT
ROLB	R0	:'T1' GOES INTO CARRY BIT
ROLB	R0	:'T2' GOES INTO CARRY BIT
MOV	(SP)+,R0	:RESTORE R0
RTS	PC	

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TEST 2 -- VIA'S SR INPUT (MODE 2) - SYSTEM CLOCK MODE

.SBTTL TEST 2 -- VIA'S SR INPUT (MODE 2) - SYSTEM CLOCK MODE

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024174
024174 004737 003514
024200 103003
024202 104460
024204 104410
024206 000612
024210 005037 002437
024214 005037 002252
024220 004537 003616
024224 120013
024226 002440
024230 103003
024232 104460
024234 104410
024236 000562
024240 113737 002440 002441
024246 142737 000034 002441
024254 004537 004042
024260 120013
024262 002441
024264 103003
024266 104460
024270 104410
024272 000526
024274 004537 003616
024300 120016
024302 002446
024304 103003
024306 104460
024310 104410
024312 000506

```

:*****
:*
:* TEST 2 -- VIA'S SR INPUT (MODE 2) - SYSTEM CLOCK MODE
:* A MASTER CLEAR IS DONE. THEN THE SHIFT REG IS PLACED IN INPUT MODE
:* UNDER CONTROL OF VIA CLK, BY SETTING ACR BIT 4 TO 0, BIT 3 TO 1, AND BIT 2
:* TO 0. THE PROGRAM CHECKS FOR THE SR FLAG (BIT 2) IN THE IFR TO BE INITIALLY
:* CLEARED. THEN, THE SR IS LOADED TO INITIALIZE THE SR OPERATION, AND THE
:* PROGRAM CHECKS FOR SR FLAG = 1 AFTER ABOUT 8 US. AND READS SR REGISTER TO
:* VERIFY THAT SHIFTING OCCURRED.
:*****

```

```

: BGNTST
:
: T2::
: JSR PC,MSTCLR ;INIT DMV & ENTER M-LOOP
: BCC 1$ ;IF NO ERROR, PROCEED WITH TESTING
: ERROR ;ELSE, REPORT ERROR
: ESCAPE TST ; & EXIT TEST TRAP CSERROR
:
: CLR TMPA+1 ;CLEAR THE 'WRITE' DATA FOR ERROR MESSAGES
: CLR TDATA ;THIS IS A FLAG TO INDICATE THAT 'SR' HASN'T
: ;BEEN LOADED YET.
: JSR R5,READ ;GET CURRENT 'ACR' CONTENTS (SHOULD BE 000)
: ACR
: TMPB
: BCC .+10 ;IF NO ERROR, PROCEED
: ERROR ;ELSE, REPORT IT
: ESCAPE TST ; AND EXIT THIS TEST TRAP CSERROR
:
: MOVB TMPB,TMPB+1 ;MOVE IT FROM I/P BUFFER TO O/P BUFFER
: BICB #<BIT2+BIT3+BIT4>,TMPB+1 ;MAKE SURE CURRENT MODE IS 0
: JSR R5,WRITE ;FORCE IT TO THAT MODE (MODE 0)
: ACR
: TMPB+1
: BCC .+10 ;IF NO ERROR, PROCEED
: ERROR ;ELSE, REPORT IT
: ESCAPE TST ; AND EXIT THIS TEST TRAP CSERROR
:
: JSR R5,READ ;READ IER INCASE IT'S NEEDED FOR ERROR MESSAGES
: IENR
: TMPE
: BCC .+10 ;IF NO ERROR, PROCEED
: ERROR ;ELSE, REPORT IT
: ESCAPE TST ; AND EXIT THIS TEST TRAP CSERROR
:
: TRAP CSERROR
: .WORD L10031-.

```

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TEST 2 -- VIA'S SR INPUT (MODE 2) - SYSTEM CLOCK MODE

```

4463 024314 004737 025022      JSR  PC,GETSR      ;SAMPLE SR INTERRUPT FLAG -- IT SHOULD BE 0
4464 024320 102002          BVC  .+6           ;IF NO ERROR, PROCEED
4465 024322          ESCAPE TST         ;ELSE, IT'S ALREADY BEEN REPORTED -- EXIT
4466 024322 104410          TRAP  C$ESCAPE
4467 024324 000474          .WORD  L10031-.
4468 024326 103014          BCC  4$           ;IT IS, GOOD.
4469 024330 004537 003616      JSR  R5,READ      ;READ SR FOR ERROR MESSAGE
4470 024334 120012          SR
4471 024336 002436          TMPA
4472 024340 103003          BCC  .+10        ;IF NO ERROR, PROCEED
4473 024342          ERROR          ;ELSE, REPORT IT
4474 024342 104460          ESCAPE TST       ;
4475 024344          AND EXIT THIS TEST          TRAP  C$ERROR
4476 024344 104410          TRAP  C$ESCAPE
4477 024346 000452          .WORD  L10031-.
4478 024350          GEDF  EM52A,ERR52 ;IT ISN'T! REPORT SR NOT INITIALLY CLEARED
4479          ; 'DEVICE FATAL' ERROR # 21
4480 024350 104455          TRAP  C$ERDF
4481 024352 000025          .WORD  21
4482 024354 021457          .WORD  EM52A
4483 024356 011270          .WORD  ERR52
4484 024360 152737 000010 002441 4$:  BISB  #BIT3,TMPB+1 ;SET SHIFT REG. TO MODE 2
4485 024366 004537 004042      JSR  R5,WRITE
4486 024372 120013          ACR
4487 024374 002441          TMPB+1
4488 024376 103003          BCC  .+10        ;IF NO ERROR, PROCEED
4489 024400          ERROR          ;ELSE, REPORT IT
4490 024400 104460          ESCAPE TST       ;
4491 024402          AND EXIT THIS TEST          TRAP  C$ERROR
4492 024402 104410          TRAP  C$ESCAPE
4493 024404 000414          .WORD  L10031-.
4494 024406 112737 000204 002447      MOVB  #BIT7+BIT2,TMPE+1 ;ENABLE SR INTERRUPTS WITHIN DMV-11
4495 024414 004537 004042      JSR  R5,WRITE      ; (WE WILL NOT BE ALLOWING THEM TO GIVE US
4496 024420 120016          IENR          ; A Q-BUS INTERRUPT)
4497 024422 002447          TMPE+1
4498 024424 103003          BCC  .+10        ;IF NO ERROR, PROCEED
4499 024426          ERROR          ;ELSE, REPORT IT
4500 024426 104460          ESCAPE TST       ;
4501 024430          AND EXIT THIS TEST          TRAP  C$ERROR
4502 024430 104410          TRAP  C$ESCAPE
4503 024432 000366          .WORD  L10031-.
4504 024434 004537 003616      JSR  R5,READ      ;READ IER INCASE IT'S NEEDED FOR ERROR MESSAGES
4505 024440 120016          IENR
4506 024442 002446          TMPE
4507 024444 103003          BCC  .+10        ;IF NO ERROR, PROCEED
4508 024446          ERROR          ;ELSE, REPORT IT
4509 024446 104460          ESCAPE TST       ;
4510 024450          AND EXIT THIS TEST          TRAP  C$ERROR
4511 024450 104410          TRAP  C$ESCAPE
4512 024452 000346          .WORD  L10031-.
4513 024454 105037 002437      CLRB  TMPA+1      ;LOAD SR WITH PROPER VALUE....
4514 024460 005737 002366      TST  BRDTP        ; NOTE: THE INPUT LEAD (CB2) WILL EITHER BE
4515 024464 001403          BEQ  5$          ; TIED HI(M8064) OR LO(M8053).
4516 024466 112737 000377 002437      MOVB  #377,TMPA+1 ; IF M8064, THEN LOAD SR WITH 000.
4517 024474 004537 004042      JSR  R5,WRITE     ; IF M8053, THEN LOAD SR WITH 377.
4518 024500 120012          SR              ; THIS ALSO STARTS THE SHIFTING OPERATION.

```

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TEST 2 -- VIA'S SR INPUT (MODE 2) - SYSTEM CLOCK MODE

```

4519 024502 002437      TMPA+1      ;
4520
4521 024504 103003      BCC        .+10      ;IF NO ERROR, PROCEED
4522 024506      ERROR      ;ELSE, REPORT IT
4523 024506 104460      ESCAPE    TST        ;      AND EXIT THIS TEST      TRAP    CSERROR
4524 024510      ;                                          ;
4525 024510 104410      ;                                          ;
4526 024512 000306      ;                                          ;      TRAP    C$ESCAPE
4527      ;                                          ;      .WORD   L10031-.
4528 024514 005337 002252      DEC        TDATA      ;INDICATE THAT 'SR' HAS BEEN LOADED NOW
4529 024520 012703 000100      MOV        #100,R3     ;GIVE THE INTERRUPT A CHANCE TO HAPPEN
4530 024524 077301      SOB        R3
4531 024526 132777 000004 155572      BITB      #BIT2,@SEL3 ;DID AN SR INTERRUPT OCCUR WITHIN THE 6502?
4532 024534 001026      BNE        6$          ;YES, GOOD.
4533 024536 004537 003616      JSR        R5,READ     ;NO, SETUP TO REPORT THE ERROR:
4534 024542 120015      IFR        ; GET INTERRUPT FLAG REGISTER
4535 024544 002444      TMPD
4536 024546 103003      BCC        .+10      ;IF NO ERROR, PROCEED
4537 024550      ERROR      ;ELSE, REPORT IT
4538 024550 104460      ESCAPE    TST        ;      AND EXIT THIS TEST      TRAP    CSERROR
4539 024552      ;                                          ;
4540 024552 104410      ;                                          ;
4541 024554 000244      ;                                          ;      TRAP    C$ESCAPE
4542 024556 004537 003616      JSR        R5,READ     ; GET FINAL SR CONTENTS -- SHOULD BE 0
4543 024562 120012      SR
4544 024564 002436      TMPA
4545 024566 103003      BCC        .+10      ;IF NO ERROR, PROCEED
4546 024570      ERROR      ;ELSE, REPORT IT
4547 024570 104460      ESCAPE    TST        ;      AND EXIT THIS TEST      TRAP    CSERROR
4548 024572      ;                                          ;
4549 024572 104410      ;                                          ;
4550 024574 000224      ;                                          ;      TRAP    C$ESCAPE
4551 024576      ;                                          ;      .WORD   L10031-.
4552      GEDF      EM52B,ERR52 ;REPORT MISSING SR INTERRUPT WITHIN DMV-11
4553      ;      ;      ;      ;      ;
4554      ;      ;      ;      ;      ;      ;      TRAP    C$ERDF
4555      ;      ;      ;      ;      ;      ;      .WORD   22
4556      ;      ;      ;      ;      ;      ;      .WORD   EM52B
4557      ;      ;      ;      ;      ;      ;      .WORD   ERR52
4558 024606 104410      ESCAPE    TST        ;FURTHER TESTING INVALID
4559 024610 000210      ;                                          ;      TRAP    C$ESCAPE
4560 024612 004537 003616      JSR        R5,READ     ;GET FINAL SR CONTENTS:
4561 024616 120012      SR          ; IF M8064, THEN SR SHOULD=377
4562 024620 002436      TMPA       ; IF M8053, THEN SR SHOULD=000
4563 024622 103003      BCC        .+10      ;IF NO ERROR, PROCEED
4564 024624      ERROR      ;ELSE, REPORT IT
4565 024624 104460      ESCAPE    TST        ;      AND EXIT THIS TEST      TRAP    CSERROR
4566 024626      ;                                          ;
4567 024626 104410      ;                                          ;
4568 024630 000170      ;                                          ;      TRAP    C$ESCAPE
4569      ;                                          ;      .WORD   L10031-.
4570 024632 005737 002366      TST        BRDYP      ;CHECK DMV-11 BOARD TYPE
4571 024636 001005      BNE        9$          ;
4572 024640 122737 000377 002436      CMPB     #377,TMPA     ;M8064::SEE IF CORRECT RESULT
4573 024646 001422      BEQ        8$          ;
4574 024650 000403      BR        7$          ;      YES:GOOD
;                          ;      NO: GO REPORT ERROR

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TEST 2 -- VIA'S SR INPUT (MODE 2) - SYSTEM CLOCK MODE

4575 024652 105737 002436
4576 024656 001416
4577 024660 004537 003616
4578 024664 120015
4579 024666 002444
4580 024670 103003
4581 024672
4582 024672 104460
4583 024674
4584 024674 104410
4585 024676 000122
4586 024700
4587
4588 024700 104455
4589 024702 000027
4590 024704 021567
4591 024706 011270
4592 024710
4593 024710 104410
4594 024712 000106
4595 024714 105077 155406
4596 024720 004537 003616
4597 024724 120012
4598 024726 002436
4599 024730 103003
4600 024732
4601 024732 104460
4602 024734
4603 024734 104410
4604 024736 000062
4605 024740 004737 005132
4606 024744 004737 005132
4607 024750 004737 005132
4608 024754 132777 000004 155344
4609 024762 001016
4610 024764 004537 003616
4611 024770 120015
4612 024772 002444
4613 024774 103003
4614 024776
4615 024776 104460
4616 025000
4617 025000 104410
4618 025002 000016
4619 025004
4620
4621 025004 104455
4622 025006 000030
4623 025010 021653
4624 025012 011270
4625 025014
4626 025014 104410
4627 025016 000002
4628 025020
4629 025020
4630 025020

```

9$:  TSTB   TMPA       ;M8053::SEE IF CORRECT RESULT
     BEQ   8$         ;         YES:GOOD.
7$:  JSR   R5,READ   ; NO: SETUP TO REPORT THE ERROR:
     IFR                               ; GET INTERRUPT FLAG REGISTER
     TMPD
     BCC   .+10       ;IF NO ERROR, PROCEED
     ERROR                               ;ELSE, REPORT IT
                                   TRAP   C$ERROR
     ESCAPE TST       ;         AND EXIT THIS TEST
                                   TRAP   C$ESCAPE
                                   .WORD  L10031-.
     GEDF   EM52C,ERR52 ;REPORT INCOMPLETE OR BAD SHIFTING OPERATION
                                   ; 'DEVICE FATAL' ERROR # 23
                                   TRAP   C$ERDF
                                   .WORD  23
                                   .WORD  EM52C
                                   .WORD  ERR52
     ESCAPE TST       ;FURTHER TESTING INVALID
                                   TRAP   C$ESCAPE
                                   .WORD  L10031-.
8$:  CLRB   @BSEL3   ;CLEAR THE INTERRUPT FLAGS
     JSR   R5,READ   ;HIT THE SHIFT REG. THIS TIME WITH A READ
     SR                               ; (WE DON'T REALLY CARE THIS TIME WHAT THE DATA
     TMPA                               ; RETURNED IS. BUT, WE HAVE TO PUT IT SOMEWHERE
     BCC   .+10       ;IF NO ERROR, PROCEED
     ERROR                               ;ELSE, REPORT IT
                                   TRAP   C$ERROR
     ESCAPE TST       ;         AND EXIT THIS TEST
                                   TRAP   C$ESCAPE
                                   .WORD  L10031-.
     JSR   PC,STALL   ;DELAY FOR A LITTLE WHILE TO LET THE INTERRUPT
     JSR   PC,STALL   ; GET THROUGH
     JSR   PC,STALL
     BITB  #BIT2,@BSEL3 ;DID WE GET AN INTERRUPT ON THE READ OPERATION?
     BNE   10$        ;YES, GOOD.
     JSR   R5,READ   ;NO, SETUP TO REPORT THE ERROR:
     IFR                               ; GET INTERRUPT FLAG REGISTER
     TMPD
     BCC   .+10       ;IF NO ERROR, PROCEED
     ERROR                               ;ELSE, REPORT IT
                                   TRAP   C$ERROR
     ESCAPE TST       ;         AND EXIT THIS TEST
                                   TRAP   C$ESCAPE
                                   .WORD  L10031-.
     GEDF   EM52D,ERR52 ;REPORT THE FAILURE.
                                   ; 'DEVICE FATAL' ERROR # 24
                                   TRAP   C$ERDF
                                   .WORD  24
                                   .WORD  EM52D
                                   .WORD  ERR52
     ESCAPE TST
                                   TRAP   C$ESCAPE
                                   .WORD  L10031-.
10$:  ENDTST

```

L10031:

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TEST 2 -- VIA'S SR INPUT (MODE 2) - SYSTEM CLOCK MODE

4631 025020 104401
 4632 025022 004537 003616
 4633 025026 120015
 4634 025030 002444
 4635 025032 103003
 4636 025034
 4637 025034 104460
 4638 025036 000262
 4639 025040 000207
 4640
 4641 025042 010046
 4642 025044 113700 002444
 4643 025050 106000
 4644 025052 106000
 4645 025054 106000
 4646 025056 012600
 4647 025060 000207
 4648

```

GETSR: JSR    R5,READ                      TRAP    C$ETST
        IFR
        TMPD
        BCC   1$                          ;GET CURRENT INTERRUPT FLAG REGISTER SETTINGS
        ERROR
        SEV
        RTS   PC                          ;IF NO ERROR, PROCEED
                                          ;ELSE, REPORT IT
                                          TRAP    C$ERROR
1$:     MOV    R0,-(SP)                    ;FLAG AN ERROR TO MAINLINE ROUTINE
        MOVB  TMPD,R0                      ; AND TAKE AN ABNORMAL RETURN
        RORB  R0
        RORB  R0
        RORB  R0
        MOV   (SP)+,R0
        RTS   PC                          ;SAVE REGISTER FOR CALLER
                                          ;PUT THEM WHERE WE CAN EASILY MASAGE THEM
                                          ;CA2 ==> CARRY BIT
                                          ;CA1 ==> CARRY BIT
                                          ;SR ==> CARRY BIT
                                          ;RESTORE REGISTER
                                          ;RETURN WITH SR INTERRUPT FLAG IN CARRY BIT
  
```

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TEST 3 -- NPR CONTROL REGISTER - MASTER CLEAR

.SBTTL TEST 3 -- NPR CONTROL REGISTER - MASTER CLEAR

```

:*****
:*
:*      TEST 3 -- NPR CONTROL REGISTER - MASTER CLEAR
:*
:* THE PROGRAM SETS THE FOLLOWING BITS IN THE NPR CONTROL REGISTER :
:* IN/OUT, BYTE OPER, AND DISABL INIT. THE REGISTER IS READ AND VERIFIED.
:* THEN, A MASTER CLEAR IS PERFORMED, AND THE REGISTER IS READ AND CHECKED FOR
:* 000.
:*
:*****

```

```

4649
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4663 025062
4664 025062 004737 003514
4665 025066 103003
4666 025070
4667 025070 104460
4668 025072
4669 025072 104410
4670 025074 000352
4671
4672 025076 004737 005134
4673 025102 103002
4674 025104
4675 025104 104460
4676 025106 000557
4677
4678 025110 012702 002740
4679 025114 010237 002256
4680 025120 010237 025234
4681
4682 025124 004537 005004
4683 025130 002632
4684 025132 002654
4685 025134 000011
4686 025136 013701 025132
4687 025142 010137 002254
4688 025146 012703 000001
4689
4690
4691
4692
4693
4694
4695
4696
4697 025152 121112
4698 025154 001003
4699 025156 022122
4700 025160 077304
4701 025162 000412
4702
4703 025164 163701 025132
4704 025170 010137 002300

```

```

:      BGNTST
:
:                               T3:
:      JSR   PC,MSTCLR          ;INIT DMV & START UP MAINT. LOOP
:      BCC   1$                 ;IF NO ERROR, PROCEED WITH TESTING
:      ERROR ;ELSE REPORT ERROR
:
:                               TRAP   C$ERROR
:      ESCAPE TST                ; & EXIT TEST
:
:                               TRAP   C$ESCAPE
:                               .WORD  L10032-.
:
:      1$:  JSR   PC,NPREAD      ;GET CONTENTS OF ALL NPR REGISTERS INTO BT2
:      BCC   30$                ;IF AN ERROR OCCURED,
:      ERROR ;REPORT IT &
:
:                               TRAP   C$ERROR
:      BR    24$                ; EXIT
:
:      30$: MOV   #BT2,R2       ;POINT TO NPR REGISTER CONTENTS
:      MOV   R2,BDATA          ;USE IT ALSO FOR ERROR HANDLING
:      MOV   R2,13$           ;SETUP ALSO FOR READ BACK
:
:      JSR   R5,MOVSW          ;GET THE 'EXPECTED' RESULTS TOO
:
:      12$: BT1
:      11$: 9.
:      MOV   12$,R1           ;POINT TO TABLE OF EXPECTED REGISTER CONTENTS
:      MOV   R1,GDATA         ;USE IT ALSO FOR ERROR HANDLING
:      MOV   #1,R3           ;COUNT OF # OF NPR REGISTERS BEING PROCESSED
:                               ;FOR NOW, ONLY THE CONTROL REGISTER IS CHECKED!!

```

```

:=====
: PLEASE NOTE THAT 'GDATA' & 'BDATA' NOW CONTAIN POINTERS -- NOT DATA!
: THIS IS A DEVIANT AND THEREFORE SHOULD BE BORNE IN MIND WHEN TRYING TO
: FOLLOW THIS DEVIOUS LOGIC.
:=====

```

```

:      2$:  CMPB   (R1),(R2)    ;CHECK ONE BYTE
:      BNE   3$                ;GO REPORT FAILURE IF ANY ERROR IS FOUND
:      CMP   (R1)+,(R2)+      ;BUMP POINTERS -- TABLES ARE ACTUALLY WORD TABLES
:      SOB   R3,2$           ;LOOP IF NOT DONE YET
:      BR    4$                ;ELSE, PROCEED WITH TESTING
:
:      3$:  SUB   12$,R1       ;CALCULATE THE REGISTER # CAUSING THE FAILURE
:      MOV   R1,REGNUM        ;IDENTIFY FAULTY REGISTER

```

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TEST 3 -- NPR CONTROL REGISTER - MASTER CLEAR

```

4705 025174          GEDF   EM26,ERR8      ;NPR ERROR -- BAD INITIALIZATION
4706                ;                'DEVICE FATAL' ERROR # 25
4707 025174 104455                TRAP   CSERDF
4708 025176 000031                .WORD 25
4709 025200 016152                .WORD EM26
4710 025202 006532                .WORD ERR8
4711 025204          ESCAPE TST          ;IF IT CAN'T BE INIT'ED, WE CAN'T TEST IT!
4712 025204 104410                TRAP   CSERDF
4713 025206 000240                .WORD L10032-.
4714 025210 004537 004054      4$:   JSR   R5,WRITEI      ;WRITE THE TEST PATTERN INTO THE CONTROL REG.
4715 025214 123004                NPRCTL
4716 025216 000355                5$:   BCC   31$          ;THIS SET SEVERAL BITS -- BUT NOT ALL!
4717 025220 103002                ERROR
4718 025222          BR   24$          ; IF AN ERROR OCCURED,
4719 025222 104460                TRAP   CSERROR      ;REPORT IT &
4720 025224 000510                ; EXIT
4721 025226 004537 003616      31$:  JSR   R5,READ         ;READ IT BACK
4722 025232 123004                NPRCTL
4723 025234 002740                13$:  BT2
4724 025236 103002                BCC   32$          ; IF AN ERROR OCCURED,
4725 025240          ERROR              ;REPORT IT &
4726 025240 104460                TRAP   CSERROR      ;EXIT
4727 025242 000501                BR   24$
4728                ; EXIT
4729 025244 013777 025216 155002 32$:  MOV   5$,@GDATA      ;BUILD THE EXPECTED RETURN VALUE
4730 025252 042777 000300 154774  BIC   #NPRABT!NPRGO,@GDATA ;WE CAN'T WRITE THESE BITS
4731                ;
4732 025260 004737 005134          JSR   PC,NPREAD      ;GET CONTENTS OF ALL NPR REGISTERS INTO BT2
4733 025264 103002                BCC   33$          ;IF AN ERROR OCCURED,
4734 025266          ERROR              ;REPORT IT &
4735 025266 104460                TRAP   CSERROR      ;EXIT
4736 025270 000466                ;POINT TO TABLE OF EXPECTED REGISTER CONTENTS
4737 025272 013701 002254      33$:  MOV   GDATA,R1       ;POINT TO NPR REGISTER CONTENTS
4738 025276 013702 002256          MOV   BDATA,R2       ;COUNT OF # OF NPR REGISTERS BEING PROCESSED
4739 025302 012703 000001          MOV   #1,R3         ;FOR NOW, ONLY THE CONTROL REGISTER IS CHECKED!!
4740                ;
4741                ;CHECK ONE BYTE
4742 025306 121112          7$:  CMPB  (R1),(R2)      ;GO REPORT FAILURE IF ANY ERROR IS FOUND
4743 025310 001003                BNE   8$           ;BUMP POINTERS -- TABLES ARE ACTUALLY WORD TABLES
4744 025312 022122                CMP   (R1)+,(R2)+
4745 025314 077304                SOB   R3,7$        ;LOOP IF NOT DONE YET
4746 025316 000412                BR   9$           ;ELSE, PROCEED WITH TESTING
4747                ;
4748 025320 163701 025132          8$:  SUB   12$,R1        ;CALCULATE THE REGISTER # CAUSING THE FAILURE
4749 025324 010137 002300          MOV   R1,REGNUM     ;IDENTIFY FAULTY REGISTER
4750 025330          GEDF   EM26A,ERR8      ;NPR ERROR -- DURING WRITE TO CONTROL REG.
4751                ;                'DEVICE FATAL' ERROR # 26
4752 025330 104455                TRAP   CSERDF
4753 025332 000032                .WORD 26
4754 025334 016204                .WORD EM26A
4755 025336 006532                .WORD ERR8
4756 025340          ESCAPE TST          ;IF IT CAN'T BE INIT'ED, WE CAN'T TEST IT!
4757 025340 104410                TRAP   CSERDF
4758 025342 000104                .WORD L10032-.
4759 025344 004737 003514          9$:  JSR   PC,MSTCLR     ;RE-INITIALIZE DMV & ENTER M-LOOP
4760 025350 103003                BCC   20$         ;IF NO ERROR, PROCEED WITH TESTING

```

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TEST 3 -- NPR CONTROL REGISTER - MASTER CLEAR

```

4761 025352          ERROR                      ;ELSE REPORT IT
4762 025352 104460  ESCAPE TST                   ;    & EXIT TEST                      TRAP  C$ERROR
4763 025354          ESCAPE TST                   ;    & EXIT TEST                      TRAP  C$ESCAPE
4764 025354 104410  ESCAPE TST                   ;    & EXIT TEST                      .WORD L10032-
4765 025356 000070  ESCAPE TST                   ;    & EXIT TEST                      .WORD L10032-
4766
4767 :          THE 'MASTER CLEAR' JUST PERFORMED SHOULD RESET THE NPR CONTROL
4768 :          REGISTER. IT SHOULD NOW EQUAL 004 AGAIN.
4769
4770 025360 013777 002632 154666 20$:  MOV      NPRMCR,@GDATA   ;RESET THE EXPECTED DATA
4771
4772 :          ALSO, THE OTHER REGISTERS SHOULD STILL BE AT THEIR INITIAL VALUES
4773
4774 025366 004737 005134  JSR      PC,NPREAD     ;GET CONTENTS OF ALL NPR REGISTERS INTO BT2
4775 025372 103002  BCC     34$           ;IF AN ERROR OCCURED,
4776 025374          ERROR          ;REPORT IT &
4777 025374 104460          ;                                          TRAP  C$ERROR
4778 025376 000423  BR      24$           ; EXIT
4779 025400 013701 002254 34$:  MOV      GDATA,R1     ;POINT TO TABLE OF EXPECTED REGISTER CONTENTS
4780 025404 013702 002256  MOV      BDATA,R2     ;POINT TO NPR REGISTER CONTENTS
4781 025410 012703 000001  MOV      #1,R3        ;COUNT OF # OF NPR REGISTERS BEING PROCESSED
4782          ;FOR NOW, ONLY THE CONTROL REGISTER IS CHECKED!!
4783
4784 025414 121112 21$:  CMPB    (R1),(R2)    ;CHECK ONE BYTE
4785 025416 001003  BNE     22$           ;GO REPORT FAILURE IF ANY ERROR IS FOUND
4786 025420 022122  CMP     (R1)+,(R2)+   ;BUMP POINTERS — TABLES ARE ACTUALLY WORD TABLES
4787 025422 077304  SOB     R3,21$       ;LOOP IF NOT DONE YET
4788 025424 000410  BR      24$           ;ELSE, PROCEED WITH TESTING
4789
4790 025426 163701 025132 22$:  SUB     12$,R1       ;CALCULATE THE REGISTER # CAUSING THE FAILURE
4791 025432 010137 002300  MOV     R1,REGNUM    ;IDENTIFY FAULTY REGISTER
4792 025436          GEDF    EM26,ERR8 ;NPR ERROR — BAD INITIALIZATION
4793          ;      'DEVICE FATAL' ERROR # 27
4794 025436 104455          ;                                          TRAP  C$ERDF
4795 025440 000033          ;                                          .WORD 27
4796 025442 016152          ;                                          .WORD EM26
4797 025444 006532          ;                                          .WORD ERR8
4798 025446          ;
4799 025446          ;
4800 025446 104401          ;

```

L10032: TRAP C\$SETST

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TEST 4 -- NPR DATA-OUT

.SBTTL TEST 4 -- NPR DATA-OUT

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4845
4846
4847
4848
4849
4850
4851
4852
4853
4854
4855
4856

025450
000004
025450 004737 003514
025454 103003
025456
025456 104460
025460
025460 104410
025462 000474

025464
025464
025464 104402
025466 013737 002506 025504

025474 004537 005266
025500 002510
025502 002654
025504 000000
025506 000044
025510 103025

```
*****
*
* TEST 4 -- NPR DATA-OUT
*
* FIRST SUBTEST :
* THE NPR OUTPUT ADDRESS REGISTER IS LOADED WITH THE ADDRESS OF A 2 BYTE
* BUFFER IN THE PROGRAM. THEN, EACH WORD OF DATA PATTERN F IS LOADED INTO THE
* NPR OUTPUT DATA REGISTER, A FULLWORD NPR OUTPUT REQUEST IS PERFORMED,
* AND THE PROGRAM CHECKS FOR THE CORRECT DATA IN THE PROGRAM BUFFER. ALSO,
* THE PROGRAM CHECKS THAT THE ABORT XFER BIT IN THE NPR CONTROL REGISTER
* NEVER GETS SET.
* DATA PATTERN F = 125252, 052525, 000000, 177777, 000001, 000002, 000004,
*                   000010, 000020, 000040, 000100, 000200, 000400, 001000,
*                   002000, 004000, 010000, 020000, 040000, 100000, 177776,
*                   177775, 177773, 177767, 177757, 177737, 177677, 177577,
*                   177377, 176777, 175777, 173777, 167777, 157777, 137777,
*                   077777, 000000
*
* SECOND SUBTEST:
* THE ABOVE OPERATIONS ARE REPEATED IN BYTE NPR TRANSFER MODE, USING THE DATA
* BYTES IN DATA PATTERN B. THE LOW BYTE OF THE PROGRAM BUFFER IS USED, AND
* THE UPPER BYTE IS CLEARED AT THE START, AND IS CHECKED TO REMAIN UNCHANGED
* THROUGHOUT THE SUBTEST.
* DATA PATTERN B = 125, 252, 000, 377, 001, 002, 004, 010, 020, 040, 100,
*                   200, 376, 375, 373, 367, 357, 337, 277, 177, 000
*****
```

```
-----
*
*          BGNTST
*
*          NPROTS = $T
*          JSR      PC,MSTCLR          ;INIT DMV & START UP MAINT. LOOP
*          BCC     1$                    ;IF NO ERROR, PROCEED WITH TEST
*          ERROR   ;ELSE, REPORT IT
*
*          ESCAPE TST                   ; & EXIT TEST
*
*                                     TRAP   CSERROR
*                                     .WORD  L10033-
*
*-----
1$:   BGNSUB                           ;----- MAIN MEMORY WORD DATA-OUT TESTING -----
*                                     T4.1:
*                                     TRAP   CSBSUB
*          MOV      PATF,4$              ;SETUP COUNT OF # OF WORDS IN TEST PATTERN
*
*          JSR      R5,NPRMOV            ;MOVE DATA THROUGH THE NPR LOGIC
2$:   PATF+2
3$:   BUFAREA
4$:   0
*                                     ;*** MODIFIED FROM ABOVE *** -- WORD COUNT
*                                     ; OPERATION TO BE UTILIZED
*          BCC     7$                    ;IF ERROR, REPORT IT
*-----
```

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TEST 4 -- NPR DATA-OUT

```

4857 025512
4858 025512 104460
4859 025514 005737 002412
4860 025520 001421
4861 025522 022737 000006 002204
4862 025530 001415
4863 025532 012737 000002 002202
4864 025540 012737 000006 002204
4865 025546 012737 016316 002206
4866 025554 012737 007314 002210
4867 025562 000753
4868
4869 025564 013701 025500 7$: MOV 2$,R1 :POINT TO GOOD DATA
4870 025570 013702 025502 MOV 3$,R2 : & ACTUAL DATA
4871 025574 013703 025504 MOV 4$,R3 :GET WORD COUNT
4872 025600 005037 002276 CLR ERRFLG :RESET ERROR FLAG
4873
4874 025604 022122 5$: CMP (R1)+,(R2)+ :CHECK RECEIVED DATA
4875 025606 001007 BNE 6$ :ERROR, GO REPORT IT
4876 025610 077303 11$: SOB R3,5$ :GOOD, IF MORE DO IT AGAIN
4877 025612 005737 002276 TST ERRFLG :ELSE, SEE IF WE MUST FINISH AN ERROR MESSAGE
4878 025616 001440 BEQ 10$ :NO, TEST IT AGAIN BUT WITH BYTE TRANSFERS
4879 025620 004737 013076 JSR PC,MULERR :YES, USE COMMON ROUTINE TO END ERROR MESSAGE
4880 025624 000435 BR 10$ :WE CAN TEST IT AGAIN BUT WITH BYTE TRANSFERS
4881
4882 025626 010146 6$: MOV R1,-(SP) :SAVE THIS FOR FURTHER TESTING
4883 025630 014137 002254 MOV -(R1),GDATA :SETUP FOR ERROR REPORT
4884 025634 014237 002256 MOV -(R2),BDATA
4885 025640 010237 002252 MOV R2,TDATA :LSI-11'S MEMORY ADDRESS
4886 025644 163701 025500 SUB 2$,R1 :CALCULATE THE OFFSET AT WHICH THE
4887 025650 006201 ASR R1 : DATA COMPARISON ERROR OCCURED
4888 025652 010137 002300 MOV R1,REGNUM :THE ERROR MESSAGE WILL REPORT THIS TOO
4889 025656 005737 002276 TST ERRFLG :HAVE WE ALREADY REPORTED AN ERROR HERE?
4890 025662 001007 BNE 8$ :YES, THEN WE ONLY PRINT DATA THIS TIME
4891 025664 005237 002276 INC ERRFLG :NO, SET FLAG & REPORT THE WHOLE MESSAGE
4892 025670 GEDF EM26B,ERR9 :WORD NPR TRANSFER DMV ==> LSI
4893 : 'DEVICE FATAL' ERROR # 28
4894 025670 104455 TRAP C$ERRDF
4895 025672 000034 .WORD 28
4896 025674 016226 .WORD EM26B
4897 025676 007300 .WORD ERR9
4898 025700 000402 BR 9$ : RESUME TESTING
4899
4900 025702 004737 012574 8$: JSR PC,ERR9$ :IDENTIFY THE FAILING DATA
4901 025706 012601 9$: MOV (SP)+,R1 :RESTORE POINTERS
4902 025710 013702 002252 MOV TDATA,R2
4903 025714 005722 TST (R2)+
4904 025716 000734 BR 11$ :AND RESUME TESTING
4905
4906 025720 10$: ENDSUB
4907 025720
4908 025720 104403 L10034: TRAP C$ESUB
4909
4910 -----
4911
4912 025722 BGNSUB :----- MAIN MEMORY BYTE DATA-OUT TESTING -----

```

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TEST 4 -- NPR DATA-OUT

T4.2:

TRAP CSBSUB

```

4913 025722
4914 025722 104402
4915 025724 013737 002456 025742
4916
4917 025732 004537 005266
4918 025736 002460
4919 025740 002654
4920 025742 000000
4921 025744 000054
4922
4923 025746 103025
4924 025750
4925 025750 104460
4926 025752 005737 002412
4927 025756 001421
4928 025760 022737 000006 002204
4929 025766 001415
4930 025770 012737 000002 002202
4931 025776 012737 000006 002204
4932 026004 012737 016316 002206
4933 026012 012737 007314 002210
4934 026020 000753
4935
4936 026022 013701 025736
4937 026026 013702 025740
4938 026032 013703 025742
4939 026036 005037 002276
4940
4941 026042 122122
4942 026044 001007
4943 026046 077303
4944 026050 005737 002276
4945 026054 001437
4946 026056 004737 013076
4947 026062 000434
4948
4949 026064 010146
4950 026066 114137 002254
4951 026072 114237 002256
4952 026076 010237 002252
4953 026102 163701 025736
4954
4955 026106 010137 002300
4956 026112 005737 002276
4957 026116 001007
4958 026120 005237 002276
4959 026124
4960
4961 026124 104455
4962 026126 000035
4963 026130 016251
4964 026132 007306
4965 026134 000402
4966
4967 026136 004737 012732
4968 026142 012601

```

```

2$:
3$:
4$:
13$:
7$:
5$:
11$:
6$:
8$:
9$:

```

```

MOV PATB,4$
JSR R5,NPRMOV
PATB+2
BUFAREA
0
NPRDLB
BCC 7$
ERROR
TST TMP0
BEQ 7$
CMP #NPRTOE,ERRNBR
BEQ 7$
MOV #T.EHRD,ERRTYP
MOV #NPRTOE,ERRNBR
MOV #EM26E,ERRMSG
MOV #ERR11,ERRBLK
BR 13$
MOV 2$,R1
MOV 3$,R2
MOV 4$,R3
CLR ERRFLG
CMPB (R1)+,(R2)+
BNE 6$
SOB R3,5$
TST ERRFLG
BEQ 10$
JSR PC,NULERR
BR 10$
MOV R1,-(SP)
MOVB -(R1),GDATA
MOVB -(R2),BDATA
MOV R2,TDATA
SUB 2$,R1
MOV R1,REGNUM
TST ERRFLG
BNE 8$
INC ERRFLG
GEDF EM26C,ERR10
BR 9$
JSR PC,ERR10$
MOV (SP)+,R1

```

```

;SETUP COUNT OF # OF WORDS IN TEST PATTERN
;MOVE DATA THROUGH THE NPR LOGIC
; ADDRESS OF DATA
; BUFFER AREA
;*** MODIFIED FROM ABOVE *** -- BYTE COUNT
; OPERATION TO BE UTILIZED
;IF ERROR, REPORT IT
;WE JUST REPORTED ONE ERROR BUT WAS IT A TIMEOUT
;ERROR? IF SO, PROCEED WITH TESTING. ELSE,
;WE WILL HAVE TO REPORT IT HERE AND NOW.
;THE TIMEOUT ERROR WAS ALREADY REPORTED.
;IT WASN'T REPORTED YET, SETUP FOR IT NOW:
;LOOP BACK TO CAUSE REPORT @ PROPER PC LOCATION
;POINT TO GOOD DATA
; & ACTUAL DATA
;GET BYTE COUNT
;RESET ERROR FLAG
;CHECK RECEIVED DATA
;ERROR, GO REPORT IT
;GOOD, IF MORE DO IT AGAIN
;ELSE, SEE IF WE MUST FINISH AN ERROR MESSAGE
;NO, THEN WE CAN EXIT THE TEST
;YES, OUTPUT THE REQUIRED BLANK LINES. NOW
;THEN WE CAN EXIT THE TEST
;SAVE THIS FOR FURTHER TESTING
;SETUP FOR ERROR REPORT
;LSI-11'S MEMORY ADDRESS
;CALCULATE THE OFFSET AT WHICH THE
; DATA COMPARISON ERROR OCCURED
;THE ERROR MESSAGE WILL REPORT THIS TOO
;HAVE WE ALREADY REPORTED AN ERROR HERE?
;YES, THEN WE ONLY PRINT DATA THIS TIME
;NO, SET FLAG & REPORT THE WHOLE MESSAGE
;BYTE NPR TRANSFER DMV ==> LSI
; "DEVICE FATAL" ERROR # 29
;
; RESUME TESTING
;IDENTIFY THE FAILING DATA
;RESTORE POINTERS

```

```

TRAP CSERDF
.WORD 29
.WORD EM26C
.WORD ERR10

```

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TEST 4 -- NPR DATA-OUT

4969 026144 013702 002252
 4970 026150 005202
 4971 026152 000735
 4972
 4973 026154
 4974 026154
 4975 026154 104403
 4976 026156
 4977 026156
 4978 026156 104401

MOV TDATA,R2
 INC R2
 BR 11\$

;AND RESUME TESTING

10\$: ENDSUB

ENDTST

L10035: TRAP C\$ESUB

L10033: TRAP C\$ETST

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TEST 5 -- NPR DATA-IN

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026160
026160 004737 003514
026164 103003
026166 104460
026170 104410
026172 000234
026174
026174 013737 002506 026212
026202 004537 005266
026206 002510
026210 002654
026212 000000
026214 000004
026216 103025
026220
026220 104460
026222 005737 002412
026226 001421
026230 022737 000006 002204
026236 001415
026240 012737 000002 002202
026246 012737 000006 002204
026254 012737 016316 002206
026262 012737 007314 002210
026270 000753
026272 013701 026206
026276 013702 026210
026302 013703 026212

.SBTTL TEST 5 -- NPR DATA-IN

```

*****
*
* TEST 5 -- NPR DATA-IN
* THE NPR INPUT ADDRESS REGISTER IS LOADED WITH THE ADDRESS OF A 2 BYTE
* BUFFER IN THE PROGRAM. THEN, EACH WORD OF DATA PATTERN F IS LOADED INTO THE
* PROGRAM BUFFER, A FULLWORD NPR INPUT REQUEST IS ISSUED AND PERFORMED,
* AND THE PROGRAM CHECKS FOR THE CORRECT DATA IN THE NPR INPUT DATA REG.
* ALSO, THE PROGRAM CHECKS THAT THE ABORT XFER BIT IN THE NPR CONTROL
* REGISTER NEVER GETS SET.
* DATA PATTERN F = 125252, 052525, 000000, 177777, 000001, 000002, 000004,
*                   000010, 000020, 000040, 000100, 000200, 000400, 001000,
*                   002000, 004000, 010000, 020000, 040000, 100000, 177776,
*                   177775, 177773, 177767, 177757, 177737, 177677, 177577,
*                   177377, 176777, 175777, 173777, 167777, 157777, 137777,
*                   077777, 000000
*****

```

```

: BGNTST
:
: JSR PC,MSTCLR ;INIT DMV & START UP MAINT. LOOP
: BCC 1$ ;IF NO ERROR, PROCEED WITH TEST
: ERROR ;ELSE, REPORT IT
: ESCAPE TST ; & EXIT TEST
: TRAP CSERROR
: .WORD L10036-
:
: 1$: MOV PATF,4$ ;SETUP COUNT OF # OF WORDS IN TEST PATTERN
: JSR R5,NPRMOV ;MOVE DATA THROUGH THE NPR LOGIC
: 2$: PATF+2 ; ADDRESS OF DATA
: 3$: BUFAREA ; BUFFER AREA
: 4$: 0 ;*** MODIFIED FROM ABOVE *** -- WORD COUNT
: NPRLD ; OPERATION TO BE UTILIZED
: BCC 7$ ;IF ERROR, REPORT IT
: 13$: ERROR
: TRAP CSERROR
: TST TMO ;WE JUST REPORTED ONE ERROR BUT WAS IT A TIMEOUT
: BEQ 7$ ;ERROR? IF SO, PROCEED WITH TESTING. ELSE,
: CMP #NPRTOE,ERRNBR ;WE WILL HAVE TO REPORT IT HERE AND NOW.
: BEQ 7$ ;THE TIMEOUT ERROR WAS ALREADY REPORTED.
: MOV #T.EHRD,ERRTYP ;IT WASN'T REPORTED YET, SETUP FOR IT NOW:
: MOV #NPRTOE,ERRNBR
: MOV #EM26E,ERRMSG
: MOV #ERR11,ERRBLK
: BR 13$ ;LOOP BACK TO CAUSE REPORT @ PROPER PC LOCATION
:
: 7$: MOV 2$,R1 ;POINT TO GOOD DATA
: MOV 3$,R2 ; & ACTUAL DATA
: MOV 4$,R3 ;GET WORD COUNT

```

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TEST 5 -- NPR DATA-IN

5035	026306	005037	002276		CLR	ERRFLG		:RESET ERROR FLAG	
5036									
5037	026312	022122		5\$:	CMP	(R1)+,(R2)+		:CHECK RECEIVED DATA	
5038	026314	001007			BNE	6\$:ERROR, GO REPORT IT	
5039	026316	077303		11\$:	SOB	R3,5\$:GOOD, IF MORE DO IT AGAIN	
5040	026320	005737	002276		TST	ERRFLG		:ELSE, SEE IF WE MUST FINISH AN ERROR MESSAGE	
5041	026324	001440			BEQ	10\$:NO, THEN WE CAN EXIT THE TEST	
5042	026326	004737	013076		JSR	PC,NULERR		:YES, USE COMMON ROUTINE TO END ERROR MESSAGE	
5043	026332	000435			BR	10\$:THEN WE CAN EXIT THE TEST	
5044									
5045	026334	010246		6\$:	MOV	R2,-(SP)		:SAVE THIS FOR FURTHER TESTING	
5046	026336	014137	002254		MOV	-(R1),GDATA		:SETUP FOR ERROR REPORT	
5047	026342	014237	002256		MOV	-(R2),BDATA			
5048	026346	010137	002252		MOV	R1,TDATA		:LSI-11'S MEMORY ADDRESS	
5049	026352	163701	026206		SUB	2\$,R1		:CALCULATE THE OFFSET AT WHICH THE	
5050	026356	006201			ASR	R1		: DATA COMPARISON ERROR OCCURED	
5051	026360	010137	002300		MOV	R1,REGNUM		:THE ERROR MESSAGE WILL REPORT THIS TOO	
5052	026364	005737	002276		TST	ERRFLG		:HAVE WE ALREADY REPORTED AN ERROR HERE?	
5053	026370	001007			BNE	8\$:YES, THEN WE ONLY PRINT DATA THIS TIME	
5054									
5055	026372	005237	002276		INC	ERRFLG		:NO, SET FLAG & REPORT THE WHOLE MESSAGE	
5056	026376				GEDF	EM26D,ERR9		:WORD NPR TRANSFER LSI ==> DMV	
5057								: 'DEVICE FATAL' ERROR # 30	
5058	026376	104455							TRAP C\$ERDF
5059	026400	000036							.WORD 30
5060	026402	016274							.WORD EM26D
5061	026404	007300							.WORD ERR9
5062	026406	000402			BR	9\$: RESUME TESTING	
5063									
5064	026410	004737	012574	8\$:	JSR	PC,ERR9\$:IDENTIFY THE FAILING DATA	
5065	026414	012602		9\$:	MOV	(SP)+,R2		:RESTORE POINTERS	
5066	026416	013701	002252		MOV	TDATA,R1			
5067	026422	005721			TST	(R1)+			
5068	026424	000734			BR	11\$:AND RESUME TESTING	
5069									
5070	026426			10\$:					
5071	026426				ENDTST				
5072	026426								
5073	026426	104401							L10036: TRAP C\$ETST

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TEST 6 -- NPR XFER ABORT

.SBTTL TEST 6 -- NPR XFER ABORT

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5095 026430
5096 026430
5097 026430 104402
5098 026432 004737 003514
5099 026436 103003
5100 026440
5101 026440 104460
5102 026442
5103 026442 104410
5104 026444 000164
5105 026446 012737 000001 002412
5106
5107 026454 012737 160000 002420
5108 026462 012737 000044 002414
5109 026470 004537 004042
5110 026474 000070
5111 026476 002420
5112 026500 103003
5113 026502
5114 026502 104460
5115 026504
5116 026504 104410
5117 026506 000122
5118 026510 004537 004042
5119 026514 000071
5120 026516 002421
5121 026520 103003
5122 026522
5123 026522 104460
5124 026524
5125 026524 104410
5126 026526 000102
5127 026530 004537 004054
5128 026534 000072
5129 026536 000200

```

```

:*****
:*
:*      TEST 6 -- NPR XFER ABORT
:*
:* FIRST SUBTEST :
:* THE PROGRAM PERFORMS AN OUTPUT NPR REQUEST TO A NON-EXISTENT MEMORY
:* LOCATION, AND CHECKS FOR THE ASSERTION OF ABORT XFER BIT IN THE NPR CONTROL
:* REGISTER. THEN, AN OUTPUT NPR IS DONE AND CHECKED, TO A LOCATION IN THE
:* PROGRAM, USING 125252 FOR DATA, AND THE PROGRAM CHECKS FOR ABORT XFER TO
:* BE CLEARED BY SETTING THE DONE BIT.
:* SECOND SUBTEST :
:* THE ABOVE SUBTEST IS REPEATED USING INPUT NPR'S.
:*****

```

```

:      BGNTST
:
:==== SUBTEST # 1 -- NPR OUTPUT TO NON-EXISTENT LOCATION FORCING NPR-ABORT =====
:
:      BGNSUB
:
:      T6.:
:
:      T6.1:
:
:      TRAP      CSBSUB
:      JSR      PC,MSTCLR      ;INIT DMV & ENTER M-LOOP
:      BCC      .+10           ;IF NO ERROR, PROCEED WITH TESTING
:      ERROR    ;ELSE, REPORT ERROR
:
:      TRAP      C$ERROR
:      ESCAPE   SUB           ; & EXIT TEST
:
:      TRAP      C$ESCAPE
:      .WORD    L10040-.
:      MOV      #1,TMP0       ;DISABLE PRINTOUT OF TIMEOUT-COUNT BY ERR11
:
:      MOV      #160000,TMP3  ;SETUP 11'S ADDRESS
:      MOV      #NPRDL,TMP1   ;CONTROL REG. VALUE FOR NPR-OUT COMMAND
:      JSR      R5,WRITE      ;SETUP ADDRESS OUT REGISTERS
:
:      BCC      .+10           ;IF NO ERROR, PROCEED
:      ERROR    ;ELSE, REPORT IT
:
:      TRAP      C$ERROR
:      ESCAPE   SUB           ; AND EXIT THIS TEST
:
:      TRAP      C$ESCAPE
:      .WORD    L10040-.
:      JSR      R5,WRITE
:      NPRAOH  TMP3+1
:      BCC      .+10           ;IF NO ERROR, PROCEED
:      ERROR    ;ELSE, REPORT IT
:
:      TRAP      C$ERROR
:      ESCAPE   SUB           ; AND EXIT THIS TEST
:
:      TRAP      C$ESCAPE
:      .WORD    L10040-.
:      JSR      R5,WRITEI
:      NPRAOX  NPRBS7
:
:      ; (THIS SETS BS7 & CLEARS EXTENDED ADDR. BITS)

```

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TEST 6 -- NPR XFER ABORT

```

5130 026540 103003 BCC .+10 ;IF NO ERROR, PROCEED
5131 026542 ERROR ;ELSE, REPORT IT
5132 026542 104460 ESCAPE SUB ; AND EXIT THIS TEST TRAP C$ERROR
5133 026544 ; ; TRAP C$ESCAPE
5134 026544 104410 ; ; .WORD L10040-.
5135 026546 000062 JSR R5,WRITE ;INITIATE THE NPR-OUT OPERATION
5136 026550 004537 004042 NPRCTL
5137 026554 123004 TMP1
5138 026556 002414 BCC .+10 ;IF NO ERROR, PROCEED
5139 026560 103003 ERROR ;ELSE, REPORT IT
5140 026562 104460 ESCAPE SUB ; AND EXIT THIS TEST TRAP C$ERROR
5141 026562 104410 ; ; TRAP C$ESCAPE
5142 026564 ; ; .WORD L10040-.
5143 026564 104410 JSR R5,READ ;READ BACK THE CONTROL-STATUS REGISTER
5144 026566 000042 NPRCTL
5145 026570 004537 003616 TMP2
5146 026574 123004 BCC .+10 ;IF NO ERROR, PROCEED
5147 026576 002416 ERROR ;ELSE, REPORT IT
5148 026600 103003 ESCAPE SUB ; AND EXIT THIS TEST TRAP C$ERROR
5149 026602 104460 ; ; TRAP C$ESCAPE
5150 026602 104410 ; ; .WORD L10040-.
5151 026604 000022 BITB #NPRABT,TMP2 ;THE ABORT BIT SHOULD BE SET
5152 026604 104410 BNE 20$ ;IT IS. EXIT SUBTEST
5153 026606 000022 000200 002416 GEDF EM26G,ERR11 ;IT DIDN'T. REPORT MISSING NPR ABORT
5154 026610 132737 ; ; 'DEVICE FATAL' ERROR # 31
5155 026616 001004 ; ; TRAP C$ERDF
5156 026620 ; ; .WORD 31
5157 ; ; .WORD EM26G
5158 026620 104455 ; ; .WORD ERR11
5159 026622 000037 20$: ENDSUB
5160 026624 016402 ; ; L10040:
5161 026626 007314 ; ; TRAP C$ESUB
5162 026630 ; ;
5163 026630 ; ;
5164 026630 104403 ; ;
5165 ; ; :=== SUBTEST #2 -- NPR OUTPUT TO EXISTENT LOCATION YIELDING NO NPR-ABORT ====
5166 ; ;
5167 026632 BGNSUB ; ;
5168 026632 ; ; T6.2:
5169 026632 104402 ; ; TRAP C$BSUB
5170 026634 004737 003514 JSR PC,MSTCLR ;INIT DMV & ENTER M-LOOP
5171 026640 103003 BCC .+10 ;IF NO ERROR, PROCEED WITH TESTING
5172 026642 ERROR ;ELSE, REPORT ERROR
5173 026642 104460 ESCAPE SUB ; & EXIT TEST TRAP C$ERROR
5174 026644 ; ;
5175 026644 104410 ; ; TRAP C$ESCAPE
5176 026646 000164 ; ; .WORD L10041-.
5177 026650 012737 000001 002412 MOV #1,TMP0 ;DISABLE PRINTOUT OF TIMEOUT-COUNT BY ERR11
5178 ; ;
5179 026656 012737 002654 002420 MOV #BUFAREA,TMP3 ;SETUP 11'S ADDRESS
5180 026664 012737 000044 002414 MOV #NPRDL,TMP1 ;CONTROL REG. VALUE FOR NPR-OUT COMMAND
5181 026672 004537 004042 JSR R5,WRITE ;SETUP ADDRESS OUT REGISTERS
5182 026676 000070 NPRAQL
5183 026700 002420 TMP3
5184 026702 103003 BCC .+10 ;IF NO ERROR, PROCEED
5185 026704 ERROR ;ELSE, REPORT IT

```

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TEST 6 -- NPR XFER ABORT

```

5186 026704 104460
5187 026706          ESCAPE SUB          :          AND EXIT THIS TEST          TRAP C$ERROR
5188 026706 104410          :          AND EXIT THIS TEST          TRAP C$ESCAPE
5189 026710 000122          :          AND EXIT THIS TEST          .WORD L10041-.
5190 026712 004537 004042    JSR R5,WRITE
5191 026716 000071    NPRAOH
5192 026720 002421    TMP3+1
5193 026722 103003    BCC .+10          ;IF NO ERROR, PROCEED
5194 026724          ERROR          ;ELSE, REPORT IT
5195 026724 104460          :          AND EXIT THIS TEST          TRAP C$ERROR
5196 026726          ESCAPE SUB          :          AND EXIT THIS TEST          TRAP C$ESCAPE
5197 026726 104410          :          AND EXIT THIS TEST          .WORD L10041-.
5198 026730 000102          :          AND EXIT THIS TEST          TRAP C$ESCAPE
5199 026732 004537 004054    JSR R5,WRITEI
5200 026736 000072    NPRAOX
5201 026740 000000    0          ; (THIS CLEARS BS7 & EXTENDED ADDR. BITS)
5202 026742 103003    BCC .+10          ;IF NO ERROR, PROCEED
5203 026744          ERROR          ;ELSE, REPORT IT
5204 026744 104460          :          AND EXIT THIS TEST          TRAP C$ERROR
5205 026746          ESCAPE SUB          :          AND EXIT THIS TEST          TRAP C$ESCAPE
5206 026746 104410          :          AND EXIT THIS TEST          .WORD L10041-.
5207 026750 000062          :          AND EXIT THIS TEST          TRAP C$ESCAPE
5208 026752 004537 004042    JSR R5,WRITE          ;INITIATE THE NPR-OUT OPERATION
5209 026756 123004    NPRCTL
5210 026760 002414    TMP1
5211 026762 103003    BCC .+10          ;IF NO ERROR, PROCEED
5212 026764          ERROR          ;ELSE, REPORT IT
5213 026764 104460          :          AND EXIT THIS TEST          TRAP C$ERROR
5214 026766          ESCAPE SUB          :          AND EXIT THIS TEST          TRAP C$ESCAPE
5215 026766 104410          :          AND EXIT THIS TEST          .WORD L10041-.
5216 026770 000042          :          AND EXIT THIS TEST          TRAP C$ESCAPE
5217 026772 004537 003616    JSR R5,READ          ;READ BACK THE CONTROL-STATUS REGISTER
5218 026776 123004    NPRCTL
5219 027000 002416    TMP2
5220 027002 103003    BCC .+10          ;IF NO ERROR, PROCEED
5221 027004          ERROR          ;ELSE, REPORT IT
5222 027004 104460          :          AND EXIT THIS TEST          TRAP C$ERROR
5223 027006          ESCAPE SUB          :          AND EXIT THIS TEST          TRAP C$ESCAPE
5224 027006 104410          :          AND EXIT THIS TEST          .WORD L10041-.
5225 027010 000022          :          AND EXIT THIS TEST          TRAP C$ESCAPE
5226 027012 132737 000200 002416    BITB #NPRABT,TMP2          ;THE ABORT BIT SHOULD BE SET
5227 027020 001404    BEQ 20$          ;IT IS. EXIT SUBTEST
5228 027022          GEDF EM26E,ERR11          ;IT DIDN'T. REPORT MISSING NPR ABORT
5229          ; 'DEVICE FATAL' ERROR # 32
5230 027022 104455          :          AND EXIT THIS TEST          TRAP C$ERDF
5231 027024 000040          :          AND EXIT THIS TEST          .WORD 32
5232 027026 016316          :          AND EXIT THIS TEST          .WORD EM26E
5233 027030 007314          :          AND EXIT THIS TEST          .WORD ERR11
5234 027032          20$: ENDSUB
5235 027032          :          AND EXIT THIS TEST          L10041:
5236 027032 104403          :          AND EXIT THIS TEST          TRAP C$ESUB
5237 027034          ENDTST
5238 027034          :          AND EXIT THIS TEST          L10037:
5239 027034 104401          :          AND EXIT THIS TEST          TRAP C$ETST

```

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TEST 7 -- NPR EXTENDED ADDRESS BIT TEST

.SBTTL TEST 7 -- NPR EXTENDED ADDRESS BIT TEST

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027036 004737 003514
027042 103003
027044
027044 104460
027046
027046 104432
027050 001506
027052 013700 002120
027056 042700 001777
027062 001002
027064
027064 104432
027066 001470
027070 010037 002436
027074
027074
027074 104402
027076 012737 177777 002434
027104 012737 002000 002442
027112 112737 000001 002451
027120 004737 030560
027124 004537 004054
027130 000074
027132 000000

*
* TEST 7 -- NPR EXTENDED ADDRESS BIT TEST
*
* THIS TEST WILL ONLY BE RUN IF THERE IS AT LEAST 32K WORDS OF MEMORY ON THE
* SYSTEM. IF THERE IS, THE PROGRAM CHOOSES A LOCATION TO USE IN THE ADDRESS
* RANGE 200000-377776 (OCTAL). THEN, THE FOLLOWING 2 SUBTESTS ARE PERFORMED :
*
* FIRST SUBTEST :
* AN INPUT NPR IS PERFORMED AND CHECKED USING THE MEMORY LOCATION, WITH
* 125252 FOR DATA. THE PROGRAM CHECKS THAT THE ABORT XFER BIT REMAINS
* CLEARED.
* SECOND SUBTEST :
* AN OUTPUT NPR IS PERFORMED AND CHECKED USING THE MEMORY LOCATION, WITH
* 125252 FOR DATA. THE PROGRAM CHECKS THAT THE ABORT XFER BIT REMAINS
* CLEARED.
*

```

:          BGNTST
:
:          T7::
:          JSR    PC,MSTCLR    ;INIT DMV & ENTER M-LOOP
:          BCC    1$          ;IF NO ERROR, PROCEED WITH TESTING
:          ERROR  ;ELSE, REPORT ERROR
:
:          EXIT    TST        ; & EXIT TEST
:
:          TRAP   C$ERROR
:
:          TRAP   C$EXIT
:          .WORD  L10042-
:
:          1$:    MOV    L$HIMEM,R0 ;GET LAST VALID 'PAR' VALUE FROM SUPERVISOR
:          BIC    #1777,R0       ;THESE BITS CORESPOND TO BITS 6 --> 15 OF THE
:
:          ;ACTUAL ADDRESS AND AREN'T OUR CONCERN HERE.
:          BNE    2$          ;IF THE RESULT IS ZERO,
:          EXIT    TST        ; THERE IS NOTHING TO TEST
:
:          TRAP   C$EXIT
:          .WORD  L10042-
:
:          ;ELSE, PROCEED TO SETUP MMU'S PAR AND DMV'S NPR
:          ; REGISTER MAXIMUM VALUES
:
:          2$:    MOV    R0,TMPA  ;THIS IS FOR THE MMU
:
:          =====
:          12$:   BGNSUB          ;TEST THE NPR-IN USING EXTENDED ADDR. BITS
:
:          T7.1:
:          TRAP   C$BSUB
:
:          MOV    #177777,TMP9   ;INITIALIZE TEMP 9
:          MOV    #BIT10,TMPC    ;INITIALIZE PAGE ADDRESS REG. VARIABLE
:          MOVB  #BIT0,TMPF+1    ;INITIALIZE NPR EXTENDED ADDRESS REG. VARIABLE
:          JSR    PC,XMINIT     ;INITIALIZE THE MMU
:
:          ***** WRITE/READ/VERIFY NPRAIH,NPRAIL *****
:          JSR    R5,WRITEI     ;POINT NPR REGISTERS TO 0
:          NPRAIL
:          0

```

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TEST 7 -- NPR EXTENDED ADDRESS BIT TEST

```

5296 027134 004537 004054 JSR R5,WRITEI
5297 027140 000075 NPRAIH
5298 027142 000000 0
5299 027144 004537 003616 JSR R5,READ ;READ BACK THE ADDRESS & VERIFY IT
5300 027150 000074 NPRAIL
5301 027152 002434 TMP9
5302 027154 004537 003616 JSR R5,READ
5303 027160 000075 NPRAIH
5304 027162 002435 TMP9+1
5305 027164 023727 002434 000000 CMP TMP9,#000000 ;IS IT CORRECT?
5306 027172 001424 BEQ 2$ ;YES, PROCEED.
5307 027174 013737 002434 002256 MOV TMP9,BDATA ;NO, SETUP FOR & REPORT LOADING FAILURE
5308 027202 012737 000000 002254 MOV #000000,GDATA
5309 027210 012737 000007 002300 MOV #7,REGNUM ;IDENTIFY NPRAIH AS THE CULPRIT
5310 027216 105737 002434 TSTB TMP9 ;IS THAT REALLY TRUE?
5311 027222 001002 BNE 1$ ;MAYBE. BUT, NPRAIL IS DEFINITELY AT FAULT
5312 027224 005237 002300 INC REGNUM ; SO IDENTIFY IT AS SUCH
5313 027230 1$: GEDF EM26A,ERR14 ;REPORT THE FAILURE
5314 ; 'DEVICE FATAL' ERROR # 33
5315 027230 104455 TRAP C$ERDF
5316 027232 000041 .WORD 33
5317 027234 016204 .WORD EM26A
5318 027236 011002 .WORD ERR14
5319 027240 ESCAPE SUB ; AND EXIT THIS SUBTEST
5320 027240 104410 TRAP C$ESCAPE
5321 027242 000506 .WORD L10043-.
5322 ;*****
5323 ;***** MAIN SUBTEST #1 LOOP STARTS HERE *****
5324 ;*****
5325 ;*****
5326 ;***** COMPLEMENT OF NPRDRH:NPRDRL => TMP3 *****
5327 027244 004537 003616 2$: JSR R5,READ ;GET THE CURRENT CONTENTS OF THE NPR DATA REG'S
5328 027250 123000 NPRDRL
5329 027252 002414 TMP1
5330 027254 103003 BCC .+10 ;IF NO ERROR, PROCEED
5331 027256 ERROR ;ELSE, REPORT IT
5332 027256 104460 TRAP C$ERROR
5333 027260 ESCAPE SUB ; AND EXIT THIS TEST
5334 027260 104410 TRAP C$ESCAPE
5335 027262 000466 .WORD L10043-.
5336 027264 004537 003616 JSR R5,READ
5337 027270 123001 NPRDRH
5338 027272 002415 TMP1+1
5339 027274 103003 BCC .+10 ;IF NO ERROR, PROCEED
5340 027276 ERROR ;ELSE, REPORT IT
5341 027276 104460 TRAP C$ERROR
5342 027300 ESCAPE SUB ; AND EXIT THIS TEST
5343 027300 104410 TRAP C$ESCAPE
5344 027302 000466 .WORD L10043-.
5345 027304 013737 002414 002420 MOV TMP1,TMP3 ;USE CURRENT DATA & BUILD BACKGROUND PATTERN
5346 027312 005137 002420 COM TMP3 ;COMPLEMENT IT TO GENERATE A BACKGROUND PATTERN
5347 ;*****
5348 ;***** TMP3 => 1ST LOCATION OF EACH EXTENDED MEMORY BLOCK *****
5349 027316 012737 002000 002446 4$: MOV #BIT10,TMPE ;REFILL ALL TEST LOCATIONS STARTING HERE
5350 027324 004537 030700 JSR R5,XMWRIT ;WRITE BACKGROUND PATTERN GENERATED ABOVE
5351 027330 002446 TMPE ; POINTER TO 'PAR' FORMAT ADDRESS

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TEST 7 -- NPR EXTENDED ADDRESS BIT TEST

```

5352 027332 002420      TMP3      ; POINTER TO DATA (TO BE WRITTEN)
5353 027334 103003      BCC      .+10    ; IF NO ERROR, PROCEED
5354 027336      ERROR      ; ELSE, REPORT IT
5355 027336 104460      ESCAPE SUB      ; AND EXIT THIS TEST          TRAP  C$ERROR
5356 027340      ;                                     ;                               TRAP  C$ESCAPE
5357 027340 104410      ;                                     ;                               .WORD L10043-
5358 027342 000406      ;                                     ;                               .WORD
5359 027344 062737 002000 002446      ADD      #BIT10,TMPE ; INCREMENT THE PAGE ADDR. REG. VALUE
5360 027352 001404      BEQ      6$      ; DONE IF IT GOES TO ZERO
5361 027354 023737 002446 002436      CMP      TMPE,TMPA ; IS THE NEW VALUE WITHIN CURRENT MEMORY?
5362 027362 101760      BLOS     4$      ; YES, THE WRITE IT TOO.
5363      ; NO, DONE.
5364      ;***** WRITE/READ/VERIFY NPRAIX *****
5365 027364 004537 004042      6$: JSR      R5,WRITE ; SETUP NPR EXTENDED ADDR. REG BITS
5366 027370 000076      NPRAIX
5367 027372 002451      TMPF+1
5368 027374 103003      BCC      .+10    ; IF NO ERROR, PROCEED
5369 027376      ERROR      ; ELSE, REPORT IT
5370 027376 104460      ESCAPE SUB      ; AND EXIT THIS TEST          TRAP  C$ERROR
5371 027400      ;                                     ;                               TRAP  C$ESCAPE
5372 027400 104410      ;                                     ;                               .WORD L10043-
5373 027402 000346      ;                                     ;                               .WORD
5374 027404 004537 003616      JSR      R5,READ  ; READ IT BACK & VERIFY THAT IT'S CORRECT
5375 027410 000076      NPRAIX
5376 027412 002450      TMPF
5377 027414 103003      BCC      .+10    ; IF NO ERROR, PROCEED
5378 027416      ERROR      ; ELSE, REPORT IT
5379 027416 104460      ESCAPE SUB      ; AND EXIT THIS TEST          TRAP  C$ERROR
5380 027420      ;                                     ;                               TRAP  C$ESCAPE
5381 027420 104410      ;                                     ;                               .WORD L10043-
5382 027422 000326      ;                                     ;                               .WORD
5383 027424 123737 002450 002451      CMPB     TMPF,TMPF+1 ; DID IT LOAD CORRECTLY?
5384 027432 001417      BEQ      8$      ; YES, PROCEED
5385 027434 113737 002451 002254      MOV      TMPF+1,GDATA ; NO, SETUP FOR ERROR HANDLER
5386 027442 113737 002450 002256      MOV      TMPF,BDATA
5387 027450 012737 000006 002300      MOV      #6,REGNUM
5388 027456      GEDF     EM26A,ERR14 ; IDENTIFY NPRAIX AS FAILING REG.
5389      ; REPORT THE FAILURE
5390 027456 104455      ; 'DEVICE FATAL' ERROR # 34
5391 027460 000042      ;                                     ;                               TRAP  C$ERDF
5392 027462 016204      ;                                     ;                               .WORD 34
5393 027464 011002      ;                                     ;                               .WORD EM26A
5394 027466      ;                                     ;                               .WORD ERR14
5395 027466 104410      ESCAPE SUB      ; AND EXIT THIS SUBTEST          TRAP  C$ESCAPE
5396 027470 000260      ;                                     ;                               .WORD L10043-
5397
5398      ;***** GENERATE/WRITE TEST WORD INTO 'TMPC' (LSI-11) *****
5399 027472 013737 002420 002254      8$: MOV      TMP3,GDATA ; GENERATE A TEST DATA PATTERN FROM BACKGROUND
5400 027500 062737 125252 002254      ADD      #125252,GDATA ; PATTERN BY ADDING THIS TO IT.
5401
5402 027506 004537 030700      JSR      R5,XMWRIT ; LOAD UP THE TEST PATTERN
5403 027512 002442      TMP3      ; POINTER TO 'PAR' FORMAT ADDRESS
5404 027514 002254      GDATA     ; POINTER TO DATA (TO BE WRITTEN)
5405 027516 103003      BCC      .+10    ; IF NO ERROR, PROCEED
5406 027520      ERROR      ; ELSE, REPORT IT
5407 027520 104460      ;                                     ;                               TRAP  C$ERROR

```

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TEST 7 -- NPR EXTENDED ADDRESS BIT TEST

```

5408 027522                ESCAPE SUB                ; AND EXIT THIS SUBTEST
5409 027522 104410                TRAP C$ESCAPE
5410 027524 000224                .WORD L10043-.
5411 027526 112737 000004 002417  MOVB #NPRLD,TMP2+1 ;SETUP CONTROL VALUE TO DO NPR-IN
5412 027534 004537 004042  JSR R5,WRITE ;PERFORM THE "EXTENDED" DATA-IN NPR
5413 027540 123004                NPRCTL
5414 027542 002417                TMP2+1
5415 027544 103003                BCC .+10 ;IF NO ERROR, PROCEED
5416 027546                ERROR ;ELSE, REPORT IT
5417 027546 104460                TRAP C$ERROR
5418 027550                ESCAPE SUB                ; AND EXIT THIS SUBTEST
5419 027550 104410                TRAP C$ESCAPE
5420 027552 000176                .WORD L10043-.
5421
5422
5423 027554 004537 003616 ;***** CHECK THE NPR OPERATION (DATA/NPRCTL) *****
5424 027560 123004                JSR R5,READ ;CHECK THE NPR OPERATION
5425 027562 002416                NPRCTL
5426 027564 103003                TMP2
5427 027566                BCC .+10 ;IF NO ERROR, PROCEED
5428 027566 104460                ERROR ;ELSE, REPORT IT
5429 027570                ESCAPE SUB                ; AND EXIT THIS TEST
5430 027570 104410                TRAP C$ERROR
5431 027572 000156                TRAP C$ESCAPE
5432 027574 004537 003616                .WORD L10043-.
5433 027600 123001                JSR R5,READ ;GET THE DATA WE SHOULD HAVE JUST LOADED INTO
5434 027602 002257                NPRDRH ; THE NPR DATA REGISTERS FROM THE
5435 027604 103003                BDATA+1 ; EXTENDED MEMORY AREA
5436 027606                BCC .+10 ;IF NO ERROR, PROCEED
5437 027606 104460                ERROR ;ELSE, REPORT IT
5438 027610                ESCAPE SUB                ; AND EXIT THIS TEST
5439 027610 104410                TRAP C$ERROR
5440 027612 000136                TRAP C$ESCAPE
5441 027614 004537 003616                .WORD L10043-.
5442 027620 123000                JSR R5,READ
5443 027622 002256                NPRDRL
5444 027624 103003                BDATA
5445 027626                BCC .+10 ;IF NO ERROR, PROCEED
5446 027626 104460                ERROR ;ELSE, REPORT IT
5447 027630                ESCAPE SUB                ; AND EXIT THIS TEST
5448 027630 104410                TRAP C$ERROR
5449 027632 000116                TRAP C$ESCAPE
5450 027634 132737 000300 002416  BITB #300,TMP2 ;DID IT ABORT OR HANG?
5451 027642 001414                BEQ 14$ ;NO, GOOD. PROCEED WITH SUBTEST
5452 027644 100005                BPL 10$ ;YES, WHICH ONE?
5453 027646                GEDF EM27A,ERR12 ;ABORT, REPORT IT AS SUCH.
5454                                ; "DEVICE FATAL" ERROR # 35
5455 027646 104455                TRAP C$ERDF
5456 027650 000043                .WORD 35
5457 027652 016437                .WORD EM27A
5458 027654 007456                .WORD ERR12
5459 027656 000404                BR 12$
5460 027660                GEDF EM27B,ERR12 ;AND EXIT
5461                                ; HANG, REPORT IT AS SUCH.
5462 027660 104455                ; "DEVICE FATAL" ERROR # 36
5463 027662 000044                TRAP C$ERDF
                                .WORD 36

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TEST 7 -- NPR EXTENDED ADDRESS BIT TEST

```

5520 030100          1$:  GEDF  EM26A,ERR14      ;REPORT THE FAILURE
5521                                     ;      'DEVICE FATAL' ERROR # 38
5522 030100 104455                                     TRAP  CSERDF
5523 030102 000046                                     .WORD 38
5524 030104 016204                                     .WORD EM26A
5525 030106 011002                                     .WORD ERR14
5526 030110          ESCAPE SUB                      ;      AND EXIT THIS SUBTEST
5527 030110 104410                                     TRAP  C$ESCAPE
5528 030112 000442                                     .WORD L10044-.
5529 030114 012737 123456 002420          MOV  #123456,TMP3      ;USE THIS AS INITIAL BACKGROUND PATTERN
5530
5531 ;*****
5532 ;***** MAIN SUBTEST #2 LOOP STARTS HERE *****
5533 ;*****
5534
5535 ;***** TMP3 => 1ST LOCATION OF EACH EXTENDED MEMORY BLOCK *****
5536 030122 062737 021475 002420 2$:  ADD  #21475,TMP3      ;GENERATE THE PATTERN WE'LL USE THIS TIME
5537 030130 013737 002420 002414          MOV  TMP3,TMP1        ;PUT HERE FOR ERROR HANDLER
5538 030136 005137 002414          COM  TMP1
5539
5540 030142 012737 002000 002446 4$:  MOV  #BIT10,TMPE      ;REFILL ALL TEST LOCATIONS STARTING HERE
5541 030150 004537 030700          JSR  R5,XMWRIT        ;WRITE BACKGROUND PATTERN GENERATED ABOVE
5542 030154 002446          TMPE                 ; POINTER TO ADDRESS (IN 'PAR' FORMAT)
5543 030156 002420          TMP3                ; POINTER TO DATA (TO BE WRITTEN)
5544 030160 103003          BCC  .+10            ;IF NO ERROR, PROCEED
5545 030162          ERROR                                     ;ELSE, REPORT IT
5546 030162 104460          ESCAPE SUB          ;      AND EXIT THIS TEST          TRAP  CSERROR
5547 030164          TRAP                                     .WORD  C$ESCAPE
5548 030164 104410                                     .WORD  L10044-.
5549 030166 000366
5550 030170 062737 002000 002446          ADD  #BIT10,TMPE      ;INCREMENT THE PAGE ADDR. REG. VALUE
5551 030176 001404          BEQ  6$             ;DONE IF IT GOES TO ZERO
5552 030200 023737 002446 002436          CMP  TMPE,TMPA        ;IS THE NEW VALUE WITHIN CURRENT MEMORY?
5553 030206 101760          BLOS 4$             ;YES, THE WRITE IT TOO.
5554                                     ;NO, DONE.
5555
5556 ;***** WRITE/READ/VERIFY NPRAIX *****
5557 030210 004537 004042 6$:  JSR  R5,WRITE        ;SETUP NPR EXTENDED ADDR. REG BITS
5558 030214 000072          NPRAOX
5559 030216 002451          TMPF+1
5560 030220 103003          BCC  .+10            ;IF NO ERROR, PROCEED
5561 030222          ERROR                                     ;ELSE, REPORT IT
5562 030222 104460          ESCAPE SUB          ;      AND EXIT THIS TEST          TRAP  CSERROR
5563 030224          TRAP                                     .WORD  C$ESCAPE
5564 030224 104410                                     .WORD  L10044-.
5565 030226 000326
5566 030230 004537 003616          JSR  R5,READ          ;READ IT BACK & VERIFY THAT IT'S CORRECT
5567 030234 000072          NPRAOX
5568 030236 002450          TMPF
5569 030240 103003          BCC  .+10            ;IF NO ERROR, PROCEED
5570 030242          ERROR                                     ;ELSE, REPORT IT
5571 030242 104460          ESCAPE SUB          ;      AND EXIT THIS TEST          TRAP  CSERROR
5572 030244          TRAP                                     .WORD  C$ESCAPE
5573 030244 104410                                     .WORD  L10044-.
5574 030246 000306
5575 030250 123737 002450 002451          CMPB TMPF,TMPF+1     ;DID IT LOAD CORRECTLY?

```

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TEST 7 -- NPR EXTENDED ADDRESS BIT TEST

```

5576 030256 001417 BEQ 8$ ;YES, PROCEED
5577 030260 113737 002451 002254 MOVB TMPF+1,GDATA ;NO, SETUP FOR ERROR HANDLER
5578 030266 113737 002450 002256 MOVB TMPF,BDATA
5579 030274 012737 000003 002300 MOV #3,REGNUM ; IDENTIFY NPRAIX AS FAILING REG.
5580 030302 GEDF EM26A,ERR14 ;REPORT THE FAILURE
5581 ; 'DEVICE FATAL' ERROR # 39
5582 030302 104455 TRAP CSERDF
5583 030304 000047 .WORD 39
5584 030306 016204 .WORD EM26A
5585 030310 011002 .WORD ERR14
5586 030312 ESCAPE SUB ; AND EXIT THIS SUBTEST
5587 030312 104410 TRAP CSESCAPE
5588 030314 000240 .WORD L10044-.
5589
5590 ;***** WRITE(LSI-11) TEST LOCATION BACKGROUND PATTERN *****
5591 030316 004537 030700 8$: JSR R5,XMWRIT ;SETUP TEST LOCATION'S BACKGROUND PATTERN
5592 030322 002442 TMPC
5593 030324 002414 TMP1
5594 030326 013737 002420 002254 MOV TMP3,GDATA ;GENERATE A TEST DATA PATTERN FROM BACKGROUND
5595 030334 062737 052525 002254 ADD #52525,GDATA ;PATTERN BY ADDING THIS TO IT.
5596
5597 ;***** LOAD(DMV) TEST PATTERN *****
5598 030342 004537 004042 JSR R5,WRITE ;LOAD UP THE TEST PATTERN
5599 030346 123001 NPRDRH
5600 030350 002255 GDATA+1
5601 030352 004537 004042 JSR R5,WRITE
5602 030356 123000 NPRDRL
5603 030360 002254 GDATA
5604 030362 112737 000044 002417 MOVB #NPRDL,TMP2+1 ;SETUP CONTROL VALUE TO DO NPR-OUT
5605
5606 ;***** PERFORM/CHECK EXTENDED NPR OPERATION *****
5607 030370 004537 004042 JSR R5,WRITE ;PERFORM THE 'EXTENDED' DATA-OUT NPR
5608 030374 123004 NPRCTL
5609 030376 002417 TMP2+1
5610 030400 103003 BCC .+10 ;IF NO ERROR, PROCEED
5611 030402 ERROR ;ELSE, REPORT IT
5612 030402 104460 TRAP CSERROR
5613 030404 ESCAPE SUB ; AND EXIT THIS SUBTEST
5614 030404 104410 TRAP CSESCAPE
5615 030406 000146 .WORD L10044-.
5616 030410 004537 003616 JSR R5,READ ;CHECK THE NPR OPERATION
5617 030414 123004 NPRCTL
5618 030416 002416 TMP2
5619 030420 103003 BCC .+10 ;IF NO ERROR, PROCEED
5620 030422 ERROR ;ELSE, REPORT IT
5621 030422 104460 TRAP CSERROR
5622 030424 ESCAPE SUB ; AND EXIT THIS SUBTEST
5623 030424 104410 TRAP CSESCAPE
5624 030426 000126 .WORD L10044-.
5625 030430 132737 000300 002416 BITB #300,TMP2 ;DID IT ABORT OR HANG?
5626 030436 001414 BEQ 14$ ;NO, GOOD. PROCEED WITH SUBTEST
5627 030440 100005 BPL 10$ ;YES, WHICH ONE?
5628 030442 GEDF EM27A,ERR12 ;ABORT, REPORT IT AS SUCH.
5629 ; 'DEVICE FATAL' ERROR # 40
5630 030442 104455 TRAP CSERDF
5631 030444 000050 .WORD 40

```

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TEST 7 -- NPR EXTENDED ADDRESS BIT TEST

```

5632 030446 016437 .WORD EM27A
5633 030450 007456 .WORD ERR12
5634 030452 000404
5635 030454 10$: BR 12$ ;AND EXIT
GEDF EM27B,ERR12 ;HANG, REPORT IT AS SUCH.
; 'DEVICE FATAL' ERROR # 41
5636
5637 030454 104455 TRAP C$ERDF
5638 030456 000051 .WORD 41
5639 030460 016461 .WORD EM27B
5640 030462 007456 .WORD ERR12
5641 030464 12$: ESCAPE SUB ; AND EXIT SUBTEST
5642 030464 104410 TRAP C$ESCAPE
5643 030466 000066 .WORD L10044-
5644 030470 004537 030776 14$: JSR R5,XMREAD ;GET THE DATA WE SHOULD HAVE JUST LOADED INTO
TMP
BDATA
5645 030474 002442
5646 030476 002256
5647 030500 023737 002256 002254 CMP BDATA,GDATA ;DID WE READ THE TEST DATA USING THE NPR?
5648 030506 001406 BEQ 20$ ;YES, WELL THIS ONE WORKED.
5649 030510 GEDF EM27C,ERR12 ;NO! REPORT THE ERROR.
5650 ; 'DEVICE FATAL' ERROR # 42
5651 030510 104455 TRAP C$ERDF
5652 030512 000052 .WORD 42
5653 030514 016502 .WORD EM27C
5654 030516 007456 .WORD ERR12
5655 030520 ESCAPE SUB ; EXIT FROM SUBTEST AFTER PRINTING ERROR MSG.
5656 030520 104410 TRAP C$ESCAPE
5657 030522 000032 .WORD L10044-
5658 030524 062737 002000 002442 20$: ADD #BIT10,TMPC ;POINT TO NEXT PAGE ADDRESS REG. VALUE
5659 030532 001410 BEQ 63$ ;IF 0, WE'RE DONE
5660 030534 023737 002442 002436 CMP TMPC,TMPA ;IF GREATER THEN MAXIMUM VALUE,
5661 030542 101004 BHI 63$ ; WE'RE DONE TOO.
5662 030544 105237 002451 INCB TMPF+1 ;ELSE, INCREMENT NPR'S EXTENDED ADDR. REG.
5663 030550 000137 030122 JMP 2$ ;AND GO BACK TO DO THIS ADDRESS
5664
5665
5666 030554 63$: ENDSUB
5667 030554 L10044: TRAP C$ESUB
5668 030554 104403
5669 030556 ENDTST
5670 030556 L10042: TRAP C$SETST
5671 030556 104401
5672
5673 ;*****
5674 ; XMINIT -- SUBROUTINE TO INITIALIZE EXTENDED MEMORY (ALIAS: MEMORY MANAGEMENT
5675 ; UNIT) HARDWARE REGISTERS.
5676 ;*****
5677
5678
5679 030560 010046 XMINIT: MOV R0,-(SP) ;SAVE WORKING REGISTERS
5680 030562 010146 MOV R1,-(SP)
5681 030564 010346 MOV R3,-(SP)
5682 030566 013737 000004 031122 MOV @#4,XM4HOL ;SETUP @4 TRAP VALUE (JUST IN CASE)
5683
5684 030574 012700 077406 MOV #77406,R0 ;'PDR' INITIALIZATION VALUE
5685 ; 774 = FULL PAGE ACCESS
5686 ; 0 = UPWARD EXPANSION
5687 ; 6 = RESIDENT READ/WRITE

```

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TEST 7 -- NPR EXTENDED ADDRESS BIT TEST

5688	030600	012701	172300	MOV	#172300,R1	:ADDRESS OF KPDR0
5689	030604	012703	000010	MOV	#8,R3	:LOOP VALUE -- # OF PDR'S
5690	030610	010021		1\$: MOV	R0,(R1)+	:SETUP 1 PDR
5691	030612	077302		SOB	R3,1\$:IF ANOTHER PDR, DO IT TOO
5692						:ELSE, FALL THROUGH & INITIALIZE PAR'S
5693						
5694	030614	005000		CLR	R0	:INITIALIZATION VALUE FOR KPAR0
5695	030616	012701	172340	MOV	#172340,R1	:ADDRESS OF KPAR0
5696	030622	012703	000007	MOV	#7,R3	:LOOP VALUE -- ONLY FIRST 7 PAR'S DONE BY LOOP
5697	030626	010021		2\$: MOV	R0,(R1)+	:SETUP 1 PAR
5698	030630	062700	000200	ADD	#200,R0	:CALCULATE NEXT PAR'S INITIALIZATION VALUE
5699	030634	077304		SOB	R3,2\$:IF ANOTHEER PAR, DO IT TOO
5700	030636	012721	177600	MOV	#177600,(R1)+	:ELSE, SETUP KPAR7 FOR I/O PAGE ACCESSING
5701						
5702	030642			SETVEC	#250,#XMINTH,#7	:SETUP OUR OWN TRAP CATCHER FOR ABORT HANDLING
5703	030642	012746	000007			MOV #7,-(SP)
5704	030646	012746	031124			MOV #XMINTH,-(SP)
5705	030652	012746	000250			MOV #250,-(SP)
5706	030656	012746	000003			MOV #3,-(SP)
5707	030662	104437				TRAP C\$SVEC
5708	030664	062706	000010			ADD #10,SP
5709	030670	012603		MOV	(SP)+,R3	:RESTORE CALLER'S REGISTERS
5710	030672	012601		MOV	(SP)+,R1	
5711	030674	012600		MOV	(SP)+,R0	
5712	030676	000207		RTS	PC	:RETURN
5713						
5714						
5715						
5716						
5717						
5718						
5719						
5720						
5721						
5722						
5723						
5724						
5725	030700	010146		XMWRIT:	MOV R1,-(SP)	:SAVE REGISTER(S)
5726						
5727	030702	012701	172354	MOV	#172354,R1	:ADDRESS OF KPAR6
5728	030706	011146		MOV	(R1)-,(SP)	:SAVE CURRENT KPAR6 VALUE
5729	030710	013511		MOV	@(R5)+,(R1)	:SETUP 'PAR' FOR THIS WRITE
5730	030712	011137	002426	MOV	(R1),TMP6	:SAVE ADDRESS FOR ERROR MESSAGE
5731	030716	012737	000060 172516	MOV	#BIT4+BITS,@#172516	:ENABLE 22 BIT & I/O PAGE ADDRESSING IN SR3
5732	030724	000241		CLC		:CLEAR OUR ERROR FLAG
5733	030726	013737	000004 031122	MOV	@#4,XM4HOL	:* SETUP TRAP CATCHER @4 (BECAUSE OF MAPPING)
5734	030734	012737	031074 000004	MOV	#XM4INT,@#4	:*
5735	030742	052737	000001 177572	BIS	#1,@#177572	:ENABLE MEMORY MANAGEMENT
5736	030750	013537	140000	MOV	@(R5)+,@#140000	:WRITE ONE WORD IN THE SPECIFIED PAGE
5737	030754	042737	000001 177572	BIC	#1,@#177572	:TURN OFF MEMORY MANAGEMENT
5738	030762	013737	031122 000004	MOV	XM4HOL,@#4	:* RESTORE SUPERVISOR TRAP VECTOR @4
5739	030770	012611		MOV	(SP)+,(R1)	:RESTORE KPAR6
5740						
5741	030772	012601		MOV	(SP)+,R1	:RESTORE CALLER'S REGISTER(S)
5742	030774	000205		RTS	R5	:RETURN
5743						

```

*****
: XMWRIT -- SUBROUTINE TO WRITE ONE WORD INTO AN EXTENDED MEMORY LOCATION.
:
: CALLING SEQUENCE:
:
: JSR R5,XMWRIT
: <PRINTER TO HIGH ORDER BITS OF ADDRESS IN 'PAR' FORMAT>
: <POINTER TO DATA TO BE WRITTEN>
*****

```

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TEST 7 -- NPR EXTENDED ADDRESS BIT TEST

```

5744
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5756 030776 010146
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5758 031000 012701 172354
5759 031004 011146
5760 031006 013511
5761 031010 011137 002426
5762 031014 012737 000060 172516
5763 031022 000241
5764 031024 013737 000004 031122
5765 031032 012737 031074 000004
5766 031040 052737 000001 177572
5767 031046 013735 140000
5768 031052 042737 000001 177572
5769 031060 013737 031122 000004
5770 031066 012611
5771
5772 031070 012601
5773 031072 000205
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5779 031074 042737 000001 177572
5780 031102 013737 031122 000004
5781 031110 000240
5782 031112 000240
5783 031114 000240
5784 031116 000177 146662
5785 031122 000000
5786
5787
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5791 031124
5792 031124
5793 031124 010037 002372
5794 031130 010137 002374
5795 031134 010237 002376
5796 031140 010337 002400
5797 031144 010437 002402
5798 031150 010537 002404
5799 031154 016637 000002 002406
    
```

```

*****
: XREAD -- SUBROUTINE TO READ FROM AN EXTENDED MEMORY LOCATION.
:
: CALLING SEQUENCE:
:
: JSR     R5,XMREAD
: <PRINTER TO HIGH ORDER BITS OF ADDRESS IN 'PAR' FORMAT>
: <POINTER TO DATA RECEIVING LOCATION>
*****
    
```

```

XMREAD: MOV     R1,-(SP)          ;SAVE REGISTER(S)
        MOV     #172354,R1      ;ADDRESS OF KPAR6
        MOV     (R1),-(SP)      ;SAVE CURRENT KPAR6 VALUE
        MOV     @R5+,(R1)       ;SETUP 'PAR' FOR THIS READ
        MOV     (R1),TMP6       ;SAVE ADDRESS FOR ERROR MESSAGE
        MOV     #BIT4+BITS,@#172516 ;ENABLE 22 BIT & I/O PAGE ADDRESSING IN SR3
        CLC                    ;CLEAR OUR ERROR FLAG
        MOV     @#4,XM4HOL      ;* SETUP TRAP CATCHER @4 (BECAUSE OF MAPPING)
        MOV     #XM4INT,@#4
        BIS     #1,@#177572     ;ENABLE MEMORY MANAGEMENT
        MOV     @#140000,@(R5)+ ;READ ONE WORD IN THE SPECIFIED PAGE
        BIC     #1,@#177572     ;TURN OFF MEMORY MANAGEMENT
        MOV     XM4HOL,@#4
        MOV     (SP)+,(R1)      ;RESTORE SUPERVISOR TRAP VECTOR @4
        ;RESTORE KPAR6
        MOV     (SP)+,R1
        RTS     R5              ;RETURN
    
```

```

*****
: HANDLER FOR @LOC 4 TRAP PROCESSING (FOR TESTS 7 & 8)
*****
XM4INT: BIC     #1,@#177572     ;TURN OFF MEMORY MANAGEMENT
        MOV     XM4HOL,@#4
        ;* RESTORE SUPERVISOR TRAP VECTOR @4
        NOP
        ;*
        NOP
        ;*
        JMP     @4              ;NOW JUMP THRU IT !
XM4HOL: 0
    
```

```

*****
: INTERRUPT HANDLER FOR MEMORY MANAGEMENT ABORT PROCESSING
*****
BGNSRV  XMINTH
        MOV     R0,REG0          ;SAVE GENERAL REGISTERS
        MOV     R1,REG1
        MOV     R2,REG2
        MOV     R3,REG3
        MOV     R4,REG4
        MOV     R5,REG5
        MOV     2(SP),REG6
    
```


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TEST 8 -- SPECIAL MFG EXTENDED BIT TEST

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5849 031276
5850
5851 031276 032737 000002 002370
5852 031304 001002
5853 031306
5854 031306 104432
5855 031310 001242
5856
5857 031312 004737 003514
5858 031316 103003
5859 031320
5860 031320 104460
5861 031322
5862 031322 104432
5863 031324 001226
5864
5865
5866
5867 031326
5868 031326
5869 031326 104402
5870 031330 004737 030560
5871 031334 005004
5872
5873 031336 005002
5874 031340 016262 032554 032572 2\$:
5875 031346 005722
5876 031350 020227 000016
5877 031354 001371
5878
5879
5880
5881

```

.SBTTL TEST 8 -- SPECIAL MFG EXTENDED BIT TEST
:*****
:*
:*   TEST 8 -- SPECIAL MFG EXTENDED BIT TEST
:* THIS TEST WAS DESIGNED SPECIFICALLY TO ALLOW MANUFACTURING TO CHECK THE
:* NPRAIX/NPRAOX BITS WITHOUT A FULL 4 M. OF MEMORY.
:* IT WILL CHECK THE 12 DMV EXTENDED ADDRESS BITS (6:NPRAIX/6:NPRAOX) ON
:* A Q22 SYSTEM IF MEMORY IS PRESENT AT THE FOLLOWING PHYSICAL ADDRESSES:
:*
:*       17600000      17400000      17200000
:*       16600000      15600000      13600000
:*       7600000
:* FIRST SUBTEST :      TEST 'NPRAIX' EXTENDED ADDRESS BITS
:* SECOND SUBTEST :      TEST 'NPRAOX' EXTENDED ADDRESS BITS
:*
:*****
:
:       BGNTST
:
:                               T8::
:
:       BIT      #2,PT.CTL      ;IS THIS A MFG SPECIAL Q22 SYSTEM?
:       BNE      .+6            ;YES: GO START TEST
:       EXIT     TST            ; NO: SKIP THIS TEST
:
:                               TRAP      C$EXIT
:                               .WORD     L10046-
:
:       JSR      PC,MSTCLR      ;INIT DMV & ENTER M-LOOP
:       BCC      1$            ;IF NO ERROR, PROCEED WITH TESTING
:       ERROR
:       ELSE, REPORT ERROR
:
:                               TRAP      C$ERROR
:       EXIT     TST            ; & EXIT TEST
:
:                               TRAP      C$EXIT
:                               .WORD     L10046-
:
:=====
:== SUBTEST #1 : TEST THE NPR-IN EXTENDED ADDRESS BITS
:=====
1$:      BGNSUB
:
:                               T8.1:      TRAP      C$BSUB
:
:       JSR      PC,XMINIT      ;INITIALIZE THE MMU
:       CLR      R4             ;CLEAR INDEX
:
:       CLR      R2             ;SETUP EXTENDED MEM BACKGROUND PATTERN
:       MOV      XLOC0(R2),XVAL0(R2) ; (IN XVAL0 => XVAL6)
:       TST     (R2)+
:       CMP     R2,#14.
:       BNE     2$
:
:===== MAIN LOOP STARTS HERE =====
:* WRITE XVAL0, XVAL1, XVAL2, ... XVAL6 INTO THE SEVEN SPECIFIC

```

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TEST 8 -- SPECIAL MFG EXTENDED BIT TEST

```

5882
5883
5884
5885 031356 005002
5886 031360 016237 032572 002440 11$: CLR R2 ;CLEAR LOCAL INDEX
5887 031366 016237 032554 002436 MOV XVAL0(R2),TMPB ;SETUP DATA POINTER
5888 031374 006337 002436 MOV XLOC0(R2),TMPA ;SETUP/ADJUST PAR VALUE
5889 031400 006337 002436 ASL TMPA
5890 031404 004537 030700 ASL TMPA
5891 031410 002436 JSR R5,XMWRIT ;WRITE BACKGROUND PATTERN
5892 031412 002440 JMPA ; POINTER TO PAR VALUE
5893 031414 103003 JMPB ; POINTER TO DATA
5894 031416 BCC .+10 ;IF NO ERROR, PROCEED
5895 031416 104460 ERROR ;ELSE, REPORT ERROR
5896 031420 ESCAPE SUB ; & EXIT SUBTEST TRAP C$ERROR
5897 031420 104410 TRAP C$ESCAPE
5898 031422 000352 .WORD L10047-.
5899
5900 031424 005722 TST (R2)+ ;BUMP INDEX
5901 031426 020227 000016 CMP R2,#14. ;ALL 'XLOC' EXTENDED ADDRESSES WRITTEN?
5902 031432 001352 BNE 11$ ; NO: WRITE ANOTHER
5903
5904 ***** SETUP DMV'S NPR ADDRESSING REGISTERS *****
5905 ***** (WRITE/READ/VERIFY NPRAIH,NPRAIL,NPRAIX) *****
5906 031434 116437 032555 031470 3$: MOVB XLOC0+1(R4),4$ ;SETUP NPRAIX VALUE.
5907 031442 004537 004054 JSR R5,WRITEI ;POINT NPR REGISTERS TO EXTENDED ADDRESS
5908 031446 000074 NPRAIL
5909 031450 000000 0
5910 031452 004537 004054 JSR R5,WRITEI
5911 031456 000075 NPRAIH
5912 031460 000000 0
5913 031462 004537 004054 JSR R5,WRITEI
5914 031466 000076 NPRAIX
5915 031470 000000 00
5916 031472 004537 003616 4$: JSR R5,READ ;READ BACK THE ADDRESS
5917 031476 000074 NPRAIL
5918 031500 002256 BDATA
5919 031502 004537 003616 JSR R5,READ
5920 031506 000075 NPRAIH
5921 031510 002257 BDATA+1
5922 031512 004537 003616 JSR R5,READ
5923 031516 000076 NPRAIX
5924 031520 002434 TMP9
5925
5926 031522 005737 002256 TST BDATA ;***** NOW CHECK THEM *****
5927 031526 001413 BEQ 6$ ;NPRAIL,NPRAIH=0 ?
5928 031530 005037 002254 CLR GDATA ; YES: TRY CHECKING NPRAIX
5929 031534 012737 000007 002300 MOV #7,REGNUM ; NO: REPORT ERROR...
5930 031542 105737 002256 TSTB BDATA ;
5931 031546 001020 BNE 7$
5932 031550 005237 002300 INC REGNUM
5933 031554 000415 BR 7$
5934 031556 013737 031470 002254 6$: MOV 4$,GDATA ;SET UP NPRAIX EXPECTED
5935 031564 013737 002434 002256 MOV TMP9,BDATA ;SET UP NPRAIX READ...
5936 031572 023737 002254 002256 CMP GDATA,BDATA ;DOES NPRAIX=EXPECTED ?
5937 031600 001411 BEQ 9$ ; YES: CONTINUE

```

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TEST 8 -- SPECIAL MFG EXTENDED BIT TEST

```

5938 031602 012737 000006 002300      MOV #6,REGNUM      ; NO: REPORT ERROR
5939 031610      GEDF EM26A,ERR14  ;
5940                                     ; 'DEVICE FATAL' ERROR # 44
5941 031610 104455      TRAP C$ERDF
5942 031612 000054      .WORD 44
5943 031614 016204      .WORD EM26A
5944 031616 011002      .WORD ERR14
5945 031620      ESCAPE SUB
5946 031620 104410      TRAP C$ESCAPE
5947 031622 000152      .WORD L10047-.
5948
5949                                     ;***** SETUP/START/CHECK THE NPR OPERATION (DATA/NPRCTL) *****
5950 031624 112737 000004 002417 9$:  MOVB #NPRLD,TMP2+1 ;SETUP CONTROL VALUE TO DO NPR-IN
5951
5952 031632 004537 004042      JSR R5,WRITE      ;PERFORM THE 'EXTENDED' DATA-IN NPR
5953 031636 123004      NPRCTL
5954 031640 002417      TMP2+1
5955 031642 004537 003616      JSR R5,READ      ;CHECK THE NPR OPERATION
5956 031646 123004      NPRCTL
5957 031650 002416      TMP2
5958 031652 004537 003616      JSR R5,READ      ;GET THE DATA WE SHOULD HAVE JUST LOADED INTO
5959 031656 123001      NPRDRH           ; THE NPR DATA REGISTERS FROM THE
5960 031660 002257      BDATA+1         ; EXTENDED MEMORY AREA
5961 031662 004537 003616      JSR R5,READ
5962 031666 123000      NPRDRL
5963 031670 002256      BDATA
5964 031672 132737 000300 002416 BITB #300,TMP2   ;DID IT ABORT OR HANG?
5965 031700 001414      BEQ 14$         ; NO: GOOD. PROCEED WITH SUBTEST
5966 031702 100005      BPL 10$         ; YES: WHICH ONE?
5967 031704      GEDF EM27A,ERR12 ;ABORT, REPORT IT AS SUCH.
5968                                     ; 'DEVICE FATAL' ERROR # 45
5969 031704 104455      TRAP C$ERDF
5970 031706 000055      .WORD 45
5971 031710 016437      .WORD EM27A
5972 031712 007456      .WORD ERR12
5973 031714 000404
5974 031716      BR 12$        ;AND EXIT
5975      GEDF EM27B,ERR12 ;HANG, REPORT IT AS SUCH.
5976                                     ; 'DEVICE FATAL' ERROR # 46
5977 031716 104455      TRAP C$ERDF
5978 031720 000056      .WORD 46
5979 031722 016461      .WORD EM27B
5980 031724 007456      .WORD ERR12
5981 031726 104410      TRAP C$ESCAPE
5982 031730 000044      .WORD L10047-.
5983
5984                                     ;***** NOW CHECK DATA READ AGAINST EXPECTED VALUE *****
5985 031732 016437 032554 002254 14$: MOV XLOC0(R4),GDATA ;SET UP EXPECTED READ VALUE
5986
5987 031740 023737 002256 002254      CMP BDATA,GDATA  ;DID WE READ THE TEST DATA USING THE NPR?
5988 031746 001406      BEQ 15$
5989 031750      GEDF EM27C,ERR12 ; NO: REPORT THE ERROR.
5990                                     ; 'DEVICE FATAL' ERROR # 47
5991 031750 104455      TRAP C$ERDF
5992 031752 000057      .WORD 47
5993 031754 016502      .WORD EM27C

```

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TEST 8 -- SPECIAL MFG EXTENDED BIT TEST

```

5994 031756 007456
5995 031760
5996 031760 104410
5997 031762 000012
5998
5999 031764 005724
6000 031766 020427 000016
6001 031772 001220
6002
6003 031774
6004 031774
6005 031774 104403
6006
6007
6008
6009
6010
6011
6012 031776
6013 031776
6014 031776 104402
6015 032000 004737 030560
6016 032004 005004
6017
6018
6019 032006 005002
6020 032010 012762 125252 032572
6021 032016 005722
6022 032020 020227 000016
6023 032024 001371
6024
6025
6026
6027
6028
6029 032026 005002
6030 032030 016237 032572 002440
6031 032036 016237 032554 002436
6032 032044 006337 002436
6033 032050 006337 002436
6034 032054 004537 030700
6035 032060 002436
6036 032062 002440
6037 032064 103003
6038 032066
6039 032066 104460
6040 032070
6041 032070 104410
6042 032072 000456
6043
6044 032074 005722
6045 032076 020227 000016
6046 032102 001352
6047
6048
6049

```

```

          ESCAPE SUB          ; AND EXIT FROM SUBTEST          .WORD  ERR12
                                           TRAP  C$ESCAPE
                                           .WORD  L10047-.
15$:   TST      (R4)+          ; YES: BUMP INDEX
        CMP      R4,#14.      ; ARE WE DONE W/ALL EXTENDED LOCATIONS
        BNE      3$           ; NO: DO NEXT EXTENDED LOCATION
63$:   ENDSUB                  ; YES: END SUBROUTINE
                                           L10047: TRAP  C$ESUB

```

```

=====
==  SUBTEST #2 : TEST THE NPR-OUT EXTENDED ADDRESS BITS
=====
          BGNSUB                  ;TEST THE NPR-OUT USING EXTENDED ADDR. BITS
                                           T8.2: TRAP  C$BSUB
          JSR     PC,XMINIT       ;INITIALIZE THE MMU
          CLR     R4              ;CLEAR INDEX

```

```

===== MAIN LOOP STARTS HERE =====
T8LP:   CLR     R2                ;SETUP EXTENDED MEM BACKGROUND PATTERN
3$:     MOV     #125252,XVAL0(R2) ;(125252 IN XVAL0 => XVAL6)
        TST     (R2)+           ; THIS IS DONE FOR ERROR REPORTING
        CMP     R2,#14.        ; PURPOSES.
        BNE     3$

```

```

;*      WRITE (USING MMU) XVAL0, XVAL1, XVAL2, ... XVAL6 (IE: 125252) INTO
;*      THE SEVEN SPECIFIC EXTENDED ADDRESSES SPECIFIED BY XLOC0 THRU XLOC6
;*      (XLOC'S SPECIFY THE UPPER TWO BYTES OF THE 3 BYTE EXTENDED ADDR)

```

```

11$:   CLR     R2                ;CLEAR LOCAL INDEX
        MOV     XVAL0(R2),TMPB   ;SETUP DATA POINTER
        MOV     XLOC0(R2),TMPA  ;SETUP/ADJUST PAR VALUE
        ASL     TMPA
        ASL     TMPA
        JSR     R5,XMWRT        ;WRITE BACKGROUND PATTERN INTO EXTENDED MEMORY
        TMPA    ; POINTER TO PAR VALUE
        TMPB    ; POINTER TO DATA (@DATA = 125252)
        BCC     .+10           ;IF NO ERROR, PROCEED
        ERROR   ;ELSE, REPORT ERROR
                                           TRAP  C$ERROR
          ESCAPE SUB          ; & EXIT SUBTEST
                                           TRAP  C$ESCAPE
                                           .WORD  L10050-.
        TST     (R2)+          ;BUMP INDEX
        CMP     R2,#14.        ;ALL 'XLOC' EXTENDED ADDRESSES WRITTEN?
        BNE     11$           ; NO: WRITE ANOTHER

```

```

;* WE NOW CHANGE ONE LOCATION IN THE 'XVAL' BACKGROUND TABLE.
;* AFTER OUR DMV NPR OUT, THIS TABLE WILL REPRESENT THE

```

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TEST 8 -- SPECIAL MFG EXTENDED BIT TEST

```

6050
6051 032104 012764 052525 032572 ;* EXPECTED VALUES OF OUR EXTENDED MEMORY.
6052          MOV          #052525,XVAL0(R4)           ;SETUP EXPECTED PATTERN AFTER NPR-OUT
6053
6054
6055          ;***** SETUP DMV NPR ADDRESSING REGISTERS *****
6056 032112 116437 032555 032146          ;***** (WRITE/READ/VERIFY NPRAOH,NPRAOL,NPRAOX) *****
6057 032120 004537 004054          MOVB          XLOC0+1(R4),5$          ;INIT NPRAOX VALUE
6058 032124 000070          JSR          R5,WRITEI          ;POINT NPR REGISTERS TO EXTENDED ADDRESS
6059 032126 000000          NPRAOL
6060 032130 004537 004054          0
6061 032134 000071          JSR          R5,WRITEI
6062 032136 000000          NPRAOH
6063 032140 004537 004054          0
6064 032144 000072          JSR          R5,WRITEI
6065 032146 000000          NPRAOX
6066 032150 004537 003616          00          5$:          ;READ BACK THE ADDRESS
6067 032154 000070          JSR          R5,READ
6068 032156 002256          NPRAOL
6069 032160 004537 003616          BDATA
6070 032164 000071          JSR          R5,READ
6071 032166 002257          NPRAOH
6072 032170 004537 003616          BDATA+1
6073 032174 000072          JSR          R5,READ
6074 032176 002434          NPRAOX
6075          TMP9
6076 032200 005737 002256          TST          BDATA          ;***** NOW CHECK THEM *****
6077 032204 001413          BEQ          6$          ;NPRAOL,NPRAOH=0 ?
6078 032206 005037 002254          CLR          GDATA          ; YES: TRY CHECKING NPRAOX
6079 032212 012737 000004 002300          MOV          #4,REGNUM          ; NO: REPORT ERROR...
6080 032220 105737 002256          TSTB         BDATA
6081 032224 001020          BNE          7$
6082 032226 005237 002300          INC          REGNUM
6083 032232 000415          BR          7$
6084 032234 013737 032146 002254          6$:          MOV          5$,GDATA          ;SET UP NPRAOX EXPECTED
6085 032242 013737 002434 002256          MOV          TMP9,BDATA          ;SET UP NPRAOX READ...
6086 032250 023737 002254 002256          CMP          GDATA,BDATA          ;DOES NPRAOX=EXPECTED ?
6087 032256 001411          BEQ          9$          ; YES: CONTINUE
6088 032260 012737 000003 002300          MOV          #3,REGNUM          ; NO: REPORT ERROR
6089 032266          GEDF         EM26A,ERR14
6090
6091 032266 104455          ;          'DEVICE FATAL' ERROR # 48
6092 032270 000060          TRAP          C$ERDF
6093 032272 016204          .WORD        48
6094 032274 011002          .WORD        EM26A
6095 032276          .WORD        ERR14
6096 032276 104410          ESCAPE SUB
6097 032300 000250          TRAP          C$ESCAPE
6098          .WORD        L10050-.
6099
6100 032302 012737 052525 002254          9$:          ;***** SETUP/START/CHECK THE NPR OPERATION (DATA/NPRCTL) *****
6101          MOV          #052525,GDATA          ;DATA DMV WILL NPR TO TOP LOC
6102
6103 032310 004537 004042          ;***** LOAD (DMV) TEST PATTERN *****
6104 032314 123001          JSR          R5,WRITE          ;LOAD UP THE TEST PATTERN TO BE
6105 032316 002255          NPRDRH         ;WRITTEN INTO EXTENDED MEMORY BY
6105          GDATA+1          ;THE DMV

```

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TEST 8 -- SPECIAL MFG EXTENDED BIT TEST

```

6106 032320 004537 004042      JSR      R5,WRITE
6107 032324 123000              NPRDRL
6108 032326 002254              GDATA
6109
6110                                ;***** PERFORM/CHECK NPR OPERATION (BUT NOT DATA) *****
6111 032330 112737 000044 002417  MOVB     #NPRDL,TMP2+1 ;SETUP CONTROL VALUE TO DO NPR-OUT
6112
6113 032336 004537 004042      JSR      R5,WRITE      ;PERFORM THE 'EXTENDED' DATA-OUT NPR
6114 032342 123004              NPRCTL
6115 032344 002417              TMP2+1
6116 032346 004537 003616      JSR      R5,READ      ;CHECK THE NPR OPERATION
6117 032352 123004              NPRCTL
6118 032354 002416              TMP2
6119 032356 132737 000300 002416  BITB     #300,TMP2    ;DID IT ABORT OR HANG?
6120 032364 001414              BEQ      14$          ;NO, GOOD. PROCEED WITH SUBTEST
6121 032366 100005              BPL      10$          ;YES, WHICH ONE?
6122 032370                    GEDF     EM27A,ERR12 ;ABORT, REPORT IT AS SUCH.
6123                                ;      'DEVICE FATAL' ERROR # 49
6124 032370 104455                                TRAP    CSERDF
6125 032372 000061                                .WORD  49
6126 032374 016437                                .WORD  EM27A
6127 032376 007456                                .WORD  ERR12
6128 032400 000404
6129 032402                    BR       12$          ;AND EXIT
6130                                GEDF     EM27B,ERR12 ;HANG, REPORT IT AS SUCH.
6131                                ;      'DEVICE FATAL' ERROR # 50
6132 032402 104455                                TRAP    CSERDF
6133 032404 000062                                .WORD  50
6134 032406 016461                                .WORD  EM27B
6135 032412 007456                                .WORD  ERR12
6136 032412 104410
6137 032414 000134                    ESCAPE  SUB      ; AND EXIT SUBTEST
6138                                TRAP    CSESCAPE
6139                                .WORD  L10050-
6140                                ;***** READ EXTENDED MEM INTO LOCAL RAM (RXVAL0-6) *****
6140 032416 005002                    14$:    CLR      R2          ;CLEAR LOCAL INDEX
6141 032420 016237 032610 002440  15$:    MOV      RXVAL0(R2),TMPB ;SETUP DATA POINTER
6142 032426 016237 032554 002436  MOV      XLOC0(R2),TMPA  ;SETUP/ADJUST PAR VALUE
6143 032434 006337 002436      ASL     TMPA
6144 032440 006337 002436      ASL     TMPA
6145 032444 004537 030776      JSR     R5,XMREAD     ;READ EXTENDED MEM BACKGROUND PATTERN
6146 032450 002436      TMPA     ; POINTER TO PAR VALUE
6147 032452 002440      TMPB     ; POINTER TO DATA STORAGE
6148 032454 103003      BCC     .+10         ;IF NO ERROR, PROCEED
6149 032456      ERROR  ;ELSE, REPORT IT
6150 032456 104460                                TRAP    CSERROR
6151 032460      ESCAPE  TST      ;      AND EXIT ENTIRE TEST!
6152 032460 104410                                TRAP    CSESCAPE
6153 032462 000070                                .WORD  L10046-
6154 032464 062702 000002      ADD     #2,R2        ;BUMP INDEX
6155 032470 020227 000014      CMP     R2,#12.     ;ALL 'XLOC' EXTENDED ADDRESSES READ?
6156 032474 001351      BNE     15$         ; NO: READ ANOTHER
6157
6158                                ;***** NOW CHECK EXPECTED VS. ACTUAL EXT. MEM VALUES *****
6159 032476 005002                    16$:    CLR      R2
6160 032500 026262 032610 032572  CMP      RXVAL0(R2),XVAL0(R2)
6161 032506 001406      BEQ     17$

```

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TEST 8 -- SPECIAL MFG EXTENDED BIT TEST

6162	032510		GEDF	EM60N,ERR60			
6163							
6164	032510	104455					
6165	032512	000063				TRAP	C\$ERDF
6166	032514	021711				.WORD	51
6167	032516	011720				.WORD	EM60N
6168	032520		ESCAPE	SUB		.WORD	ERR60
6169	032520	104410				TRAP	C\$ESCAPE
6170	032522	000026				.WORD	L10050-
6171	032524	005722					
6172	032526	020227	000016	17\$:	TST (R2)+		
6173	032532	001362			CMP R2,#14.		
6174					BNE 16\$		
6175	032534	005724		20\$:	TST (R4)+		
6176	032536	020427	000016		CMP R4,#14.		
6177	032542	001402			BEQ 63\$		
6178	032544	000137	032006		JMP T8LP		
6179	032550			63\$:	ENDSUB		
6180	032550						
6181	032550	104403					L10050:
6182	032552			MFEND:	ENDTST		TRAP
6183	032552						C\$ESUB
6184	032552	104401					L10046:
6185							TRAP
6186							C\$ETST
6187	032554	037400		XLOC0:	37400		:ADDRESS
6188	032556	037000		XLOC1:	37000		17600000
6189	032560	036400		XLOC2:	36400		POINTER
6190	032562	035400		XLOC3:	35400		:ADDRESS
6191	032564	033400		XLOC4:	33400		17400000
6192	032566	027400		XLOC5:	27400		POINTER
6193	032570	017400		XLOC6:	17400		:ADDRESS
6194							17200000
6195	032572	000000		XVAL0:	0		POINTER
6196	032574	000000		XVAL1:	0		:ADDRESS
6197	032576	000000		XVAL2:	0		16600000
6198	032600	000000		XVAL3:	0		POINTER
6199	032602	000000		XVAL4:	0		:ADDRESS
6200	032604	000000		XVAL5:	0		15600000
6201	032606	000000		XVAL6:	0		POINTER
6202							:ADDRESS
6203	032610	000000		RXVAL0:	0		13600000
6204	032612	000000		RXVAL1:	0		POINTER
6205	032614	000000		RXVAL2:	0		:ADDRESS
6206	032616	000000		RXVAL3:	0		07600000
6207	032620	000000		RXVAL4:	0		POINTER
6208	032622	000000		RXVAL5:	0		
6209	032624	000000		RXVAL6:	0		

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TEST 9 -- Q-BUS INTERRUPT 'A' & 'B' SELECTION

.SBTTL TEST 9 -- Q-BUS INTERRUPT 'A' & 'B' SELECTION

TEST 9 -- Q-BUS INTERRUPT 'A' & 'B' SELECTION

THIS TEST CONTAINS SUBTESTS IN WHICH A SEQUENCE OF STEPS IS PERFORMED. IN GENERAL, EACH SUBTEST PERFORMS THE FOLLOWING:

1. INTERRUPTS ARE DISABLED FOR BOTH 'A' & 'B'
2. THE INTERRUPT REQUEST REGISTER IS WRITTEN INTO
3. A TEST IS MADE TO BE SURE THAT NEITHER INTERRUPT OCCURS
4. BOTH INTERRUPTS ARE ENABLES
5. A TEST IS MADE TO BE SURE THAT IF AN INTERRUPT IS EXPECTED, IT IS RECEIVED AND IF IT ISN'T EXPECTED IT DOESN'T HAPPEN.

ALL TESTING IS DONE HERE WITH THE PROCESSOR'S PRIORITY SET AT 0.

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 6236 032626 004737 003514
 6237 032632 103003
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 6239 032634 104460
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 6241 032636 104432
 6242 032640 001272
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 6261 032642
 6262 032642
 6263 032642 104402
 6264 032644 012737 177777 002274
 6265 032652 005037 002272

BGNTST

```

JSR    PC,MSTCLR      :ISSUE MASTER CLEAR & ENTER MAINT. LOOP
BCC    1$             :IF NO ERROR, CONTINUE
ERROR   :ELSE, REPORT IT AND
EXIT    TST            :EXIT THIS TEST
TRAP    C$ERROR
TRAP    C$EXIT
        .WORD        L10051-
```

TEST FOR NO INTERRUPT WHEN ENABLED

1. DISABLE BOTH INTERRUPTS
2. ASSERT BOTH REQUEST BITS TO 1
3. CHECK FOR NO 'A' INTERRUPT
4. CHECK FOR NO 'B' INTERRUPT
5. ENABLE BOTH INTERRUPTS
6. CHECK FOR NO 'A' INTERRUPT
7. CHECK FOR NO 'B' INTERRUPT

1\$: BGNSUB

```

MOV    #-1,INTWCH    :TELL BOTH HANDLERS TO 'WATCH' FOR INTERRUPTS
CLR    INTFLG        :CLEAR BOTH INTERRUPT FLAGS
TRAP    C$BSUB
```

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TEST 9 -- Q-BUS INTERRUPT 'A' & 'B' SELECTION

```

6266 032656 112777 000000 147434      MOVB   #0, @BSELO      :DISABLE BOTH INTERRUPTS
6267 032664 004537 004054              JSR    R5, WRITEI     :LOAD THE INTERRUPT CONTROL REGISTER WITH
6268 032670 123005                      IRQREG                : BOTH BITS SET. THIS SHOULD NOT CAUSE
6269 032672 000006                      !RQA! IRQB           : AN INTERRUPT AT EITHER LEVEL
6270 032674 103003                      BCC    30$           :IF AN ERROR OCCURED,
6271 032676                      ERROR                :REPORT IT &
6272 032676 104460                                ESCAPE  TST           : QUIT
6273 032700                                TRAP   CSERROR
6274 032700 104410                                TRAP   C$ESCAPE
6275 032702 001230                                .WORD L10051-.
6276
6277 032704 105737 002272      30$: TSTB   INTFLG      :DID AN 'A' INTERRUPT OCCUR?
6278 032710 001407                BEQ    5$             :NO, GOOD. GO TEST THE 'B' INTERRUPT
6279 032712 012737 000001 002254      MOV    #1, GDATA     :YES, TELL ERROR HANDLER WHAT WE HAD DONE
6280 032720                GEDF   EM34, ERR1   :REPORT THE UNEXPECTED INTERRUPT
6281                                : 'DEVICE FATAL' ERROR # 52
6282 032720 104455                                TRAP   CSERDF
6283 032722 000064                                .WORD  52
6284 032724 016526                                .WORD  EM34
6285 032726 006212                                .WORD  ERR1
6286
6287 032730 105737 002273      5$:  TSTB   INTFLG+1  :DID A 'B' INTERRUPT OCCUR?
6288 032734 001407                BEQ    6$             :NO, GOOD. NOW TRY LETTING ONE THROUGH
6289 032736 012737 000002 002254      MOV    #2, GDATA     :YES, TELL ERROR HANDLER WHAT WE HAD DONE
6290 032744                GEDF   EM34B, ERR1 :REPORT THE UNEXPECTED INTERRUPT
6291                                : 'DEVICE FATAL' ERROR # 53
6292 032744 104455                                TRAP   CSERDF
6293 032746 000065                                .WORD  53
6294 032750 016540                                .WORD  EM34B
6295 032752 006212                                .WORD  ERR1
6296
6297 032754 005037 002272      6$:  CLR    INTFLG      :CLEAR BOTH INTERRUPT FLAGS
6298 032760 112777 000021 147332      MOVB   #IENBA! IENBB, @BSELO :ENABLE BOTH INTERRUPTS
6299 032766 012703 001000      MOV    #1000, R3     :GIVE THE INTERRUPT SOME TIME TO HAPPEN
6300 032772 077301                SOB    R3             : BY SITTING HERE FOR A WHILE
6301 032774 105737 002272      TSTB   INTFLG      :DID AN 'A' INTERRUPT OCCUR?
6302 033000 001407                BEQ    7$             :NO, GOOD. GO TEST THE 'B' INTERRUPT
6303 033002 012737 000003 002254      MOV    #3, GDATA     :YES, TELL ERROR HANDLER WHAT WE HAD DONE
6304 033010                GEDF   EM34, ERR1   :REPORT THE UNEXPECTED INTERRUPT
6305                                : 'DEVICE FATAL' ERROR # 54
6306 033010 104455                                TRAP   CSERDF
6307 033012 000066                                .WORD  54
6308 033014 016526                                .WORD  EM34
6309 033016 006212                                .WORD  ERR1
6310
6311 033020 105737 002273      7$:  TSTB   INTFLG+1  :DID A 'B' INTERRUPT OCCUR?
6312 033024 001407                BEQ    8$             :NO, GOOD. NOW TRY LETTING ONE THROUGH
6313 033026 012737 000004 002254      MOV    #4, GDATA     :YES, TELL ERROR HANDLER WHAT WE HAD DONE
6314 033034                GEDF   EM34B, ERR1 :REPORT THE UNEXPECTED INTERRUPT
6315                                : 'DEVICE FATAL' ERROR # 55
6316 033034 104455                                TRAP   CSERDF
6317 033036 000067                                .WORD  55
6318 033040 016540                                .WORD  EM34B
6319 033042 006212                                .WORD  ERR1
6320
6321 033044      8$:  ENDSUB

```

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TEST 9 -- Q-BUS INTERRUPT 'A' & 'B' SELECTION

```

6322 033044
6323 033044 104403
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6343 033046
6344 033046
6345 033046 104402
6346 033050 012737 177777 002274
6347 033056 005037 002272
6348 033062 112777 000000 147230
6349 033070 004537 004054
6350 033074 123005
6351 033076 000002
6352 033100 103003
6353 033102
6354 033102 104460
6355 033104
6356 033104 104410
6357 033106 001024
6358
6359 033110 105737 002272 31$:
6360 033114 001407
6361 033116 012737 000005 002254
6362 033124
6363
6364 033124 104455
6365 033126 000070
6366 033130 016526
6367 033132 006212
6368
6369 033134 105737 002273 10$:
6370 033140 001407
6371 033142 012737 000006 002254
6372 033150
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6374 033150 104455
6375 033152 000071
6376 033154 016540
6377 033156 006212

```

L10052: TRAP C\$ESUB

TEST FOR 'A' INTERRUPT WHEN ENABLED

1. DISABLE BOTH INTERRUPTS
2. ASSERT 'B' REQUEST BIT TO 1:
DISABLING 'B' & FORCING 'A'
3. CHECK FOR NO 'A' INTERRUPT
4. CHECK FOR NO 'B' INTERRUPT
5. ENABLE BOTH INTERRUPTS
6. CHECK FOR 'A' INTERRUPT
7. CHECK FOR NO 'B' INTERRUPT

BGNSUB

```

MOV #-1,INTWCH ;TELL BOTH HANDLERS TO 'WATCH' FOR INTERRUPTS
CLR INTFLG ;CLEAR BOTH INTERRUPT FLAGS
MOVB #0,ABSELO ;DISABLE INTERRUPTS AGAIN
JSR R5,WRITEI ;CAUSE AN INTERRUPT PENDING ON 'A'
; BUT NOT ON 'B'
BCC 31$ ;IF AN ERROR OCCURED,
ERROR ;REPORT IT &
ESCAPE TST ; QUIT TRAP C$ERROR
; .WORD C$ESCAPE
; .WORD L10051-.

```

T9.2: TRAP C\$SUB

```

31$: TSTB INTFLG ;DID AN 'A' INTERRUPT OCCUR?
BEQ 10$ ;NO, GOOD. GO TEST THE 'B' INTERRUPT
MOV #5,GDATA ;YES, TELL ERROR HANDLER WHAT WE HAD DONE
GEDF EM34,ERR1 ;REPORT THE UNEXPECTED INTERRUPT
; 'DEVICE FATAL' ERROR # 56
; TRAP C$ERDF
; .WORD 56
; .WORD EM34
; .WORD ERR1

```

```

10$: TSTB INTFLG+1 ;DID A 'B' INTERRUPT OCCUR?
BEQ 11$ ;NO, GOOD. NOW TRY LETTING ONE THROUGH
MOV #6,GDATA ;YES, TELL ERROR HANDLER WHAT WE HAD DONE
GEDF EM34B,ERR1 ;REPORT THE UNEXPECTED INTERRUPT
; 'DEVICE FATAL' ERROR # 57
; TRAP C$ERDF
; .WORD 57
; .WORD EM34B
; .WORD ERR1

```

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TEST 9 -- Q-BUS INTERRUPT 'A' & 'B' SELECTION

```

6378
6379 033160 005037 002272 11$: CLR INTFLG ;CLEAR BOTH INTERRUPT FLAGS
6380 033164 112777 000021 147126 MOVB #IENBA!IENBB, @BSEL0 ;ENABLE BOTH INTERRUPTS
6381 033172 012703 001000 MOV #1000,R3 ;GIVE THE INTERRUPT SOME TIME TO HAPPEN
6382 033176 077301 SOB R3 ; BY SITTING HERE FOR A WHILE
6383 033200 105737 002272 TSTB INTFLG ;DID AN 'A' INTERRUPT OCCUR?
6384 033204 001007 BNE 12$ ;YES, GOOD. GO TEST THE 'B' INTERRUPT
6385 033206 012737 000007 002254 MOV #7,GDATA ;NO, TELL ERROR HANDLER WHAT WE HAD DONE
6386 033214 GEDF EM35,ERR1 ;REPORT MISSING INTERRUPT ON 'ENABLE'
6387 ; 'DEVICE FATAL' ERROR # 58
6388 033214 104455 TRAP CSERDF
6389 033216 000072 .WORD 58
6390 033220 016552 .WORD EM35
6391 033222 006212 .WORD ERR1
6392
6393 033224 105737 002273 12$: TSTB INTFLG+1 ;DID A 'B' INTERRUPT OCCUR?
6394 033230 001407 BEQ 13$ ;NO, GOOD. NOW TRY HITTING THE 'B' INTERRUPT
6395 033232 012737 000010 002254 MOV #8,GDATA ;YES, TELL ERROR HANDLER WHAT WE HAD DONE
6396 033240 GEDF EM34B,ERR1 ;REPORT THE UNEXPECTED INTERRUPT
6397 ; 'DEVICE FATAL' ERROR # 59
6398 033240 104455 TRAP CSERDF
6399 033242 000073 .WORD 59
6400 033244 016540 .WORD EM34B
6401 033246 006212 .WORD ERR1
6402
6403 033250 13$: ENDSUB
6404 033250
6405 033250 104403 L10053: TRAP CSERDF
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6425 033252
6426 033252
6427 033252 104402 T9.3: TRAP CSBSUB
6428 033254 012737 177777 002274 MOV #-1,INTWCH ;TELL BOTH HANDLERS TO 'WATCH' FOR INTERRUPTS
6429 033262 005037 002272 CLR INTFLG ;CLEAR BOTH INTERRUPT FLAGS
6430 033266 112777 000000 147024 MOVB #0,@BSEL0 ;DISABLE INTERRUPTS AGAIN
6431 033274 004537 004054 JSR R5,WRITEI ;CAUSE AN INTERRUPT PENDING ON 'B'
6432 033300 123005 IRQREG ; BUT NOT ON 'A'
6433 033302 000004 IRQA

```

TEST FOR 'B' INTERRUPT WHEN ENABLED

1. DISABLE BOTH INTERRUPTS
2. ASSERT 'A' REQUEST BIT TO 1:
DISABLING 'A' & FORCING 'B'
3. CHECK FOR NO 'A' INTERRUPT
4. CHECK FOR NO 'B' INTERRUPT
5. ENABLE BOTH INTERRUPTS
6. CHECK FOR NO 'A' INTERRUPT
7. CHECK FOR 'B' INTERRUPT

BGNSUB

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TEST 9 -- Q-BUS INTERRUPT 'A' & 'B' SELECTION

```

6434 033304 103003          BCC      32$          ;IF AN ERROR OCCURED,
6435 033306                ERROR          ;REPORT IT &
6436 033306 104460                ESCAPE  TST          TRAP      C$ERROR
6437 033310                TST          ; EXIT          TRAP      C$ESCAPE
6438 033310 104410                TST          ;          .WORD    L10051-
6439 033312 000620                GEDF          ;
6440
6441 033314 105737 002272    32$:  TSTB      INTFLG      ;DID AN 'A' INTERRUPT OCCUR?
6442 033320 001407          BEQ      14$          ;NO, GOOD. GO TEST THE 'B' INTERRUPT
6443 033322 012737 000011 002254    MOV      #9,,GDATA    ;YES, TELL ERROR HANDLER WHAT WE HAD DONE
6444 033330                GEDF      EM34,ERR1   ;REPORT THE UNEXPECTED INTERRUPT
6445                                ;      'DEVICE FATAL' ERROR # 60
6446 033330 104455                TRAP      C$ERDF
6447 033332 000074                .WORD    60
6448 033334 016526                .WORD    EM34
6449 033336 006212                .WORD    ERR1
6450
6451 033340 105737 002273    14$:  TSTB      INTFLG+1  ;DID A 'B' INTERRUPT OCCUR?
6452 033344 001407          BEQ      15$          ;NO, GOOD. NOW TRY LETTING ONE THROUGH
6453 033346 012737 000012 002254    MOV      #10,,GDATA   ;YES, TELL ERROR HANDLER WHAT WE HAD DONE
6454 033354                GEDF      EM34B,ERR1 ;REPORT THE UNEXPECTED INTERRUPT
6455                                ;      'DEVICE FATAL' ERROR # 61
6456 033354 104455                TRAP      C$ERDF
6457 033356 000075                .WORD    61
6458 033360 016540                .WORD    EM34B
6459 033362 006212                .WORD    ERR1
6460
6461 033364 005037 002272    15$:  CLR        INTFLG      ;CLEAR BOTH INTERRUPT FLAGS
6462 033370 112777 000021 146722    MOV      #IENBA!IENBB,ABSELO ;ENABLE BOTH INTERRUPTS
6463 033376 012703 001000    MOV      #1000,R3     ;GIVE THE INTERRUPT SOME TIME TO HAPPEN
6464 033402 077301          SOB      R3           ; BY SITTING HERE FOR A WHILE
6465 033404 105737 002272    TSTB      INTFLG      ;DID AN 'A' INTERRUPT OCCUR?
6466 033410 001407          BEQ      16$          ;NO, GOOD. GO TEST THE 'B' INTERRUPT
6467 033412 012737 000013 002254    MOV      #11,,GDATA   ;YES, TELL ERROR HANDLER WHAT WE HAD DONE
6468 033420                GEDF      EM34,ERR1   ;REPORT THE UNEXPECTED INTERRUPT
6469                                ;      'DEVICE FATAL' ERROR # 62
6470 033420 104455                TRAP      C$ERDF
6471 033422 000076                .WORD    62
6472 033424 016526                .WORD    EM34
6473 033426 006212                .WORD    ERR1
6474
6475 033430 105737 002273    16$:  TSTB      INTFLG+1  ;DID A 'B' INTERRUPT OCCUR?
6476 033434 001007          BNE      17$          ;YES, GOOD. NOW TRY HITTING THE 'B' INTERRUPT
6477 033436 012737 000014 002254    MOV      #12,,GDATA   ;NO, TELL ERROR HANDLER WHAT WE HAD DONE
6478 033444                GEDF      EM35B,ERR1 ;REPORT MISSING INTERRUPT ON 'ENABLE'
6479                                ;      'DEVICE FATAL' ERROR # 63
6480 033444 104455                TRAP      C$ERDF
6481 033446 000077                .WORD    63
6482 033450 016573                .WORD    EM35B
6483 033452 006212                .WORD    ERR1
6484
6485 033454                17$:  ENDSUB
6486 033454                L10054:
6487 033454 104403                TRAP      C$ESUB
6488
6489

```

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TEST 9 -- Q-BUS INTERRUPT 'A' & 'B' SELECTION

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033456 104402
033460 012737 177777 002274
033466 005037 002272
033472 112777 000000 146620
033500 004537 004054
033504 123005
033506 000000
033510 103003
033512
033512 104460
033514
033514 104410
033516 000414
033520 105737 002272 31\$:
033524 001407
033526 012737 000015 002254
033534
033534 104455
033536 000100
033540 016526
033542 006212
033544 105737 002273 10\$:
033550 001407
033552 012737 000016 002254
033560
033560 104455
033562 000101
033564 016540
033566 006212
033570 005037 002272 11\$:
033574 112777 000021 146516
033602 012703 001000

- : TEST FOR 'A' INTERRUPT BUT NO 'B' WHEN BOTH ENABLED & FORCED
- : 1. DISABLE BOTH INTERRUPTS
- : 2. ASSERT BOTH 'A' & 'B' REQUEST BITS TO 0:
: FORCING BOTH 'A' & 'B' (BUT ONLY GETTING 'A')
- : 3. CHECK FOR NO 'A' INTERRUPT
- : 4. CHECK FOR NO 'B' INTERRUPT
- : 5. ENABLE BOTH INTERRUPTS
- : 6. CHECK FOR 'A' INTERRUPT
- : 7. CHECK FOR NO 'B' INTERRUPT

BGNSUB

```

                                T9.4:
MOV    #-1,INTWCH              ;TELL BOTH HANDLERS TO 'WATCH' FOR INTERRUPTS
CLR    INTFLG                  ;CLEAR BOTH INTERRUPT FLAGS
MOVB   #0,@BSELO              ;DISABLE INTERRUPTS AGAIN
JSR    R5,WRITEI              ;CAUSE AN INTERRUPT PENDING ON BOTH 'A' & 'B'
IRQREG 0
BCC    31$                    ;IF AN ERROR OCCURED,
ERROR  :REPORT IT &
ESCAPE TST                    ; QUIT
                                TRAP    C$ERROR
                                .WORD  C$ESCAPE
                                .WORD  L10051-.

31$:  TSTB   INTFLG            ;DID AN 'A' INTERRUPT OCCUR?
      BEQ    10$              ;NO, GOOD. GO TEST THE 'B' INTERRUPT
      MOV    #13, GDATA       ;YES, TELL ERROR HANDLER WHAT WE HAD DONE
      GEDF  EM34,ERR1        ;REPORT THE UNEXPECTED INTERRUPT
      ;    'DEVICE FATAL' ERROR # 64
                                TRAP    C$ERDF
                                .WORD  64
                                .WORD  EM34
                                .WORD  ERR1

10$:  TSTB   INTFLG+1         ;DID A 'B' INTERRUPT OCCUR?
      BEQ    11$              ;NO, GOOD. NOW TRY LETTING ONE THROUGH
      MOV    #14, GDATA       ;YES, TELL ERROR HANDLER WHAT WE HAD DONE
      GEDF  EM34B,ERR1       ;REPORT THE UNEXPECTED INTERRUPT
      ;    'DEVICE FATAL' ERROR # 65
                                TRAP    C$ERDF
                                .WORD  65
                                .WORD  EM34B
                                .WORD  ERR1

11$:  CLR    INTFLG           ;CLEAR BOTH INTERRUPT FLAGS
      MOVB  #IENBA!IENBB,@BSELO ;ENABLE BOTH INTERRUPTS
      MOV   #1000,R3          ;GIVE THE INTERRUPT SOME TIME TO HAPPEN
  
```

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TEST 9 -- Q-BUS INTERRUPT 'A' & 'B' SELECTION

```

6546 033606 077301          SOB      R3          ; BY SITTING HERE FOR A WHILE
6547 033610 105737 002272    TSTB     INTFLG     ; DID AN 'A' INTERRUPT OCCUR?
6548 033614 001007          BNE      12$        ; YES, GOOD. GO TEST THE 'B' INTERRUPT
6549 033616 012737 000017 002254 MOV      #15,GDATA ; NO, TELL ERROR HANDLER WHAT WE HAD DONE
6550 033624          GEDF     EM35,ERR1 ; REPORT MISSING INTERRUPT ON 'ENABLE'
6551          ;           'DEVICE FATAL' ERROR # 66
6552 033624 104455          TRAP     C$ERDF
6553 033626 000102          .WORD   66
6554 033630 016552          .WORD   EM35
6555 033632 006212          .WORD   ERR1
6556
6557 033634 105737 002273    12$:   TSTB     INTFLG+1 ; DID A 'B' INTERRUPT OCCUR?
6558 033640 001407          BEQ      13$        ; NO, GOOD. NOW TRY HITTING THE 'B' INTERRUPT
6559 033642 012737 000020 002254 MOV      #16,GDATA ; YES, TELL ERROR HANDLER WHAT WE HAD DONE
6560 033650          GEDF     EM34B,ERR1 ; REPORT THE UNEXPECTED INTERRUPT
6561          ;           'DEVICE FATAL' ERROR # 67
6562 033650 104455          TRAP     C$ERDF
6563 033652 000103          .WORD   67
6564 033654 016540          .WORD   EM34B
6565 033656 006212          .WORD   ERR1
6566
6567 033660          13$:   ENDSUB
6568 033660
6569 033660 104403          L10055: TRAP     C$ESUB
6570
6571
6572          -----
6573          TEST FOR 'A' INTERRUPT WHILE ENABLED
6574          1. ENABLE BOTH INTERRUPTS
6575          2. ASSERT 'B' REQUEST BIT TO 1:
6576             DISABLING 'B' & FORCING 'A'
6577          3. CHECK FOR 'A' INTERRUPT
6578          4. CHECK FOR NO 'B' INTERRUPT
6579          :
6580          :
6581          :
6582          :
6583          BGNSUB
6584          T9.5:
6585 033662 104402          TRAP     C$BSUB
6586 033664 012737 177777 002274 MOV      #-1,INTWCH ; TELL BOTH HANDLERS TO 'WATCH' FOR INTERRUPTS
6587 033672 005037 002272 CLR      INTFLG     ; CLEAR BOTH INTERRUPT FLAGS
6588 033676 112777 000021 146414 MCVB    #IENB,IENBB,ABSELO ; ENABLE BOTH INTERRUPTS
6589 033704 004537 004054 JSR      R5,WRITEI ; CAUSE AN INTERRUPT PENDING ON 'A'
6590 033710 123005          ; BUT NOT ON 'B'
6591 033712 000002          IRQREG
6592 033714 103003          IRQB
6593 033716 104460          BCC     31$        ; IF AN ERROR OCCURED,
6594 033716 104460          ERROR          ; REPORT IT &
6595 033720          ESCAPE TST          ; QUIT
6596 033720 104410          TRAP     C$ERROR
6597 033722 000210          .WORD   L10051-.
6598
6599 033724 012703 001000    31$:   MOV      #1000,R3  ; GIVE THE INTERRUPT SOME TIME TO HAPPEN
6600 033730 077301          SOB      R3          ; BY SITTING HERE FOR A WHILE
6601 033732 105737 002272    TSTB     INTFLG     ; DID AN 'A' INTERRUPT OCCUR?

```

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TEST 9 -- Q-BUS INTERRUPT 'A' & 'B' SELECTION

```

6602 033736 001007      BNE      12$      ;YES, GOOD. GO TEST THE 'B' INTERRUPT
6603 033740 012737 000023 002254  MOV      #19.,GDATA ;NO, TELL ERROR HANDLER WHAT WE HAD DONE
6604 033746      GEDF      EM35,ERR1 ;REPORT MISSING INTERRUPT ON 'ENABLE'
6605      ;          'DEVICE FATAL' ERROR # 68
6606 033746 104455      TRAP      C$ERDF
6607 033750 000104      .WORD    68
6608 033752 016552      .WORD    EM35
6609 033754 006212      .WORD    ERR1
6610
6611 033756 105737 002273 12$: TSTB     INTFLG+1 ;DID A 'B' INTERRUPT OCCUR?
6612 033762 001407      BEQ      13$      ;NO, GOOD. NOW TRY HITTING THE 'B' INTERRUPT
6613 033764 012737 000024 002254  MOV      #20.,GDATA ;YES, TELL ERROR HANDLER WHAT WE HAD DONE
6614 033772      GEDF      EM34B,ERR1 ;REPORT THE UNEXPECTED INTERRUPT
6615      ;          'DEVICE FATAL' ERROR # 69
6616 033772 104455      TRAP      C$ERDF
6617 033774 000105      .WORD    69
6618 033776 016540      .WORD    EM34B
6619 034000 006212      .WORD    ERR1
6620
6621 034002      13$: ENDSUB
6622 034002
6623 034002 104403      L10056: TRAP      C$ESUB
6624
6625 -----
6626          TEST FOR 'B' INTERRUPT WHILE ENABLED
6627
6628          1. ENABLE BOTH INTERRUPTS
6629
6630          2. ASSERT 'A' REQUEST BIT TO 1:
6631             DISABLING 'A' & FORCING 'B'
6632
6633          3. CHECK FOR NO 'A' INTERRUPT
6634
6635          4. CHECK FOR 'B' INTERRUPT
6636
6637          BGNSUB
6638
6639          T9.6: TRAP      C$BSUB
6640 034004 104402      MOV      #-1,INTWCH ;TELL BOTH HANDLERS TO 'WATCH' FOR INTERRUPTS
6641 034006 012737 177777 002274  CLR      INTFLG      ;CLEAR BOTH INTERRUPT FLAGS
6642 034014 005037 002272      MOV      #IENBA,IENBB,ABSELO ;ENABLE BOTH INTERRUPTS
6643 034020 112777 000021 146272  JSR      R5,WRITEI ;CAUSE AN INTERRUPT PENDING ON 'B'
6644 034026 004537 004054      ; BUT NOT ON 'A'
6645 034032 123005      IRQREG
6646 034034 000004      IRQA
6647 034036 103003      BCC      32$      ;IF AN ERROR OCCURED,
6648 034040      ERROR          ;REPORT IT &
6649 034042      ESCAPE TST          ; QUIT
6650 034042 104410      TRAP      C$ERROR
6651 034044 000066      .WORD    L10051-.
6652
6653 034046 012703 001000 32$: MOV      #1000,R3 ;GIVE THE INTERRUPT SOME TIME TO HAPPEN
6654 034052 077301      SOB      R3 ; BY SITTING HERE FOR A WHILE
6655 034054 105737 002272      TSTB     INTFLG      ;DID AN 'A' INTERRUPT OCCUR?
6656 034060 001407      BEQ      16$      ;NO, GOOD. GO TEST THE 'B' INTERRUPT
6657 034062 012737 000025 002254  MOV      #21.,GDATA ;YES, TELL ERROR HANDLER WHAT WE HAD DONE

```

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TEST 9 -- Q-BUS INTERRUPT 'A' & 'B' SELECTION

```

6658 034070          GEDF  EM34,ERR1      ;REPORT THE UNEXPECTED INTERRUPT
6659                                     ;   'DEVICE FATAL' ERROR # 70
6660 034070 104455                                     TRAP  C$ERDF
6661 034072 000106                                     .WORD 70
6662 034074 016526                                     .WORD EM34
6663 034076 006212                                     .WORD ERR1
6664
6665 034100 105737 002273      16$:  TSTB  INTFLG+1      ;DID A 'B' INTERRUPT OCCUR?
6666 034104 001007                BNE  17$          ;YES, GOOD. NOW TRY HITTING THE 'B' INTERRUPT
6667 034106 012737 000026 002254  MOV  #22,,GDATA  ;NO, TELL ERROR HANDLER WHAT WE HAD DONE
6668 034114                GEDF  EM35B,ERR1 ;REPORT MISSING INTERRUPT ON 'ENABLE'
6669                                     ;   'DEVICE FATAL' ERROR # 71
6670 034114 104455                                     TRAP  C$ERDF
6671 034116 000107                                     .WORD 71
6672 034120 016573                                     .WORD EM35B
6673 034122 006212                                     .WORD ERR1
6674
6675 034124                17$:  ENDSUB
6676 034124
6677 034124 104403                L10057: TRAP  C$ESUB
6678
6679 034126 005037 002274      CLR  INTWCH      ;TELL HANDLERS TO STOP WATCHING FOR INTERRUPTS
6680 034132                ENDTST
6681 034132
6682 034132 104401                L10051: TRAP  C$ETST

```

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TEST 10 -- BUS RESET WITH DISABLE INIT SET

.SBTTL TEST 10 -- BUS RESET WITH DISABLE INIT SET

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034134
034134 032737 000001 002316
034142 001031
034144 004737 003514
034150 103003
034152
034152 104460
034154
034154 104410
034156 000050
034160 004537 004054
034164 123004
034166 000105
034170 112777 000377 146130
034176
034176 104433
034200 012703 001000
034204 077301
034206 122777 000377 146112
034214 001404
034216
034216 104455
034220 000110
034222 016614
034224 006212

```
*****
*
* TEST 10 -- BUS RESET WITH DISABLE INIT SET
*
* A BYTE SELECT REGISTER (BSEL3) IS LOADED WITH 377, DISABLE INIT BIT IS SET
* IN THE NPR CONTROL REGISTER, AND A BUS RESET INSTRUCTION IS EXECUTED. THE
* PROGRAM THEN CHECKS THAT THE DMV-11 WAS NOT CLEARED, BY CHECKING FOR 377
* STILL IN BSEL3
*****
```

```

          BGNTST
          T10::
          BIT    #BIT0,PFLAG   ;IF BUS RESETS ARE NOT ALLOWED,
          BNE    10$           ; BYPASS THIS TEST
                              ;ELSE,
          JSR    PC,MSTCLR     ;INIT DMV & START UP THE MAINT. LOOP
          BCC    1$           ;IF AN ERROR OCCURED,
          ERROR  ;REPORT IT &
                              TRAP    CSERROR
          ESCAPE TST          ; EXIT
                              TRAP    C$ESCAPE
                              .WORD  L10060-
1$:      JSR    R5,WRITEI     ;NOW SET 'DISABLE INIT'
          NPRCTL
          DMVDAI!DMVPU!NPRGO
                              ;THE 'NPRGO' BIT IS SET BECAUSE ASSERTING IT
                              ;TO A ZERO WOULD KICK OFF AN NPR OPERATION!
                              ;THE 'DMVPU' BIT MUST ALWAYS BE SET WHENEVER
                              ;THE NPR-CONTROL REGISTER IS LOADED.
          MOVB  #377,@BSEL3  ;THIS REGISTER WILL ONLY GET ALTERED IF THE
                              ;DMV-11 IS SUCCESSFULLY RESET: THE 'DMVPU'
                              ;BIT WILL BE CLEARED, THE MICRO-DIAGNOSTIC
                              ;WILL BE STARTED, AND FINDING 'DMVPU' CLEARED,
                              ;IT WILL CLEAR ALL BSEL REGISTERS (INCLUDING
                              ;BSEL3) AND PERFORM THE 17 TESTS IS CONTAINS.
                              ;OF COURSE, IF THIS ALL HAPPENS, THAN THIS
                              ;TEST WILL HAVE FAILED!
          BRESET
                              ;THE 'SUPERVISOR' WILL DO A BUS RESET FOR US
                              TRAP    CSRESET
          MOV   #1000,R3      ;DELAY FOR A BIT SO THE MICRO-DIAG. CAN DO
          SOB  R3            ;ITS THING IF IT'S GOING TO
          CMPB #377,@BSEL3  ;IF A FAILURE OCCURED, THIS SHOULD HAVE BEEN
          BEQ  10$          ;ALTERED BY NOW. IF NOT, ALL'S WELL -- EXIT
          GEDF EM40,ERR1    ;ELSE, 'DISABL INIT' DIDN'T STOP 'BUS RESET'
                              ; 'DEVICE FATAL' ERROR # 72
                              TRAP    CSERDF
                              .WORD  72
                              .WORD  EM40
                              .WORD  ERR1
```

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TEST 10 -- BUS RESET WITH DISABLE INIT SET

6739 034226
6740 034226
6741 034226 104401

10\$: ENDTST

L10060: TRAP CSETST

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TEST 11 -- MASTER CLEAR WITH DISABLE INIT SET

.SBTTL TEST 11 -- MASTER CLEAR WITH DISABLE INIT SET

```

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6753
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6756
6757 034230
6758 034230 004737 003514
6759 034234 103003
6760 034236
6761 034236 104460
6762 034240
6763 034240 104410
6764 034242 000030
6765 034244 004537 004054
6766 034250 123004
6767 034252 000105
6768
6769
6770
6771
6772
6773 034254 004737 003514
6774 034260 103004
6775 034262
6776
6777 034262 104455
6778 034264 000111
6779 034266 016671
6780 034270 006212
6781 034272
6782 034272
6783 034272 104401
    
```

```

:*****
:*
:*      TEST 11 -- MASTER CLEAR WITH DISABLE INIT SET
:*
:* THE 'DISABL INIT' BIT IN THE NPR CONTROL REGISTER IS SET AND A MASTER CLEAR
:* IS ISSUED. IF THE MASTER CLEAR SUBROUTINE DETECTS AN ERROR, THE MASTER
:* CLEAR WILL NOT HAVE FUNCTIONED PROPERLY. WHERE THE NORMAL ERROR MESSAGE
:* (QUEUED UP BY 'MASCLR') IS NORMALLY PRINTED, THIS TEST WILL PRINT ITS OWN
:* INSTEAD.
:*****
    
```

```

:      BGNTST
:
:              T11::
JSR    PC,MSTCLR    ;INIT DMV & START UP THE MAINT. LOOP
BCC    1$           ;IF AN ERROR OCCURED,
ERROR  ;REPORT IT &
:              TRAP    CSERROR
ESCAPE TST         ; EXIT
:              TRAP    C$ESCAPE
:              .WORD   L10061-.
1$:   JSR    R5,WRITEI    ;NOW SET 'DISABLE INIT'
      NPRCTL
      DMVDAL!DMVPU!NPRGO
:
: THE 'NPRGO' BIT IS SET BECAUSE ASSERTING IT
: TO A ZERO WOULD KICK OFF AN NPR OPERATION!
: THE 'DMVPU' BIT MUST ALWAYS BE SET WHENEVER
: THE NPR-CONTROL REGISTER IS LOADED.
:
: INIT THE DMV & RESTART THE M-LOOP
: IF AN ERROR OCCURED, IGNORE QUEUED ERROR AND
: REPORT THAT 'DISABL INIT' STOPPED MASTER CLEAR
: 'DEVICE FATAL' ERROR # 73
:              TRAP    CSERDF
:              .WORD   73
:              .WORD   EM41
:              .WORD   ERR1
2$:   ENDTST
:
:              L10061:
:              TRAP    C$ETST
    
```

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TEST 12 -- DCOK H LO BIT

.SBTTL TEST 12 -- DCOK H LO BIT

```

:*****
:*
:*      TEST 12 -- DCOK H LO BIT
:*
:* DCOK H LO IS SET IN THE NPR CONTROL REGISTER WHICH SHOULD CAUSE A VECTOR TO
:* THE FIRST INTERRUPT HANDLER WHERE THE VECTOR IS CHANGED TO POINT TO THE
:* SECOND HANDLER. THIS SECOND HANDLER WILL THEN STALL FOR A WHILE WAITING FOR
:* THE POWER-UP INTERRUPT WHICH SHOULD KICK US INTO THE SECOND HANDLER. IN
:* BOTH HANDLERS FLAGS ARE SET TO SAY THAT WE GOT THERE. WHEN WE FINALLY
:* RETURN TO OUR MAINLINE CODE, WE WILL RESUME THE DELAY FUNCTION WE WERE IN
:* AND THEN CHECK THE FLAGS.
:*
:* IN SUBTEST # 1, WE EXPECT THE DMV TO BE RESET.

```

```

:*****
:
:      BGNTST
:
:      DCOKTS = ST      ;DEFINE TEST # FOR "INIT" SECTION
:      BIT     #PU24,PT.CTL ;IS POWER-UP STRAPPED FOR OPTION 0?
:      BNE     1$      ;YES, THEN WE CAN DO THIS TEST
:      EXIT    TST     ;NO, WE CAN'T DO THIS TEST UNLESS IT IS!
:
:      1$:    BIT     #BIT0,PFLAG ;IF BUS RESETS ARE ALLOWED,
:             BEQ     2$      ; PERFORM THIS TEST
:             EXIT    TST     ;ELSE, BYPASS IT
:
:      2$:    DELAY   40.      ;DELAY TO PREVENT TST # ROACHING
:
:      MOV     #40.,(PC)+
:      .WORD  0
:      MOV     LSDLY,(PC)+
:      .WORD  0
:      DEC     -6(PC)
:      BNE     -4
:      DEC     -22(PC)
:      BNE     -20

```

SUBTEST #1: DCOK H LO (RESET DMV)

```

:*****
:      BGNSUB          ; <====> TEST FOR POWER-DOWN/UP & DMV-11 RESET
:
:      JSR     PC,MSTCLR ;INIT DMV & START UP THE MAINT. LOOP
:      BCC     3$      ;IF AN ERROR OCCURED,
:      ERROR   ;REPORT IT &
:
:      ESCAPE SUB      ; EXIT
:
:      TRAP   CSBSUB
:
:      TRAP   CSERROR
:
:      TRAP   CSESCAPE
:      .WORD  L10063-.

```

```

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6785
6786
6787
6788
6789
6790
6791
6792
6793
6794
6795
6796
6797
6798
6799
6800
6801
6802
6803
6804 034274
6805      000014
6806 034274 032737 000001 002370
6807 034302 001002
6808 034304
6809 034304 104432
6810 034306 000476
6811
6812 034310 032737 000001 002316 1$:
6813 034316 001402
6814 034320
6815 034320 104432
6816 034322 000462
6817
6818 034324
6819 034324 012727 000050
6820 034330 000000
6821 034332 013727 002116
6822 034336 000000
6823 034340 005367 177772
6824 034344 001375
6825 034346 005367 177756
6826 034352 001367
6827
6828
6829
6830 034354
6831 034354
6832 034354 104402
6833 034356 004737 003514
6834 034362 103003
6835 034364
6836 034364 104460
6837 034366
6838 034366 104410
6839 034370 000220

```

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TEST 12 -- DCOK H LO BIT

```

6840 034372
6841 034372 012746 000007
6842 034376 012746 034500
6843 034402 012746 000024
6844 034406 012746 000003
6845 034412 104437
6846 034414 062706 000010
6847 034420 112737 177777 002274
6848 034426 105037 002272
6849 034432 152777 000001 145660
6850
6851 034440 010637 002454
6852
6853
6854
6855
6856 034444 012777 177506 145662
6857 034452 012777 123004 145650
6858 034460 112777 000042 145636
6859
6860
6861
6862
6863
6864
6865
6866
6867
6868
6869 034466 000001
6870
6871
6872
6873
6874
6875
6876
6877 034470
6878
6879 034470 104455
6880 034472 000112
6881 034474 016746
6882 034476 006212
6883
6884 034500
6885
6886
6887
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6893
6894
6895

```

```

3$: SETVEC #24,#58,#7 ;SETUP VECTOR FOR POWER FAIL INTERRUPT HANDLER
MOV #7,-(SP)
MOV #58,-(SP)
MOV #24,-(SP)
MOV #3,-(SP)
TRAP C$SVEC
ADD #10,SP

MOVB #-1,INTWCH ;EXPECT AN 'A' INTERRUPT (IF 'DCOK' FAILS!)
CLRB INTFLG ;CLEAR THE FLAG IN CASE WE WANT TO DETECT IT
BISB #IENBA,@BSEL0 ;NOW ENABLE 'A' INTERRUPTS

MOV SP,OLDSP ;SAVE THE STACK POINTER

: SETUP 'DISABL INIT' TO ALLOW BINIT TO RESET THE DMV AND THEN CAUSE
: A POWER-FAIL CONDITION TO BE SIMULATED.

MOV #177400!LSIDCL!DMVPU!NPRGO,@SEL6 ;VALUE TO BE LOADED INTO
MOV #NPRCTL,@SEL4 ;THE NPR CONTROL REGISTER
MOVB #WRILOC!<IRQA*8.>,@SEL2 ;TELL M-LOOP TO WRITE IT & INTERRUPT
;US JUST BEFORE REQUESTING ANOTHER
;M-LOOP COMMAND.

: SETTING 'NPRGO' PREVENTS AN 'NPR' OPERATION FROM OCCURRING. THE HIGH
: ORDER BYTE IS SET TO -1 AND IS USED AS A FLAG — WHEN THE RESET
: OCCURS, THE SELECT REGISTERS WILL ALL BE CLEARED; WE WILL BE
: LOOKING AT THE RUN BIT TO BE SET AND AT BSEL7 TO BE CLEARED AS POSITIVE
: PROOF THAT THE DMV GOT RESET.

4$: WAIT ;HANG HERE UNTIL INTERRUPTED

: IF THE DCOK WORKS AS IT SHOULD, THE NEXT INSTRUCTION TO BE EXECUTED
: IS AT THE LABEL '5$'. IF I DOESN'T WORK, THE DMV SHOULD FINISH THE
: 'WRITE' COMMAND AND GENERAT A Q-BUS INTERRUPT. ON RETURNING FROM
: THAT INTERRUPT, WE FALL INTO THE ERROR CALL BELOW:

GEDF EM42A,ERR1 ;REPORT MISSING POWER-UP
; 'DEVICE FATAL' ERROR # 74
TRAP C$ERDF
.WORD 74
.WORD EM42A
.WORD ERR1

5$:

: IN EITHER CASE: RESTORE THE POWER FAIL VECTOR, STACK POINTER, AND
: THE FLAGS USED BY THE Q-BUS INTERRUPT SERVICE ROUTINE.

: BUT F I R S T !

: IN SOME CASES THE Q-BUS GETS CONFUSED WHEN WE PERFORM THE ABOVE
: (HIGHLY NON- STANDARD) 'DCOK' MANIPULATION. EXPERIENCE HAS SHOWN
: THAT THE INSTRUCTION BEING EXECUTED CAN BE CORRUPTED — USUALLY (BUT
: NOT ALWAYS) BEING CLEARED TO ZERO (A HALT INSTRUCTION). THIS IS NOT
: A FAILURE OF THE DMV AND THEREFORE SHOULD NOT BE OUR CONCERN HERE.

```

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TEST 12 -- DCOK H LO BIT

```

6896      : THE FAILURE SHOULD NOT AFFECT THE TEST WITHIN THE PASS IN WHICH IT
6897      : OCCURS BUT IN A SUBSEQUENT PASS. IN AN EFFORT TO ELIMINATE ANY
6898      : PROBLEMS FROM THIS GLITCH, WE RESTORE THE INSTRUCTION:
6899
6900 034500 012737 000001 034466      MOV      #1,4$           ;RESTORE THE 'WAIT' INSTRUCTION -- JUST IN
6901      : CASE IT GOT MODIFIED!
6902 034506 013706 002454      MOV      OLDSP,SP       ;RESTORE THE STACK
6903 034512 142777 000001 145600      BICB    #IENBA,@BSELO ;DISABLE THE 'A' INTERRUPT
6904 034520 105037 002274      CLRB    INTWCH         ;STOP EXPECTING Q-BUS INTERRUPTS
6905 034524      CLRVEC    #24         ;RETURN THE VECTOR TO THE SUPERVISOR
6906 034524 012700 000024      MOV      #24,R0       MOV      #24,R0
6907 034530 104436      TRAP    CSCVEC        TRAP    CSCVEC
6908 034532      SETPRI   #0           ;MAKE SURE WE'RE BACK RUNNING AT 0 AGAIN!
6909 034532 012700 000000      MOV      #0,R0       MOV      #0,R0
6910 034536 104441      TRAP    CSSPRI        TRAP    CSSPRI
6911
6912 034540 013701 002262      MOV      DELAY1,R1    ;INITIALIZE THE LOOP COUNTER FOR DELAY LOOP
6913 034544 001402 10$:      BEQ     11$           ; EXIT DELAY LOOP IF TIME HAS EXPIRED
6914 034546 005301      DEC     R1           ; ELSE, DECREMENT THE LOOP COUNTER AND
6915 034550 000775      BR      10$         ; CONTINUE TO LOOP
6916 034552 11$:      TIME UP !
6917 034552 132777 000200 145542      BITB    #RUN,@BSEL1  ;CHECK RUN BIT
6918 034560 001403      BEQ     12$         ; NOT SET... REPORT ERROR.
6919 034562 105777 145550      TSTB    @BSEL7       ;THIS REGISTER SHOULD HAVE BEEN CLEARED
6920 034566 001404      BEQ     13$         ;IT IS, EVERYTHING HERE IS OK -- EXIT SUBTEST
6921 034570 12$:      GEDF    EM42B,ERR1  ;NO, THEN REPORT THE FAILURE
6922      : 'DEVICE FATAL' ERROR # 75
6923 034570 104455      TRAP    CSERDF       TRAP    CSERDF
6924 034572 000113      .WORD   75           .WORD   75
6925 034574 017020      .WORD   EM42B       .WORD   EM42B
6926 034576 006212      .WORD   ERR1        .WORD   ERR1
6927 034600 004737 003346      13$:      JSR     PC,MASCLR    ;RESTORE DMV-11 TO A NORMAL STATE!
6928 034604 103001      BCC     14$         ;NO ERRORS, EXIT SUBTEST
6929 034606      ERROR      ;REPORT MSTCLR ERROR
6930 034606 104460      TRAP    CSERROR     TRAP    CSERROR
6931 034610 14$:      ENDSUB
6932 034610
6933 034610
6934 034610 104403      L10063: TRAP    CSESUB
6935
6936      :-----
6937      : SUBTEST #2: DCOK H LO (DMV-11 SHOULDN'T BE RESET)
6938      :-----
6939      : SINCE HITTING 'DCOK H LO' WITHOUT 'HALT' OCCASIONALLY CORRUPTS
6940      : PROGRAM MEMORY: WE SET BOTH 'HALT' AND 'DCOK H LO' IN THIS
6941      : SUB-TEST (IF HALT FAILS, THIS TEST MAY BLOW UP).
6942 034612      BGNSUB      ; <==> TEST FOR POWER-DOWN/UP & NO DMV-11 RESET
6943 034612      T12.2:
6944 034612 104402      TRAP    CSBSUB
6945
6946 034614 004737 003514      JSR     PC,MSTCLR    ;INIT DMV & START UP THE MAINT. LOOP
6947 034620 103003      BCC     2$         ;IF AN ERROR OCCURED,
6948 034622      ERROR      ;REPORT IT &
6949 034622 104460      TRAP    CSERROR     TRAP    CSERROR
6950 034624      ESCAPE   SUB       ; EXIT
6951 034624 104410      TRAP    CSESCAPE    TRAP    CSESCAPE

```

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TEST 12 -- DCOK H LO BIT

```

6952 034626 000154
6953 034630
6954 034630 012746 000007
6955 034634 012746 034732
6956 034640 012746 000024
6957 034644 012746 000003
6958 034650 104437
6959 034652 062706 000010
6960 034656 010637 002454
6961
6962 034662 004537 004166
6963 034666 035006
6964 034670 000040
6965
6966 034672 112777 000377 145434
6967
6968 034700 012777 000077 145422
6969 034706 012777 000005 145410
6970
6971
6972
6973 034714 012703 001000
6974 034720 077301
6975
6976
6977
6978 034722
6979
6980 034722 104455
6981 034724 000114
6982 034726 016746
6983 034730 006212
6984
6985
6986
6987 034732 013706 002454
6988 034736
6989 034736 012700 000024
6990 034742 104436
6991 034744
6992 034744 012700 000000
6993 034750 104441
6994 034752 122777 000377 145354
6995 034760 001404
6996 034762
6997
6998 034762 104455
6999 034764 000115
7000 034766 017074
7001 034770 006212
7002
7003 034772 004737 003346
7004 034776 103001
7005 035000
7006 035000 104460
7007 035002

```

```

2$: SETVEC #24,#5$,#7 ;SETUP VECTOR FOR POWER FAIL INTERRUPT HANDLER
;
;MOV #7,-(SP)
;MOV #5,-(SP)
;MOV #24,-(SP)
;MOV #3,-(SP)
;TRAP CSSVEC
;ADD #10,SP
;SAVE THE STACK POINTER
;MOVE THE MICRO CODE INTO THE DMV
; THIS IS WHERE IT STARTS
; THIS IS ITS SIZE IN BYTES
;WRITE ALL 1'S TO BSEL6
;START ADDRESS OF MICROCODE
;INITIATE M-CODE
; *** IF THE RESET GETS THROUGH, THE MICRO-DIAGNOSTIC WILL CLEAR BSEL4 ***
;STALL FOR A BIT (UCODE SHOULD HALT US HERE)
; IF WE GET HERE, WE NEVER GOT THE EXPECTED POWER-UP SEQUENCE!
;REPORT MISSING POWER-UP
; 'DEVICE FATAL' ERROR # 76
;TRAP CSERDF
;.WORD 76
;.WORD EM42A
;.WORD ERR1
;IN EITHER CASE, RESTORE THE VECTOR & STACK AND SEE IF THE DMV GOT RESET
5$: MOV OLDSP,SP ;RESTORE THE STACK
CLRVEC #24 ;RETURN THE VECTOR TO THE SUPERVISOR
;MOV #24,R0
;TRAP CSCVEC
;MAKE SURE WE'RE BACK RUNNING AT 0 AGAIN!
;MOV #0,R0
;TRAP CSSPRI
;THIS REGISTER SHOULD NOT HAVE BEEN CLEARED
;IT ISN'T, ALL IS OK -- EXIT SUBTEST
;IT IS, THEN REPORT THE FAILURE
; 'DEVICE FATAL' ERROR # 77
;TRAP CSERDF
;.WORD 77
;.WORD EM42C
;.WORD ERR1
;* AT THIS POINT 'DINIT' IS STILL SET *
;MAKE SURE THE DMV IS PROPERLY RESET!
;EVERYTHING OK, EXIT SUBR AND TEST.
;REPORT MASCLR ERROR
;TRAP CSERROR
10$: JSR PC,MASCLR
BCC 11$
ERROR
11$:

```

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TEST 12 -- DCOK H LO BIT

7008	035002	
7009	035002	
7010	035002	104403
7011	035004	
7012	035004	
7013	035004	104401

ENDSUB

ENDTST

L10064: TRAP C\$ESUB

L10062: TRAP C\$ETST

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SUBTEST 2'S M-CODE -- COMPLETE

```

7014
7015
7016
7017
7018 035006
7019 035006      251      125
7020 035010      215      004      246
7021 035013      251      105
7022 035015      215      004      246
7023 035020      352
7024 035021      352
7025 035022      352
7026 035023      251      107
7027 035025      215      004      246
7028 035030      251      105
7029 035032      215      004      246
7030 035035      251      003
7031 035037      205      000
7032 035041      306      000
7033 035043      320      374
7034 035045      140
7035
7036 035046
7037
    
```

```

.SBTTL SUBTEST 2'S M-CODE -- COMPLETE
-----
: 6502 MICROCODE FOR TEST #11/ SUBTEST #2
-----
SMCODE:
.BYTE 251,125 :A9 55 LDA #GOBSY1!HALT!PWRUP!DINIT
.BYTE 215,4,246 :8D 04 A6 STA NPRCTL :SET DISABLE INIT/HALT
.BYTE 251,105 :A9 45 LDA #GOBSY1!PWRUP!DINIT
.BYTE 215,4,246 :8D 04 A6 STA NPRCTL :CLEAR HALT
.BYTE 352 :EA NOP :WAIT A WHILE
.BYTE 352 :EA NOP
.BYTE 352 :EA NOP
.BYTE 251,107 :A9 47 LDA #GOBSY1!PWRUP!SDCOK!DINIT
.BYTE 215,4,246 :8D 04 A6 STA NPRCTL :SET DCOK
.BYTE 251,105 :A9 45 LDA #GOBSY1!PWRUP!DINIT
.BYTE 215,4,246 :8D 04 A6 STA NPRCTL :CLEAR DCOK
.BYTE 251,3 :A9 03 LDA #S03 :DELAY FOR 16.8 USEC
.BYTE 205,0 :85 00 STA SPO :BUS INIT IS 10 USEC
.BYTE 306,0 :C6 00 5$:DEC SPO :
.BYTE 320,374 :D0 FC BNE 5$ :
.BYTE 140 :60 RTS :RETURN TO M-LOOP
.EVEN
EMCODE:
-----
    
```


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TEST 13 -- HALT MODE VERIFICATION

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7149

```

: *
: * WE SHOULD GO
: * THROUGH A POWER-UP SEQUENCE. R6 IS
: * RESTORED FROM OLDSP, INTERRUPT
: * PRIORITY LEVEL IS RESTORED TO 0, &
: * INTERRUPT VECTOR 24 IS RETURNED TO
: * THE DIAGNOSTIC SUPERVISOR. SEL4 IS
: * COMPARED AGAINST TMO -- THEY SHOULD
: * BE EQUAL.
: *
: * NOW CLEAR BSEL7.
: *
: *-----
: * SUBTEST # 2:
: *
: * HERE THE HALT MODE IS USED IN A WAY WHICH VERY CLOSELY MATCHES THE DMV-11
: * MICROCODE'S UTILIZATION DURING A 'MOP BOOT' OPERATION. THE INTERRUPT
: * VECTOR AREA IS COMPLETELY OVERWRITTEN BY THE DMV NPR'S AND IS THEREFORE
: * BACKED UP ELSEWHERE IN THE 11 CPU'S MEMORY. THERE IS ALSO THE POSSIBLE
: * CONTENTION WITH THE DIAGNOSTIC SUPERVISOR -- TO HELP HERE, AS MUCH AS
: * POSSIBLE WILL BE DONE AT INTERRUPT LEVEL 7.
: *
: *          11 CPU'S OPERATIONS:                                DMV-11'S OPERATIONS:
: *
: * THE MICROCODE IS MOVED INTO THE DMV.
: *
: * THE INTERRUPT VECTOR AREA IS BACKED-
: * UP IN AN I/O BUFFER FOLLOWING THE
: * PROGRAM
: *
: * THE MICROCODE IS INITIATED & BSEL7 IS
: * SET TO -1 AS A FLAG.
: *
: * WAIT FOR BSEL7 TO BE CLEARED
: *
: * CLEAR TMO AND SAVE R6 FOR RECOVERY
: * LATER. SET BSEL7 AGAIN AND WAIT FOR
: * TMO TO BE SET.
: *
: * ENTRY INTO THE CONSOLE 'ODT' WILL
: * BE INITIATED.
: *
: *
: * CLEAR BSEL7 AND WAIT FOR IT TO GO
: * NON-ZERO AGAIN PUTTING BOTH
: * PROCESSORS IN SYNC. WITH EACH
: * OTHER
: *
: * SET HALT, 'DCOK H LO', & 'DISABL
: * INIT' AND PERFORM 2 NOP'S AS A
: * 1 MICROSECOND DELAY
: *
: * CLEAR 'DCOK H LO', SET 'HALT' &
: * 'DISABL INIT'
: *
: * NPR-OUT THE FOLLOWING:
: * LOC: CONTENTS
: * 24 000000 VECTOR TO LOC 0
: * 26 000340 @ PRIORITY 7
: * 0 012700 MOV #-1,R0
: * 2 177777
    
```


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TEST 13 -- HALT MODE VERIFICATION

```

7206 ;      BGNTST
7207 035046
7208 000015
7209 035046 032737 000001 002370 HLTEST = $T      ;DEFINE TEST # FOR "INIT" SECTION
7210 035054 001002          ;IS POWER-UP STRAPPED FOR OPTION 0?
7211 035056          ;YES, THEN WE CAN DO THIS TEST
7212 035056 104432          ;NO, WE CAN'T DO THIS TEST UNLESS IT IS!
7213 035060 000772          TRAP      C$EXIT
7214          ;          .WORD      L10065-.
7215 035062 032737 000001 002316 5$:   BIT      #BIT0,PFLAG  ;IF BUS RESETS ARE ALLOWED,
7216 035070 001402          ;BEQ      2$          ;PERFORM THIS TEST
7217 035072          ;EXIT      TST          ;ELSE, BYPASS IT
7218 035072 104432          TRAP      C$EXIT
7219 035074 000756          ;          .WORD      L10065-.
7220
7221          2$:   DELAY  40.          ;DELAY TO PREVENT TST # ROACHING
7222 035076 012727 000050          MOV      #40.,(PC)+
7223 035102 000000          ;          .WORD      0
7224 035104 013727 002116          MOV      LSDLY,(PC)+
7225 035110 000000          ;          .WORD      0
7226 035112 005367 177772          DEC      -6(PC)
7227 035116 001375          BNE      -4
7228 035120 005367 177756          DEC      -22(PC)
7229 035124 001367          BNE      -20
7230
7231 035126          1$:   BGNSUB
7232 035126          ;          T13.1:
7233 035126 104402          TRAP      C$SUB
7234 035130 004737 003514          JSR      PC,MSTCLR  ;RESET DMV & ENTER M-LOOP
7235 035134 103003          BCC      2$          ;IF NO ERROR HERE, CONTINUE
7236 035136          ERROR          ;ELSE, REPORT THE ERROR
7237 035136 104460          TRAP      C$ERROR
7238 035140          ESCAPE TST          ;      & EXIT THE TEST
7239 035140 104410          TRAP      C$ESCAPE
7240 035142 000710          ;          .WORD      L10065-.
7241 035144
7242 035144 004537 004166          2$:   JSR      R5,MOVLTD ;MOVE THE MICRO CODE INTO THE DMV
7243 035150 036054          ;          MC1          ;THIS IS WHERE IT STARTS
7244 035152 000114          ;          MC2-MC1      ;THIS IS ITS SIZE IN BYTES
7245
7246 035154 005037 002412          CLR      TMO
7247 035160          SETVEC #24,#24$,#7 ;INITIALIZE THE COUNTER
7248          ;          ;SETUP POWER-UP VECTOR
7249 035160 012746 000007          MOV      #7,-(SP)
7250 035170 012746 035334          MOV      #24,-(SP)
7251 035174 012746 000024          MOV      #24,-(SP)
7252 035200 104437          MOV      #3,-(SP)
7253 035202 062706 000010          TRAP      C$SVEC
7254 035206 012777 000077 145114          ADD      #10,SP
7255 035214 112777 177777 145114          MOV      #77,@SEL4 ;START ADDRESS OF MICROCODE
7256 035222 012777 000005 145074          MOV      #-1,@SEL7 ;SET FLAG (BSEL7)
7257          MOV      #EXECUT,@SEL2 ;INITIATE M-CODE
7258 035230 005002
7259 035232 105777 145100          3$:   CLR      R2
7260 035236 001405          TSTB   @SEL7          ;WAIT FOR FLAG TO BE CLEARED
7261 035240 077204          BEQ     5$
7261          SOB    R2,3$

```

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TEST 13 -- HALT MODE VERIFICATION

```

7262 035242          GEDF  EM43A,ERR1      ;TIMEOUT,..M-CODE IS HUNG!
7263                ;                   'DEVICE FATAL' ERROR # 78
7264 035242 104455          TRAP          C$ERDF
7265 035244 000116          .WORD          78
7266 035246 017153          .WORD          EM43A
7267 035250 006212          .WORD          ERR1
7268
7269 035252 010637 002454 5$:  MOV      SP,OLDSP      ;SAVE STACK POINTER FOR LATER
7270 035256 012777 002412 145044 MOV      #TMP0,@SEL4  ;PASS ADDRESS OF TMP0 TO M-CODE
7271 035264 112777 177777 145044 MOV      #-1,@SEL7    ;TELL M-CODE TO PROCEED
7272
7273 035272 005237 002412 4$:  INC      TMP0          ;LOOP HERE UNTIL TMP0 GOES TO 0 AGAIN
7274 035276 001375          BNE      4$
7275 035300 000240          NOP
7276 035302 000240          NOP
7277 035304 000240          NOP
7278 035306          GEDF  EM43A,ERR1      ;DMV SEEMS TO HAVE HUNG!
7279                ;                   'DEVICE FATAL' ERROR # 79
7280 035306 104455          TRAP          C$ERDF
7281 035310 000117          .WORD          79
7282 035312 017153          .WORD          EM43A
7283 035314 006212          .WORD          ERR1
7284 035316 000240          NOP
7285 035320 004737 003346  JSR      PC,MASCLR    ; (FOR PATCHING)
7286 035324 103001          BCC      9$          ;RESET THE DMV
7287 035326          ERROR          ;RESET SUCCEEDED, ESCAPE TEST
7288 035326 104460          ; REPORT RESET ERROR
7289 035330 9$:  ESCAPE  TST          ; & GET OUT          TRAP          C$ERROR
7290 035330 104410          ;                                     TRAP          C$ESCAPE
7291 035332 000520          .WORD          L10065-.
7292
7293 035334 013706 002454 24$: MOV      OLDSP,SP      ;RESTORE R6 FIRST!
7294 035340          CLRVEC  #24          ;RETURN THE VECTORE TO THE SUPERVISOR
7295 035340 012700 000024          MOV      #24,R0
7296 035344 104436          TRAP          C$CVEC
7297 035346          SETPRI  #0          ;RESTORE PRIORITY LEVEL TO 0
7298 035346 012700 000000          MOV      #0,R0
7299 035352 104441          TRAP          C$SPRI
7300 035354 027737 144750 002412  CMP      @SEL4,TMP0    ;THESE SHOULD BE EQUAL
7301 035362 001405          BEQ      30$          ;THEY ARE -- TEST PASSED
7302 035364          GEDF  EM43B,ERR1      ;THEY AREN'T -- HALT DIDN'T WORK
7303                ;                   'DEVICE FATAL' ERROR # 80
7304 035364 104455          TRAP          C$ERDF
7305 035366 000120          .WORD          80
7306 035370 017177          .WORD          EM43B
7307 035372 006212          .WORD          ERR1
7308 035374 000240          NOP
7309 035376 105077 144734 30$: CLRB    @BSEL7      ; (FOR PATCHING)
7310 035402          ENDSUB      ;TELL M-CODE TO CLEAR DINIT
7311 035402          L10066:
7312 035402 104403          TRAP          C$ESUB
7313                -----
7314 035404          BGNSUB
7315 035404          T13.2:
7316 035404 104402          TRAP          C$BSUB
7317 035406 004737 003514  JSR      PC,MSTCLR    ;RESET DMV & ENTER M-LOOP

```

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TEST 13 -- HALT MODE VERIFICATION

```

7318 035412 103003          BCC      1$      ;IF NO ERROR HERE, CONTINUE
7319 035414                ERROR          ;ELSE, REPORT THE ERROR
7320 035414 104460          ESCAPE TST      :           & EXIT THE TEST
7321 035416                TRAP      CSERROR
7322 035416 104410          TRAP      CSESCAPE
7323 035420 000432          .WORD   L10065-
7324 035422                1$:
7325 035422 004537 004166    JSR      R5,MOVLTD ;MOVE THE MICRO CODE INTO THE DMV
7326 035426 036170          MC2           ; THIS IS WHERE IT STARTS
7327 035430 000243          MC2END-MC2      ; THIS IS ITS SIZE IN BYTES
7328
7329 035432 004537 005004    JSR      R5,MOVSW  ;SAVE THE INTERRUPT VECTORS
7330 035436 000000          0
7331 035440 002654          BUFAREA      ; IN THE BUFFER AREA
7332 035442 000200          400/2       ; LOC'S 0 ==> 377 WILL BE SAVED
7333
7334 035444                SETVEC  #376,#0,#7 ;FAKE OUT THE SUPERVISOR, WE'RE JUST
7335 035444 012746 000007          MOV      #7,-(SP)
7336 035450 012746 000000          MOV      #0,-(SP)
7337 035454 012746 000376          MOV      #376,-(SP)
7338 035460 012746 000003          MOV      #3,-(SP)
7339 035464 104437          TRAP      C$$VEC
7340 035466 062706 000010          ADD      #10,SP
7341
7342                ; SETTING LOCATION 376 TO ZERO (0)
7343 035472 012777 000077 144630    MOV      #77,@SEL4 ;START ADDRESS OF MICROCODE
7344 035500 112777 177777 144630    MOV      #-1,@BSEL7 ;SET FLAG (BSEL7)
7345 035506 012777 000005 144610    MOV      #EXECUT,@SEL2 ;INITIATE M-CODE
7346
7347 035514 005002          CLR      R2
7348 035516 105777 144614    3$:      TSTB    @BSEL7    ;WAIT FOR FLAG TO BE CLEARED
7349 035522 001406          BEQ     5$
7350 035524 077204          SOB    R2,3$
7351 035526          GEDF   EM43A,ERR1 ;TIMEOUT...M-CODE IS HUNG!
7352                ; 'DEVICE FATAL' ERROR # 81
7353 035526 104455          TRAP      C$ERDF
7354 035530 000121          .WORD   81
7355 035532 017153          .WORD   EM43A
7356 035534 006212          .WORD   ERR1
7357 035536 000447          BR      22$      ;EXIT
7358
7359 035540 010637 002454    5$:      MOV      SP,OLDSP ;SAVE STACK POINTER FOR LATER
7360 035544 005037 002412    CLR      TMP0     ;RESET EXECUTION INDICATOR (TMP0)
7361 035550 112777 177777 144560    MOV      #-1,@BSEL7 ;TELL M-CODE TO PROCEED
7362
7363 035556 005003          CLR      R3
7364                ;WE'LL WAIT THIS LONG FOR THE M-CODE TO
7365 035560 005737 000376    10$:     TST      @#376    ; INTERRUPT OUR SEQUENCE OF OPERATION
7366 035564 001017          BNE    20$       ;LOOK FOR THE M-CODE TO LOAD THIS LOCATION
7367 035566 077304          SOB    R3,10$   ;WE SHOULD NEVER SEE THIS HAPPEN!!!
7368                ;LOOP UNTIL WE'RE INTERRUPTED
7369                ;IF WE AREN'T, WE HAVE A REAL PROBLEM
7370 035570 004537 005004    JSR      R5,MOVSW ;RESTORE THE INTERRUPT VECTORS
7371 035574 002654          BUFAREA      ; FROM THE BUFFER AREA
7372 035576 000000          0
7373 035600 000200          400/2       ; TO LOC'S 0 ==> 377

```


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TEST 13 -- HALT MODE VERIFICATION

Address	Code	Hex	Hex	Label	Instruction	Comments	Trap Info
7430	035734	104455					TRAP C\$ERDF
7431	035736	000124					.WORD 84
7432	035740	017242					.WORD EM43D
7433	035742	006212					.WORD ERR1
7434	035744	000240					
7435	035746	020227	177777	5\$:	NOP CMP R2,#-1	;(FOR PATCHING) ;THE LOADED ROUTINE SHOULD HAVE DONE THIS	
7436	035752	001003			BNE 8\$;IT DIDN'T -- IT FAILED	
7437	035754	023702	002412		CMP TMP0,R2	;THESE SHOULD BE EQUAL TOO	
7438	035760	001412			BEQ 10\$;OK -->	
7439	035762	010237	002256	8\$:	MOV R2,BDATA	;WHAT!!! SETUP & REPORT AN ERROR	
7440	035766	012737	177777 002254		MOV #-1,GDATA		
7441	035774				GEDF EM43D,ERR1	;LOADED ROUTINE FAILED	
7442						;'DEVICE FATAL' ERROR # 85	
7443	035774	104455					TRAP C\$ERDF
7444	035776	000125					.WORD 85
7445	036000	017242					.WORD EM43D
7446	036002	006212					.WORD ERR1
7447	036004	000240					
7448	036006	020637	002454	10\$:	NOP CMP SP,OLDSP	;(FOR PATCHING) ;THESE SHOULD ALSO BE EQUAL	
7449	036012	001412			BEQ 15\$;OK -->	
7450	036014	010637	002256		MOV SP,BDATA	;WHAT!!! SETUP & REPORT AN ERROR	
7451	036020	013737	002454 002254		MOV OLDSP,GDATA		
7452	036026				GEDF EM43D,ERR1	;LOADED ROUTINE FAILED	
7453						;'DEVICE FATAL' ERROR # 86	
7454	036026	104455					TRAP C\$ERDF
7455	036030	000126					.WORD 86
7456	036032	017242					.WORD EM43D
7457	036034	006212					.WORD ERR1
7458	036036	000240					
7459	036040	004737	003346	15\$:	NOP JSR PC,MASCLR	;(FOR PATCHING) ;MAKE SURE THE DMV IS RESET	
7460	036044	103001			BCC 16\$; RESET OK, CONTINUE	
7461	036046				ERROR	;RESET FAILED, REPORT ERROR	
7462	036046	104460					TRAP C\$ERROR
7463	036050			16\$:	ENDSUB		
7464	036050						
7465	036050						L10067: TRAP C\$ESUB
7466	036050	104403					
7467	036052				ENDTST		
7468	036052						L10065: TRAP C\$ETST
7469	036052	104401					

SUBTEST 1'S M-CODE -- ASSIGNMENTS

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7476 036054
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.SBTTL SUBTEST 1'S M-CODE -- ASSIGNMENTS

```
*****
: MICRO-CODE FOR SUBROUTINE # 1
*****
```

MC1:

ASSEMBLED BY: COMPAS MICROSYSTEMS MINMIC (V6A)
(WITH CHANGES EDITED IN)

```
: LINE# LOC CODE
: 0002 0000
: 0003 0000
: 0004 0000
: 0005 0000
: 0006 0000
: 0007 0000
: 0008 0000
: 0009 0000
: 0010 0000
: 0011 0000
: 0012 0000
: 0013 0000
: 0014 0000
: 0015 0000
: 0016 0000
: 0017 0000
: 0018 0000
: 0019 0000
: 0020 0000
: 0021 0000
: 0022 0000
: 0023 0000
: 0024 0000
: 0025 0000
: 0026 0000
: 0027 0000
: 0028 0000
: 0029 0000
: 0030 0000
: 0031 0000
: 0032 0000
: 0033 0000
: 0034 0000
: 0035 0000
: 0036 0000
: 0037 0000
: 0038 0000
: 0039 0000
: 0040 0000
: 0041 0000
: 0042 0000
: 0043 0000
: 0044 0000
: 0045 0000
: 0046 0000
```

LINE
*= \$0000

:EQUATES FOR BIT DEFINITIONS

```
BIT0 = a1
BIT1 = a2
BIT2 = a4
BIT4 = a20
BIT5 = a40
BIT6 = a100
```

:ADDRESS EQUATES FOR CSR REGISTERS

```
BSEL4 = $14
BSEL5 = BSEL4+1
BSEL7 = BSEL4+3
```

:NPR ADDRESS REGISTER EQUATES

```
NPRAIL = $003C ;IN NPR ADRS LO REG
NPRAIH = NPRAIL+1 ;IN NPR ADRS HI REG
NPRAIX = NPRAIL+2 ;IN NPR EXTENDED ADRS REG
```

:NPR DATA REG EQUATES

```
NPRDIL = $A600 ;IN NPR DATA LO REG
NPRDIH = NPRDIL+1 ;IN NPR DATA HI REG
```

:NPR CONTROL REG EQUATES

```
NPRCTL = $A604 ;NPR CONTROL REGISTER
NONPR = BIT6 ;USED TO PREVENT AN NPR
INOUT = BIT5 ;SET TO 1 FOR INPUT, SET TO 0 FOR OUTPUT NPR
HALT = BIT4 ;SET DURING MOP MODE ONLY
PWRUP = BIT2 ;CLEARED BY BUS INIT TO INDICATE PWR UP
SDCLOW = BIT1 ;SET TO 1 TO RESET LSI-11 FOR MOP BOOT
DISINI = BIT0 ;SET TO 1 TO DISABLE BUS INIT TO 6502
```

:NPR REQUEST FUNCTIONS

```
NPPRED = PWRUP ;IN/OUT BIT = 0 FOR READ TO DMV-11
```

:MISCELLANEOUS EQUATES

```
STARAM = $003F ;STARTING ADRS OF GEN'L PURPOSE RAM TO TEST
```

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SUBTEST 1'S M-CODE -- ASSIGNMENTS

Address	Label	LOC	CODE	LINE	Comments
7526			:0047 0000		
7527			.SBTTL SUBTEST 1'S M-CODE -- ROUTINE		
7528			:0048 0000		*=STARAM ;START OF MICROCODE IN RAM
7529					
7530			:LINE# LOC CODE LINE		
7531					
7532	036054	251	000		
7533			.BYTE 251,00		
7534	036056	205	027		
7535			:0050 003F A9 00	LDA #0	;CLEAR BSEL7
7536			.BYTE 205,27		
7537	036060	245	027		
7538			:0051 0041 85 17	STA BSEL7	
7539	036062	360	374		
7540			:0052 0043		
7541			.BYTE 245,27		
7542			:0053 0043 A5 17	WAIT1 LDA BSEL7	;WAIT FOR IT TO GO <> 0
7543			.BYTE 360,374		
7544	036064	245	024		
7545			:0054 0045 F0 FC	BEQ WAIT1	
7546	036066	205	074		
7547			:0055 0047		; WE SHOULD NOW BE IN SYNC WITH THE 11 PROCESSOR
7548	036070	245	025		
7549			:0056 0047		
7550	036072	205	075		
7551			:0057 0047		
7552			.BYTE 245,24		
7553	036074	251	124		
7554			:0058 0047 A5 14	LDA BSEL4	;GET & SAVE THE ADDRESS
7555	036076	215	004		
7556			.BYTE 205,74		
7557			:0059 0049 85 3C	STA NPRAIL	;OF 'TMP0' AND USE IT TO
7558			.BYTE 245,25		
7559	036101	240	041		
7560			:0060 004B A5 15	LDA BSEL5	;SETUP FOR AN NPR-IN
7561	036103	210			
7562			:0061 004D 85 3D	STA NPRAIH	;OPERATION LATER
7563	036104	320	375		
7564			:0062 004F		
7565			.BYTE 251,124		
7566			:0063 004F A9 54	LDA #NONPR!HALT!PWRUP	
7567			.BYTE 215,4,246		
7568			:0064 0051 8D 04 A6	STA NPRCTL	;HALT THE 11 CPU
7569			.BYTE 240,41		
7570	036106	251	000		
7571			:0064 0054 A0 21	LDY #S21	;INITIAL VALUE OF COUNTER
7572	036110	205	076		
7573			.BYTE 210		
7574	036112	251	024		
7575			:0064 0056 83	DELAY DEY	; (33. FOR .6 US CYCLE)
7576	036114	215	004		
7577			.BYTE 320,375		
7578			:0064 0057 D0 FD	BNE DELAY	
7579	036117	054	004		
7580			:0065 0059		; WE NOW HAVE TO READ THE 11 CPU'S LOCATION WHO'S
7581	036122	160	373		; ADDRESS WE PREVIOUSLY READ FROM SEL6
			:0066 0059		
			.BYTE 251,00		
			:0069 0059 A9 00	LDA #0	;CLEAR THE EXTENDED-ADDRESS-IN
			.BYTE 205,76		
			:0070 005B 85 3E	STA NPRAIX	
			.BYTE 251,24		
			:0071 005D A9 14	LDA #NPRRED!HALT	
			.BYTE 215,4,246		
			:0072 005F 8D 04 A6	STA NPRCTL	;READ ONE WORD FROM THE 11 CPU
			.BYTE 54,4,246		
			:0073 0062		
			.BYTE 160,373		
			:0074 0062 2C 04 A6	NPRWAT BIT	;WAIT FOR IT TO 'ALMOST' COMPLETE

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SUBTEST 1'S M-CODE -- ROUTINE

```

7582 :0075 0065 70 FB          BVS   NPRWAT
7583 036124 352      .BYTE 352
7584 :0075 0067 EA              NOP                    ; SHOULD COMPLETE HERE
7585 :0076 0068
7586 036125 255      000 246 .BYTE 255,0,246
7587 :0077 0068 AD 00 A6        LDA   NPRDIL ;MOVE THE WORD JUST READ INTO
7588 036130 205      024 .BYTE 205,24
7589 :0078 0068 85 14          STA   BSEL4  ; SEL4
7590 036132 255      001 246 .BYTE 255,1,246
7591 :0079 006D AD 01 A6        LDA   NPRDIH
7592 036135 205      025 .BYTE 205,25
7593 :0080 0070 85 15          STA   BSEL5
7594 :0088 0072
7595 :0089 0072          ; DROP 'HALT' AND SET 'D'COK H LO' & 'DISABL INIT'
7596 :0090 0072
7597 036137 251      125 .BYTE 251,125
7598 :0091 0072 A9 55          LDA   #NONPR!PWRUP!DISINI!HALT
7599 036141 215      004 246 .BYTE 215,4,246
7600 :0092 0074 8D 04 A6        STA   NPRCTL
7601 036144 251      107 .BYTE 251,107
7602 :0093 0077 A9 47          LDA   #NONPR!PWRUP!SDCLOW!DISINI
7603 036146 215      004 246 .BYTE 215,4,246
7604 :0094 0079 8D 04 A6        STA   NPRCTL
7605 :0095 007C
7606 :0096 007C          ; NOW LET THE 11 CPU GO THROUGH THE POWER-UP SEQUENCE
7607 :0097 007C
7608 036151 251      105 .BYTE 251,105
7609 :0098 007C A9 45          LDA   #NONPR!PWRUP!DISINI
7610 036153 215      004 246 .BYTE 215,4,246
7611 :0099 007E 8D 04 A6        STA   NPRCTL
7612 :0100 0081
7613 :0101 0081          ; WHEN BSEL7 IS CLEARED, CLEAR 'DISABL INIT'
7614 :0102 0081
7615 036156 245      027 .BYTE 245,27
7616 :0103 0081 A5 17        WAIT2 LDA   BSEL7
7617 036160 320      374 .BYTE 320,374
7618 :0104 0083 D0 FC          BNE   WAIT2
7619 :0105 0085
7620 036162 251      104 .BYTE 251,104
7621 :0106 0085 A9 44          LDA   #NONPR!PWRUP
7622 036164 215      004 246 .BYTE 215,4,246
7623 :0107 0087 8D 04 A6        STA   NPRCTL
7624 :0108 008A
7625 :0109 008A          ; USE A STANDARD SUBROUTINE RETURN TO GET BACK INTO
7626 :0110 008A          ; THE MAINTENANCE LOOP
7627 :0111 008A
7628 036167 140      .BYTE 140
7629 :0112 008A 60            RTS
7630 :0113 008B
7631
7632
7633 ; ERRORS = 0000
7634 .SBTTL SUBTEST 1'S M-CODE -- SYMBOL TABLE
7635 ; BIT0      0001      BIT1      0002      BIT2      0004      BIT4      0010
7636
7637

```

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SUBTEST 1'S M-CODE -- SYMBOL TABLE

```

7638 : BITS 0020 BIT6 0040 BSEL4 0014 BSEL5 0015
7639 : BSEL7 0017 DELAY 006E DISINI 0001 HALT 0010
7640 : INOUT 0020 NONPR 0040 NPRAIH 003D NPRAIL 003C
7641 : NPRAIX 003E NPRCTL A604 NPRDIH A601 NPRDIL A600
7642 : NPRRED 0004 NPRWAT 005D PWRUP 0004 SDCLOW 0002
7643 : STARAM 003F WAIT1 0043 WAIT2 0078
7644 :END OF ASSEMBLY(V6A)
7645 :SYMBOLS LEFT = 1473 OUT OF 1500
7646
7647
7648
7649

```

.SBTTL SUBTEST 1'S M-CODE -- CROSS REFERENCE TABLE (CREF V01-05)

```

7650 :BIT0 5# 37
7651 :BIT1 6# 36
7652 :BIT2 7# 35
7653 :BIT4 8# 34
7654 :BITS 9# 33
7655 :BIT6 10# 32
7656 :BSEL4 14# 58 78
7657 :BSEL5 15# 60 80
7658 :BSEL7 16# 15 16 51 53 100
7659 :DELAY 86# 87
7660 :DISINI 37# 91 96
7661 :HALT 34# 63 71
7662 :INOUT 33#
7663 :NONPR 32# 63 91 96 103
7664 :NPRAIH 21# 61
7665 :NPRAIL 20# 21 22 59
7666 :NPRAIX 22# 70
7667 :NPRCTL 31# 64 72 74 92 104
7668 :NPRDIH 27# 79
7669 :NPRDIL 26# 27 77
7670 :NPRRED 41# 71
7671 :NPRWAT 74# 75
7672 :PWRUP 35# 41 63 91 96 103
7673 :SDCLOW 36# 91
7674 :STARAM 45# 48
7675 :WAIT1 53# 54
7676 :WAIT2 100# 101

```

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SUBTEST 1'S M-CODE -- CROSS REFERENCE TABLE (CREF V01-05)

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7702
7703
7704
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7720
7721
7722

036170 000010

```

.EVEN
.SBTTL SUBTEST 2'S M-CODE -- ASSIGNMENTS

:*****
:      MICRO-CODE FOR SUBROUTINE # 2
:*****
.RADIX 8.
MC2:
:LINE# LOC CODE LINE
:0002 0000 *=$0000
:0003 0000
:0004 0000 ;EQUATES FOR BIT DEFINITIONS
:0005 0000 BIT0 =a1
:0006 0000 BIT1 =a2
:0007 0000 BIT2 =a4
:0008 0000 BIT4 =a20
:0009 0000 BIT6 =a100
:0010 0000
:0011 0000
:0012 0000 ;ADDRESS EQUATES FOR CSR REGISTERS
:0013 0000 BSEL7 =$17
:0014 0000
:0015 0000
:0016 0000 ;NPR ADDRESS REGISTER EQUATES
:0017 0000 NPRAOL = $0038 ;OUT NPR ADRS LO REG
:0018 0000 NPRAOH = NPRAOL+1 ;OUT NPR ADRS HI REG
:0019 0000 NPRAOX = NPRAOL+2 ;OUT NPR EXTENDED ADRS REG
:0020 0000
:0021 0000
:0022 0000 ;NPR DATA REG EQUATES
:0023 0000 NPRDOL = $A600 ;OUT NPR DATA LO REG
:0024 0000 NPRDOH = NPRDOL+1 ;OUT NPR DATA HI REG
:0025 0000
:0026 0000
:0027 0000 ;NPR CONTROL REG EQUATES
:0028 0000 NPRCTL = $A604 ;NPR CONTROL REGISTER
:0029 0000 NONPR = BIT6 ;USED TO PREVENT AN NPR
:0030 0000 HALT = BIT4 ;SET DURING MOP MODE ONLY
:0031 0000 PWRUP = BIT2 ;CLEARED BY BUS INIT
:0032 0000 SDCLOW = BIT1 ;SET TO 1 TO RESET LSI-11 FOR MOP BOOT
:0033 0000 DISINI = BIT0 ;SET TO 1 TO DISABLE BUS INIT TO 6502
:0034 0000
:0035 0000
:0036 0000 ;MISCELLANEOUS EQUATES
:0037 0000 STARAM = $003F ;STARTING ADRS OF GEN'L PURPOSE RAM
:0038 0000

```

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SUBTEST 2'S M-CODE -- ROUTINE

```

7723 .SBTTL SUBTEST 2'S M-CODE -- ROUTINE
7724 :0039 0000
7725 :0040 0000      *=STARAM      ;START OF MICROCODE IN RAM
7726 036170 251 000      .BYTE 251,000
7727 :0042 003F A9 00      LDA #0 ;CLEAR BSEL7
7728 036172 205 027      .BYTE 205,027
7729 :0043 0041 85 17      STA BSEL7
7730 :0044 0043
7731 036174 245 027      .BYTE 245,027
7732 :0045 0043 A5 17      WAIT1 LDA BSEL7 ;WAIT FOR IT TO GO <> 0
7733 036176 360 374      .BYTE 360,374
7734 :0046 0045 F0 FC      BEQ WAIT1
7735 :0047 0047
7736 :0048 0047      ; WE SHOULD NOW BE IN SYNC WITH THE 11 PROCESSOR
7737 :0049 0047
7738 :0050 0047      ; THE NEXT SEQUENCE WILL SEND THE 11 CPU THROUGH
7739 :0051 0047      ; A POWER-UP SEQUENCE AND RESET EVERYTHING ELSE ON THE
7740 :0052 0047      ; Q-BUS. WE HAVE PREVENTED OURSELVES FROM BEING RESET
7741 :0053 0047      ; BY SETTING 'DISABL INIT'.
7742 :0054 0047
7743 036200 251 125      .BYTE 251,125
7744 :0054 0047 A9 55      LDA #NONPR!HALT!PWRUP!DISINI
7745 036202 215 004      246 .BYTE 215,004,246
7746 :0054 0049 8D 04 A6      STA NPRCTL
7747 :0054 004C
7748 :0054 004C      ; DELAY TO ALLOW 'HALT' TO TAKE EFFECT (ABOUT
7749 :0054 004C      ; 100 MICROSECONDS).
7750 036205 240 041      .BYTE 240,41
7751 :0054 004C A0 21      LDY #S21 ;INITIAL VALUE OF COUNTER
7752 036207 210          .BYTE 210
7753 :0054 004E 88          DELAY DEY ;(33. FOR .6 US CYCLE)
7754 036210 320 375      .BYTE 320,375
7755 :0054 004F D0 FD      BNE DELAY
7756 036212 251 127      .BYTE 251,127
7757 :0055 0051 A9 57      LDA #NONPR!HALT!PWRUP!SDCLOW!DISINI
7758 036214 215 004      246 .BYTE 215,004,246
7759 :0056 0053 8D 04 A6      STA NPRCTL ;HANG THE 11 CPU ETC.
7760 036217 251 165      .BYTE 251,165
7761 :0057 0056 A9 75      LDA #NONPR!HALT!PWRUP!DISINI!NPROUT
7762 036221 215 004      246 .BYTE 215,004,246
7763 :0058 0058 8D 04 A6      STA NPRCTL ;NOW LET IT 'POWER-UP'
7764 :0059 0058
7765 036224 251 000      .BYTE 251,000
7766 :0060 0058 A9 00      LDA #0 ;SETUP NPR ADDR OUT HIGH FOR
7767 036226 205 071      .BYTE 205,071
7768 :0061 005D 85 39      STA NPRAOH ; ALL NPR'S
7769 036230 205 072      .BYTE 205,072
7770 :0062 005F 85 3A      STA NPRAOX ;THE EXTENDED BYTE TOO
7771 036232 252          .BYTE 252
7772 :0063 0061 AA          TAX ;INITALIZE DATA TABLE INDEX
7773 :0064 0062
7774 :0065 0062      ; WE ARE NOW SETUP TO MOVE THE 'MOV' INSTRUCTION INTO
7775 :0066 0062      ; LOCATION 0 OF THE 11'S MEMORY
7776 :0067 0062
7777 036233 251 024      .BYTE 251,024
7778 :0068 0062 A9 14      LDA #A24 ;POINT TO 11'S POWER-UP VEC.

```

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SUBTEST 2'S M-CODE -- ROUTINE

```

7779 036235      205   070      .BYTE 205,070
7780           :0069 0064 85 38      STA  NPRAOL
7781 036237      040   305    000 .BYTE 040,305,000
7782           :0070 0066 20 C5 00      JSR  NPR      ;MOVE TO 24
7783 036242      040   305    000 .BYTE 040,305,000
7784           :0071 0069 20 C5 00      JSR  NPR      ;MOVE TO 26
7785           :0072 006C
7786 036245      251   000      .BYTE 251,000
7787           :0073 006C A9 00      LDA  #0
7788 036247      205   070      .BYTE 205,070
7789           :0074 006E 85 38      STA  NPRAOL ;POINT TO THE 11'S LOC. 0
7790 036251      040   305    000 .BYTE 040,305,000
7791           :0075 0070 20 C5 00      JSR  NPR      ;MOVE TO LOC 0
7792 036254      040   305    000 .BYTE 040,305,000
7793           :0076 0073 20 C5 00      JSR  NPR      ;MOVE TO LOC 2
7794 036257      040   305    000 .BYTE 040,305,000
7795           :0077 0076 20 C5 00      JSR  NPR      ;MOVE TO 4
7796           :0078 0079
7797           :0079 0079
7798           :0080 0079
7799           :0081 0079
7800           :0082 0079
7801           :0083 0079
7802 036262      251   107      .BYTE 251,107
7803           :0084 0079 A9 47      LDA  #NONPR!PWRUP!SDCLOW!DISINI
7804 036264      215   004    246 .BYTE 215,004,246
7805           :0085 007B 8D 04 A6      STA  NPRCTL
7806 036267      251   145      .BYTE 251,145
7807           :0086 007E A9 65      LDA  #NONPR!PWRUP!DISINI!NPROUT
7808 036271      215   004    246 .BYTE 215,004,246
7809           :0087 0080 8D 04 A6      STA  NPRCTL
7810 036274      352
7811           .BYTE 352
7812 036275      352
7813           :0087 0083 EA      NOP
7814           .BYTE 352
7815           :0087 0084 EA      NOP
7816           :0088 0085
7817 036276      040   305    000 .BYTE 040,305,000
7818           :0089 0085
7819 036301      240   166      .BYTE 240,166
7820           :0090 0085
7821 036303      040   305    000 .BYTE 040,305,000
7822           :0091 0085 20 C5 00      JSR  NPR      ;MOVE TO LOC 6
7823 036306      312
7824           .BYTE 312
7825 036307      312
7826           :0092 0088 A0 76      LDY  #@166 ;THIS IS HOW MANY WE'LL DO
7827 036310      210
7828           .BYTE 210
7829 036311      320   370      .BYTE 320,370
7830           :0093 008A 20 C5 00      FILOOP JSR  NPR      ;WRITE 1 WORD
7831 036313      240   006      .BYTE 240,006
7832           :0094 008D CA      DEX      ;BACK UP THE DATA POINTER
7833 036315      350
7834           .BYTE 350
7834           :0095 008E CA      DEX      ; -- WE WANT THE SAME WORD
7834           :0096 008F 88      DEY      ;IF NOT DONE,
7834           :0097 0090 D0 F8      BNE  FILOOP ; DO IT AGAIN
7834           :0098 0092 A0 06      LDY  #@6   ;ELSE, SETUP TO DO 6 MORE
7834           :0099 0094 E8      INX      ; WORDS

```

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SUBTEST 2'S M-CODE -- ROUTINE

```

7835 036316 350 .BYTE 350
7836 .:0100 0095 E8 INX ;POINT TO NEXT WORD
7837 036317 040 305 000 .:0101 0096 20 C5 00 ENDLOP JSR NPR ;MOVE THE LAST 6 WORDS
7838 .:0102 0099 88 DEY ;IF NOT DONE,
7839 036322 210 .:0103 009A D0 FA BNE ENDLOP ; DO IT AGAIN
7840 036323 320 372 .:0104 009C ;ELSE, THE SIMULATED 'DOWN-
7841 .:0105 009C ; LINE LOAD' IS COMPLETE
7842 .:0106 009C
7843 .:0107 009C A9 04 LDA #4 ; AND THE 11'S LOC. 4
7844 .:0108 009E 85 38 STA NPRAOL
7845 .:0109 00A0 20 C5 00 JSR NPR ;OVER-WRITE THE 'BR .'
7846 036325 251 004 .:0110 00A3 ;INSTRUCTION TO LET THE JUST
7847 .:0111 00A3 ;LOADED ROUTINE BE EXECUTED
7848 036327 205 070 .:0112 00A3
7849 .:0113 00A3 A9 44 LDA #NONPR!PWRUP
7850 036331 040 305 000 .:0114 00A5 8D 04 A6 STA NPRCTL ;LET BINIT RESET US AGAIN
7851 .:0115 00A8
7852 .:0116 00A8 60 RTS ;RETURN TO MAINTENANCE LOOP
7853 .:0117 00A9
7854 .:0118 00A9
7855 036334 251 104 .:0119 00A9
7856 .:0120 00A9
7857 036336 215 004 246 .:0121 00A9
7858 .:0122 00A9
7859 .:0123 00A9
7860 036341 140 .:0124 00A9
7861 .:0125 00A9
7862 .:0126 00A9
7863 .:0127 00A9
7864 .:0128 00A9 00 00 DATABL -- DATA TABLE CONTAINING THE DATA THAT
7865 .:0128 00AB 00 E0 ; IS TO BE NPR'D INTO THE 11'S MEMORY
7866 .:0129 00AD 15 C0
7867 .:0129 00AF FF FF
7868 .:0129 00B1 01 FF
7869 .:0130 00B3 0A 01
7870 .:0131 00B5 65 C1
7871 .:0132 00B7 10 1F
7872 .:0133 00B9 00 00
7873 036342 000000 .:0134 00BB 17 C6
7874 036344 000340 .DBYTE 0,a340 ;LOC'S 24 & 26
7875 .:0128 00AB 00 E0
7876 036346 012700 177777 MOV #-1,R0 .DBYTE @012700,-1,@777 ;LOC'S 0 --> 4
7877 .:0129 00AD 15 C0
7878 .:0129 00AF FF FF
7879 036352 000777 BR
7880 .:0129 00B1 01 FF
7881 036354 005001 CLR R1 .DBYTE @005001 ;LOC. 6
7882 .:0130 00B3 0A 01
7883 036356 062701 .WORD 062701 .DBYTE @062701 ;LOC'S 10 --> 362
7884 .:0131 00B5 65 C1
7885 036360 010037 002412 MOV R0,@TMP0 .DBYTE @010037 ;LOC 364 'MOV'
7886 .:0132 00B7 10 1F
7887 .:0133 00B9 00 00
7888 036364 013706 002454 MOV @#OLDSP,SP .DBYTE 0 ;LOC 366
7889 .:0134 00BB 17 C6
7890 .DBYTE @013706 ;LOC 370 'MOV'

```

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SUBTEST 2'S M-CODE -- ROUTINE

```

7891          :0135 00BD 00 00
7892 036370 000137 035672   JMP      @WHLTST2
7893          :0136 00BF 00 5F
7894          :0137 00C1 00 00
7895 036374 000240   NOP
7896          :0138 00C3 00 A0
7897          :0139 00C5
7898          :0140 00C5
7899          :0141 00C5
7900          :0142 00C5
7901          :0143 00C5
7902          :0144 00C5
7903          :0145 00C5
7904          :0146 00C5
7905          :0147 00C5
7906          :0148 00C5
7907          :0149 00C5
7908          :0150 00C5
7909          :0151 00C5
7910          :0152 00C5
7911          :0153 00C5
7912          :0154 00C5
7913          :0155 00C5
7914          :0156 00C5
7915          :0157 00C5
7916          :0158 00C5
7917          :0159 00C5
7918          :0160 00C5
7919          :0161 00C5
7920 036376      265      251   .BYTE    265,251
7921          :0162 00C5 B5 A9
7922 036400      215      000      246   .BYTE    215,000,246
7923          :0163 00C7 8D 00 A6
7924 036403      265      252   .BYTE    265,252
7925          :0164 00CA B5 AA
7926 036405      215      001      246   .BYTE    215,001,246
7927          :0165 00CC 8D 01 A6
7928          :0166 00CF
7929 036410      255      004      246   .BYTE    255,004,246
7930          :0167 00CF AD 04 A6
7931 036413      215      004      246   .BYTE    215,004,246
7932          :0168 00D2 8D 04 A6
7933          :0169 00D5
7934 036416      350
7935          :0170 00D5 E8
7936 036417      350   .BYTE    350
7937          :0171 00D6 E8
7938          :0172 00D7
7939          :0173 00D7
7940          :0174 00D7
7941 036420      054      004      246   .BYTE    054,004,246
7942          :0175 00D7 2C 04 A6
7943 036423      160      373   .BYTE    160,373
7944          :0176 00DA 70 FB
7945 036425      352   .BYTE    352
7946          :0177 00DC EA

```

```

.DBYTE 0          ;LOC 372
.DBYTE @000137 ;LOC 374 'JMP'
.DBYTE 0          ;LOC 376
.DBYTE @000240 ;'NOP' FOR LOC 4
: THE THREE WORDS FOR LOCATIONS 366, 372, & 376 ARE
: ASSEMBLED IN WHEN THIS CODE IS INCLUDED INTO THE
: DIAGNOSTIC

```

```

*****
'NPR' SUBROUTINE:
1 TAKE THE DATA FROM THE DATA TABLE AS INDEXED BY
  'X' AND PUT IT INTO THE NPR DATA OUT REGISTERS
2 GET THE CURRENT SETTING OF THE NPR CONTROL REG.
  AND WRITE IT BACK TO CAUSE A WORD NPR-OUT
3 INCREMENT THE NPR-OUT-ADDRESS-LOW REGISTER
4 WAIT FOR 'GOBUSY' TO GO LOW
5 RETURN TO CALLER
*****

```

```

NPR   LDA   DATABL,X ;LOAD THE DATA-OUT REG'S
      STA   NPRDOL
      LDA   DATABL+1,X
      STA   NPRDOH
      LDA   NPRCTL
      STA   NPRCTL ;KICK OFF A WORD NPR-OUT
      INX                      ;POINT TO THE NEXT DATA
      INX                      ; WORD
NPRWAT
      BIT   NPRCTL ;WAIT FOR THE NPR TO
      BVS  NPRWAT ; COMPLETE
      NOP

```

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SUBTEST 2'S M-CODE -- ROUTINE

7947 036426 346 070
 7948
 7949 036430 346 070
 7950
 7951
 7952 036432 140
 7953
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 7955
 7956
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 7959
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 7963
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 7980
 7981
 7982
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 7984
 7985
 7986
 7987
 7988
 7989
 7990
 7991
 7992
 7993
 7994
 7995
 7996
 7997
 7998
 7999
 8000
 8001
 8002 036433

```

    .BYTE 346,070
:0178 00DD E6 38      INC  NPRAOL ;POINT TO THE NEXT WORD
    .BYTE 346,070
:0179 00DF E6 38      INC  NPRAOL ;OF THE 11'S MEMORY
:0180 00E1
    .BYTE 140
:0181 00E1 60         RTS          ;RETURN TO CALLER
:0182 00E2
:
:
:ERRORS = 0000
:
:SBTTL SUBTEST 2'S M-CODE -- SYMBOL TABLE
:
:BIT0      0001      BIT1      0002      BIT2      0004      BIT4      0010
:BIT6      0040      BSEL7     0017      DATABL    009D      DISINI    0001
:ENDLOP    008A      FILOOP    007E      HALT      0010      NONPR     0040
:NPR        00B9      NPRAOH    0039      NPRAOL    0038      NPRAOX    003A
:NPRCTL     A604      NPRDOH    A601      NPRDOL    A600      NPRWAT    00CB
:PWRUP      0004      SDCLOW    0002      STARAM    003F      WAIT1     0043
:
:END OF ASSEMBLY(V6A)
:SYMBOLS LEFT = 1476 OUT OF 1500
:
:COMPAS MICROSYSTEMS MINMIC CROSS ASSEMBLER PAGE C-1
:SBTTL SUBTEST 2'S M-CODE -- CROSS REFERENCE TABLE (CREF V01-05 )
:
:BIT0      5#       33
:BIT1      6#       32
:BIT2      7#       31
:BIT4      8#       30
:BIT6      9#       29
:BSEL7     13#      43      45
:DATABL    127#     162      164
:DISINI     33#     55      57      84      86
:ENDLOP    101#    103
:FILOOP     93#     97
:HALT       30#     55      57
:NONPR      29#     55      57      84      86      113
:NPR         70     71      75      76      77      91      93
:
:NPRAOH     18#     61      162#
:NPRAOL     17#     18      19      69      74      108     178
:
:NPRAOX     19#     62
:NPRCTL     28#     56      58      85      87      114     167
:
:NPRDOH     24#     165
:NPRDOL     23#     24      163
:NPRWAT     174#    176
:PWRUP      31#     55      57      84      86      113
:SDCLOW     32#     55      84
:STARAM     37#     40
:WAIT1      45#     46
MC2END:

```

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SUBTEST 2'S M-CODE -- CROSS REFERENCE TABLE (CREF V01-05)

8003

036434

.EVEN

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HARDWARE PARAMETER CODING SECTION

.SBTTL HARDWARE PARAMETER CODING SECTION

```

://////
:/ THE HARDWARE PARAMETER CODING SECTION CONTAINS MACROS
:/ THAT ARE USED BY THE SUPERVISOR TO BUILD P-TABLES. THE
:/ MACROS ARE NOT EXECUTED AS MACHINE INSTRUCTIONS BUT ARE
:/ INTERPRETED BY THE SUPERVISOR AS DATA STRUCTURES. THE
:/ MACROS ALLOW THE SUPERVISOR TO ESTABLISH COMMUNICATIONS
:/ WITH THE OPERATOR.
://////

```

8004
8005
8006
8007
8008
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8010
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8012
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8015
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8018
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8020
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8051
8052
8053
8054
8055
8056

036434
036434 000030
036436
036436
036440 036516
036442 160020
036444 177776
036446
036446 001031
036450 036544
036452 000000
036454 000674
036456
036456 002032
036460 036575
036462 007000
036464 000000
036466 000007
036470
036470 007130
036472 036711
036474 000100
036476
036476 005032
036500 036626
036502 000007
036504 000000
036506 000002
036510
036510 007130
036512 036773
036514 000200
036516
036516
036516 042504 044526 042503
036544 042504 044526 042503
036575 104 053105 041511

BGNHRD

GPRMA ADDRES,0,0,160020,177776,YES

GPRMA VECTOR,2,0,0,674,YES

GPRMD PRIRTY,4,0,7000,0,7,YES

GPRML PU24.M,16,100,YES

GPRMD BDTY.M,12,0,7,0,2,YES

GPRML XMFG.M,16,200,YES

ENDHRD

.WORD L10070-LSHARD/2
LSHARD::

.WORD TSCODE
.WORD ADDRES
.WORD TSLOLIM
.WORD TSHILIM

.WORD TSCODE
.WORD VECTOR
.WORD TSLOLIM
.WORD TSHILIM

.WORD TSCODE
.WORD PRIRTY
.WORD 7000
.WORD TSLOLIM
.WORD TSHILIM

.WORD TSCODE
.WORD PU24.M
.WORD 100

.WORD TSCODE
.WORD BDTY.M
.WORD 7
.WORD TSLOLIM
.WORD TSHILIM

.WORD TSCODE
.WORD XMFG.M
.WORD 200

.EVEN
L10070:

.NLIST BEX
ADDRES: .ASCIZ /DEVICE CSR ADDRESS : /
VECTOR: .ASCIZ /DEVICE VECTOR ADDRESS : /
PRIRTY: .ASCIZ /DEVICE PRIORITY LEVEL : /

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HARDWARE PARAMETER CODING SECTION

036626	047502	051101	020104	BDTY.M: .ASCIZ	/BOARD TYPE (0=M8064, 1=M8053-V.35, 2=M8053-EIA) : /
036711	111	020123	044124	PU24.M: .ASCIZ	/IS THE PROCESSOR STRAPPED TO MODE 0 ON POWER-UP? /
036773	111	020123	044124	XMFG.M: .ASCIZ	/IS THIS A MANUFACTURING TEST STAND? /
8057	037040			.LIST	BEX
				.EVEN	

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SOFTWARE PARAMETER CODING SECTION

.SBTTL SOFTWARE PARAMETER CODING SECTION

8058
 8059
 8060
 8061
 8062
 8063
 8064
 8065
 8066
 8067
 8068
 8069
 8070
 8071
 8072
 8073
 8074
 8075
 8076

037040
 037040 000000
 037042
 037042
 037042

```

://////
:/ THE SOFTWARE PARAMETER CODING SECTION CONTAINS MACROS
:/ THAT ARE USED BY THE SUPERVISOR TO BUILD P-TABLES. THE
:/ MACROS ARE NOT EXECUTED AS MACHINE INSTRUCTIONS BUT ARE
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:/ MACROS ALLOW THE SUPERVISOR TO ESTABLISH COMMUNICATIONS
:/ WITH THE OPERATOR.
://////
    
```

BGNSFT
 ENDSFT

```

.LWORD L10071-LSSOFT/2
LSSOFT::
.EVEN
L10071:
    
```

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PATCH AREA FOR DEBUG

8077		
8078	037042	
8079		037142
8080	037142	000240
8081	037144	000240
8082	037146	000240
8083		
8084		
8085		
8086		
8087	037150	
8088	037150	
8089		
8090	037150	000000
8091	037152	000000
8092	037154	
8093		000001

.SBTTL PATCH AREA FOR DEBUG
PATCH:

.=.+100
NOP
NOP
NOP

.SBTTL 'ENDMOD' & 'LASTAD'
ENDMOD
LASTAD

LSLAST::
.END

.EVEN
.WORD 0
.WORD 0

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CROSS REFERENCE TABLE -- USER SYMBOLS

EVL = 000004	G	1426#												
EXECUT = 000005		1531#	6969	7256	7345									
ESEND = 002100		1173#												
ESLOAD = 000035		1173#	1271											
FMT02A 013127		3343	3669	3699#										
FMT06 013530		2984	3041	3699#										
FMT06A 013542		2974	3699#											
FMT07 013476		2968	3313	3699#										
FMT09 013621		3618	3656	3699#										
FMT09A 013673		3635	3676	3699#										
FMT10 013714		3329	3628	3699#										
FMT11 013770		2933	2950	3261	3281	3699#								
FMT12 007672		3175	3225#											
FMT12A 007732		3181	3225#											
FMT12D 010020		3198	3225#											
FMT12E 010063		3206	3225#											
FMT12F 010145		3216	3225#											
FMT13A 010546		3233	3300#											
FMT13B 010611		3239	3300#											
FMT13C 010656		3245	3300#											
FMT13D 010721		3251	3300#											
FMT13E 010764		3271	3291	3300#										
FMT16 014007		2999	3013	3031	3699#									
FMT16A 014032		3061	3081	3101	3699#									
FMT17 014074		3145	3699#											
FMT17A 014153		3153	3699#											
FMT17B 014234		3160	3699#											
FMT17C 014307		3137	3699#											
FMT4 013213		2923	3520	3699#										
FMT4A 013253		3534	3576	3699#										
FMT4B 013306		2940	3541	3562	3583	3699#								
FMT4C 013313		3555	3597	3699#										
FMT5 013346		2896	3699#											
FMT5A 013411		2913	3699#											
FMT50A 014367		3699#												
FMT50B 014441		3699#												
FMT50C 014522		3699#												
FMT50D 014562		3699#												
FMT50E 014577		3699#												
FMT50M 014614		3699#												
FMT51A 014673		3378	3699#											
FMT52A 011557		3413	3448#											
FMT52B 011607		3430	3448#											
FMT52C 011652		3438	3448#											
FMT52H 011514		3400	3448#											
FMT60 012034		3464	3492#											
FMT61 012073		3470	3492#											
FMT62 012134		3480	3492#											
FRSPAS 002304		1656#	3794*											
FRSTIM 002302		1655#	3889*											
FSAU = 000015		1173#	4005	4007										
FSAUTO= 000020		1173#	3921	3957										
F\$BGN = 000040		1173#	1175	2801	2835	2869	2878	2888	2963	3114	3123	3132	3173	3231
		3306	3358	3392	3462	3708	3721	3921	3971	3989	4005	4053	4055	4064
		4073	4078	4098	4103	4113	4131	4149	4161	4169	4190	4195	4215	4220
		4240	4250	4259	4264	4285	4290	4309	4314	4324	4337	4355	4359	4423

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CROSS REFERENCE TABLE -- USER SYMBOLS

4429	4441	4452	4461	4466	4476	4492	4502	4511	4525	4540	4549	4558
4567	4584	4593	4603	4617	4626	4630	4663	4669	4712	4757	4764	4779
4833	4840	4846	4907	4913	4974	4977	5001	5007	5072	5092	5096	5103
5116	5125	5134	5143	5152	5163	5168	5175	5188	5197	5206	5215	5224
5235	5238	5262	5268	5275	5285	5320	5334	5343	5357	5372	5381	5395
5409	5419	5430	5439	5448	5467	5478	5488	5493	5527	5548	5564	5573
5587	5614	5623	5642	5656	5667	5670	5792	5849	5854	5862	5868	5897
5946	5981	5996	6004	6013	6041	6096	6136	6152	6169	6180	6183	6235
6241	6262	6274	6322	6344	6356	6404	6426	6438	6486	6508	6520	6568
6584	6596	6622	6638	6650	6676	6681	6697	6706	6740	6757	6763	6782
6804	6809	6815	6831	6838	6933	6943	6951	7009	7012	7207	7212	7218
7232	7239	7290	7311	7315	7322	7410	7465	7468	8018	8072	8088	
1173#	3971	3979										
1173#	3989	3994										
1173#	1175	2822	2856	2874	2884	2959	3110	3119	3128	3169	3225	3300
3353	3388	3447	3492	3893	3959	3981	3996	4009	4053	4055	4064	4073
4078	4098	4103	4113	4131	4149	4161	4169	4190	4195	4215	4220	4240
4250	4259	4264	4285	4290	4309	4314	4324	4337	4355	4357	4359	4361
4423	4429	4441	4452	4461	4466	4476	4492	4502	4511	4525	4540	4549
4558	4567	4584	4593	4603	4617	4626	4630	4632	4663	4669	4712	4757
4764	4799	4801	4833	4840	4846	4907	4909	4913	4974	4976	4977	4979
5001	5007	5072	5074	5092	5096	5103	5116	5125	5134	5143	5152	5163
5165	5168	5175	5188	5197	5206	5215	5224	5235	5237	5238	5240	5262
5268	5275	5285	5320	5334	5343	5357	5372	5381	5395	5409	5419	5430
5439	5448	5467	5478	5488	5490	5493	5527	5548	5564	5573	5587	5614
5623	5642	5656	5667	5669	5670	5672	5825	5849	5854	5862	5868	5897
5946	5981	5996	6004	6006	6013	6041	6096	6136	6152	6169	6180	6182
6183	6185	6235	6241	6262	6274	6322	6324	6344	6356	6404	6406	6426
6438	6486	6488	6508	6520	6568	6570	6584	6596	6622	6624	6638	6650
6676	6678	6681	6683	6697	6706	6740	6742	6757	6763	6782	6784	6804
6809	6815	6831	6838	6933	6935	6943	6951	7009	7011	7012	7014	7207
7212	7218	7232	7239	7290	7311	7313	7315	7322	7410	7465	7467	7468
7470	8055	8077	8088									
1173#	8018	8053										
1173#	1323	1349										
1173#	3721	3891										
1173#	5268	5275	5854	5862	6241	6809	6815	7212	7218			
1173#	1175	8088										
1173#	2869	2872	2878	2882	2888	2957	2963	3108	3114	3117	3123	3126
3132	3167	3173	3223	3231	3298	3306	3351	3358	3386	3392	3445	3462
3490												
1173#	3708	3713										
1173#												
1173#												
1173#												
1173#	8072	8075										
1173#	2801	2820	2835	2854	5792	5823						
1173#	4056	4355	4847	4907	4914	4974	5097	5163	5169	5235	5286	5488
5494	5667	5869	6004	6014	6180	6263	6322	6345	6404	6427	6486	6509
6568	6585	6622	6639	6676	6832	6933	6944	7009	7233	7311	7316	7465
1173#	1358	1362										
1173#	4054	4359	4424	4630	4664	4799	4834	4977	5002	5072	5093	5238
5263	5670	5850	6183	6236	6681	6698	6740	6758	6782	6805	7012	7208
7468												
1642#	2030*	2070*	2113*	2176*	2890	2912	2990	3019	3046	3191	3328	3342
3506	3627	3668	4687*	4729*	4730*	4737	4770*	4779	4883*	4950*	5046*	5308*

FSCLEA= 000007
FSDU = 000016
FSEND = 000041

FSHARD= 000004
FSHW = 000013
FSINIT= 000006
FSJMP = 000050
FSMOD = 000000
FMSG = 000011

FSPROT= 000021
FSPWR = 000017
FSRPT = 000012
FSSEG = 000003
FSSOFT= 000005
FSSRV = 000010
FSSUB = 000002

FSSW = 000014
FSTEST= 000001

GDATA 002254

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CROSS REFERENCE TABLE -- USER SYMBOLS

	5385*	5399*	5400*	5404	5469	5515*	5577*	5594*	5595*	5600	5603	5647	5928*
	5934*	5936	5985*	5987	6078*	6084*	6086	6100*	6105	6108	6279*	6289*	6303*
	6313*	6361*	6371*	6385*	6395*	6443*	6453*	6467*	6477*	6525*	6535*	6549*	6559*
	6603*	6613*	6657*	6667*	7427*	7440*	7451*						
GETBSR 004232	1990	2237#	2805	2839									
GETPRM 022230	3748	3798#	3807										
GETSR 025022	4463	4632#											
GETT2 024134	4075	4100	4166	4192	4217	4237	4261	4287	4311	4334	4390#		
GETWSR 004374	2029	2069	2112	2175	2258#								
GSCNTO= 000200	1173#												
GSDLM= 000372	1173#	6819	7222										
GSDISP= 000003	1173#												
GSEXCP= 000400	1173#												
GSHILI= 000002	1173#												
GSLOLI= 000001	1173#												
GSND = 000000	1173#												
GSOFFS= 000400	1173#	8022	8027	8032	8038	8042	8048						
GSOFSI= 000376	1173#	8022	8027	8032	8038	8042	8048						
GSPRMA= 000001	1173#	8022	8027										
GSPRMD= 000002	1173#	8032	8042										
GSPRML= 000000	1173#	8038	8048										
GSRADA= 000140	1173#												
GSRADB= 000000	1173#												
GSRADD= 000040	1173#												
GSRADL= 000120	1173#	8038	8048										
GSRADO= 000020	1173#	8022	8027	8032	8042								
GSXFER= 000004	1173#												
GSYES = 000010	1173#	8022	8027	8032	8038	8042	8048						
HELP = 000000	1165#	1192	1289	1314	1933								
HLTST= 000015	3854	7208#											
HLTST2 035672	7412#	7892											
HOE = 100000	1439#												
IBE = 010000	1436#												
IDU = 000040	1429#												
IENBA = 000001	1508#	6298	6380	6462	6544	6588	6642	6849	6903				
IENBB = 000020	1509#	6298	6380	6462	6544	6588	6642						
IENR = 120016	1576#	2316	2398	4455	4496	4505							
IER = 020000	1437#												
IFR = 120015	1575#	4391	4534	4578	4611	4633							
IFRCA1= 000002	1587#												
IFRCA2= 000001	1588#												
IFRCB1= 000020	1584#												
IFRCB2= 000010	1585#												
IFRIRQ= 000200	1581#												
IFRSR = 000004	1586#												
IFRT1 = 000100	1582#												
IFRT2 = 000040	1583#												
IHLNK 006136	2815	2817	2823#	3878*									
IHLNK 006210	2849	2851	2857#	3886*									
INITT1 004456	2295#												
INITT2 004630	2376#	4066	4347										
INTFLG 002272	1649#	2814*	2848*	6265*	6277	6287	6297*	6301	6311	6347*	6359	6369	6379*
	6383	6393	6429*	6441	6451	6461*	6465	6475	6511*	6523	6533	6543*	6547
	6557	6587*	6601	6611	6641*	6655	6665	6848*					
INTWCH 002274	1651#	2803	2837	3887*	6264*	6346*	6428*	6510*	6586*	6640*	6679*	6847*	6904*
IRQA = 000004	1484#	6269	6433	6645	6858								

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CROSS REFERENCE TABLE -- USER SYMBOLS

LSAUTO	023014	G	1279	3921#	
LSCCP	002106	G	1276#		
LSCLEA	023140	G	1277	3971#	
LSCO	002032	G	1232#		
LSDEPO	002011	G	1214#		
LSDESC	003274	G	1269	1922#	
LSDESP	002076	G	1268#		
LSDEVP	002060	G	1254#		
LSDISP	002124	G	1239	1299#	
LSDLY	002116	G	1284#	6821	7224
LSDTP	002040	G	1238#		
LSDTYP	002034	G	1234#		
LSDU	023156	G	1265	3989#	
LSDUT	002072	G	1264#		
LSDVTY	003254	G	1255	1910#	
LSEF	002052	G	1249#		
LEENVI	002044	G	1242#		
LSERRT	002202	G	1273	1604#	
LSETP	002102	G	1272#		
LSEXP1	002046	G	1244#		
LSEXP4	002064	G	1258#		
LSEXP5	002066	G	1260#		
LSHARD	036436	G	1221	8018	8019#
LSHIME	002120	G	1286#	5270	
LSHPCP	002016	G	1220#		
LSHPTP	002022	G	1224#		
LSHW	002160	G	1225	1323	1324#
LSICP	002104	G	1274#		
LSINIT	022020	G	1275	3721#	
LSLADP	002026	G	1228#		
LSLAST	037154	G	1229	8092#	
LSLOAD	002100	G	1270#		
LSLUN	002074	G	1266#		
LSPREV	002050	G	1246#		
LSNAME	002000	G	1203#		
LSPRIO	002042	G	1240#		
LSPROT	022012	G	1281	3708#	
LSPRT	002112	G	1280#		
LSREPP	002062	G	1256#		
LSREV	002010	G	1212#		
LSSOFT	037042	G	8072	8073#	
LSSPC	002056	G	1252#		
LSSPCP	002020	G	1222#		
LSSPTP	002024	G	1226#		
LSSTA	002030	G	1230#		
LSSW	002202	G	1358	1359#	
LSTEST	002114	G	1282#		
LSTIML	002014	G	1218#		
LSUNIT	002012	G	1216#		
L10000	002200		1323	1349#	
L10001	002202		1358	1362#	
L10002	006134		2820#		
L10003	006206		2854#		
L10004	006216		2872#		
L10005	006230		2882#		
L10006	006530		2957#		

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CROSS REFERENCE TABLE -- USER SYMBOLS

SEL16	002354	1732#	2265												
SEL2	002324	1714#	2259	6969*	7256*	7345*									
SEL4	002330	1717#	2058*	2101*	2143*	2164*	2260	6857*	6968*	7254*	7270*	7300	7343*		
SEL6	002334	1720#	2124	2144*	2165*	2261	6856*								
SFPTBL	002202 G	1360#													
SLT0 =	000020	1545#	1546	1547	1548	1549	1550	1551	1552	1553	1554	1555	1556		
SLT2 =	000022	1548#													
SLT4 =	000024	1551#													
SLT6 =	000026	1554#													
SMCODE	035006	6963	6964	7018#											
SR =	120012	1572#	4470	4518	4543	4561	4597								
STALL	005132	2537#	4605	4606	4607										
STARES	002306	1657#	3754*	3787*	3793*	3848									
STARST	022072	3729	3754#												
SVCGBL=	000000	1173#	1175	1182#	1203	1212	1214	1216	1218	1220	1222	1224	1226	1228	
		1230	1232	1234	1236	1238	1240	1242	1244	1246	1249	1252	1254	1256	
		1258	1260	1262	1264	1266	1268	1270	1272	1274	1276	1278	1280	1282	
		1284	1286	1299	1324	1325	1359	1360	1304	1910	1922	2801	2835	2869	
		2878	2888	2963	3114	3123	3132	3173	3231	3306	3358	3392	3462	3708	
		3721	3921	3971	3989	4005	5792	8019	8073	8092#	8093				
SVCINS=	000001	1173#	1179#	1204	1205	1206	1207	1208	1209	1210	1211	1213	1215	1217	
		1219	1221	1223	1225	1227	1229	1231	1233	1235	1237	1239	1241	1243	
		1245	1247	1248	1250	1251	1253	1255	1257	1259	1261	1263	1265	1267	
		1269	1271	1273	1275	1277	1279	1281	1283	1285	1287	1298	1300	1301	
		1302	1303	1304	1305	1306	1307	1308	1309	1310	1311	1312	1323	1358	
		1911	1914	1923	1930	1993	1994	1995	1996	2033	2034	2035	2036	2073	
		2074	2075	2076	2116	2117	2118	2119	2179	2180	2181	2182	2732	2733	
		2734	2735	2808	2809	2810	2811	2821	2842	2843	2844	2845	2855	2873	
		2883	2894	2895	2896	2897	2898	2899	2900	2910	2911	2912	2913	2914	
		2915	2916	2917	2921	2922	2923	2924	2925	2926	2927	2929	2930	2931	
		2932	2933	2934	2935	2936	2937	2939	2940	2941	2942	2943	2944	2946	
		2947	2948	2949	2950	2951	2952	2953	2954	2958	2966	2967	2968	2969	
		2970	2971	2972	2974	2975	2976	2977	2978	2983	2984	2985	2986	2987	
		2988	2992	2993	2994	2995	2996	2997	2998	2999	3000	3001	3002	3003	
		3006	3007	3008	3009	3010	3011	3012	3013	3014	3015	3016	3017	3024	
		3025	3026	3027	3028	3029	3030	3031	3032	3033	3034	3035	3040	3041	
		3042	3043	3044	3045	3048	3049	3050	3051	3052	3053	3054	3055	3056	
		3057	3058	3059	3060	3061	3062	3063	3064	3065	3068	3069	3070	3071	
		3072	3073	3074	3075	3076	3077	3078	3079	3080	3081	3082	3083	3084	
		3085	3088	3089	3090	3091	3092	3093	3094	3095	3096	3097	3098	3099	
		3100	3101	3102	3103	3104	3105	3109	3118	3127	3136	3137	3138	3139	
		3140	3141	3143	3144	3145	3146	3147	3148	3149	3151	3152	3153	3154	
		3155	3156	3157	3159	3160	3161	3162	3163	3164	3168	3175	3176	3177	
		3178	3179	3181	3182	3183	3184	3185	3189	3190	3191	3192	3193	3194	
		3195	3196	3197	3198	3199	3200	3201	3202	3206	3207	3208	3209	3210	
		3216	3217	3218	3219	3220	3224	3224	3233	3234	3235	3236	3237	3239	
		3241	3242	3243	3245	3246	3247	3248	3249	3251	3252	3253	3254	3255	
		3257	3258	3259	3260	3261	3262	3263	3264	3265	3267	3268	3269	3270	
		3271	3272	3273	3274	3275	3277	3278	3279	3280	3281	3282	3283	3284	
		3285	3287	3288	3289	3290	3291	3292	3293	3294	3295	3299	3311	3312	
		3313	3314	3315	3316	3317	3326	3327	3328	3329	3330	3331	3332	3333	
		3337	3338	3339	3340	3341	3342	3343	3344	3345	3346	3347	3352	3373	
		3374	3375	3376	3377	3378	3379	3380	3381	3382	3387	3400	3401	3402	
		3403	3404	3406	3407	3408	3409	3410	3411	3412	3413	3414	3415	3416	
		3417	3419	3420	3421	3422	3423	3424	3425	3426	3427	3428	3429	3430	
		3431	3432	3433	3434	3438	3439	3440	3441	3442	3446	3464	3465	3466	

CROSS REFERENCE TABLE -- USER SYMBOLS

3467	3468	3470	3471	3472	3473	3474	3477	3478	3479	3480	3481	3482
3483	3484	3491	3518	3519	3520	3521	3522	3523	3524	3526	3527	3528
3529	3530	3531	3532	3533	3534	3535	3536	3537	3538	3540	3541	3542
3543	3544	3545	3547	3548	3549	3550	3551	3552	3553	3554	3555	3556
3557	3558	3559	3561	3562	3563	3564	3565	3566	3568	3569	3570	3571
3572	3573	3574	3575	3576	3577	3578	3579	3580	3582	3583	3584	3585
3586	3587	3589	3590	3591	3592	3593	3594	3595	3596	3597	3598	3599
3600	3601	3610	3611	3612	3613	3614	3617	3618	3619	3620	3621	3622
3625	3626	3627	3628	3629	3630	3631	3632	3634	3635	3636	3637	3638
3639	3648	3649	3650	3651	3652	3655	3656	3657	3658	3659	3660	3663
3664	3665	3666	3667	3668	3669	3670	3671	3672	3673	3675	3676	3677
3678	3679	3680	3688	3689	3690	3691	3692	3726	3727	3729	3732	3733
3735	3738	3739	3741	3744	3745	3747	3760	3761	3762	3763	3764	3765
3768	3769	3778	3779	3780	3781	3782	3783	3801	3802	3803	3805	3854
3855	3856	3857	3858	3859	3860	3862	3863	3864	3865	3866	3872	3873
3874	3875	3876	3877	3880	3881	3882	3883	3884	3885	3892	3924	3925
3926	3927	3928	3929	3946	3947	3951	3952	3958	3973	3974	3976	3977
3980	3992	3995	4008	4056	4062	4064	4065	4071	4073	4074	4078	4079
4083	4084	4085	4086	4096	4098	4099	4103	4104	4108	4109	4110	4111
4113	4114	4129	4131	4132	4137	4138	4139	4140	4147	4149	4150	4156
4157	4158	4159	4161	4162	4169	4170	4175	4176	4177	4178	4188	4190
4191	4195	4196	4200	4201	4202	4203	4213	4215	4216	4220	4221	4225
4226	4227	4228	4240	4241	4245	4246	4247	4248	4250	4251	4257	4259
4260	4264	4265	4269	4270	4271	4272	4283	4285	4286	4290	4291	4295
4296	4297	4298	4307	4309	4310	4314	4315	4319	4320	4321	4322	4324
4325	4337	4338	4342	4343	4344	4345	4352	4356	4360	4395	4427	4429
4430	4439	4441	4442	4450	4452	4453	4459	4461	4462	4466	4467	4474
4476	4477	4480	4481	4482	4483	4490	4492	4493	4500	4502	4503	4509
4511	4512	4523	4525	4526	4538	4540	4541	4547	4549	4550	4553	4554
4555	4556	4558	4559	4565	4567	4568	4582	4584	4585	4588	4589	4590
4591	4593	4594	4601	4603	4604	4615	4617	4618	4621	4622	4623	4624
4626	4627	4631	4637	4667	4669	4670	4675	4707	4708	4709	4710	4712
4713	4719	4726	4735	4752	4753	4754	4755	4757	4758	4762	4764	4765
4777	4794	4795	4796	4797	4800	4838	4840	4841	4847	4858	4894	4895
4896	4897	4908	4914	4925	4961	4962	4963	4964	4975	4978	5005	5007
5008	5021	5058	5059	5060	5061	5073	5097	5101	5103	5104	5114	5116
5117	5123	5125	5126	5132	5134	5135	5141	5143	5144	5150	5152	5153
5158	5159	5160	5161	5164	5169	5173	5175	5176	5186	5188	5189	5195
5197	5198	5204	5206	5207	5213	5215	5216	5222	5224	5225	5230	5231
5232	5233	5236	5239	5266	5268	5269	5275	5276	5286	5315	5316	5317
5318	5320	5321	5332	5334	5335	5341	5343	5344	5355	5357	5358	5370
5372	5373	5379	5381	5382	5390	5391	5392	5393	5395	5396	5407	5409
5410	5417	5419	5420	5428	5430	5431	5437	5439	5440	5446	5448	5449
5455	5456	5457	5458	5462	5463	5464	5465	5467	5468	5473	5474	5475
5476	5478	5479	5489	5494	5522	5523	5524	5525	5527	5528	5546	5548
5549	5562	5564	5565	5571	5572	5574	5582	5583	5584	5585	5587	5588
5612	5614	5615	5621	5623	5624	5630	5631	5632	5633	5637	5638	5639
5640	5642	5643	5651	5652	5653	5654	5656	5657	5668	5671	5703	5704
5705	5706	5707	5708	5815	5816	5817	5818	5824	5854	5855	5860	5862
5863	5869	5895	5897	5898	5941	5942	5943	5944	5946	5947	5969	5970
5971	5972	5976	5977	5978	5979	5981	5982	5991	5992	5993	5994	5996
5997	6005	6014	6039	6041	6042	6091	6092	6093	6094	6096	6097	6124
6125	6126	6127	6131	6132	6133	6134	6136	6137	6150	6152	6153	6164
6165	6166	6167	6169	6170	6181	6184	6239	6241	6242	6263	6272	6274
6275	6282	6283	6284	6285	6292	6293	6294	6295	6306	6307	6308	6309
6316	6317	6318	6319	6323	6345	6354	6356	6357	6364	6365	6366	6367

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CROSS REFERENCE TABLE -- USER SYMBOLS

		6374	6375	6376	6377	6388	6389	6390	6391	6398	6399	6400	6401	6405
		6427	6436	6438	6439	6446	6447	6448	6449	6456	6457	6458	6459	6470
		6471	6472	6473	6480	6481	6482	6483	6487	6509	6518	6520	6521	6528
		6529	6530	6531	6538	6539	6540	6541	6552	6553	6554	6555	6562	6563
		6564	6565	6569	6585	6594	6596	6597	6606	6607	6608	6609	6616	6617
		6618	6619	6623	6639	6648	6650	6651	6660	6661	6662	6663	6670	6671
		6672	6673	6677	6682	6704	6706	6707	6727	6734	6735	6736	6737	6741
		6761	6763	6764	6777	6778	6779	6780	6783	6809	6810	6815	6816	6819
		6820	6821	6822	6823	6824	6825	6826	6832	6836	6838	6839	6841	6842
		6843	6844	6845	6846	6879	6880	6881	6882	6906	6907	6909	6910	6923
		6924	6925	6926	6930	6934	6944	6949	6951	6952	6954	6955	6956	6957
		6958	6959	6980	6981	6982	6983	6989	6990	6992	6993	6998	6999	7000
		7001	7006	7010	7013	7212	7213	7218	7219	7222	7223	7224	7225	7226
		7227	7228	7229	7233	7237	7239	7240	7248	7249	7250	7251	7252	7253
		7264	7265	7266	7267	7280	7281	7282	7283	7288	7290	7291	7295	7296
		7298	7299	7304	7305	7306	7307	7312	7316	7320	7322	7323	7335	7336
		7337	7338	7339	7340	7353	7354	7355	7356	7376	7377	7381	7382	7383
		7384	7394	7395	7399	7400	7401	7402	7407	7410	7411	7422	7423	7430
		7431	7432	7433	7443	7444	7445	7446	7454	7455	7456	7457	7462	7466
		7469	8018	8022	8023	8024	8025	8027	8028	8029	8030	8032	8033	8034
		8035	8036	8038	8039	8040	8042	8043	8044	8045	8046	8048	8049	8050
		8053	8072	8075	8089	8090	8091							
SVCSUB=	000001	1173#	1181#	4055	4846	4913	5096	5168	5285	5493	5868	6013	6262	6344
SVCTAG=	000001	1173#	1183#	1349	1362	2820	2854	2872	2882	2957	3108	3117	3126	3167
		3223	3298	3351	3386	3445	3490	3891	3957	3979	3994	4007	4355	4359
		4630	4799	4907	4974	4977	5072	5163	5235	5238	5488	5667	5670	5823
		6004	6180	6183	6322	6404	6486	6568	6622	6676	6681	6740	6782	6933
SVCTST=	000001	7009	7012	7311	7465	7468	8054	8076						
		1173#	1180#	4053	4423	4663	4833	5001	5092	5262	5849	6235	6697	6757
		6804	7207											
SWPBOT=	121000	1498#												
SWPPDC=	121400	1499#												
SSL SYM=	010000	1173#	1350#	1363#	2821#	2855#	2873#	2883#	2958#	3109#	3118#	3127#	3168#	3224#
		3299#	3352#	3387#	3446#	3491#	3892#	3958#	3980#	3995#	4008#	4356#	4360#	4631#
		4800#	4908#	4975#	4978#	5073#	5164#	5236#	5239#	5489#	5668#	5671#	5824#	6005#
		6181#	6184#	6323#	6405#	6487#	6569#	6623#	6677#	6682#	6741#	6783#	6934#	7010#
		7013#	7312#	7466#	7469#	8055#	8077#							
TDATA	002252	1641#	3435	3634	3675	4432*	4528*	4885*	4902	4952*	4969	5048*	5066	
TMPA	002436	1763#	3411	3428	4431*	4471	4513*	4516*	4519	4544	4562	4572	4575	4598
		5280*	5361	5482	5552	5660	5887*	5888*	5889*	5891	6031*	6032*	6033*	6035
		6142*	6143*	6144*	6146									
TMPB	002440	1764#	2298*	2299*	2322	2325	2327*	2331	2379*	2380*	2404	2407	2409*	2413
		3360	3395	3409	3426	4436	4443*	4444*	4447	4484*	4487	5886*	5892	6030*
		6036	6141*	6147										
TMPC	002442	1765#	3398	3424	5288*	5403	5480*	5482	5495*	5592	5645	5658*	5660	
TMPD	002444	1766#	3422	4392	4400	4535	4579	4612	4634	4642				
TMPE	002446	1767#	2313*	2317	2395*	2399	3407	3420	4456	4494*	4497	4506	5349*	5351
		5359*	5361	5540*	5542	5550*	5552							
TMPF	002450	1768#	3197	5289*	5367	5376	5383	5385	5386	5484*	5496*	5559	5568	5575
		5577	5578	5662*										
TMPO	002412	1753#	2721*	2722	3133	3136	3260	3930*	3948	3960*	4859	4926	5022	5105*
		5177*	5802*	7246*	7270	7273*	7300	7360*	7437	7886*				
TMP1	002414	1754#	2726*	3144	3213	3259	5108*	5138	5180*	5210	5329	5338	5345	5537*
		5538*	5593	5803*										
TMP2	002416	1755#	2727*	3152	3193	3258	5147	5154	5219	5226	5411*	5414	5425	5450

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CROSS REFERENCE TABLE -- USER SYMBOLS

TMP3	002420	5604*	5609	5618	5625	5804*	5950*	5954	5957	5964	6111*	6115	6118	6119
		1756#	2728*	2729*	3159	3203	3257	5107*	5111	5120	5179*	5183	5192	5345*
		5346*	5352	5399	5529*	5536*	5537	5543	5594	5805*				
TMP4	002422	1757#	3270	5807*										
TMP5	002424	1758#	3269	5809*										
TMP6	002426	1759#	2296*	2336	3268	5730*	5761*							
TMP7	002430	1760#	2297*	2341	3267	5811*								
TMP8	002432	1761#	2377*	2418	3374	4126	4133	4210	4280	4374*	4378			
TMP9	002434	1762#	2378*	2423	3376	4090*	4093	4144	4151	4185	4254	4304	4375*	4381
		5287*	5301	5304	5305	5307	5310	5508	5511	5512	5514	5517	5924	5935
		6074	6085											
TXTMLT	021746	2910	3699#											
TXTML0	015515	3699#												
TXTML1	015521	3699#												
TXTML2	015535	3699#												
TXTML3	015552	3699#												
TXTML4	015574	3699#												
TXTML5	015615	3699#												
TXTML6	015645	3699#												
TXTML7	015657	3699#												
TXTNP	015745	2967	3312	3699#										
TXTNPT	021770	2966	3311	3699#										
TXTNP0	015752	3699#												
TXTNP1	015762	3699#												
TXTNP2	015772	3699#												
TXTNP3	016002	3699#												
TXTNP4	016017	3699#												
TXTNP5	016034	3699#												
TXTNP6	016051	3699#												
TXTNP7	016065	3699#												
TXTNP8	016101	3699#												
TXTNUL	015513	3699#												
TXT1	014761	3518	3699#											
TXT11A	015437	2983	3699#											
TXT11B	015460	3040	3699#											
TXT2	015017	3540	3699#											
TXT2A	015061	3561	3699#											
TXT2B	015120	3582	3699#											
TXT3	015162	3519	3699#											
TXT4	015212	2921	3699#											
TXT4A	015252	2939	3699#											
TXT6	015313	2922	3699#											
TXT8A	015336	2998	3060	3699#										
TXT8B	015353	3012	3080	3699#										
TXT8C	015370	3030	3100	3699#										
TXT8D	015405	3412	3699#											
TXT8E	015422	3429	3699#											
TSARGC=	000001	1204#	1205#	1206#	1207#	1208#	1209#	2894#	2900	2910#	2917	2921#	2927	2929#
		2937	2939#	2944	2946#	2954	2966#	2972	2974#	2978	2983#	2988	2992#	3003
		3006#	3017	3024#	3035	3040#	3045	3048#	3065	3068#	3085	3088#	3105	3136#
		3141	3143#	3149	3151#	3157	3159#	3164	3175#	3179	3181#	3185	3189#	3202
		3206#	3210	3216#	3220	3233#	3237	3239#	3243	3245#	3249	3251#	3255	3257#
		3265	3267#	3275	3277#	3285	3287#	3295	3311#	3317	3326#	3333	3337#	3347
		3373#	3382	3400#	3404	3406#	3417	3419#	3434	3438#	3442	3464#	3468	3470#
		3474	3477#	3484	3518#	3524	3526#	3538	3540#	3545	3547#	3559	3561#	3566
		3568#	3580	3582#	3587	3589#	3601	3610#	3614	3617#	3622	3625#	3632	3634#

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CROSS REFERENCE TABLE -- USER SYMBOLS

	3639	3648#	3652	3655#	3660	3663#	3673	3675#	3680	3688#	3692	3778#	3783
	3854#	3860	3862#	3866									
TSCODE= 007130	8022#	8027#	8032#	8038#	8042#	8048#							
TSERRN= 000126	1173#	2809#	2843#	4084#	4109#	4138#	4157#	4176#	4201#	4226#	4246#	4270#	4296#
	4320#	4343#	4481#	4554#	4589#	4622#	4708#	4753#	4795#	4895#	4962#	5059#	5159#
	5231#	5316#	5391#	5456#	5463#	5474#	5523#	5583#	5631#	5638#	5652#	5942#	5970#
	5977#	5992#	6092#	6125#	6132#	6165#	6283#	6293#	6307#	6317#	6365#	6375#	6389#
	6399#	6447#	6457#	6471#	6481#	6529#	6539#	6553#	6563#	6607#	6617#	6661#	6671#
	6735#	6778#	6880#	6924#	6981#	6999#	7265#	7281#	7305#	7354#	7382#	7400#	7431#
	7444#	7455#											
TSEXCP= 000000	8022#	8026	8027#	8031	8032#	8037	8042#	8047					
TSFLAG= 000040	4064#	4073#	4078#	4098#	4103#	4113#	4131#	4149#	4161#	4169#	4190#	4195#	4215#
	4220#	4240#	4250#	4259#	4264#	4285#	4290#	4309#	4314#	4324#	4337#	4429#	4441#
	4452#	4461#	4466#	4476#	4492#	4502#	4511#	4525#	4540#	4549#	4558#	4567#	4584#
	4593#	4603#	4617#	4626#	4669#	4712#	4757#	4764#	4840#	5007#	5103#	5116#	5125#
	5134#	5143#	5152#	5175#	5188#	5197#	5206#	5215#	5224#	5268#	5275#	5320#	5334#
	5343#	5357#	5372#	5381#	5395#	5409#	5419#	5430#	5439#	5448#	5467#	5478#	5527#
	5548#	5564#	5573#	5587#	5614#	5623#	5642#	5656#	5854#	5862#	5897#	5946#	5981#
	5996#	6041#	6096#	6136#	6152#	6169#	6241#	6274#	6356#	6438#	6520#	6596#	6650#
	6706#	6763#	6809#	6815#	6838#	6951#	7212#	7218#	7239#	7290#	7322#	7410#	
TSGMAN= 000000	1173#												
TSHILI= 000002	8022#	8025	8027#	8030	8032#	8036	8042#	8046					
TSLAST= 000001	1173#	8090#											
TSLOLI= 000000	8022#	8024	8027#	8029	8032#	8035	8042#	8045					
TLSYM= 010000	1173#	1350	1363	2821	2855	2873	2883	2958	3109	3118	3127	3168	3224
	3299	3352	3387	3446	3491	3892	3958	3980	3995	4008	4356	4360	4631
	4800	4908	4975	4978	5073	5164	5236	5239	5489	5668	5671	5824	6005
	6181	6184	6323	6405	6487	6569	6623	6677	6682	6741	6783	6934	7010
	7013	7312	7466	7469	8055	8077							
TSLTNO= 000015	8093#												
TSNEST= 177777	1173#	1175#	1323#	1349#	1358#	1362#	2801#	2820#	2835#	2854#	2869#	2872#	2878#
	2882#	2888#	2957#	2963#	3108#	3114#	3117#	3123#	3126#	3132#	3167#	3173#	3223#
	3231#	3298#	3306#	3351#	3358#	3386#	3392#	3445#	3462#	3490#	3708#	3713#	3721#
	3891#	3921#	3957#	3971#	3979#	3989#	3994#	4005#	4007#	4054#	4056#	4355#	4359#
	4424#	4630#	4664#	4799#	4834#	4847#	4907#	4914#	4974#	4977#	5002#	5072#	5093#
	5097#	5163#	5169#	5235#	5238#	5263#	5286#	5488#	5494#	5667#	5670#	5792#	5823#
	5850#	5869#	6004#	6014#	6180#	6183#	6236#	6263#	6322#	6345#	6404#	6427#	6486#
	6509#	6568#	6585#	6622#	6639#	6676#	6681#	6698#	6740#	6758#	6782#	6805#	6832#
	6933#	6944#	7009#	7012#	7208#	7233#	7311#	7316#	7465#	7468#	8018#	8053#	8072#
	8075#	8088#											
TSNS0 = 000000	1175#	8088											
TSNS1 = 000005	1323#	1349	1358#	1362	2801#	2820	2835#	2854	2869#	2872	2878#	2882	2888#
	2957	2963#	3108	3114#	3117	3123#	3126	3132#	3167	3173#	3223	3231#	3298
	3306#	3351	3358#	3386	3392#	3445	3462#	3490	3708#	3713	3721#	3891	3921#
	3957	3971#	3979	3989#	3994	4005#	4007	4054#	4359	4424#	4630	4664#	4799
	4834#	4977	5002#	5072	5093#	5238	5263#	5670	5792#	5823	5850#	6183	6236#
	6681	6698#	6740	6758#	6782	6805#	7012	7208#	7468	8018#	8053	8072#	8075
TSNS2 = 000002	4056#	4355	4847#	4907	4914#	4974	5097#	5163	5169#	5235	5286#	5488	5494#
	5667	5869#	6004	6014#	6180	6263#	6322	6345#	6404	6427#	6486	6509#	6568
	6585#	6622	6639#	6676	6832#	6933	6944#	7009	7233#	7311	7316#	7465	
TSPTNU= 000000	1173#												
TSSAVL= 177777	1173#												
TSSGL= 177777	1173#												
TSSUBN= 000002	1173#	4053#	4055#	4423#	4663#	4833#	4846#	4913#	5001#	5092#	5096#	5168#	5262#
	5285#	5493#	5849#	5868#	6013#	6235#	6262#	6344#	6426#	6508#	6584#	6638#	6697#
	6757#	6804#	6831#	6943#	7207#	7232#	7315#						

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CROSS REFERENCE TABLE -- USER SYMBOLS

T\$TAGL= 177777
T\$TAGN= 010072

T\$TEMP= 000000

T\$TEST= 000015

T\$TSTM= 177777

1173#	1173#	1323#	1358#	2801#	2835#	2869#	2878#	2888#	2963#	3114#	3123#	3132#	3173#
3231#	3306#	3358#	3392#	3462#	3708#	3721#	3921#	3971#	3989#	4005#	4054#	4056#	4056#
4424#	4664#	4834#	4847#	4914#	5002#	5093#	5097#	5169#	5263#	5286#	5494#	5792#	5792#
5850#	5869#	6014#	6236#	6263#	6345#	6427#	6509#	6585#	6639#	6698#	6758#	6805#	6805#
6832#	6944#	7208#	7233#	7316#	8018#	8072#	1307#	1308#	1309#	1310#	1311#	1312#	1312#
1300#	1301#	1302#	1303#	1304#	1305#	1306#	2957#	3108#	3117#	3126#	3167#	3223#	3223#
1313#	1349#	1362#	2820#	2854#	2872#	2882#	3957#	3979#	3994#	4007#	4064#	4065	4065
3298#	3351#	3386#	3445#	3490#	3713#	3891#	4103#	4104	4113#	4114	4131#	4132	4149#
4073#	4074	4078#	4079	4098#	4099	4103#	4104	4195#	4196	4215#	4216	4220#	4221
4150	4161#	4162	4169#	4170	4190#	4191	4195#	4196	4285#	4286	4290#	4291	4309#
4240#	4241	4250#	4251	4259#	4260	4264#	4265	4285#	4286	4290#	4291	4309#	4309#
4310	4314#	4315	4324#	4325	4337#	4338	4355#	4359#	4429#	4430	4441#	4442	4442
4452#	4453	4461#	4462	4466#	4467	4476#	4477	4492#	4493	4502#	4503	4511#	4511#
4512	4525#	4526	4540#	4541	4549#	4550	4558#	4559	4567#	4568	4584#	4585	4585
4593#	4594	4603#	4604	4617#	4618	4626#	4627	4630#	4669#	4670	4712#	4713	4713
4757#	4758	4764#	4765	4799#	4840#	4841	4907#	4974#	4977#	5007#	5008	5072#	5072#
5103#	5104	5116#	5117	5125#	5126	5134#	5135	5143#	5144	5152#	5153	5163#	5163#
5175#	5176	5188#	5189	5197#	5198	5206#	5207	5215#	5216	5224#	5225	5235#	5235#
5238#	5268#	5269	5275#	5276	5320#	5321	5334#	5335	5343#	5344	5357#	5358	5358
5372#	5373	5381#	5382	5395#	5396	5409#	5410	5419#	5420	5430#	5431	5439#	5439#
5440	5448#	5449	5467#	5468	5478#	5479	5488#	5527#	5528	5548#	5549	5564#	5564#
5565	5573#	5574	5587#	5588	5614#	5615	5623#	5624	5642#	5643	5656#	5657	5657
5667#	5670#	5823#	5854#	5855	5862#	5863	5897#	5898	5946#	5947	5981#	5982	5982
5996#	5997	6004#	6041#	6042	6096#	6097	6136#	6137	6152#	6153	6169#	6170	6170
6180#	6183#	6241#	6242	6274#	6275	6322#	6356#	6357	6404#	6438#	6439	6486#	6486#
6520#	6521	6568#	6596#	6597	6622#	6650#	6651	6676#	6681#	6706#	6707	6740#	6740#
6763#	6764	6782#	6809#	6810	6815#	6816	6838#	6839	6933#	6951#	6952	7009#	7009#
7012#	7212#	7213	7218#	7219	7239#	7240	7290#	7291	7311#	7322#	7323	7410#	7410#
7411	7465#	7468#	8022#	8027#	8032#	8038#	8042#	8048#	8053#	8075#	8088#	8088#	8088#
1173#	4053#	4055	4423#	4663#	4833#	4846	4913	5001#	5092#	5096	5168	5262#	5262#
5285	5493	5849#	5868	6013	6235#	6262	6344	6426	6508	6584	6638	6697#	6697#
6757#	6804#	6831	6943	7207#	7232	7315	8093	2936	2943	2953	2958	2971	2971
1173#	2808	2842	2873	2883	2899	2916	2926	3104	3109	3118	3127	3140	3140
2977	2987	3002	3016	3034	3044	3064	3084	3219	3224	3236	3242	3248	3248
3148	3156	3163	3168	3178	3184	3201	3209	3322	3346	3352	3381	3387	3403
3254	3264	3274	3284	3294	3299	3316	3332	3491	3523	3537	3544	3558	3565
3416	3433	3441	3446	3467	3473	3483	3491	3659	3672	3679	3691	3727	3727
3579	3586	3600	3613	3621	3631	3638	3651	3859	3865	3876	3884	3892	3928
3733	3739	3745	3764	3769	3782	3802	3859	3995	4008	4056	4062	4064	4071
3947	3952	3958	3974	3977	3980	3992	3995	4113	4129	4131	4137	4147	4149
4073	4078	4083	4096	4098	4103	4108	4113	4200	4213	4215	4220	4225	4240
4156	4161	4169	4175	4188	4190	4195	4285	4285	4290	4295	4307	4309	4314
4245	4250	4257	4259	4264	4269	4283	4285	4427	4429	4439	4441	4450	4450
4319	4324	4337	4342	4352	4356	4360	4395	4492	4500	4502	4509	4511	4511
4452	4459	4461	4466	4474	4476	4480	4490	4565	4567	4582	4584	4588	4588
4523	4525	4538	4540	4547	4549	4553	4558	4637	4667	4669	4675	4707	4707
4593	4601	4603	4615	4617	4621	4626	4631	4777	4794	4800	4838	4840	4840
4712	4719	4726	4735	4752	4757	4762	4764	4978	5005	5007	5021	5058	5058
4847	4858	4894	4908	4914	4925	4961	4975	5132	5134	5141	5143	5150	5150
5073	5097	5101	5103	5114	5116	5123	5125	5195	5197	5204	5206	5213	5213
5152	5158	5164	5169	5173	5175	5186	5188	5275	5286	5315	5320	5332	5332
5215	5222	5224	5230	5236	5239	5266	5268	5381	5390	5395	5407	5409	5409
5334	5341	5343	5355	5357	5370	5372	5379	5455	5462	5467	5473	5478	5478
5417	5419	5428	5430	5437	5439	5446	5448	5571	5573	5582	5587	5612	5612
5489	5494	5522	5527	5546	5548	5562	5564						

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CROSS REFERENCE TABLE -- USER SYMBOLS

W1	= 003056	1880#	1881	3027										
W2	= 003060	1881#	1882	3025										
W3	= 003062	1882#	1883	3086										
W4	= 003064	1883#	1884											
W5	= 003066	1884#	1885											
W6	= 003070	1885#	1886											
W7	= 003072	1886#	1887											
W8	= 003074	1887#	1888											
W9	= 003076	1888#	1889											
XDATA	002260	1644#	3189	3326	3338	3507*	3508*	3625	3664					
XLOC0	032554	3479	5874	5887	5906	5985	6031	6056	6142	6187#				
XLOC1	032556	6188#												
XLOC2	032560	6189#												
XLOC3	032562	6190#												
XLOC4	032564	6191#												
XLOC5	032566	6192#												
XLOC6	032570	6193#												
XPMFG.M	036773	8049	8056#											
XMINT	030560	5290	5497	5679#	5870	6015								
XMINTH	031124	G 5704	5792#											
XPREAD	030776	5644	5756#	6145										
XPWRIT	030700	5350	5402	5541	5591	5725#	5890	6034						
XP4HOL	031122	5682*	5733*	5738	5764*	5769	5780	5785#						
XP4INT	031074	5734	5765	5779#										
XORGB	012162	3187	3318	3505#	3623	3661								
XORSW	005064	2505#	3018											
XVAL0	032572	3478	5874*	5886	6020*	6030	6051*	6160	6195#					
XVAL1	032574	6196#												
XVAL2	032576	6197#												
XVAL3	032600	6198#												
XVAL4	032602	6199#												
XVAL5	032604	6200#												
XVAL6	032606	6201#												
X\$ALWA=	000000	1173#												
X\$FALS=	000040	1173#												
X\$OFFS=	000400	1173#												
X\$TRUE=	000020	1173#												
SE	= 000126	1591#	1992#	2032#	2072#	2115#	2178#	2731#	2736	2807#	2841#	4082#	4107#	4136#
		4155#	4174#	4199#	4224#	4244#	4268#	4294#	4318#	4341#	4479#	4552#	4587#	4620#
		4706#	4751#	4793#	4893#	4960#	5057#	5157#	5229#	5314#	5389#	5454#	5461#	5472#
		5521#	5581#	5629#	5636#	5650#	5814#	5940#	5968#	5975#	5990#	6090#	6123#	6130#
		6163#	6281#	6291#	6305#	6315#	6363#	6373#	6387#	6397#	6445#	6455#	6469#	6479#
		6527#	6537#	6551#	6561#	6605#	6615#	6659#	6669#	6733#	6776#	6878#	6922#	6979#
		6997#	7263#	7279#	7303#	7352#	7380#	7398#	7429#	7442#	7453#			
\$LSTIN=	000001	1177#												
\$LSTTA=	000001	1178#												
\$MPCSR=	160000	G 1706#	1711	1712	1714	1715	1717	1718	1720	1721	1723	1724	1726	1727
		1729	1730	1732	1733									
\$T	= 000015	1591#	4009#	4407#	4649#	4801#	4834	4979#	5074#	5240#	5826#	6210#	6683#	6742#
		6784#	6805	7038#	7208									
.	= 037154	1169#	1776	1801#	1802	1873#	1914#	1930#	2004#	3226#	3301#	3449#	3493#	3903#
		4065	4069	4074	4076	4079	4094	4099	4101	4104	4114	4127	4132	4145
		4150	4162	4167	4170	4186	4191	4193	4196	4211	4216	4218	4221	4238
		4241	4251	4255	4260	4262	4265	4281	4286	4288	4291	4305	4310	4312
		4315	4325	4335	4338	4350	4430	4437	4442	4448	4453	4457	4462	4464
		4467	4472	4477	4488	4493	4498	4503	4507	4512	4521	4526	4530	4536

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CROSS REFERENCE TABLE -- MACRO NAMES

INLOOP	1#	1173#													
IOSETU	1#	1173#													
IOSTAR	1#	1173#													
KT11	1#	1173#													
LASTAD	1#	1173#	8088												
MANUAL	1#	1173#													
MEMORY	1#	1173#													
MSG	4009#	4015	4407#	4413	4649#	4655	4801#	4807	4979#	4985	5074#	5080	5240#	5246	5826#
	5832	6210#	6216	6683#	6689	6742#	6748	6784#	6790	7038#	7044				
MSBYTE	1#	1173#	1203#	1209	1210	1211									
MSCHEC	1#	1173#	5268#	5275#	5854#	5862#	6241#	6809#	6815#	7212#	7218#				
MSCNTO	1#	1173#	8022#	8027#	8032#	8038#	8042#	8048#							
MSCOUN	1#	1173#	2894#	2910#	2921#	2929#	2939#	2946#	2966#	2974#	2983#	2992#	3006#	3024#	3040#
	3048#	3068#	3088#	3136#	3143#	3151#	3159#	3175#	3181#	3189#	3206#	3216#	3233#	3239#	3245#
	3251#	3257#	3267#	3277#	3287#	3311#	3326#	3337#	3373#	3400#	3406#	3419#	3438#	3464#	3470#
	3477#	3518#	3526#	3540#	3547#	3561#	3568#	3582#	3589#	3610#	3617#	3625#	3634#	3648#	3655#
	3663#	3675#	3688#	3778#	3854#	3862#									
MSDATA	1#	1173#	1203#	1212	1214	1216	1218	1220	1222	1224	1226	1228	1230	1232	1234
	1236	1238	1240	1242#	1244	1246	1249	1252	1254	1256	1258	1260	1262	1264	1266
	1268	1270	1272	1274	1276	1278	1280	1282	1284	1286	1910#	1922#			
MSDECR	1#	1173#	1349#	1362#	2820#	2854#	2872#	2882#	2957#	3108#	3117#	3126#	3167#	3223#	3298#
	3351#	3386#	3445#	3490#	3713#	3891#	3957#	3979#	3994#	4007#	4355#	4359#	4630#	4799#	4907#
	4974#	4977#	5072#	5163#	5235#	5238#	5488#	5667#	5670#	5823#	6004#	6180#	6183#	6322#	6404#
	6486#	6568#	6622#	6676#	6681#	6740#	6782#	6933#	7009#	7012#	7311#	7465#	7468#	8053#	8075#
	8088#														
MSDEFA	1#	1173#	8022#	8027#	8032#	8038#	8042#	8048#							
MSENDE	1#	1173#	1349#	1362#	2820#	2854#	2872#	2882#	2957#	3108#	3117#	3126#	3167#	3223#	3298#
	3351#	3386#	3445#	3490#	3891#	3957#	3979#	3994#	4007#	4355#	4359#	4630#	4799#	4907#	4974#
	4977#	5072#	5163#	5235#	5238#	5488#	5667#	5670#	5823#	6004#	6180#	6183#	6322#	6404#	6486#
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CROSS REFERENCE TABLE -- MACRO NAMES

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CROSS REFERENCE TABLE -- MACRO NAMES

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CROSS REFERENCE TABLE -- MACRO NAMES

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CROSS REFERENCE TABLE -- MACRO NAMES

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	4525#	4538#	4540#	4547#	4549#	4553	4558#	4565#	4567#	4582#	4584#	4588	4593#	4601#	4603#
	4615#	4617#	4621	4626#	4630#	4631	4637#	4667#	4669#	4675#	4707	4712#	4719#	4726#	4735#
	4752	4757#	4762#	4764#	4777#	4794	4799#	4800	4838#	4840#	4846#	4847	4858#	4894	4907#
	4908	4913#	4914	4925#	4961	4974#	4975	4977#	4978	5005#	5007#	5021#	5058	5072#	5073
	5096#	5097	5101#	5103#	5114#	5116#	5123#	5125#	5132#	5134#	5141#	5143#	5150#	5152#	5158
	5163#	5164	5168#	5169	5173#	5175#	5186#	5188#	5195#	5197#	5204#	5206#	5213#	5215#	5222#
	5224#	5230	5235#	5236	5238#	5239	5266#	5268#	5275#	5285#	5286	5315	5320#	5332#	5334#
	5341#	5343#	5355#	5357#	5370#	5372#	5379#	5381#	5390	5395#	5407#	5409#	5417#	5419#	5428#
	5430#	5437#	5439#	5446#	5448#	5455	5462	5467#	5473	5478#	5488#	5489	5493#	5494	5522
	5527#	5546#	5548#	5562#	5564#	5571#	5573#	5582	5587#	5612#	5614#	5621#	5623#	5630	5637
	5642#	5651	5656#	5667#	5668	5670#	5671	5703#	5707	5854#	5860#	5862#	5868#	5869	5895#
	5897#	5941	5946#	5969	5976	5981#	5991	5996#	6004#	6005	6013#	6014	6039#	6041#	6091
	6096#	6124	6131	6136#	6150#	6152#	6164	6169#	6180#	6181	6183#	6184	6239#	6241#	6262#
	6263	6272#	6274#	6282	6292	6306	6316	6322#	6323	6344#	6345	6354#	6356#	6364	6374
	6388	6398	6404#	6405	6426#	6427	6436#	6438#	6446	6456	6470	6480	6486#	6487	6508#
	6509	6518#	6520#	6528	6538	6552	6562	6568#	6569	6584#	6585	6594#	6596#	6606	6616

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CROSS REFERENCE TABLE -- MACRO NAMES

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6845	6879	6906#	6907	6909#	6910	6923	6930#	6933#	6934	6943#	6944	6949#	6951#	6954#	
6958	6980	6989#	6990	6992#	6993	6998	7006#	7009#	7010	7012#	7013	7212#	7218#	7232#	
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7312	7315#	7316	7320#	7322#	7335#	7339	7353	7376#	7377	7381	7394#	7395	7399	7407#	
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6292#	6306#	6316#	6323#	6345#	6354#	6356#	6364#	6374#	6388#	6398#	6405#	6427#	6436#	6438#	
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4582#	4584#	4588#	4593#	4601#	4603#	4615#	4617#	4621#	4626#	4631#	4637#	4667#	4669#	4675#	
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5175#	5186#	5188#	5195#	5197#	5204#	5206#	5213#	5215#	5222#	5224#	5230#	5236#	5239#	5266#	
5268#	5275#	5286#	5315#	5320#	5332#	5334#	5341#	5343#	5355#	5357#	5370#	5372#	5379#	5381#	
5390#	5395#	5407#	5409#	5417#	5419#	5428#	5430#	5437#	5439#	5446#	5448#	5455#	5462#	5467#	

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CROSS REFERENCE TABLE -- MACRO NAMES

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	5614#	5621#	5623#	5630#	5637#	5642#	5651#	5656#	5668#	5671#	5707#	5854#	5860#	5862#	5869#
	5895#	5897#	5941#	5946#	5969#	5976#	5981#	5991#	5996#	6005#	6014#	6039#	6041#	6091#	6096#
	6124#	6131#	6136#	6150#	6152#	6164#	6169#	6181#	6184#	6239#	6241#	6263#	6272#	6274#	6282#
	6292#	6306#	6316#	6323#	6345#	6354#	6356#	6364#	6374#	6388#	6398#	6405#	6427#	6436#	6438#
	6446#	6456#	6470#	6480#	6487#	6509#	6518#	6520#	6528#	6538#	6552#	6562#	6569#	6585#	6594#
	6596#	6606#	6616#	6623#	6639#	6648#	6650#	6660#	6670#	6677#	6682#	6704#	6706#	6727#	6734#
	6741#	6761#	6763#	6777#	6783#	6809#	6815#	6832#	6836#	6838#	6845#	6879#	6907#	6910#	6923#
	6930#	6934#	6944#	6949#	6951#	6958#	6980#	6990#	6993#	6998#	7006#	7010#	7013#	7212#	7218#
	7233#	7237#	7239#	7252#	7264#	7280#	7288#	7290#	7296#	7299#	7304#	7312#	7316#	7320#	7322#
	7339#	7353#	7377#	7381#	7395#	7399#	7407#	7410#	7423#	7430#	7443#	7454#	7462#	7466#	7469#
MSWORD	1#	1173#	1242#	1251	1298#	1300	1301	1302	1303	1304	1305	1306	1307	1308	1309
	1310	1311	1312	2808#	2809	2810	2811	2842#	2843	2844	2845	4083#	4084	4085	4086
	4108#	4109	4110	4111	4137#	4138	4139	4140	4156#	4157	4158	4159	4175#	4176	4177
	4178	4200#	4201	4202	4203	4225#	4226	4227	4228	4245#	4246	4247	4248	4269#	4270
	4271	4272	4295#	4296	4297	4298	4319#	4320	4321	4322	4342#	4343	4344	4345	4480#
	4481	4482	4483	4553#	4554	4555	4556	4588#	4589	4590	4591	4621#	4622	4623	4624
	4707#	4708	4709	4710	4752#	4753	4754	4755	4794#	4795	4796	4797	4894#	4895	4896
	4897	4961#	4962	4963	4964	5058#	5059	5060	5061	5158#	5159	5160	5161	5230#	5231
	5232	5233	5268#	5275#	5315#	5316	5317	5318	5390#	5391	5392	5393	5455#	5456	5457
	5458	5462#	5463	5464	5465	5473#	5474	5475	5476	5522#	5523	5524	5525	5582#	5583
	5584	5585	5630#	5631	5632	5633	5637#	5638	5639	5640	5651#	5652	5653	5654	5854#
	5862#	5941#	5942	5943	5944	5969#	5970	5971	5972	5976#	5977	5978	5979	5991#	5992
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	6165	6166	6167	6241#	6282#	6283	6284	6285	6292#	6293	6294	6295	6306#	6307	6308
	6309	6316#	6317	6318	6319	6364#	6365	6366	6367	6374#	6375	6376	6377	6388#	6389
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	6619	6660#	6661	6662	6663	6670#	6671	6672	6673	6734#	6735	6736	6737	6777#	6778
	6779	6780	6809#	6815#	6879#	6880	6881	6882	6923#	6924	6925	6926	6980#	6981	6982
	6983	6998#	6999	7000	7001	7212#	7218#	7264#	7265	7266	7267	7280#	7281	7282	7283
	7304#	7305	7306	7307	7353#	7354	7355	7356	7381#	7382	7383	7384	7399#	7400	7401
	7402	7430#	7431	7432	7433	7443#	7444	7445	7446	7454#	7455	7456	7457	8022#	8027#
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MSXFER	1#	1173#													
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NTST	1591#	4009	4407	4649	4801	4979	5074	5240	5826	6210	6683	6742	6784	7038	
OPEN	1#	1173#													
POINTE	1#	1173#	1190												
PRINTB	1#	1173#	2965	3310	3687										
PRINTF	1#	1173#	3777	3853	3861										
PRINTS	1#	1173#													
PRINTX	1#	1173#	2893	2909	2920	2928	2938	2945	2973	2982	2991	3005	3023	3039	3047
	3067	3087	3135	3142	3150	3158	3174	3180	3188	3205	3215	3232	3238	3244	3250
	3256	3266	3276	3286	3325	3336	3372	3399	3405	3418	3437	3463	3469	3476	3517
	3525	3539	3546	3560	3567	3581	3588	3609	3616	3624	3633	3647	3654	3662	3674
READBU	1#	1173#													
READEF	1#	1173#	3725	3731	3737	3743									
RFLAGS	1#	1173#													
SETDF	1591#	1993	2033	2073	2116	2179	2732	5815							
SETHRD	1591#														
SETPRI	1#	1173#	6908	6991	7297	7375	7393	7421							
SETSF	1591#														
SETSFT	1591#														
SETVEC	1#	1173#	3759	3871	3879	3923	5702	6840	6953	7247	7334				

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CROSS REFERENCE TABLE -- MACRO NAMES

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STARS	1#	1173#													
SVC	1#	1171#	1172												
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XFER	1#	1173#	5268#	5275#	5854#	5862#	6241#	6809#	6815#	7212#	7218#				
XFERF	1#	1173#													
XFERT	1#	1173#													
\$GEDF	1591#	2807	2841	4082	4107	4136	4155	4174	4199	4224	4244	4268	4294	4318	4341
	4479	4552	4587	4620	4706	4751	4793	4893	4960	5057	5157	5229	5314	5389	5454
	5461	5472	5521	5581	5629	5636	5650	5940	5968	5975	5990	6090	6123	6130	6163
	6281	6291	6305	6315	6363	6373	6387	6397	6445	6455	6469	6479	6527	6537	6551
	6561	6605	6615	6659	6669	6733	6776	5878	6922	6979	6997	7263	7279	7303	7352
	7380	7398	7429	7442	7453										
\$GEHRD	1591#														
\$GESF	1591#														
\$GESFT	1591#														
\$GTDF	1591#	1992	2032	2072	2115	2178	2731	5814							
\$GTHRD	1591#														
\$GTSF	1591#														
\$GTSFT	1591#														

. ABS. 037154 000

ERRORS DETECTED: 0

CVDMBA.BIN, CVDMBA.SEQ/CRF/SOL=SVC34R.MAC, CVDMBA.P11

RUN-TIME: 40 49 5 SECONDS

RUN-TIME RATIO: 156/95=1.6

CORE USED: 21K (41 PAGES)