

TSV05

TSV05 CTRL LT1  
CVTSAA0

AH-T094A-MC  
FICHE 1 OF 2

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IDENTIFICATION

PRODUCT ID: AC-T093A-MC  
PRODUCT TITLE: CVTSAAD TSV05 CTRL LT1  
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## 1.0 GENERAL INFORMATION

### 1.1 PROGRAM ABSTRACT

THIS IS A PDP-11/23 RESIDENT DIAGNOSTIC WHICH CHECKS THE FUNCTIONALITY OF A TSV05 MAGTAPE SUBSYSTEM WHILE CONNECTED TO A PDP-11/23 SYSTEM (Q-BUS). THE PROGRAM PROVIDES ERROR MESSAGES WHICH IDENTIFY FAILING FUNCTIONS THAT AID IN THE REPAIR OF THE DEVICE. THIS DIAGNOSTIC CONSIST OF ELEVEN TEST WHICH ARE EXECUTED IN SEQUENCE.

THIS DIAGNOSTIC HAS BEEN WRITTEN FOR USE WITH THE DIAGNOSTIC RUNTIME SERVICES SOFTWARE (SUPERVISOR). THESE SERVICES PROVIDE THE INTERFACE TO THE OPERATOR AND TO THE SOFTWARE ENVIRONMENT. THIS PROGRAM CAN BE USED WITH XXDP+, ACT, APT, SLIDE AND PAPER TAPE. FOR A COMPLETE DESCRIPTION OF THE RUNTIME SERVICES, REFER TO THE XXDP+ USER'S MANUAL. THERE IS A BRIEF DESCRIPTION OF THE RUNTIME SERVICES IN SECTION 2 OF THIS DOCUMENT.

### 1.2 SYSTEM REQUIREMENTS

PDP-11/23 PROCESSOR AND MEMORY  
CAUTION:DIAGNOSTIC REQUIRES 32K WORDS OF MEMORY  
(28K USEABLE I.E. 4K FOR I/O PAGE)  
TSV05 MAGTAPE SUBSYSTEM (DRIVE AND CONTROLLER)  
CONSOLE TERMINAL  
PDP-11 DIAGNOSTIC SUPERVISOR (HSAAA.SYS VERSION 34 OR LATER)  
PDP-11 DIAGNOSTIC LOADER/MONITOR (XXDP+)

### 1.3 RELATED DOCUMENTS AND STANDARDS

#### DIGITAL EQUIPMENT CORPORATION DOCUMENTS:

1. CHQUS XXDP+ USERS GUIDE; DOCUMENT NUMBER AC-F348E-MC  
DATE: 14 JULY 1980.
2. TSV05 TRANSPORT SUBSYSTEM USER'S GUIDE; DOCUMENT NUMBER EK-TSV05-UG-001  
DATE: AUGUST 1982
3. TSV05 TRANSPORT SUBSYSTEM TECHNICAL MANUAL; DOCUMENT NUMBER EK-TSV05-TM-001  
DATE: AUGUST 1982
4. TSV05 TRANSPORT SUBSYSTEM INSTALLATION MANUAL; DOCUMENT NUMBER EK-TSV05-IN-001  
DATE: AUGUST 1982

### 1.4 DIAGNOSTIC HIERARCY PREREQUISITES

FUNCTIONAL PDP-11/23 CENTRAL PROCESSOR AND MEMORY  
FUNCTIONAL CONSOLE TERMINAL  
FUNCTIONAL STANDALONE DIAGNOSTIC SUPERVISOR  
FUNCTIONAL DIAGNOSTIC LOADER/MONITOR (XXDP+)



## 1.5 ASSUMPTIONS

ALL HARDWARE EXCEPT THE HARDWARE UNDER TEST IS ASSUMED TO WORK PROPERLY OR FALSE ERRORS CAN BE REPORTED.  
THE TAPE BEING USED ON THE TSV05 TRANSPORT IS A KNOWN GOOD REEL OF TAPE.

## 2.0 OPERATING INSTRUCTIONS

THIS SECTION CONTAINS A BRIEF DESCRIPTION OF THE RUNTIME SERVICES. FOR DETAILED INFORMATION, REFER TO THE XXDP+ USER'S MANUAL (CHQUS).

## 2.1 COMMANDS

THERE ARE ELEVEN LEGAL COMMANDS FOR THE DIAGNOSTIC RUNTIME SERVICES (SUPERVISOR). THIS SECTION LISTS THE COMMANDS AND GIVES A VERY BRIEF DESCRIPTION OF THEM. THE XXDP+ USER'S MANUAL HAS MORE DETAILS.

COMMAND	EFFECT
START	START THE DIAGNOSTIC FROM AN INITIAL STATE
RESTART	START THE DIAGNOSTIC WITHOUT INITIALIZING
CONTINUE	CONTINUE AT TEST THAT WAS INTERRUPTED (AFTER ^C)
PROCEED	CONTINUE FROM AN ERROR HALT
EXIT	RETURN TO XXDP+ MONITOR (XXDP+ OPERATION ONLY!)
ADD	ACTIVATE A UNIT FOR TESTING (ALL UNITS ARE CONSIDERED TO BE ACTIVE AT START TIME)
DROP	DEACTIVATE A UNIT
PRINT	PRINT STATISTICAL INFORMATION (IF IMPLEMENTED BY THE DIAGNOSTIC - SECTION 4.0)
DISPLAY	TYPE A LIST OF ALL DEVICE INFORMATION
FLAGS	TYPE THE STATE OF ALL FLAGS (SEE SECTION 2.3)
ZFLAGS	CLEAR ALL FLAGS (SEE SECTION 2.3)

A COMMAND CAN BE RECOGNIZED BY THE FIRST THREE CHARACTERS. SO YOU MAY, FOR EXAMPLE, TYPE "STA" INSTEAD OF "START".

## 2.1.1 OPERATOR COMMANDS

THE TSV05 DIAGNOSTIC IS A PDP-11/23 DIAGNOSTIC SUPERVISOR COMPATIBLE PROGRAM. ALL LOADING AND RUNTIME INSTRUCTIONS CAN BE REFERENCED IN THE CHQUS XXDP+ USERS GUIDE, DOCUMENT NUMBER AC-F348E-MC. THE USER ENTRY IS IN QUOTES.

## BOOT THE DIAGNOSTIC MEDIA

```
.R VTSA??
DIAG. RUN-TIME SERVICES REV D. APR 79
CVTSA-A-0
****TSV05 LOGIC DIAGNOSTIC****
UNIT IS TSV05
>DR
```



2.2 SWITCHES

THERE ARE SEVERAL SWITCHES WHICH ARE USED TO MODIFY SUPERVISOR OPERATION. THESE SWITCHES ARE APPENDED TO THE LEGAL COMMANDS. ALL OF THE LEGAL SWITCHES ARE TABULATED BELOW WITH A BRIEF DESCRIPTION OF EACH. IN THE DESCRIPTIONS BELOW, A DECIMAL NUMBER IS DESIGNATED BY "DDDD".

SWITCH	EFFECT
/TESTS:LIST	EXECUTE ONLY THOSE TESTS SPECIFIED IN THE LIST. LIST IS A STRING OF TEST NUMBERS, FOR EXAMPLE - /TESTS:1:5:7-10. THIS LIST WILL CAUSE TESTS 1,5,7,8,9,10 TO BE RUN. ALL OTHER TESTS WILL NOT BE RUN.
/PASS:DDDD	EXECUTE DDDDD PASSES (DDDD = 1 TO 64000)
/FLAGS:FLGS	SET SPECIFIED FLAGS. FLAGS ARE DESCRIBED IN SECTION 2.3.
/EOP:DDDD	REPORT END OF PASS MESSAGE AFTER EVERY DDDDD PASSES ONLY. (DDDD = 1 TO 64000)
/UNITS:LIST	TEST/ADD/DROP ONLY THOSE UNITS SPECIFIED IN THE LIST. LIST EXAMPLE - /UNITS:0:5:10-12 USE UNITS 0,5,10,11,12 (UNIT NUMBERS = 0-63)

EXAMPLE OF SWITCH USAGE:

START/TESTS:1-5/PASS:1000/EOP:100

THE EFFECT OF THIS COMMAND WILL BE: 1) TESTS 1 THROUGH 5 WILL BE EXECUTED, 2) ALL UNITS WILL TESTED 1000 TIMES AND 3) THE END OF PASS MESSAGES WILL BE PRINTED AFTER EACH 100 PASSES ONLY. A SWITCH CAN BE RECOGNIZED BY THE FIRST THREE CHARACTERS. YOU MAY, FOR EXAMPLE, TYPE "/TES:1-5" INSTEAD OF "/TESTS:1-5".

BELOW IS A TABLE THAT SPECIFIES WHICH SWITCHES CAN BE USED BY EACH COMMAND.

	TESTS	PASS	FLAGS	EOP	UNITS
START	X	X	X	X	X
RESTART	X	X	X	X	X
CONTINUE		X	X	X	
PROCEED			X		
DROP					X
ADD					X
PRINT					
DISPLAY					X
FLAGS					
ZFLAGS					
EXIT					

2.3 FLAGS

FLAGS ARE USED TO SET UP CERTAIN OPERATIONAL PARAMETERS SUCH AS LOOPING ON ERROR. ALL FLAGS ARE CLEARED AT STARTUP AND REMAIN CLEARED UNTIL EXPLICITLY SET USING THE FLAGS SWITCH. FLAGS



ARE ALSO CLEARED AFTER A START COMMAND UNLESS SET USING THE FLAG SWITCH. THE ZFLAGS COMMAND MAY ALSO BE USED TO CLEAR ALL FLAGS. WITH THE EXCEPTION OF THE START AND ZFLAGS COMMANDS, NO COMMANDS AFFECT THE STATE OF THE FLAGS; THEY REMAIN SET OR CLEARED AS SPECIFIED BY THE LAST FLAG SWITCH.

FLAG	EFFECT
HOE	HALT ON ERROR - CONTROL IS RETURNED TO RUNTIME SERVICES COMMAND MODE
LOE	LOOP ON ERROR
IER*	INHIBIT ALL ERROR REPORTS
IBR*	INHIBIT ALL ERROR REPORTS EXCEPT FIRST LEVEL (FIRST LEVEL CONTAINS ERROR TYPE, NUMBER, PC, TEST AND UNIT)
IXE*	INHIBIT EXTENDED ERROR REPORTS (THOSE CALLED BY PRINTX MACRO'S)
PRI	DIRECT MESSAGES TO LINE PRINTER
PNT	PRINT TEST NUMBER AS TEST EXECUTES
BOE	'BELL' ON ERROR
UAM	UNATTENDED MODE (NO MANUAL INTERVENTION)
ISR	INHIBIT STATISTICAL REPORTS (DOES NOT APPLY TO DIAGNOSTICS WHICH DO NOT SUPPORT STATISTICAL REPORTING)
IDR	INHIBIT PROGRAM DROPPING OF UNITS
ADR	EXECUTE AUTODROP CODE
LOT	LOOP ON TEST

\*ERROR MESSAGES ARE DESCRIBED IN SECTION 3.1

SEE THE XXDP+ USER'S MANUAL FOR MORE DETAILS ON FLAGS. YOU MAY SPECIFY MORE THAN ONE FLAG WITH THE FLAG SWITCH. FOR EXAMPLE, TO CAUSE THE PROGRAM TO LOOP ON ERROR, INHIBIT ERROR REPORTS AND TYPE A 'BELL' ON ERROR, YOU MAY USE THE FOLLOWING STRING:

```
/FLAGS:LOE:IER:BOE
```

#### 2.4 HARDWARE QUESTIONS

WHEN A DIAGNOSTIC IS STARTED, THE RUNTIME SERVICES WILL PROMPT THE USER FOR HARDWARE INFORMATION BY TYPING "CHANGE HW (L) ?" YOU MUST ANSWER "Y" AFTER A START COMMAND UNLESS THE HARDWARE INFORMATION HAS BEEN "PRELOADED" USING THE SETUP UTILITY (SEE CHAPTER 14 OF THE XXDP+ USER'S MANUAL). WHEN YOU ANSWER THIS QUESTION WITH A "Y", THE RUNTIME SERVICES WILL ASK FOR THE NUMBER OF UNITS (IN DECIMAL).

AFTER INITIAL STARTING OF THE PROGRAM (START COMMAND TO THE DIAGNOSTIC SUPERVISOR), THE PROGRAM WILL ISSUE THE "CHANGE HW?" QUESTION TO ASK IF THE HARDWARE PARAMETERS ARE TO BE CHANGED (BY THE OPERATOR).

ON A "N" (NO) RESPONSE TO THE "CHANGE HW?" QUESTION, THE DIAGNOSTIC WILL RUN USING THE DEFAULT VALUES FOR ALL QUESTIONS. THE DEFAULT ADDRESS AND VECTOR ARE:



TSBA/TSDB = 172520, VECTOR = 224

ON A "Y" (YES) RESPONSE TO THE QUESTION, THE FOLLOWING QUESTIONS WILL THEN BE ASKED TO ALLOW THE OPERATOR TO SELECT THE UNITS TO BE TESTED. A VALUE, IF PRESENT, LOCATED TO THE LEFT OF THE QUESTION MARK IS THE DEFAULT VALUE THAT WILL BE TAKEN IF ONLY A CARRIAGE RETURN IS TYPED AS A RESPONSE. A "(D)" IN A QUESTION INDICATES THAT A DECIMAL NUMBER IS REQUIRED AS A RESPONSE. AN "(O)" INDICATES AN OCTAL NUMBER IS BEING SOLICITED. AN "(L)" INDICATES THAT A LOGICAL RESPONSE IS TO BE MADE: "Y" FOR YES, "N" FOR NO.

# UNITS (D) ? <ENTER THE NUMBER OF M7196 CONTROLLERS  
PRESENT TO BE TESTED>

UNIT 0

DEVICE ADDRESS (O) 172520 ? <ENTER THE ADDRESS OF THE  
TSBA/TSDB REGISTER>

VECTOR (O) 224 ? <ENTER ADDRESS OF INTERRUPT  
VECTOR>

THE ADDRESS AND VECTOR QUESTIONS WILL BE ASKED FOR EACH OF THE NUMBER OF UNITS (CONTROLLERS) SPECIFIED IN THE "# UNITS?" QUESTION. LOGICAL UNIT NUMBERS ARE ASSIGNED IN ORDER, BEGINNING AT 0. UP TO FOUR UNITS CAN BE SELECTED FOR TESTING AS FOLLOWS:  
UP TO 4 TSV05 CONTROLLERS PER 11/23 AND UP TO 2 DRIVES PER CONTROLLER

## 2.5 SOFTWARE QUESTIONS

AFTER YOU HAVE ANSWERED THE HARDWARE QUESTIONS OR AFTER A RESTART OR CONTINUE COMMAND, THE RUNTIME SERVICES WILL ASK FOR SOFTWARE PARAMETERS. THESE PARAMETERS WILL GOVERN SOME DIAGNOSTIC SPECIFIC OPERATION MODES. YOU WILL BE PROMPTED BY "CHANGE SW (L) ?" IF YOU WISH TO CHANGE ANY PARAMETERS, ANSWER BY TYPING "Y". THE SOFTWARE QUESTIONS AND THE DEFAULT VALUES ARE DESCRIBED IN THE NEXT PARAGRAPH(S).

THE FOLLOWING QUESTIONS ARE ASKED ON A START, RESTART, OR CONTINUE. THEY ALLOW FLEXIBILITY IN THE WAY THE PROGRAM BEHAVES.

CHANGE SW (L) ? <TYPE Y TO CAUSE THE FOLLOWING  
QUESTIONS TO BE ASKED>

INHIBIT ITERATIONS (L) N ? <TYPE "Y" TO PREVENT MULTIPLE  
ITERATIONS OF CERTAIN TESTS.  
THIS CAUSES EACH TEST PASS TO  
RUN AS QUICKLY AS POSSIBLE.  
ONLY QUICK-RUNNING LOGIC  
TESTS USE MULTIPLE  
ITERATIONS.>

## 2.6 EXTENDED P-TABLE DIALOGUE

WHEN YOU ANSWER THE HARDWARE QUESTIONS, YOU ARE BUILDING ENTRIES



IN A TABLE THAT DESCRIBES THE DEVICES UNDER TEST. THE SIMPLEST WAY TO BUILD THIS TABLE IS TO ANSWER ALL QUESTIONS FOR EACH UNIT TO BE TESTED. IF YOU HAVE A MULTIPLEXED DEVICE SUCH AS A MASS STORAGE CONTROLLER WITH SEVERAL DRIVES OR A COMMUNICATION DEVICE WITH SEVERAL LINES, THIS BECOMES TEDIOUS SINCE MOST OF THE ANSWERS ARE REPETITIOUS.

TO ILLUSTRATE A MORE EFFICIENT METHOD, SUPPOSE YOU ARE TESTING A DEVICE, THE XY11. SUPPOSE THIS DEVICE CONSISTS OF A CONTROL MODULE WITH EIGHT UNITS (SUB-DEVICES) ATTACHED TO IT. THESE UNITS ARE DESCRIBED BY THE OCTAL NUMBERS 0 THROUGH 7. THERE IS ONE HARDWARE PARAMETER THAT CAN VARY AMONG UNITS CALLED THE Q-FACTOR. THIS Q-FACTOR MAY BE 0 OR 1. BELOW IS A SIMPLE WAY TO BUILD A TABLE FOR ONE XY11 WITH EIGHT UNITS.

```
# UNITS (D) ? 8<CR>

UNIT 1
CSR ADDRESS (O) ? 160000<CR>
SUB-DEVICE # (O) ? 0<CR>
Q-FACTOR (O) 0 ? 1<CR>

UNIT 2
CSR ADDRESS (O) ? 160000<CR>
SUB-DEVICE # (O) ? 1<CR>
Q-FACTOR (O) 1 ? 0<CR>

UNIT 3
CSR ADDRESS (O) ? 160000<CR>
SUB-DEVICE # (O) ? 2<CR>
Q-FACTOR (O) 0 ? <CR>

UNIT 4
CSR ADDRESS (O) ? 160000<CR>
SUB-DEVICE # (O) ? 3<CR>
Q-FACTOR (O) 0 ? <CR>

UNIT 5
CSR ADDRESS (O) ? 160000<CR>
SUB-DEVICE # (O) ? 4<CR>
Q-FACTOR (O) 0 ? <CR>

UNIT 6
CSR ADDRESS (O) ? 160000<CR>
SUB-DEVICE # (O) ? 5<CR>
Q-FACTOR (O) 0 ? <CR>

UNIT 7
CSR ADDRESS (O) ? 160000<CR>
SUB-DEVICE # (O) ? 6<CR>
Q-FACTOR (O) 0 ? 1<CR>

UNIT 8
CSR ADDRESS (O) 160000<CR>
SUB-DEVICE # (O) ? 7<CR>
Q-FACTOR (O) 1 ? <CR>
```



NOTICE THAT THE DEFAULT VALUE FOR THE Q-FACTOR CHANGES WHEN A NON-DEFAULT RESPONSE IS GIVEN. BE CAREFUL WHEN SPECIFYING MULTIPLE UNITS!

AS YOU CAN SEE FROM THE ABOVE EXAMPLE, THE HARDWARE PARAMETERS DO NOT VARY SIGNIFICANTLY FROM UNIT TO UNIT. THE PROCEDURE SHOWN IS NOT VERY EFFICIENT.

THE RUNTIME SERVICES CAN TAKE MULTIPLE UNIT SPECIFICATIONS HOWEVER. LET'S BUILD THE SAME TABLE USING THE MULTIPLE SPECIFICATION FEATURE.

```
# UNITS (D) ? 8<CR>

UNIT 1
CSR ADDRESS (0) ? 160000<CR>
SUB-DEVICE # (0) ? 0,1<CR>
Q-FACTOR (0) 0 ? 1,0<CR>

UNIT 3
CSR ADDRESS (0) ? 160000<CR>
SUB-DEVICE # (0) ? 2-5<CR>
Q-FACTOR (0) 0 ? 0<CR>

UNIT 7
CSR ADDRESS (0) ? 160000<CR>
SUB-DEVICE # (0) ? 6,7<CR>
Q-FACTOR (0) 0 ? 1<CR>
```

AS YOU CAN SEE IN THE ABOVE DIALOGUE, THE RUNTIME SERVICES WILL BUILD AS MANY ENTRIES AS IT CAN WITH THE INFORMATION GIVEN IN ANY ONE PASS THROUGH THE QUESTIONS. IN THE FIRST PASS, TWO ENTRIES ARE BUILT SINCE TWO SUB-DEVICES AND Q-FACTORS WERE SPECIFIED. THE SERVICES ASSUME THAT THE CSR ADDRESS IS 160000 FOR BOTH SINCE IT WAS SPECIFIED ONLY ONCE. IN THE SECOND PASS, FOUR ENTRIES WERE BUILT. THIS IS BECAUSE FOUR SUB-DEVICES WERE SPECIFIED. THE "-" CONSTRUCT TELLS THE RUNTIME SERVICES TO INCREMENT THE DATA FROM THE FIRST NUMBER TO THE SECOND. IN THIS CASE, SUB-DEVICES 2, 3, 4 AND 5 WERE SPECIFIED. (IF THE SUB-DEVICE WERE SPECIFIED BY ADDRESSES, THE INCREMENT WOULD BE BY 2 SINCE ADDRESSES MUST BE ON AN EVEN BOUNDARY.) THE CSR ADDRESSES AND Q-FACTORS FOR THE FOUR ENTRIES ARE ASSUMED TO BE 160000 AND 0 RESPECTIVELY SINCE THEY WERE ONLY SPECIFIED ONCE. THE LAST TWO UNITS ARE SPECIFIED IN THE THIRD PASS.

THE WHOLE PROCESS COULD HAVE BEEN ACCOMPLISHED IN ONE PASS AS SHOWN BELOW.

```
# UNITS (D) ? 8<CR>

UNIT 1
CSR ADDRESS (0) ? 160000<CR>
SUB-DEVICE # (0) ? 0-7<CR>
Q-FACTOR (0) 0 ? 0,1,0,,,,,1,1<CR>
```

AS YOU CAN SEE FROM THIS EXAMPLE, NULL REPLIES (COMMAS ENCLOSING A NULL FIELD) TELL THE RUNTIME SERVICES TO REPEAT THE LAST REPLY.



## 2.7 QUICK START-UP PROCEDURE (XXDP+)

TO START-UP THIS PROGRAM:

1. BOOT XXDP+
2. TYPE 'R NAME', WHERE NAME IS THE NAME OF THE BIN OR BIC FILE FOR THIS PROGRAM
3. TYPE "START"
4. ANSWER THE "CHANGE HW" QUESTION WITH "Y"
5. ANSWER ALL THE HARDWARE QUESTIONS
6. ANSWER THE "CHANGE SW" QUESTION WITH "N"

WHEN YOU FOLLOW THIS PROCEDURE YOU WILL BE USING ONLY THE DEFAULTS FOR FLAGS AND SOFTWARE PARAMETERS. THESE DEFAULTS ARE DESCRIBED IN SECTIONS 2.3 AND 2.5.

## 3.0 ERROR INFORMATION

### 3.1 TYPES OF ERROR MESSAGES

THERE ARE THREE LEVELS OF ERROR MESSAGES THAT MAY BE ISSUED BY A DIAGNOSTIC: GENERAL, BASIC AND EXTENDED. GENERAL ERROR MESSAGES ARE ALWAYS PRINTED UNLESS THE "IER" FLAG IS SET (SECTION 2.3). THE GENERAL ERROR MESSAGE IS OF THE FORM:

```
NAME TYPE NUMBER ON UNIT NUMBER TST NUMBER PC:XXXXXX  
ERROR MESSAGE
```

WHERE: NAME = DIAGNOSTIC NAME  
TYPE = ERROR TYPE (SYS FATAL, DEV FATAL, HARD OR SOFT)  
NUMBER = ERROR NUMBER  
UNIT NUMBER = 0 - N (N IS LAST UNIT IN PTABLE)  
TST NUMBER = TEST AND SUBTEST WHERE ERROR OCCURRED  
PC:XXXXXX = ADDRESS OF ERROR MESSAGE CALL

BASIC ERROR MESSAGES ARE MESSAGES THAT CONTAIN SOME ADDITIONAL INFORMATION ABOUT THE ERROR. THESE ARE ALWAYS PRINTED UNLESS THE "IER" OR "IBR" FLAGS ARE SET (SECTION 2.3). THESE MESSAGES ARE PRINTED AFTER THE ASSOCIATED GENERAL MESSAGE.

EXTENDED ERROR MESSAGES CONTAIN SUPPLEMENTARY ERROR INFORMATION SUCH AS REGISTER CONTENTS OR GOOD/BAD DATA. THESE ARE ALWAYS PRINTED UNLESS THE "IER", "IBR" OR "IXE" FLAGS ARE SET (SECTION 2.3). THESE MESSAGES ARE PRINTED AFTER THE ASSOCIATED GENERAL ERROR MESSAGE AND ANY ASSOCIATED BASIC ERROR MESSAGES.

### 3.2 SPECIFIC ERROR MESSAGES

BELOW ARE SAMPLE ERROR MESSAGES. EACH ERROR MESSAGE REPRESENTS DIFFERENT TYPES OF ERRORS DETECTED BY THIS DIAGNOSTIC.



## ERROR MESSAGE EXAMPLE 1

THIS ERROR IS INDICATIVE OF AN INCORRECT REGISTER OR STATUS WORD RETURNED TO THE DIAGNOSTIC. THE FIRST PART DEFINES THE TEST FUNCTION AND UNIT THAT FAILED. THE SECOND PART PROVIDES THE REGISTER BITS AND THEIR MNEMONICS FOR THE INCORRECT REGISTER OR STATUS WORDS. THE THIRD PART IS THE EXPECTED AND RECEIVED DATA.

TST: 016 FIFO EXERCISER TEST  
 CVTSA HRD ERR 01610 ON UNIT 00 TST 016 SUB 002 PC: 040624  
 FIFO STATUS (IN WORD 9) INCORRECT AFTER WRITE FIFO

TAPE BUS SIGNALS IN WORD #8: - DESIGNATOR <BIT #>  
 PARERR<15> IEOT <12> IFMK <9> IRDY<6> IRWD<2>  
 IRESV2<14> IIDENT<11> IHER <8> IONL<5> IFBY<1>  
 IRESV1<13> ICER <10> ISPEED<7> ILDP<4> IFPT<0>

TAPE BUS SIGNALS IN WORD #9:  
 DATMIS<7> ILW<6> OUTRDY<5> INRDY<4>

MESSAGE BUFFER ADDRESS = 047352

MESSAGE BUFFER CONTENTS:

WORD #0	EXPD: 100020	RECV: 100020	XOR: 000000
WORD #1	EXPD: 000012	RECV: 000012	XOR: 000000
WORD #2	EXPD: 000000	RECV: 000000	XOR: 000000
WORD #3	EXPD: 000010	RECV: 000010	XOR: 000000
WORD #4	EXPD: 000000	RECV: 000000	XOR: 000000
WORD #5	EXPD: 000000	RECV: 000000	XOR: 000000
WORD #6	EXPD: 000000	RECV: 000000	XOR: 000000
WORD #7	EXPD: 000000	RECV: 000000	XOR: 000000
WORD #8	EXPD: 070217	RECV: 070217	XOR: 000000
WORD #9	EXPD: 000074	RECV: 000034	XOR: 000040

## ERROR MESSAGE EXAMPLE 2

THIS ERROR SHOWS A FATAL FUNCTION ERROR FROM THE TAPE DRIVE, IN THIS INSTANCE A UNRECOVERABLE ERROR OCCURED WHICH INDICATES THAT THE CONTROLLER MAY BE DEFECTIVE.

CVTSA HRD ERR 00159 ON UNIT 00 TST 001 SUB 005 PC: 026202  
 TSSR NOT CORRECT AFTER SPACE RECORDS COMMAND

TSSR = 100214

TSSR BITS SET: SC, SSR

TERMINATION CLASS CODE = UNRECOVERABLE ERROR

PACKET ADDRESS = 026420

PACKET WORD # = 140010

PACKET WORD # = 000010

PACKET WORD # = 000000

PACKET WORD # = 000024

## ERROR MESSAGE EXAMPLE 3

THIS ERROR SHOWS THAT THE MOTION BIT DID NOT GET SET WHILE DOING A REWIND



WITH EXTENDED FEATURES MODE ENABLED.

CVTS HRD ERR 00121 ON UNIT 00 TST 001 SUB 002 PC: 023306  
MOT BIT (XSTO) NOT SET DURING REWIND (EXTENDED FEATURES MODE)  
EXPD: 000312 RECV: 000112 XOR: 000200

#### 4.0 PERFORMANCE AND PROGRESS REPORTS

AT THE END OF EACH PASS, THE PASS COUNT IS GIVEN ALONG WITH THE TOTAL NUMBER OF ERRORS REPORTED SINCE THE DIAGNOSTIC WAS STARTED. THE "EOP" SWITCH CAN BE USED TO CONTROL HOW OFTEN THE END OF PASS MESSAGE IS PRINTED. SECTION 2.2 DESCRIBES SWITCHES.

#### SUCCESSFUL RUN EXAMPLE (PDP-11/23)

```
DR>STA/FLA:PNT:HOE
UNITS (D) ? 1
UNIT 0
DEVICE ADDRESS (O) 172520 ? <CR>
VECTOR (O) 224 ? <CR>
CHANGE SW (L) ? N<CR>
```

THE ABOVE COMMAND WILL START THE DIAGNOSTIC. THE COMMAND HAS TWO SWITCHES ON WHICH ARE "PRINT EACH TEST NBR AS EXECUTED" AND "HALT ON ERROR".

```
TST: 001 INITIALIZE #1
TST: 002 WRAP DATA HIGH BYTE TEST
TST: 003 WRAP DATA LOW BYTE TEST
TST: 004 RAM TEST
TST: 005 INITIALIZE 2 TEST
TST: 006 COMMAND REJECT TEST
TST: 007 WRITE CHARACTERISTICS TEST
TST: 008 VOLUME CHECK
TST: 009 COMPLETION INTERRUPT TEST
TST: 010 BASIC PACKET PROTOCOL TEST
TST: 011 NON-TAPE-MOTION COMMANDS TEST
```

0 ERRORS

NOTE: THE DIAGNOSTIC WILL RUN CONTINUOUSLY UNLESS A PASS NUMBER LIMIT HAS BEEN SPECIFIED WITH THE "/PASS:" SWITCH.

#### PROGRAM RUN TIMES

THE AVERAGE RUN TIMES OF THE PROGRAM ARE LISTED BELOW. THESE FIGURES ARE TO BE USED AS A GUIDE. THE TIMING WAS DONE ON A PDP-11/23 PROCESSOR WITH A LA34 CONSOLE.

THE PROGRAM RUNS IN TWO MODES: NO ITERATIONS AND DEFAULT MODE. IN THE NO ITERATIONS MODE, EACH TEST IS RUN ONCE, WITH NO ITERATIONS. IN THE DEFAULT MODE EACH TEST IS REPEATED BY THE NUMBER OF TIMES INDICATED BY THE ITERATION COUNT. NO ITERATIONS MODE IS SELECTED BY ANSWERING THE INHIBIT ITERATIONS QUESTION WITH A 'Y' (YES).

TEST NUMBER	N/I SECS.	ITER SECS	DEF SECS.
1	1	30	29
2	1	10	9
3	1	8	7
4	25	120	95
5	5	140	135
6	25	475	450
7	20	20	0
8	1	10	9
9	20	20	0
10	1	2	1
11	8	11	3

THE TIMES REQUIRED TO RUN TESTS 1 THROUGH 12 IN ONE COMMAND:

Q.V. 1 MIN 57 SECONDS  
 DEFAULT 12 MINS

#### 5.0 DEVICE INFORMATION TABLES

WHENEVER THE PROGRAM IS STARTED, VIA THE STA(RT) COMMAND, THE SUPERVISOR REQUESTS THE FOLLOWING P-TABLES PARAMETER CHANGES:

CHANGE HW (L) ?

# UNITS (D) ? <ENTER THE NUMBER OF M7196 CONTROLLERS PRESENT TO BE TESTED>

UNIT 0

DEVICE ADDRESS (O) 172520 ? <ENTER THE ADDRESS OF THE TSBA/TSDB REGISTER>

VECTOR (O) 224 ? <ENTER ADDRESS OF INTERRUPT VECTOR>

THE ADDRESS AND VECTOR QUESTIONS WILL BE ASKED FOR EACH OF THE NUMBER OF UNITS (CONTROLLERS) SPECIFIED IN THE "# UNITS?" QUESTION. LOGICAL UNIT NUMBERS ARE ASSIGNED IN ORDER, BEGINNING AT 0. UP TO FOUR UNITS CAN BE SELECTED FOR TESTING.

IN ADDITION, ON A START, RESTART OR CONTINUE THE SUPERVISOR REQUESTS CHANGES TO THE SOFTWARE OPERATING PARAMETERS, AS FOLLOWS:



CHANGE SW (L) ?

INHIBIT ITERATIONS (L) N ?

## 6.0 TEST SUMMARIES

### TEST 1: BUS RESET TEST

THIS TEST VERIFIES THAT THE M7196 MODULE'S DEVICE REGISTERS ARE ACCESSIBLE ON THE BUS (SUBTEST 1) AND THEN CHECKS THAT THE BUILT-IN INITIALIZATION SELF-TEST MICRODIAGNOSTIC DID NOT FIND ANY BASIC PROBLEMS IN THE MODULE. AREAS OF LOGIC TESTED BY THE SELF-TEST SEQUENCE ARE AS FOLLOWS: ROM AND PIPELINE REGISTER, SEQUENCER, INTERNAL BUSES, 2901 MICROPROCESSOR, AND, RAM. THIS TEST INITIALIZES THE CONTROLLER BY ISSUING THE BUS INIT SIGNAL VIA A RESET INSTRUCTION, OR BY WRITING INTO THE TSSR REGISTER, WAITS A PERIOD OF TIME (TO ALLOW THE CONTROLLER'S INITIALIZATION MICRODIAGNOSTIC SEQUENCE TO BE COMPLETED), AND THEN CHECKS THE CONTENTS OF THE TSSR REGISTER. SUCCESSFUL INITIALIZATION IS INDICATED BY SUBSYSTEM READY (SSR) AND NEED BUFFER ADDRESS (NBA) BITS BEING SET (1) AND ALL OTHER BITS (EXCEPT A17 AND A16 AND OFL, WHICH ARE IGNORED FOR THIS TEST) BEING CLEAR (0). IF THE CONTENTS OF TSSR ARE NOT AS EXPECTED, AN ERROR REPORT IS ISSUED LISTING THE EXPECTED DATA, ACTUAL DATA, AND THE DISCREPANCIES. THE ERROR REPORT ANALYZES THE TSSR CONTENTS AND DISCERNS AND REPORTS ONE OF THREE POSSIBILITIES:

1. TSSR CONTENTS ARE AMBIGUOUS (ANY OF BITS 11-14 ARE SET, OR STATES OF SSR AND SC BITS DO NOT CORRESPOND TO THE APPARENT ERROR CODE IN BITS 0-5): INDICATES THAT THE TSSR CONTENT CANNOT BE TRUSTED. INDICATES A CATASTROPHIC CONTROLLER MALFUNCTION. THIS IS A FATAL ERROR (EXECUTION IS ABORTED). FIELD ACTION WOULD BE TO REPLACE THE M7196. IF THE M7196 ITSELF IS BEING DEBUGGED, THE PROGRAM SHOULD BE RESTARTED WITH LOOP ON ERROR ENABLED IN ORDER TO PROBE FOR THE PROBLEM.
2. SSR = 0, SC = 0 AND THE ERROR CODE IN BITS 0-5 IS IN THE RANGE 17-13: THIS IS A FATAL ERROR. THE ERROR CODE IS DECODED AND THE APPROPRIATE DESCRIPTION GIVEN. INDICATES THAT A SERIOUS PROBLEM EXISTS.

### TEST 2: WRAP DATA - HIGH BYTE

THIS TEST VERIFIES OPERATION OF:

1. PART OF THE 11/23 BUS INTERFACE SECTION OF THE M7196 MODULE: PART OF THE INPUT FILE (TSDB HIGH BYTE), PART OF THE OUTPUT FILE (TSSR HIGH BYTE AND TSBA, BOTH

BYTES), PART OF THE DC005 TRANSCEIVER CIRCUITS (ADDRESS DECODER, BDAL DRIVERS, HIGH BYTE OF INTERNAL DAL BUS DRIVERS), AND BASIC PROGRAMMED I/O CONTROL SEQUENCES AND LOGIC;

2. PART OF 2901 MICROPROCESSOR ELEMENTS (Q-REGISTER, REGISTER 0, ROTATE AND NEGATE FUNCTIONS
3. Y AND SOURCE BUSES;
4. BASIC MICROPROGRAM SEQUENCES.

THE PROGRAM WRITES A TEST DATA BYTE INTO THE HIGH BYTE OF TSDB, WAITS FOR THE SSR BIT IN TSSR TO SET, THEN CHECKS THE CONTENTS OF BOTH TSBA AND TSSR. THE MODULE IS FUNCTIONING CORRECTLY IF DATA WRITTEN APPEARS IN BOTH BYTES OF TSBA AND THE FINAL CONTENT OF TSSR IS CORRECT (SAME AS AFTER INITIALIZATION EXCEPT FOR BITS 8 AND 9, WHICH SHOULD CONTAIN BITS 8 AND 9 OF THE DATA PATTERN WRITTEN. AN ERROR IS REPORTED AND A DESCRIPTIVE ANALYSIS GIVEN IF A DISCREPANCY IN TSBA OR TSSR IS DETECTED. THE ANALYSIS LISTS LIKELY FAULTY CANDIDATES FROM THE LOGIC ELEMENTS LISTED ABOVE. THE TEST IS REPEATED FOR ALL COMBINATIONS OF TEST DATA BYTES (0-377 OCTAL).

#### TEST 3: WRAP DATA - LOW BYTE

THIS TEST FURTHER VERIFIES OPERATION OF MANY OF THE SAME ELEMENTS TESTED IN TEST 2, AND ADDITIONALLY VERIFIES:

1. LOW BYTE OF THE TSDB INPUT FILE REGISTER,
2. LOW BYTE OF INTERNAL DAL BUS DRIVERS ON THE DC005 TRANSCEIVER CIRCUITS,
3. BASIC FUNCTIONING OF PARTS OF THE RAM.

THE PROGRAM WRITES A TEST DATA BYTE INTO THE LOW BYTE OF TSDB, WAITS FOR THE SSR BIT IN TSSR TO SET, THEN CHECKS THE CONTENTS OF BOTH TSBA AND TSSR. THE MODULE IS FUNCTIONING CORRECTLY IF DATA WRITTEN APPEARS IN BOTH BYTES OF TSBA AND THE FINAL CONTENT OF TSSR IS CORRECT (SAME AS AFTER INITIALIZATION EXCEPT FOR BITS 8 AND 9, WHICH SHOULD CONTAIN BITS 8 AND 9 OF THE DATA PATTERN WRITTEN. AN ERROR IS REPORTED AND A DESCRIPTIVE ANALYSIS GIVEN IF A DISCREPANCY IN TSBA OR TSSR IS DETECTED. THE ANALYSIS LISTS LIKELY FAULTY CANDIDATES FROM THE LOGIC ELEMENTS LISTED ABOVE. THE TEST IS REPEATED FOR ALL COMBINATIONS OF TEST DATA BYTES (0-377 OCTAL).

#### TEST 4: RAM TEST

THIS TEST VERIFIES THAT ALL LOCATIONS OF THE RAM ON THE M7196



CAN PROPERLY STORE AND READ BACK ALL DATA PATTERNS, AND THAT EACH RAM LOCATION IS UNIQUELY ADDRESSED (I.E., THAT ONE AND ONLY ONE LOCATION IS ACCESSED BY ANY PARTICULAR ADDRESS). THE BYPRODUCT OF THESE TESTS IS A VERIFICATION OF TWO REGISTERS IN THE 2901 AND THE CAPABILITY OF THE 2901 TO CORRECTLY PERFORM AN ADD.

#### TEST 5: SECOND INITIALIZATION TEST

THIS TEST VERIFIES THE SAME ELEMENTS AS DID INITIALIZATION TEST #1 AND ALSO CHECKS THAT CERTAIN PARTS OF RAM IS CLEARED TO ZERO AND THAT 2901 REGISTERS 10 AND 11 ARE ALSO CLEARED TO ZERO. THIS IS A CONFIDENCE CHECK OF A PART OF THE SELF-TEST SEQUENCE (I.E., THAT IT IS REALLY BEING EXECUTED). FOR EACH OF TWO SUBTESTS (ONE FOR INITIALIZING VIA A BUS INIT, THE OTHER FOR INITIALIZING BY WRITING INTO THE TSSR), THE FOLLOWING SEQUENCE IS PERFORMED:

1. EACH RAM LOCATION AND 2901 REGISTERS 10 AND 11 ARE SET TO ALL 1'S BY USING WRITES INTO THE TSDB REGISTER (LOW BYTE AND MAINTENANCE MODE WORD WRITES).
2. THE CONTROLLER IS INITIALIZED AND THE VARIOUS CHECKS ON THE TSSR DESCRIBED IN INITIALIZATION TEST #1 ARE PERFORMED.
3. #1'S (377 OCTAL) ARE WRITTEN INTO THE LOW BYTE OF TSDB, WHICH SHOULD CAUSE RAM LOCATION 0 TO BE WRITTEN TO ALL 1'S SINCE 2901 REGISTERS 10 AND 11, SPECIFYING THE RAM ADDRESS, SHOULD BE 0. RAM LOCATION 0 IS VERIFIED BY WRITING A WORD OF ZEROS INTO THE TSDB. THE RESULTING LOW BYTE OF TSBA SHOULD CONTAIN ALL 1'S.
4. THE ENTIRE RAM IS SCANNED. LOCATION 0 SHOULD CONTAIN ALL 1'S AND THE REMAINING LOCATIONS, EXCEPT FOR THE MESSAGE BUFFER IMAGE AREA, SHOULD CONTAIN 0. DISCREPANCIES ARE REPORTED. AN ERROR AT THIS POINT IS MOST LIKELY DUE TO A ROM, PIPELINE OR SEQUENCER PROBLEM OR A TIMING PROBLEM.

#### TEST 6: COMMAND REJECT

THIS TEST VERIFIES THAT ALL COMMANDS OTHER THAN WRITE CHARACTERISTICS ARE REJECTED DUE TO THE NEED BUFFER ADDRESS (NBA) BIT BEING SET IN TSSR, AND THAT THE TSBA AND TSSR REGISTERS ARE LEFT IN THE PROPER STATE AFTER EACH COMMAND IS REJECTED. THIS TEST CHECKS MICROPROCESSOR SEQUENCING, BASIC COMMAND DECODING AND DATA DMA HANDLING. THIS TEST CONTAINS TWO SUBTESTS: SUBTEST 1 SEQUENCES THROUGH ALL COMMAND WORDS (OTHER THAN WRITE CHARACTERISTICS) WITH THE INTERRUPT ENABLE (IE) BIT CLEAR AND VERIFIES THAT AN INTERRUPT IS NOT GENERATED BY THE REJECTED COMMAND; SUBTEST 2 PERFORMS SIMILARLY TO SUBTEST 1 BUT

SETS THE IE BIT IN EACH COMMAND WORD AND VERIFIES THAT AN INTERRUPT IS GENERATED WHEN THE COMMAND IS REJECTED.

#### TEST 7: WRITE CHARACTERISTICS

THIS TEST VERIFIES BASIC OPERATION OF THE WRITE CHARACTERISTICS COMMAND. IT VERIFIES THAT THE COMMAND BLOCK AND CHARACTERISTICS DATA BLOCK ARE FETCHED PROPERLY FROM CPU MEMORY, THE NEED BUFFER ADDRESS (NBA) BIT IN TSSR IS HANDLED PROPERLY, AND THAT A PROPER MESSAGE PACKET IS STORED, WHERE APPROPRIATE. THIS TEST DOES NOT CHECK THAT THE VARIOUS FUNCTIONS ENABLED BY CHARACTERISTIC MODE DATA BITS OPERATE PROPERLY; THE FUNCTIONING OF THESE BITS IS VERIFIED IN SUBSEQUENT TESTS. ALL COMMANDS EXECUTED IN THIS TEST HAVE THE INTERRUPT ENABLE (IE) BIT CLEARED TO ZERO, SO NO INTERRUPTS SHOULD BE GENERATED. HOWEVER, THE PROGRAM RUNS AT PROCESSOR PRIORITY 0, WITH THE INTERRUPT SERVICE ROUTINE SET UP TO FLAG UNEXPECTED INTERRUPTS. IF AN INTERRUPT OCCURS, A PROBLEM EXISTS IN EITHER THE 11/23 BUS INTERFACE SECTION OR IN THE ROM OR PIPELINE.

#### TEST 8: VOLUME CHECK

THIS TEST VERIFIES THAT THE VOLUME CHECK (VCK) BIT, A FLAG HELD WITHIN THE M7196 AND APPEARING IN XST0, IS SET BY INITIALIZE AND CLEARED BY EXECUTING A WRITE CHARACTERISTICS COMMAND WITH THE CVC BIT SET. IT IS ALSO VERIFIED THAT A WRITE CHARACTERISTICS COMMAND WITH THE CVC BIT CLEAR DOES NOT AFFECT THE STATE OF THE VOLUME CHECK BIT. THE ACTUAL FUNCTION OF VOLUME CHECK, THAT OF PREVENTING OR ALLOWING A TAPE MOTION COMMAND DEPENDING UPON WHETHER VOLUME CHECK IS SET OR CLEAR, IS NOT CHECKED BY THIS TEST; THIS FUNCTIONALITY IS CHECKED IN THE INDIVIDUAL TESTS OF TAPE MOTION COMMANDS.

#### TEST 9: COMPLETION INTERRUPT

THIS TEST VERIFIES THAT AN INTERRUPT IS GENERATED AT THE COMPLETION OF THE WRITE CHARACTERISTICS COMMAND IF THE INTERRUPT ENABLE (IE) BIT IN THE COMMAND HEADER WORD IS SET. THIS TEST CHECKS THE FUNCTIONING OF THE INTERRUPT LOGIC AND BASIC PROCESSING OF THE IE BIT.

THE SEQUENCES OF TEST 7 ARE REPEATED, EXCEPT THAT THE INTERRUPT SERVICE ROUTINE IS SET UP TO EXPECT INTERRUPTS AND EACH WRITE CHARACTERISTICS COMMAND IS ISSUED WITH THE IE BIT SET (1). IT IS VERIFIED, WHERE APPROPRIATE, THAT THE IE STATUS BIT IN XST0 OF ANY MESSAGE PACKET IS SET AND THAT A COMPLETION INTERRUPT IS GENERATED. FINALLY, A SEQUENCE OF TWO COMMANDS ARE ISSUED, THE FIRST WITH IE=1 AND THE SECOND WITH IE=0. IT IS VERIFIED THAT NO INTERRUPT IS GENERATED AFTER THE SECOND COMMAND AND THAT THE IE BIT IN XST0 IS 0.

#### TEST 10: BASIC PACKET PROTOCOL

THIS TEST VERIFIES BASIC OPERATION OF THE MESSAGE BUFFER RELEASE COMMAND, THE FUNCTION OF THE ACK BIT IN THE COMMAND HEADER WORD, AND THE REGISTER MODIFICATION REFUSED (RMR) LOGIC.



## TEST 11: NON-TAPE MOTION COMMANDS

THIS TEST VERIFIES PROPER OPERATION OF THE INITIALIZE COMMAND. TWO SUBTESTS ARE USED. THE FIRST VERIFIES THAT THE COMMAND RUNS TO COMPLETION AND STORES A VALID MESSAGE PACKET. THE SECOND VERIFIES THAT NON-ZERO VALUES IN THE COMMAND MODE FIELD CAUSES COMMAND REJECT.

## 7.0 MAINTENANCE HISTORY

REVISION A - MARCH 1982

2  
3  
4  
10  
11 000000  
12  
13  
19 000000  
20 002000 002000  
21 002000 002000  
22  
23  
24  
25  
26  
27  
28  
29 002000  
30 002000  
002000  
002000 103  
002001 126  
002002 124  
002003 123  
002004 101  
002005 000  
002006 000  
002007 000  
002010  
002010 101  
002011  
002011 060  
002012  
002012 000000  
002014  
002014 001217  
002016  
002016 045634  
002020  
002020 045766  
002022  
002022 002154  
002024  
002024 002164  
002026  
002026 046404  
002030  
002030 000000  
002032  
002032 000000  
002034  
002034 000000  
002036  
002036 000000  
002040  
002040 002124

```

.TITLE TSV2 - PROGRAM HEADER
.SBTTL PROGRAM HEADER

.MCALL SVC
SVC ; INITIALIZE SUPERVISOR MACROS
.ENABLE LC
.NLIST BEX,CND
.ENABL ABS,AMA
.=2000
BGNMOD TSV2

TSV2::

:++
: THE PROGRAM HEADER IS THE INTERFACE BETWEEN
: THE DIAGNOSTIC PROGRAM AND THE SUPERVISOR.
:--

POINTER BGNSW,BGNSFT,BGNAU,BGNDU,BGNRPT
HEADER CVTSA,A,0,655.,0
LSNAME:: ;DIAGNOSTIC NAME
.ASCII /C/
.ASCII /V/
.ASCII /T/
.ASCII /S/
.ASCII /A/
.BYTE 0
.BYTE 0
.BYTE 0
LSREV:: ;REVISION LEVEL
.ASCII /A/
LSDEPO:: ;0
.ASCII /0/
LSUNIT:: ;NUMBER OF UNITS
.WORD 0
LSTIML:: ;LONGEST TEST TIME
.WORD 655.
LSHPCP:: ;PTR. TO H.W. QUES.
.WORD LSHARD
LSSPCP:: ;PTR. TO S.W. QUES.
.WORD LSSOFT
LSHPTP:: ;PTR. TO DEF. H.W. PTABLE
.WORD LSHW
LSSPTP:: ;PTR. TO S.W. PTABLE
.WORD LSSW
LSLADP:: ;DIAG. END ADDRESS
.WORD LSLAST
LSSTA:: ;RESERVED FOR APT STATS
.WORD 0
LSCO::
.WORD 0
LSDTYP:: ;DIAGNOSTIC TYPE
.WORD 0
LSAPT:: ;APT EXPANSION
.WORD 0
LSDTP:: ;PTR. TO DISPATCH TABLE
.WORD LSDISPATCH
    
```



002042		L\$PRIO::		;DIAGNOSTIC RUN PRIORITY
002042	000000		.WORD 0	
002044		L\$ENVI::		;FLAGS DESCRIBE HOW IT WAS SETUP
002044	000000		.WORD 0	
002046		L\$EXP1::		;EXPANSION WORD
002046	000000		.WORD 0	
002050		L\$MREV::		;SVC REV AND EDIT #
002050	003		.BYTE C\$REVISION	
002051	003		.BYTE C\$EDIT	
002052		L\$EF::		;DIAG. EVENT FLAGS
002052	000000		.WORD 0	
002054	000000		.WORD 0	
002056		L\$SPC::		
002056	000000		.WORD 0	
002060		L\$DEVP::		; POINTER TO DEVICE TYPE LIST
002060	003376		.WORD LSDVTYP	
002062		L\$REPP::		;PTR. TO REPORT CODE
002062	022654		.WORD L\$RPT	
002064		L\$EXP4::		
002064	000000		.WORD 0	
002066		L\$EXP5::		
002066	000000		.WORD 0	
002070		L\$AUT::		;PTR. TO ADD UNIT CODE
002070	022342		.WORD L\$AU	
002072		L\$DUT::		;PTR. TO DROP UNIT CODE
002072	022440		.WORD L\$DU	
002074		L\$LUN::		;LUN FOR EXERCISERS TO FILL
002074	000000		.WORD 0	
002076		L\$DESP::		;POINTER TO DIAG. DESCRIPTION
002076	003404		.WORD L\$DESC	
002100		L\$LOAD::		;GENERATE SPECIAL AUTOLOAD EMT
002100	104035		EMT ESLOAD	
002102		L\$ETP::		;POINTER TO ERR_TBL
002102	000000		.WORD 0	
002104		L\$ICP::		;PTR. TO INIT CODE
002104	021546		.WORD L\$INIT	
002106		L\$CCP::		;PTR. TO CLEAN-UP CODE
002106	022626		.WORD L\$CLEAN	
002110		L\$ACP::		;PTR. TO AUTO CODE
002110	022546		.WORD L\$AUTO	
002112		L\$PRT::		;PTR. TO PROTECT TABLE
002112	021536		.WORD L\$PROT	
002114		L\$TEST::		;TEST NUMBER
002114	000000		.WORD 0	
002116		L\$DLY::		;DELAY COUNT
002116	000000		.WORD 0	
002120		L\$HIME::		;PTR. TO HIGH MEM
002120	000000		.WORD 0	

.SBTTL DISPATCH TABLE

:+  
: THE DISPATCH TABLE CONTAINS THE STARTING ADDRESS OF EACH TEST.  
: IT IS USED BY THE SUPERVISOR TO DISPATCH TO EACH TEST.  
:--

33  
34  
35  
36  
37  
38  
39  
40 002122  
002122 000013  
002124  
002124 023436  
002126 023656  
002130 024354  
002132 025046  
002134 026402  
002136 027506  
002140 030764  
002142 034352  
002144 035256  
002146 040372  
002150 043474

DISPATCH 11  
.WORD 11  
LSDISPATCH::  
.WORD T1  
.WORD T2  
.WORD T3  
.WORD T4  
.WORD T5  
.WORD T6  
.WORD T7  
.WORD T8  
.WORD T9  
.WORD T10  
.WORD T11

41



```
43 .SBTTL DEFAULT HARDWARE P-TABLE
44
45
46 :++
47 : THE DEFAULT HARDWARE P-TABLE CONTAINS DEFAULT VALUES OF
48 : THE TEST-DEVICE PARAMETERS. THE STRUCTURE OF THIS TABLE
49 : IS IDENTICAL TO THE STRUCTURE OF THE RUN-TIME P-TABLE.
50 :--
    BGNHW DFPTBL ;DEFAULT HARD-P-TABLE
    .WORD L10000-LSHW/2
LSHW::
DFPTBL::
    .WORD 172520 ; 1ST (OF 2) REGISTERS.
    .WORD 224 ; INTERRUPT VECTOR
    .WORD PRI04 ; INTERRUPT PRIORITY.
    ENHW
L10000:
    002152 000003
    002154 172520
    002156 000224
    002160 000200
    002162
```

57  
58  
59  
60  
61  
62  
63 002162  
002162 000004  
002164  
002164  
64  
65 002164 000000  
66 002166 000000  
67  
68  
69 002170 000017  
70 002172 000310  
71 002174  
002174  
72  
73 002174

.SBTTL SOFTWARE P-TABLE

```

:++
: THE SOFTWARE P-TABLE CONTAINS THE VALUES OF THE PROGRAM
: PARAMETERS THAT CAN BE CHANGED BY THE OPERATOR.
:--
      BGNSW   SFPTBL
      .WORD  L10001-L$SW/2
L$SW::
SFPTBL::

TRANSTST::      .WORD  0      ; ENABLE TEST OF TRANSPORT(S) IF =1
NOITS::         .WORD  0      ; INHIBIT ITERATION OPTION.
                ; ... 0 = ITERATE.
                ; ...NZ = INHIBIT ITERATE.
LERRMAX::       .WORD  15.    ; LOCAL (PER TEST) ERROR LIMIT
GERRMAX::       .WORD  200.   ; GLOBAL (PER UNIT) ERROR LIMIT
                ENDSW
L10001:

                ENDMOD
```



7  
8  
13  
19  
20 002174  
002174  
21  
22  
23  
24  
25  
26  
27  
28  
29  
33 002174

.TITLE TSV3 - GLOBAL AREAS  
.SBTTL GLOBAL EQUATES SECTION

TSV3:: BGNMOD TSV3

.SBTTL GLOBAL EQUATES SECTION

;++  
: THE GLOBAL EQUATES SECTION CONTAINS PROGRAM EQUATES THAT  
: ARE USED IN MORE THAN ONE TEST.  
:--

EQUALS ; GET STANDARD EQUATES.

: BIT DIFINITIONS

100000	BIT15== 100000
040000	BIT14== 40000
020000	BIT13== 20000
010000	BIT12== 10000
004000	BIT11== 4000
002000	BIT10== 2000
001000	BIT09== 1000
000400	BIT08== 400
000200	BIT07== 200
000100	BIT06== 100
000040	BIT05== 40
000020	BIT04== 20
000010	BIT03== 10
000004	BIT02== 4
000002	BIT01== 2
000001	BIT00== 1

001000	BIT9== BIT09
000400	BIT8== BIT08
000200	BIT7== BIT07
000100	BIT6== BIT06
000040	BIT5== BIT05
000020	BIT4== BIT04
000010	BIT3== BIT03
000004	BIT2== BIT02
000002	BIT1== BIT01
000001	BIT0== BIT00

: EVENT FLAG DEFINITIONS  
: EF32:EF17 RESERVED FOR SUPERVISOR TO PROGRAM COMMUNICATION

000040	EF.START== 32.	: START COMMAND WAS ISSUED
000037	EF.RESTART== 31.	: RESTART COMMAND WAS ISSUED
000036	EF.CONTINUE== 30.	: CONTINUE COMMAND WAS ISSUED
000035	EF.NEW== 29.	: A NEW PASS HAS BEEN STARTED
000034	EF.PWR== 28.	: A POWER-FAIL/POWER-UP OCCURRED

```
000340 ; PRIORITY LEVEL DEFINITIONS
000300 .
000240 PRI07== 340
000200 PRI06== 300
000140 PRI05== 240
000100 PRI04== 200
000040 PRI03== 140
000000 PRI02== 100
PRI01== 40
PRI00== 0
```

```
000004 ; OPERATOR FLAG BITS
000010 .
000020 EVL== 4
000040 LOT== 10
000100 ADR== 20
000200 IDU== 40
000400 ISR== 100
001000 UAM== 200
002000 BOE== 400
004000 PNT== 1000
010000 PRI== 2000
020000 IXE== 4000
040000 IBE== 10000
100000 IER== 20000
LOE== 40000
HOE== 100000
```

34  
35 002174

```
000250 ;DEFINE MEMORY MANAGEMENT REGISTERS
177572 .SBTTL KT11 MEMORY MANAGEMENT DEFINITIONS
177574 ;*KT11 VECTOR ADDRESS
177576 MMVEC= 250
172516 ;*KT11 STATUS REGISTER ADDRESSES
SR0= 177572
SR1= 177574
SR2= 177576
SR3= 172516
;IF NB
;*USER 'I' PAGE DESCRIPTOR REGISTERS
UIPDR0= 177600
UIPDR1= 177602
UIPDR2= 177604
UIPDR3= 177606
UIPDR4= 177610
UIPDR5= 177612
UIPDR6= 177614
UIPDR7= 177616
;IF NB
;*USER 'D' PAGE DESCRIPTOR REGISTORS
UDPDR0= 177620
UDPDR1= 177622
UDPDR2= 177624
UDPDR3= 177626
UDPDR4= 177630
UDPDR5= 177632
UDPDR6= 177634
UDPDR7= 177636
```



```
.ENDC
:*USER 'I' PAGE ADDRESS REGISTERS
UIPAR0= 177640
UIPAR1= 177642
UIPAR2= 177644
UIPAR3= 177646
UIPAR4= 177650
UIPAR5= 177652
UIPAR6= 177654
UIPAR7= 177656
.IF NB
:*USER 'D' PAGE ADDRESS REGISTERS
UDPAR0= 177660
UDPAR1= 177662
UDPAR2= 177664
UDPAR3= 177666
UDPAR4= 177670
UDPAR5= 177672
UDPAR6= 177674
UDPAR7= 177676
.ENDC
.ENDC
.IF NB
:*SUPERVISOR 'I' PAGE DESCRIPTOR REGISTERS
SIPDR0= 172200
SIPDR1= 172202
SIPDR2= 172204
SIPDR3= 172206
SIPDR4= 172210
SIPDR5= 172212
SIPDR6= 172214
SIPDR7= 172216
.IF NB
:*SUPERVISOR 'D' PAGE DESCRIPTOR REGISTERS
SDPDR0= 172220
SDPDR1= 172222
SDPDR2= 172224
SDPDR3= 172226
SDPDR4= 172230
SDPDR5= 172232
SDPDR6= 172234
SDPDR7= 172236
.ENDC
:*SUPERVISOR 'I' PAGE ADDRESS REGISTERS
SIPAR0= 172240
SIPAR1= 172242
SIPAR2= 172244
SIPAR3= 172246
SIPAR4= 172250
SIPAR5= 172252
SIPAR6= 172254
SIPAR7= 172256
.IF NB
:*SUPERVISOR 'D' PAGE ADDRESS REGISTERS
SDPAR0= 172260
SDPAR1= 172262
SDPAR2= 172264
```

```
SDPAR3= 172266
SDPAR4= 172270
SDPAR5= 172272
SDPAR6= 172274
SDPAR7= 172276
.ENDC
.ENDC
;*KERNEL 'I' PAGE DESCRIPTOR REGISTERS
172300 KIPDR0= 172300
172302 KIPDR1= 172302
172304 KIPDR2= 172304
172306 KIPDR3= 172306
172310 KIPDR4= 172310
172312 KIPDR5= 172312
172314 KIPDR6= 172314
172316 KIPDR7= 172316
.IF NB
;*KERNEL 'D' PAGE
DESCRIPTOR REGISTERS
KDPDR0= 172320
KDPDR1= 172322
KDPDR2= 172324
KDPDR3= 172326
KDPDR4= 172330
KDPDR5= 172332
KDPDR6= 172334
KDPDR7= 172336
.ENDC
;*KERNEL 'I' PAGE ADDRESS REGISTERS
172340 KIPAR0= 172340
172342 KIPAR1= 172342
172344 KIPAR2= 172344
172346 KIPAR3= 172346
172350 KIPAR4= 172350
172352 KIPAR5= 172352
172354 KIPAR6= 172354
172356 KIPAR7= 172356
.IF NB
;*KERNEL 'D' PAGE ADDRESS REGISTERS
KDPAR0= 172360
KDPAR1= 172362
KDPAR2= 172364
KDPAR3= 172366
KDPAR4= 172370
KDPAR5= 172372
KDPAR6= 172374
KDPAR7= 172376
.ENDC
```



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.SBTTL TSV05 REGISTER AND PACKET DEFINITIONS

;  
; SOME GENERAL EQUATES.  
;

000004	ERRVEC==	4	; POINTER TO ERROR VECTOR FOR BUS TIME OUT.
000060	TTIVEC==	60	; INTERRUPT VECTOR FOR CONSOLE INPUT
177560	TTICSR==	177560	; BUS ADDRESS OF CONSOLE INPUT
177562	TTIBFR==	177562	; CONSOLE INPUT DATA BUFFER
177520	BDVPCR==	177520	; BDV11 PAGE CONTROL REGISTER

;  
;+  
;BIT DEFINITIONS FOR TSSR REGISTER  
;-

100000	SC=	BIT15	;SPECIAL CONDITION
040000	BIE=	BIT14	;BUS INTERFACE ERROR
020000	SCE=	BIT13	;SANITY CHECK ERROR
010000	RMR=	BIT12	;MODIFICATION REFUSED
004000	NXM=	BIT11	;NONEXISTANT MEMORY ERROR
002000	NBA=	BIT10	;NEED BUFFER ADDRESS
001400	HIADDR=	BIT9!BIT8	;EXTENDED ADDRESS BITS
000200	SSR=	BIT7	;SUB SYSTEM READY
000100	OFL=	BIT6	;OFF LINE BIT
000060	FATERR=	BIT4!BIT5	;FATAL TERMINATION ERROR CODES
000016	TERCLS=	BIT3!BIT2!BIT1	;TERMINATION CODES

;  
;+  
;BIT DEFINITIONS FOR EXTENDED STATUS REGISTER 0  
;(XST0)  
;  
;-

100000	XSOTMK=	BIT15	;TAPE MARK DETECTED
040000	XSORLS=	BIT14	;RECORD LENGTH SHORT
020000	XSOLET=	BIT13	;LOGICAL END OF TAPE
010000	XSORLL=	BIT12	;RECORD LENGTH LONG
004000	XSOYLE=	BIT11	;WRITE LOCK ERROR
002000	XSONEF=	BIT10	;NON EXECUTABLE FUNCTION
001000	XSOILC=	BIT9	;ILLEGAL COMMAND
000400	XSOILA=	BIT8	;ILLEGAL ADDRESS
000200	XSOMOT=	BIT7	;TAPE IN MOTION
000100	XSOONL=	BIT6	;TRANSPORT ON LINE
000040	XSOIE=	BIT5	;INTERRUPT ENABLE
000020	XSOVCK=	BIT4	;VOLUME CHECK BIT
000010	XSOPED=	BIT3	;PHASE ENCODED DRIVE
000004	XSOVLK=	BIT2	;WRITE LOCKED
000002	XSOBOT=	BIT1	;BEGINNING OF TAPE
000001	XSOEOT=	BIT0	;END OF TAPE

;  
;+  
;BIT DEFINITIONS FOR EXTENDED STATUS REGISTER 1  
;(XST1)

```

97
98      100000      :-
99      040000      X1.DLT = BIT15      ;DATA LATE
100     020000      X1.SPARE= BIT14      ;NOT USED
101     017375      X1.COR = BIT13      ;CORRECTABLE DATA ERROR
102     000400      X1.MBZ = BIT12+BIT11+BIT10+BIT9+BIT7+BIT6+BIT5+BIT4+BIT3+BIT2+BIT0 ;ALWAYS 0
103     000002      X1.RBP = BIT8      ;READ BUS PARITY ERROR
104
105
106     :-
107     ;+
108     ;BIT DEFINITIONS FOR EXTENDED STATUS REGISTER 2
109     ;(XST2)
110     :-
111     100000      X2.OPM = BIT15      ;OPERATION IN PROGRESS (TAPE MOVING)
112     040000      X2.RCE = BIT14      ;RAM CHECKSUM ERROR
113     035400      X2.SPARE= BIT13+BIT12+BIT11+BIT9+BIT8 ;NOT USED BY TSV05 (ALWAYS=0)
114     002000      X2.WCF = BIT10      ;WRITE CLOCK FAILURE (FIFO NOT EMPTIED BY TRANSPORT)
115     000200      X2.EXTF = BIT7      ;IF WRITE CHAR CMD THEN = EXTENDED FEATURES ENABLED
116     000100      X2.BUFE = BIT6      ;IF WRITE CHAR CMD THEN = BUFFERING ENABLED
117     000077      X2.REV = 000077    ;IF WRITE CHAR CMD THEN = MICROCODE REVISION LEVEL
118     000007      X2.UNIT = BIT2+BIT1+BIT0 ;IF GET STATUS THEN = CURRENTLY SELECTED UNIT NO.
119
120     ;+
121     ;BIT DEFINITIONS FOR EXTENDED STATUS REGISTER 3
122     ;(XST3)
123     :-
124     177400      X3.MDE = 177400    ;MICRO-DIAGNOSTIC ERROR CODE
125     000200      X3.SPARE= BIT7      ;NOT USED BY TSV05
126     000100      X3.OPI = BIT6      ;OPERATION INCOMPLETE
127     000040      X3.REV = BIT5      ;REVERSE
128     000020      X3.TRF = BIT4      ;TRANSPORT RESPONSE FAILURE
129     000010      X3.DCK = BIT3      ;DENSITY CHECK
130     000006      X3.MBZ =BIT2+BIT1    ;NOT USED ALWAYS 0
131     000001      X3.RIB = BIT0      ;REVERSE INTO BOT
132
133     ;+
134     ;BIT DEFINITIONS FOR EXTENDED STATUS REGISTER 4
135     ;(XST4)
136     :-
137     100000      X4.HSP = BIT15      ;HIGH SPEED
138     040000      X4.RCE = BIT14      ;RETRY COUNT EXCEEDED
139     020000      X4.TSM = BIT13      ;TRANSPORT SPECIAL MODE
140     017400      X4.MBZ = BIT12+BIT11+BIT10+BIT9+BIT8 ;NOT USED ALWAYS 0
141     000377      X4.WRC = 000377    ;WRITE RETRY COUNT FIELD
142
143     ;+
144     ;TSSR TERMINATION CODES (BIT 0-2)
145     :-
146
147
148     000006      TSREJ= 3*2      ;COMMAND REJECTED
149     000006      UNREC= 6      ;UNRECOVERABLE ERROR
150
151     ;+
152     ;DEVICE REGISTER OFFSETS
153
    
```



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154
155      :-
156
157      000000      TSBA== 0
158      000000      TSDB== 0      ;TSDB/TSBA REGISTER
159      000001      TSBH== 1
160      00C001      TSDBH== 1      ;TSDB/TSBA REGISTER HIGH BYTE
161      000002      TSSR== 2      ;TSSR REGISTER
162      000003      TSSRH== 3      ;TSSR REGISTER HIGH BYTE
163
164      :-+
165      :- TSDB ADDRESS BIT DEFINITIONS
166      :-
167      000003      A1716 = BIT1+BIT0      ;ADDRESS BITS 17:16 ARE IN 1:0
168
169      :-+
170      :- COMMAND DEFINITIONS
171      :-
172      000017      P.GETSTAT      = 17      ;GET STATUS
173      000013      P.INIT          = 13      ;INITIALIZE
174      000012      P.CONTROL      = 12      ;CONTROL COMMANDS
175      000011      P.FORMAT       = 11      ;FORMAT
176      000010      P.POSITION     = 10      ;POSITION
177      000006      P.WRTSUB       = 6       ;SUBSYSTEM WRITE
178      000005      P.WRITE        = 5       ;WRITE
179      000004      P.WRTCHAR      = 4       ;WRITE CHARACTERISTICS
180      000001      P.READ         = 1       ;READ
181
182      :-+
183      :- COMMAND PACKET HEADER WORD BIT DEFINITIONS
184      :-
185      100000      P.ACK          = BIT15      ;BUFFER AVAIL FOR CONTROLLER
186      040000      P.CVC          = BIT14      ;CLEAR VOLUME CHECK
187      020000      P.OPP          = BIT13      ;REVERSE SEQUENCE OF DATA BITS
188      010000      P.SWB          = BIT12      ;SWAP BYTES IN MEMORY
189      007400      P.MODE         = BIT11!BIT10!BIT9!BIT8 ;EXTENDED COMMAND MODE FIELD
190      000200      P.IE           = BIT7       ;INTERRUPT ENABLE
191      000140      P.FMT= BIT6!BITS      ;PACKET HEADER TYPE (ALWAYS=0)
192      000037      P.CMD          = 37        ;MAJOR COMMAND FIELD
193
194      :-+
195      :- CONTROL COMMAND MODE CODES
196      :-
196      000000      PC.RELEASE     = 0*256.    ;RELEASE BUFFER
197      000400      PC.REWIND      = 1*256.    ;REWIND
198      001000      PC.NOOP        = 2*256.    ;NO-OP
199      002000      PC.IEREW       = 4*256.    ;REWIND IMMEDIATE INTERRUPT
200      002400      PC.ERASE       = 5*256.    ;SECURITY ERASE
201
202      :-+
203      :- CONTROLLER RAM DEFINITIONS
204      :-
205      000167      RMCHBEG = 167      ;CHARACTERISTICS IO DATA BEGIN RAM ADDRESS
206      000200      RMCHEND = 200     ;CHARACTERISTICS IO DATA END RAM ADDRESS
207      000201      RMPKTBEG= 201     ;COMMAND PACKET BEGIN RAM ADDRESS
208      000210      RMPKTEND= 210     ;COMMAND PACKET END RAM ADDRESS
209      000215      RMSGGBEG= 215     ;MESSAGE BUFFER BEGIN RAM ADDRESS
210      000234      RMSGGENG= 234     ;MESSAGE BUFFER END RAM ADDRESS

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217      000006      XST0== 6           :EXTENDED STATUS REGISTER 0 (WORD 4)
218      000010      XST1== 8           :EXTENDED STATUS REGISTER 1 (WORD 5)
219      000012      XST2== 10          :EXTENDED STATUS REGISTER 2 (WORD 6)
220      000014      XST3== 12          :EXTENDED STATUS REGISTER 3 (WORD 7)
221      000016      XST4== 14          :EXTENDED STATUS REGISTER 4 (WORD 8)
222
223
224
225
226      :+
227      :REGISTER DEFINITIONS IN THE MESSAGE BUFFER
228      :-
229
230
231
232
233
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235
236
237      :+
238      :OFFSETS TO WORD LOCATIONS IN PACKET DEFINITIONS
239      :-
240
241      000002      PKLOW = 2           :LOW ORDER CHARACTERISTIC DATA POINTER
242      000004      PKHI  = 4           :HIGH ORDER CHARACTERISTIC DATA POINTER
243      000006      PKBCNT = 6          :NUMBER OF BYTES IN DATA PACKET
244
245      000010      EXBCNT=10          :NUMBER OF BYTES IN EXTENDED DATA PACKET
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268      000040      WC.I1TAD      = BIT5      :ITAD1 - TRANSPORT ADDRESS BIT 1
269      000020      WC.I5RESV     = BIT4      :IRESV5 - RESERVED #5
270      000010      WC.IREW      = BIT3      :IREW   - REWIND
271      000004      WC.IRWU      = BIT2      :IRWU   - REWIND AND UNLOAD
272      000002      WC.IFEN      = BIT1      :IFEN   - FORMATTER ENABLE
273      000001      WC.IGO       = BIT0      :GO
274
275      :+
276      :BSEL1 CODES FOR WRITE FORMAT
277      :-
278      000200      WF.IHISP     = BIT7      :IHISP  - HIGH SPEED
279      000100      WF.IWRT     = BIT6      :IWRT   - WRITE
280      000040      WF.IREV     = BIT5      :IREV   - REVERSE
281      000020      WF.IWFM     = BIT4      :IWFM   - WRITE FILE MARK
282      000010      WF.IEDIT    = BIT3      :IEDIT  - EDIT
283      000004      WF.IERASE   = BIT2      :IERASE - ERASE
284      000002      WF.I3RESV   = BIT1      :IRESV3 - RESERVED #3
285      000001      WF.I4RESV   = BIT0      :IRESV4 - RESERVED #4
286
287
288      :+
289      :BSEL1 CODES FOR WRITE MISCELLANEOUS SUBCOMMAND
290      :-
291      000200      MS.EXT      = BIT7      :INVERT SENSE OF EXTENDED FEATURES SWITCH
292      000020      MS.RSFIFO    = BIT4      :RESET FIFO AND INPUT PARITY ERRORR
293      000010      MS.RSTAPE    = BIT3      :RESET TAPE STATUS IN 2 FLIP-FLOPS
294      000006      MS.ATTN     = BIT2!BIT1 :ATTENTION TRIGGER FIELD
295      000001      MS.RSD      = BIT0      :RESET TIMER A,B THEN DELAY TIMES IN SEL2
296
297      :+
298      : MS.ATTN SUBCODES
299      :-
300      000000      MSA.NOP     = 0*2      :NO-OP (NOTHING TRIGGERED)
301      000002      MSA.VOL     = 1*2      :SIMULATE ON-LINE/OFF-LINE TRANSITION
302      000004      MSA.NRAM    = 2*2      :FORCE NON-FATAL RAM ERROR (FORCES ERRCODE 54)
303      000006      MSA.FRAME   = 3*2      :FORCE FATAL RAM ERROR (CAUSES SCE TO SET)
304
305      :+
306      : WRITE SUBSYSTEM WRITE NPR BSEL1 BIT DEFINITIONS
307      :-
308      000200      NP.IR       = BIT7      :INTERRUPT REQUEST (0-1 TRANSITION)
309      000100      NP.OUT      = BIT6      :TAPE DATA DIRECTION OUT (0= IN)
310      000040      NP.LOOP     = BIT5      :ENABLE TRANSPORT LOOPBACK
311      000020      NP.WRP      = BIT4      :WRITE CORRECT PARITY (SET=0 TO WRITE WRONG)
312
313      :+
314      : READ STATUS MESSAGE BUFFER BIT DEFINITIONS
315      :-
316      000200      S2.DIM      = BIT7      :WORD #9 BYTE 2 DATA IN MISS
317      000100      S2.ILW     = BIT6      :ILW H
318      000040      S2.OUTRDY   = BIT5      :OUT RDY H
319      000020      S2.INRDY    = BIT4      :IN RDY H
320      000010      S2.ATIMR    = BIT3      :TIMER A FLAG H
321      000004      S2.BTIMR    = BIT2      :TIMER B FLAG H
322      000003      S2.UNDEF    = BIT1+BIT0 : (UNDEFINED)
323      100000      S1.PARIN    = BIT15     :WORD #8 BYTE 1 PARIN H
324      040000      S1.I2RESV   = BIT14     :IRESV2
325      020000      S1.I1RESV   = BIT13     :IRESV1
326      010000      S1.IEOT     = BIT12     :IEOT L

```

325	004000	S1.IIDENT	= BIT11	:	IIDENT H
326	002000	S1.ICER	= BIT10	:	ICER H
327	001000	S1.IFMK	= BIT9	:	IFMK H
328	000400	S1.IHER	= BIT8	:	IHER H
329	000200	SO.ISPEED	= BIT7	:WORD #8 BYTE 0	ISPEED H
330	000100	SO.IRDY	= BIT6	:	IRDY L
331	000040	SO.IONL	= BIT5	:	IONL L
332	000020	SO.ILDPA	= BIT4	:	ILDPA L
333	000010	SO.IDBY	= BIT3	:	IDBY L
334	000004	SO.IRWD	= BIT2	:	IRWD L
335	000002	SO.IFBY	= BIT1	:	IFBY L
336	000001	SO.IFPT	= BIT0	:	IFPT L
337				:	
338				:	



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```
.SBTTL SPECIAL MACROS AND OPDEFS.

:+
:SAVE GENERAL REGS 1 TO 5
:-

.MACRO SAVREG
JSR R5,REGSAV
.ENDM

:+
:MACRO TO FORCE AN ERROR
:-
.MACRO FORCERROR TAG,NOTSSR
.NLIST
.IIF NDF LISTALL, .NLIST
.LIST
.IF B NOTSSR
MOV TSSR(R5),R1 ;READ TSSR
.ENDC
MOV FORCER,FORCER ;IS FORCER SET? (LEAVE C BIT ALONE)
BNE TAG ;BR IF YES
.NLIST
.IIF NDF LISTALL, .LIST
.LIST
.ENDM

:+
:MACRO TO FORCE AN EXIT TO AVOID SECTION ITERATIONS
: WILL EXIT TO A LABEL IF FORCER IS NEGATIVE
: SO TO FORCE ERRORS AND EXIT ON 1 ERROR SET
: FORCER TO 177777
: TO FORCE ERRORS AND ITERATIONS SET FORCER TO 1.
:-
.MACRO FORCEEXIT TAG
.NLIST
.IIF NDF LISTALL, .NLIST
.LIST
MOV FORCER,FORCER ;IS FORCER NEGATIVE?
BMI TAG ;BR IF YES
.NLIST
.IIF NDF LISTALL, .LIST
.LIST
.ENDM

:+
:MACRO TO INCREMENT ERROR COUNTS
:-
.MACRO NEXT.ERRNO
.NLIST
::: .IIF NDF LISTALL, .NLIST
ERRNO=ERRNO+1
::: .IIF NDF LISTALL, .LIST
.LIST
.ENDM

:+
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:MACRO TO PERFORM XOR  
:-

```
.MACRO XOR A,B
MOV A,-(SP)
BIC B,(SP)
BIC A,B
BIS (SP)+,B
.ENDM
```

000000

```
EN=0 ; INITIALIZE ERROR NUMBER
.SBTTL FORCER - FORCE ERROR FLAG
```

:  
: THE FOLLOWING LOCATIONS MAY BE PATCHED BY THE USER  
: TO OBTAIN THE RESULTS DESCRIBED FOR EACH.  
:

002174 000000

```
FORCER:: 0 ; FORCE TYPE ALL HARD ERRORS (THE ONES CALLED -
: - BY THE MACRO 'IFERROR'). AN ERROR NEED NOT -
: - EXIST, JUST ASSUME AND TYPE THE MESSAGE.
```





.SBTTL TSTBLK - TEST DATA TABLE

```

: +
: THIS TABLE CONTAINS TEST DATA USED IN SEVERAL TESTS
: IN SEQUENCE THE DATA IS:
:
:     ALL ZEROS
:     ALL ONES
:     WALKING ONES
:     WALKING ZEROS
:     ALTERNATING ONES AND ZEROS
: -
    
```

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479 002750
480 002750 000000
481 002752 177777
482 002754 000001
483 002756 000002
484 002760 000004
485 002762 000010
486 002764 000020
487 002766 000040
488 002770 000100
489 002772 000200
490 002774 000400
491 002776 001000
492 003000 002000
493 003002 004000
494 003004 010000
495 003006 020000
496 003010 040000
497 003012 100000
498 003014 177776
499 003016 177775
500 003020 177773
501 003022 177767
502 003024 177737
503 003026 177677
504 003030 177577
505 003032 177377
506 003034 176777
507 003036 175777
508 003040 173777
509 003042 167777
510 003044 157777
511 003046 137777
512 003050 077777
513 003052 125252
514 003054 052525
515      003056
    
```

```

TSTBLK::
: .WORD 0 :ALL ZEROS
: .WORD 177777 :ALL ONES
: .WORD BIT0 :DATA FOR WALKING ONES
: .WORD BIT1
: .WORD BIT2
: .WORD BIT3
: .WORD BIT4
: .WORD BIT5
: .WORD BIT6
: .WORD BIT7
: .WORD BIT8
: .WORD BIT9
: .WORD BIT10
: .WORD BIT11
: .WORD BIT12
: .WORD BIT13
: .WORD BIT14
: .WORD BIT15
: .WORD ^CBIT0 :DATA FOR WALKING ZEROS
: .WORD ^CBIT1
: .WORD ^CBIT2
: .WORD ^CBIT3
: .WORD ^CBIT5
: .WORD ^CBIT6
: .WORD ^CBIT7
: .WORD ^CBIT8
: .WORD ^CBIT9
: .WORD ^CBIT10
: .WORD ^CBIT11
: .WORD ^CBIT12
: .WORD ^CBIT13
: .WORD ^CBIT14
: .WORD ^CBIT15
: .WORD 125252 :ALTERNATING ONES, ZEROS
: .WORD 052525 :ALTERNATING ONES, ZERO OPPOSITE FROM ABOVE

TBLEND==.
    
```



```

517                                     .SBTTL GLOBAL ENVIRONMENT STORAGE
518
519                                     ;STORAGE FOR DEVICE REGISTERS
520
521 003056 000000 100000 000000 DUMMY: 0,100000,0,0 ;DUMMY DEVICE REGISTERS...
522 003066 000000 000000 000000      0,0,0,0,0,0,0,0,0 ;...FOR MULTI-UNIT CHECKOUT.
523
524
525
526 003106 000000 DUFLG::          .WORD 0          ;'DROPPED UNIT' FLAG.
527                                     ;INHIBITS CODE IN 'CLEAN-UP'.
528 003110 000000 NODEV::          .WORD 0          ;FLAG TO SAY NO DEVICE.
529
530 003112 000000 TEMP1::          .WORD 0          ;SOME TEMP LOCATIONS.
531 003114 000000 TEMP2::          .WORD 0
532 003116 000000 XXCOMM::        .WORD 0          ;XXDP+ COMM BLOCK POINTER.
533 003120 000000 FREE::          .WORD 0          ;1ST FREE MEMORY ADDRESS...
534 003122 000000 FRESIZ::        .WORD 0          ;...AND SIZE (IN WORDS).
535 003124 000000 FREEHI: .WORD 0          ;LAST WORD IN FREE SPACE
536 003126 000000 KTFLG::          .WORD 0          ;KT11, MEM AVAIL FLAG -
537                                     ;- .WORD 0 = <24K OR NO KT -
538                                     ;- NZ = >24K AND KT.
539 003130 000000 KTENABLE::        .WORD 0          ;SET BY TEST ROUTINES TO FLAG >28K UNDER TEST
540 003132 000000 NXMFLG::        .WORD 0          ;SET IF WE CAN TEST CLEARED OTHERWISE
541 003134 000000 NXMLO::          .WORD 0          ;NXM LO ADDRESS BITS
542 003136 000000 NXMHI::          .WORD 0          ;NXM HI ADDRESS BITS FOR DAL'S 16-21
543 003140 000000 T23A::          .WORD 0          ;11/23A FLAG
544 003142 000000 T23B::          .WORD 0          ;11/23B FLAG
545 003144 000000 T3BFLG::        .WORD 0          ;TEST 3B FLAG ^0
546 003146 002000 PST32W::        .WORD 2000       ;32W BLOCK ADDRESS FOR 32K START
547 003150 000000 SIFLAG::        .WORD 0
548 003152 000000 BADDAT::        .WORD 0          ;ACTUAL DATA
549 003154 000000 GDDAT::          .WORD 0          ;EXPECTED DATA
550 003156 000000 LOOPFL::        .WORD 0
551 003160 CTAB::          ;CONFIGURATION TABLES.
552 003160 000000 CTABM::          .WORD 0          ;CONFIG WORK.
553 003162 000000 .WORD 0
554 003164 000000 .WORD 0
555 003166 000000 .WORD 0
556 003170 177777 .WORD -1          ;END OF MEM TABLE.
557 003172
558 CTABE::
559 ;ERROR STATISTICS TABLE (1 WORD PER UNIT), 64 UNITS MAX:
560
561 0 = UNIT NOT TESTED
562 100000 = UNIT ONLINE, NO ERRORS
563 10XXXX = UNIT ONLINE, ENCOUNTERED XXXX ERRORS
564 160000 = UNIT DROPPED, NON-EXISTENT DEVICE REGISTER
565 160001 = UNIT DROPPED, NOT IDLE AT START
566 14XXXX = UNIT DROPPED, ENCOUNTERED XXXX ERRORS
567 003172
568 003372 000000 ERTABL: .BLKW 64.
569 ERTABE: .WORD 0
570 003374 000000 SKIPT: .WORD 0          ;1=SKIP SUBTEST 0=NO SKIP OF SUBTEST
    
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.SBTTL GLOBAL TEXT MESSAGES  
:++  
: THE GLOBAL TEXT SECTION CONTAINS FORMAT STATEMENTS,  
: MESSAGES, AND ASCII INFORMATION THAT ARE USED IN  
: MORE THAN ONE TEST.  
:--

:+  
: NAMES OF DEVICES SUPPORTED  
:-

DEV TYP <TSV05>  
LSDVTYP::  
.ASCIZ /TSV05/  
.EVEN

:+  
: TEST DESCRIPTION  
:-

DESCRIPT <\*\*\*\* TSV05 LOGIC DIAGNOSTIC - REPLACE M7196 IF ERROR \*\*\*\*>  
L\$DESC::  
.ASCIZ /\*\*\*\* TSV05 LOGIC DIAGNOSTIC - REPLACE M7196 IF ERROR \*\*\*\*/  
.EVEN

:+  
: BIT TO ASCII CONVERSION FOR TSSR REGISTER  
:-

TSSRBIT::  
.WORD 1\$,2\$,3\$,4\$,5\$,6\$,7\$,8\$  
.WORD 9\$,10\$,11\$,12\$,13\$,14\$,15\$,16\$  
1\$: .ASCIZ 'SC'  
2\$: .ASCIZ 'BIE'  
3\$: .ASCIZ 'SCE'  
4\$: .ASCIZ 'RMR'  
5\$: .ASCIZ 'NXM'  
6\$: .ASCIZ 'NBA'  
7\$: .ASCIZ 'BIT9'  
8\$: .ASCIZ 'BIT8'  
9\$: .ASCIZ 'SSR'  
10\$: .ASCIZ 'OFL'  
11\$: .ASCIZ 'BIT5'  
12\$: .ASCIZ 'BIT4'  
13\$: .ASCIZ 'BIT3'  
14\$: .ASCIZ 'BIT2'  
15\$: .ASCIZ 'BIT1'  
16\$: .ASCIZ 'BIT0'  
.EVEN  
SFIERR: .ASCIZ 'TSSR ERROR AFTER SOFT INIT'  
SFHERR: .ASCIZ 'TSSR ERROR AFTER BUS RESET'  
NXR: .ASCIZ / NON-EXISTANT DEVICE REGISTER/  
NXRX: .ASCIZ /% ADDRESS: %06/  
TSSX: .ASCII /% TSBA,TSSR EXP'D: %06%,%06%N/  
.ASCIZ /% TSBA,TSSR REC'D: %06%,%06%

003541  
003603  
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111  
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040  
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003545  
003607  
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040  
040  
040



```
644 004113 045 116 045 FUSI: .ASCII /%N%/
645 004117 040 040 125 USI: .ASCIZ / UNEXPECTED INTERRUPT/
646 004146 040 040 111 NSI: .ASCIZ / INTERRUPT EXPECTED, NOT RECEIVED/
647 004211 045 116 045 FNOINTR: .ASCII /%N%/
648 004215 040 040 116 NOINTR: .ASCIZ / NO INTERRUPT WAS GENERATED/
649 004252 040 040 111 IFAULT: .ASCIZ / INTERRUPT FAULT/
650 004274 045 101 040 INTX: .ASCIZ /%A CPU PC: %06% TSBA: %06/
651 004331 040 040 042 NOINIT: .ASCIZ / 'BUS-INIT' DIDN'T INITIALIZE CONTROLLER/
652 004403 040 040 042 NSINIT: .ASCIZ / 'SOFT-INIT' DIDN'T INITIALIZE THE DPU/
653 004453 040 040 042 BRINIT: .ASCIZ / 'BUS-RESET' DIDN'T INITIALIZE THE DPU/
654
655 004523 000 NUL: .ASCIZ //
656 004524 045 116 000 NULCR: .ASCIZ /%N/
657 004527 045 101 040 EXPGOT: .ASCIZ /%A EXP'D: %06%, REC'D: %06/
658 004563 045 116 045 EXPGT2: .ASCIZ /%N% EXP'D: %06%, %06%N% REC'D: %0%, %06/
659 004637 045 101 040 DUAD12: .ASCIZ /%A REG(W) WRITTEN TO: %06% REG(R) READ; EXP'D: %06%, REC'D: %06/
660 004741 122 101 115 PKTRAM:: .ASCIZ 'RAM Contents Do Not Match Packet Sent'
661 005007 040 040 103 SCME: .ASCIZ / CONFIG DOESN'T MATCH MFG. MASTER/
662 005052 127 122 111 WRTMSG: .ASCIZ 'WRITE CHARACTERISTICS Failed'
663 005107 124 123 123 WRTERR: .ASCIZ 'TSSR Incorrect After WRITE Command, More Bits Set Than SSR'
664 005202 124 123 123 RDERR: .ASCIZ 'TSSR Incorrect After READ Command, More Bits Set Than SSR'
665 005274 106 101 124 SCHERR: .ASCIZ 'FATAL ERROR IN SUBTEST - CHECK TAPE, CABLES, TRANSPORT etc.'
666 005366 105 122 122 RETERR: .ASCIZ 'ERROR IN SUBTEST - WRITE DATA RETRY FIVE TIMES FAILED'
667 005454 045 116 045 NOMEM: .ASCIZ '%N% ***** NO NXM ADDRESS--CANNOT TEST NXM TIMEOUT. *****%N'
668 005550 045 116 045 M8186: .ASCIZ '%N% ***** 11/23A SYSTEM *****%N'
669 005641 045 116 045 M8189: .ASCIZ '%N% ***** 11/23B SYSTEM *****%N'
670 .EVEN
671
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```

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683 005732
    005732
684 005732
    005732 013746 003110
    005736 012746 003773
    005742 012746 000002
    005746 010600
    005750 104415
    005752 062706 000006
685 005756 004737 005764
686 005762
    005762
    005762 104423
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693 005764 005727
694 005766 000000
695 005770 001402
696 005772 004777 177770
697 005776
    005776 012746 004524
    006002 012746 000701
    006006 010600
    006010 104415
    006012 062706 000004
698 006016 000207
    
```

.SBTTL GLOBAL ERROR REPORT SECTION

```

:++
: THE GLOBAL ERROR REPORT SECTION CONTAINS THE PRINTB AND PRINTX
: CALLS THAT ARE USED IN MORE THAN ONE TEST.
: ASCII TEXT STRINGS ARE FOUND IN THE GLOBAL TEXT SECTION.
:--
    
```

```

NXRERR: BGNMSG NXRERR ;NON-EXISTANT DEVICE REGISTER.
        PRINTX #NXRX,NODEV ;NODEV = NEXM ADDRESS.
        MOV NODEV,-(SP)
        MOV #NXRX,-(SP)
        MOV #2,-(SP)
        MOV SP,R0
        TRAP C$PNTX
        ADD #6,SP
        JSR PC,EXTEND ; PRINT EXTENSION IF REQUIRED.
        ENDMSG
L10002: TRAP C$MSG
    
```

```

:
: THIS ROUTINE APPENDS A UNIQUE EXTENSION (IF REQUIRED)
: TO ANY OF THE ABOVE ERROR SIGNATURES.
:
    
```

```

EXTEND: TST (PC)+
EXTA: 0 ; 0 = NO EXTENSION.
      BEQ 1$
      JSR PC,@EXTA ; APPEND EXTENSION TEXT.
1$: PRINTX #NULCR ; PRINT A BLANK LINE
      MOV #NULCR,-(SP)
      MOV #1,-(SP)
      MOV SP,R0
      TRAP C$PNTX
      ADD #4,SP
      RTS PC
    
```



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719 006020
720 006020
721 006024 010104
722 006026
    006026 010446
    006030 012746 006411
    006034 012746 000002
    006040 010600
    006042 104414
    006044 062706 000006
723 006050 010400
724 006052 004737 016034
725 006056 103410
726 006060
    006060 012746 006631
    006064 012746 000001
    006070 010600
    006072 104415
    006074 062706 000004
727 006100 010403
728 006102 042703 001476
729 006106 001434
730 006110 012702 002630
731 006114 012701 003476
732 006120 005703
733 006122 001413
734 006124 000241
735 006126 006103
736 006130 103006
737 006132 011100
738 006134 112022
739 006136 001376
740 006140 112762 000054 177777
741 006146 005721
742 006150 000763
743 006152 105042
744 006154
    006154 012746 002630
    006160 012746 006602
  
```

.SBTTL PRITSSR - PRINT TSSR CONTENTS

```

:ROUTINE TO DISPLAY THE CONTENTS, AND BIT DEFINITIONS, OF
:THE TSSR REGISTER. THIS ROUTINE IS NORMALLY CALLED ONLY
:BY A MESSAGE PRINTING ROUTINE
  
```

:INPUTS:

R1 CONTENTS OF TSSR

:SUBORDINATE ROUTINES:

CHKAMB CHECK FOR AMBIGUOUS CONTENTS

PRITSSR:

```

SAVREG          :SAVE GENERAL REGISTERS
MOV R1,R4       :SAVE THE TSSR CONTENTS
PRINTB #TSSRFOR,R4 :PRINT THE CONTENTS OF TSSR
MOV R4,-(SP)
MOV #TSSRFOR,-(SP)
MOV #2,-(SP)
MOV SP,R0
TRAP C$PNTB
ADD #6,SP
MOV R4,R0       :GET TSSR BACK FOR CHKAMB
JSR PC,CHKAMB  :ARE CONTENTS AMBIGUOUS ?
BCS 5$         :BRANCH IF NOT
PRINTX #AMBTSSR :SHOW CONTENTS ARE AMBIGUOUS
MOV #AMBTSSR,-(SP)
MOV #1,-(SP)
MOV SP,R0
TRAP C$PNTX
ADD #4,SP
5$: MOV R4,R3      :CONTENTS OF TSSR
    BIC #HIADDR!FATERR!TERCLS,R3 :CLEAR ALL MULTIPLE BIT FIELDS
    BEQ 20$       :NO BITS ARE SET
    MOV #TMPBFR,R2 :TEMPORARY ASCII BUFFER
    MOV #TSSRBIT,R1 :ASCII EQUIVALENT OF BITS
10$: TST R3       :REMAINING BITS TO CONVERT
    BEQ 15$      :BRANCH WHEN ALL ARE DONE
    CLC         :CLEAR CARRY FOR SHIFT
    ROL R3      :SHIFT NEXT BIT TO CARRY
    BCC 13$     :BRANCH IF BIT NOT SET
    MOV (R1),R0 :POINTER TO BIT DEFINITION
11$: MOVB (R0)+,(R2)+ :MOVE ASCII TO BUFFER
    BNE 11$     :MOVE ALL BITS
    MOVB #' , -1(R2) :INSERT A COMMA TO TERMINATE
13$: TST (R1)+  :POINT TO NEXT DESCRIPTION
    BR 10$     :GET THE REMAINING BITS
15$: CLRB -(R2) :TERMINATE THE LINE
    PRINTX #TSSDEF,#TMPBFR :PRINT THE BIT DEFINITIONS
    MOV #TMPBFR,-(SP)
    MOV #TSSDEF,-(SP)
  
```

```

006164 012746 000002      MOV      #2,-(SP)
006170 010600      MOV      SP,R0
006172 104415      TRAP    C$PNTX
006174 062706 000006      ADD      #6,SP
745
746 006200 010403      20$:    MOV      R4,R3          ;GET THE TSSR CONTENTS
747 006202 042703 177761      BIC      #^CTERCLS,R3   ;CLEAR ALL BUT TERMINATION
748 006206 016303 006672      MOV      TCOCOD(R3),R3  ;GET THE TERMINATION CODE MEANING
749 006212      PRINTX #TCOASC,R3      ;PRINT THE TERMINATION CODE
      006212 010346      MOV      R3,-(SP)
      006214 012746 006472      MOV      #TCOASC,-(SP)
      006220 012746 000002      MOV      #2,-(SP)
      006224 010600      MOV      SP,R0
      006226 104415      TRAP    C$PNTX
      006230 062706 000006      ADD      #6,SP
750 006234 010403      MOV      R4,R3          ;TSSR CONTENTS AGAIN
751 006236 042703 177717      BIC      #^CFATERR,R3   ;CLEAR ALL BUT FATAL TERMINATION
752 006242 001416      BEQ     25$             ;DON'T PRINT IF ZERO
753 006244 006203      ASR     R3
754 006246 006203      ASR     R3
755 006250 006203      ASR     R3
756 006252 016303 007232      MOV      TSFCOD(R3),R3  ;ALINE TERMINATION CODE FOR INDEX
757 006256      PRINTX #TFCASC,R3      ;GET THE FATAL TERMINATION CODE
      006256 010346      MOV      R3,-(SP)      ;PRINT THE FATAL TERMINATION CODE
      006260 012746 006533      MOV      #TFCASC,-(SP)
      006264 012746 000002      MOV      #2,-(SP)
      006270 010600      MOV      SP,R0
      006272 104415      TRAP    C$PNTX
      006274 062706 000006      ADD      #6,SP
758 006300 042704 176377      25$:    BIC      #^CHIADDR,R4   ;CLEAR ALL BUT EXTENDED ADDRESS
759 006304 001411      BEQ     30$             ;DON'T PRINT IF ZERO
760 006306      PRINTX #TEXASC,R4      ;PRINT THE EXTENDED ADDRESS BITS
      006306 010446      MOV      R4,-(SP)
      006310 012746 006431      MOV      #TEXASC,-(SP)
      006314 012746 000002      MOV      #2,-(SP)
      006320 010600      MOV      SP,R0
      006322 104415      TRAP    C$PNTX
      006324 062706 000006      ADD      #6,SP
761 006330 013703 002176      30$:    MOV      EPRTSW,R3      ;PRINT MEASGE BUFFER ADDRESS
762 006334      PRINTX R3              ;PRINT PROPER MESSAGE
      006334 010346      MOV      R3,-(SP)
      006336 012746 000001      MOV      #1,-(SP)
      006342 010600      MOV      SP,R0
      006344 104415      TRAP    C$PNTX
      006346 062706 000004      ADD      #4,SP
763 006352 000207      RTS      PC              ;RETURN TO CALLER
764
766 006354      EPRT2:
767 006354      045      116      045      EPRT1: .ASCIZ  '%NZA *****REPLACE M7196*****'
782 006411      045      116      045      TSSRFOR: .ASCIZ  '%NZA TSSR = %06'
783 006431      045      116      045      TEXASC: .ASCIZ  '%NZA Extended Address Bits = %06'
784 006472      045      116      045      TCOASC: .ASCIZ  '%NZA Termination Class Code = %T'
785 006533      045      116      045      TFCASC: .ASCIZ  '%NZA Fatal Termination Class Code = %T'
786 006602      045      116      045      TSSDEF: .ASCIZ  '%NZA TSSR Bits Set: %T'
787 006631      045      116      045      AMBTSSR: .ASCIZ  '%NZA TSSR Contents Are Ambiguous'
788
789 006672 006712 006735 006763 TCOCOD: .EVEN .WORD  1$,2$,3$,4$,5$,6$,7$,8$
    
```



790	006712	116	157	162	1\$:	.ASCIZ	'Normal Termination'
791	006735	124	145	162	2\$:	.ASCIZ	'Termination Condition'
792	006763	124	141	160	3\$:	.ASCIZ	'Tape Status Alert'
793	007005	106	165	156	4\$:	.ASCIZ	'Function Reject'
794	007025	122	145	143	5\$:	.ASCIZ	'Recoverable Error - Tape Position One Record Down'
795	007107	122	145	143	6\$:	.ASCIZ	'Recoverable Error - Tape Was Not Moved'
796	007156	125	156	162	7\$:	.ASCIZ	'Unrecoverable Error'
797	007202	106	141	164	8\$:	.ASCIZ	'Fatal Controller Error'
798						.EVEN	
799							
800	007232	007242	007276	007307	TSFCOD:	.WORD	1\$,2\$,3\$,4\$
801	007242	111	156	164	1\$:	.ASCIZ	'Internal Diagnostic Failure'
802	007276	122	145	163	2\$:	.ASCIZ	'Reserved'
803	007307	102	165	163	3\$:	.ASCIZ	'Bus Interface or Sanity Check Error'
804	007353	122	145	163	4\$:	.ASCIZ	'Reserved'
805						.EVEN	

807 .SBTTL PRIPKT - PRINT THE ADDRESS/CONTENTS OF COMMAND PACKET

808  
 809  
 810 :+ THIS ROUTINE PRINTS THE ADDRESS AND CONTENTS OF A COMMAND PACKET.  
 811 : THIS ROUTINE IS NORMALLY ONLY CALLED FROM A PRINT ROUTINE.  
 812

813 : INPUT:  
 814 :  
 815 : R0 NUMBER OF WORDS IN PACKET  
 816 : R3 HIGH ORDER COMMAND PACKET ADDRESS  
 817 : R4 ADDRESS OF COMMAND PACKET  
 818 :  
 819 : NOTE: R3 IS IGNORED IF THE KTENABLE FLAG IS CLEAR.  
 820 : -

821  
 822 PRIPKT::  
 823 SAVREG ;SAVE THE REGISTERS  
 824 MOV R0,R5 ;SAVE NO. OF WORDS IN PACKET  
 825 TST KTENABLE ;ABOVE 28K UNDER TEST?  
 826 BNE 10\$ ;BR IF YES  
 827 CLR R3 ;SET HIGH ORDER ADDRESS TO 0  
 828 10\$: MOV R3,R1 ;COPY HIGH ORDER ADDRESS  
 829 MOV R4,R0 ;GET LOWER ADDRESS  
 830 ROL R0 ;SHIFT BIT 15 INTO C BIT  
 831 ROL R1 ;AND INTO HIGH ORDER.  
 832 PRINTB #PKTADD,R1,R4 ;PRINT PACKET ADDRESS  
 MOV R4,-(SP)  
 MOV R1,-(SP)  
 MOV #PKTADD,-(SP)  
 MOV #3,-(SP)  
 MOV SP,R0  
 TRAP C\$PNTB  
 ADD #10,SP  
 15\$: MOV R3,R0 ;GET HIGH ORDER ADDRESS  
 BEQ 20\$ ;BR IF NOT ABOVE 28K.  
 MOV R4,R1 ;GET LOW ORDER ADDRESS  
 JSR PC,SETMAP ;SETUP PAR6 MAPPING FOR 18 BIT ADDRESS  
 MOV R0,R4 ;GET RETURNED PAR6 ADDRESS BIAS  
 20\$: CLR R1 ;SAVE WORD NUMBER  
 25\$: MOV (R4)+,R2 ;GET PACKET CONTENTS  
 PRINTB #PKTFRM,R1,R2 ;PRINT THE DATA  
 MOV R2,-(SP)  
 MOV R1,-(SP)  
 MOV #PKTFRM,-(SP)  
 MOV #3,-(SP)  
 MOV SP,R0  
 TRAP C\$PNTB  
 ADD #10,SP  
 INC R1 ;NEXT WORD NUMBER  
 CMP R1,R5 ;DONE ALL PACKET WORDS?  
 BLT 25\$ ;LOOP TILL ALL DONE  
 RTS PC ;RETURN

846 007512 045 116 045 PKTFRM: .ASCIZ '%N% Packet Word #D1% = %06'  
 847 007550 045 116 045 PKTADD: .ASCIZ '%N% Packet Address = %01%05'  
 848 .EVEN  
 849



```

851                                     .SBTTL PRIBXOR - PRINT EXPD, RECV AND XOR BYTE
852
853
854                                     :+
855                                     :PRINT EXPECTED DATA, RECEIVED DATA, AND XOR OF THE DATA BYTE
856                                     :THIS ROUTINE IS NORMALLY CALLED ONLY FOR PRINT ROUTINES.
857
858                                     :INPUTS:
859
860                                     R1      RECEIVED DATA
861                                     R2      EXPECTED DATA
862
863                                     :OUTPUT:
864
865                                     R0      XOR OF EXPECTED/RECEIVED DATA
866
867                                     :-
868
869 007606 PRIBXOR::
870 007606 SAVREG                                ;SAVE THE REGISTERS
871 007612 010203 MOV R2,R3                    ;EXPECTED DATA
872 007614 XOR R1,R3                        ;FORM THE EXCLUSIVE OR
873 007624 012700 177400 MOV #^C<377>,R0 ;BYTE MASK
874 007630 040001 BIC R0,R1                    ;SAVE LOW BYTE RECV
875 007632 040002 BIC R0,R2                    ;SAVE LOW BYTE EXPD
876 007634 040003 BIC R0,R3                    ;SAVE LOW BYTE XOR
877 007636 PRINTB #XORBFOR,R2,R1,R3 ;PRINT THE MESSAGE
      007636 010346 MOV R3,-(SP)
      007640 010146 MOV R1,-(SP)
      007642 010246 MOV R2,-(SP)
      007644 012746 007670 MOV #XORBFOR,-(SP)
      007650 012746 000004 MOV #4,-(SP)
      007654 010600 MOV SP,R0
      007656 104414 TRAP C$PNTB
878 007664 010300 000012 ADD #12,SP
879 007666 000207 MOV R3,R0                    ;R0 HAS XOR ON RETURN
880                                     ;RETURN TO CALLER
881 007670 045 116 045 XORBFOR: .ASCIZ '%N% EXPD: %03% RECV: %03% XOR: %03%'
882                                     .EVEN
883

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.SBTTL PRI XOR - PRINT EXPD, RECV AND XOR

```

: +
: PRINT EXPECTED DATA, RECEIVED DATA, AND XOR OF THE TWO
: THIS ROUTINE IS NORMALLY CALLED ONLY FOR PRINT ROUTINES.

```

: INPUTS:

```

R1 RECEIVED DATA
R2 EXPECTED DATA

```

: OUTPUT:

```

R0 XOR OF EXPECTED/RECEIVED DATA

```

: -

PRI XOR::

```

SAVREG ;SAVE THE REGISTERS
MOV R2,R3 ;EXPECTED DATA
XOR R1,R3 ;FORM THE EXCLUSIVE OR
PRINTB #XORFOR,R2,R1,R3 ;PRINT THE MESSAGE
MOV R3,-(SP)
MOV R1,-(SP)
MOV R2,-(SP)
MOV #XORFOR,-(SP)
MOV #4,-(SP)
MOV SP,R0
TRAP C$PNTB
ADD #12,SP
MOV R3,R0 ;R0 HAS XOR ON RETURN
RTS PC ;RETURN TO CALLER

```

010203

010346

010146

010246

012746 010006

012746 000004

010600

104414

062706 000012

010300

000207

045

116

045

```

XORFOR: .ASCIZ '%NZA EXPD: %06XA RECV: %06XA XOR: %06'
.EVEN

```



```

914 .SBTTL PRIEQU - PRINT BIT NUMBERS AS ASCII EQUIVALENT
915
916 :+
917 :ROUTINE TO CONVERT BIT VALUES TO ASCII AND PRINT THE STRING
918 :THIS ROUTINE IS NORMALLY CALLED FROM A PRINT ROUTINE
919
920 :INPUTS:
921
922 R0 OCTAL VALUE TO CONVERT
923 R1 TABLE OF POINTERS TO ASCII EQUIVALENT
924
925
926
927
928 010054 PRIEQU: SAVREG ;SAVE THE REGISTERS
929 010054 RTS PC ;RETURN TO CALLER
930 010060 000207
931
932
933
934
935 .SBTTL PRIRAM - PRINT RAM ADDRESS
936
937 :+
938 :PRINT CONTROLLER RAM ADDRESS.
939 :THIS ROUTINE IS NORMALLY CALLED ONLY FROM PRINT ROUTINES.
940
941 :INPUTS:
942
943 R4 RAM ADDRESS
944
945
946 010062 PRIRAM: SAVREG ;SAVE R1-R5 UNTIL NEXT RETURN
947 010062 PRINTB #RAMFOR,R4 ;PRINT RAM ADDRESS IN ERROR
948 010066 010446 MOV R4,-(SP)
010070 012746 010112 MOV #RAMFOR,-(SP)
010074 012746 000002 MOV #2,-(SP)
010100 010600 MOV SP,R0
010102 104414 TRAP C$PNTB
010104 062706 000006 ADD #6,SP
949 010110 000207 RTS PC ;RETURN
950
951 010112 045 116 045 RAMFOR: .ASCIZ '%NZA CONTROLLER RAM ADDRESS = %06'
952 .EVEN
953
954
955 .SBTTL PRIADD - PRINT MEMORY ERROR ADDRESS
956
957 :+
958 :PRINT MEMORY ADDRESS
959 :THIS ROUTINE IS NORMALLY CALLED ONLY FROM PRINT ROUTINES.
960
961 :IMPLICIT INPUTS
962
963 ERRHI - HIGH ORDER ADDRESS
964 ERRLO - LOW ORDER ADDRESS
    
```

```

965
966
967 010154
968 010154
969 010160 013700 002234
970 010164 013701 002236
971 010170 010102
972 010172 006101
973 010174 006100
974 010176
    010176 010246
    010200 010046
    010202 012746 010224
    010206 012746 000003
    010212 010600
    010214 104414
    010216 062706 000010
975 010222 000207
976
977 010224 045 116 045 PRIA0: .ASCIZ '%NZA MEMORY ERROR ADDRESS = %01%05'
978 .EVEN
979
980
981 .SBTTL PRITADD - PRINT MEMORY TEST ADDRESS
982
983
984 :PRINT MEMORY ADDRESS
985 :THIS ROUTINE IS NORMALLY CALLED ONLY FROM PRINT ROUTINES.
986
987 :IMPLICIT INPUTS
988
989 ERRHI - HIGH ORDER ADDRESS
990 ERRLO - LOW ORDER ADDRESS
991
992
993
994
995 010270
996 010270
997 010274 013702 002234
998 010300 013701 002236
999
1000 010304
    010304 010146
    010306 012746 010352
    010312 012746 000002
    010316 010600
    010320 104414
    010322 062706 000006
1001 010326
    010326 010246
    010330 012746 010415
    010334 012746 000002
    010340 010600
    010342 104414
    010344 062706 000006
1002 010350 000207
    
```

```

:
:
:PRIADD:
    SAVREG                :SAVE R1-R5 UNTIL NEXT RETURN
    MOV ERRHI,R0           :GET HIGH ADDRESS
    MOV ERRLO,R1          :GET LOW ADDRESS
    MOV R1,R2              :COPY LOW ADDRESS
    ROL R1                 :SHIFT BIT 15 TO C BIT
    ROL R0                 :SHIFT INTO HIGH ORDER
    PRINTB #PRIA0,R0,R2   :PRINT MEMORY ADDRESS IN ERROR
    MOV R2,-(SP)
    MOV R0,-(SP)
    MOV #PRIA0,-(SP)
    MOV #3,-(SP)
    MOV SP,R0
    TRAP C$PNTB
    ADD #10,SP
    RTS PC                 :RETURN

:
:
:PRIA0: .ASCIZ '%NZA MEMORY ERROR ADDRESS = %01%05'
: .EVEN

:
:
:SBTTL PRITADD - PRINT MEMORY TEST ADDRESS
:
:PRINT MEMORY ADDRESS
:THIS ROUTINE IS NORMALLY CALLED ONLY FROM PRINT ROUTINES.
:
:IMPLICIT INPUTS
:
:ERRHI - HIGH ORDER ADDRESS
:ERRLO - LOW ORDER ADDRESS
:
:
:PRITADD:
    SAVREG                :SAVE R1-R5 UNTIL NEXT RETURN
    MOV ERRHI,R2           :GET HIGH ADDRESS
    MOV ERRLO,R1          :GET LOW ADDRESS
    :MOV R1,R2              :COPY LOW ADDRESS
    :ROL R1                 :SHIFT BIT 15 TO C BIT
    :ROL R0                 :SHIFT INTO HIGH ORDER
    PRINTB #PRIT0,R1     :PRINT MEMORY ADDRESS LOW IN ERROR
    MOV R1,-(SP)
    MOV #PRIT0,-(SP)
    MOV #2,-(SP)
    MOV SP,R0
    TRAP C$PNTB
    ADD #6,SP
    PRINTB #PRIT1,R2     :PRINT MEMORY ADDRESS HIGH IN ERROR
    MOV R2,-(SP)
    MOV #PRIT1,-(SP)
    MOV #2,-(SP)
    MOV SP,R0
    TRAP C$PNTB
    ADD #6,SP
    RTS PC                 :RETURN
    
```





1011  
1012  
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010462  
010462  
010466  
010474  
010502  
010504  
010506  
010512  
010514  
010520  
010524  
010532  
010536  
010542  
010546  
010550  
010550  
010554  
010556  
010562  
010564  
010570

.SBTTL SPACE - SPACE RECORDS (FORWARD AND REVERSE) COMMAND

```

:ROUTINE TO ISSUE A SPACE RECORDS
:COMMAND (FORWARD OR REVERSE)
:INPUT:
    R3      NUMBER OF RECORDS TO BE SPACED OVER
           BIT15 CONTROLS DIRECTION
           BIT15 = 0 IS FORWARD
           BIT15 = 1 IS REVERSE
    R5      FIRST DEVICE UNIBUS ADDRESS
           REQUIRES A WRITE CHARACTERISTICS DONE PREVIOUSLY
:OUTPUT:
    CARRY   SET - SPACE RECORDS COMMAND OK
           CLR - SPACE RECORDS FAILED
    R0      THE CONTENTS OF R4 IS MOVED TO R0
:IMPLICIT OUTPUT:
    TAPE HAS BEEN MOVED
:SIDE EFFECTS:
    -
    
```

```

SPACE::
    SAVREG
    MOV     #500,SDELAY           ;SAVE THE GENERAL REGISTERS
    MOV     #140010,80$          ;SET UP DELAY
    TST     R3                   ;SET UP COMMAND, SPACE FORWARD
    BMI     5$                   ;CHECK FOR DIRECTION
    MOV     R3,90$               ;BR, IF REVERSE INDICATED
    BR      10$                  ;LOAD UP NUMBER OF RECORDS TO SPACE
    MOV     R3,90$               ;GO DO COMMAND
    BR      10$                  ;CLEAR DIRECTION BIT
    MOV     R3,90$               ;LOAD UP NUMBER OF RECORDS TO SPACE
    BIS     #BIT8,80$           ;SET REVERSE BIT IN COMMAND PACKET
    MOV     #80$,R4              ;SET UP R4 WITH PACKET ADDRESS
    MOV     R4,TSDB(R5)         ;SEND OUT COMMAND
    JSR     PC,WAITF            ;WAIT FOR SSR
    BCS     20$                  ;BR, IF SSR IS SET AND OK
    DELAY   250                  ;DELAY ABOUT .25 SECONDS
    MOV     #250,(PC)+
    .WORD   0
    MOV     LSDLY,(PC)+
    .WORD   0
    DEC     -6(PC)
    BNE     -4
    
```

```

010462 000764 010650
010462 140010 010640
005703
100403
010337 010642
000407
042703 100000
010337 010642
052737 000400 010640
012704 010640
010465 000000
004737 016240
103420
012727 000250
000000
013727 002116
000000
005367 177772
001375
    
```



	010572	005367	177756		DEC	-22(PC)	
	010576	001367			BNE	.-20	
1062	010600	005337	010650		DEC	SDELAY	:BUMP DELAY COUNTER DOWN
1063	010604	001356			BNE	15\$	:BR, IF MORE DELAY
1064	010606	000411			BR	60\$	:BR IF TROUBLE CARRY = CLEAR
1065	010610	016501	000002	20\$:	MOV	TSSR(R5),R1	:READ TSSR
1066	010614	012702	000200		MOV	#SSR,R2	:SET UP EXPECTED
1067	010620	020201		25\$:	CMP	R2,R1	:ARE THEY OK
1068	010622	001401			BEQ	40\$	:BR, IF EQUAL = OK
1069	010624	000402			BR	60\$	:TROUBLE EXIT
1070	010626	000261		40\$:	SEC		:SET CARRY NO TROUBLE
1071	010630	000401			BR	70\$	:EXIT
1072	010632	000241		60\$:	CLC		:CARRY CLEAR = ERROR
1073	010634			70\$:			
1074	010634	010400			MOV	R4,R0	:PASS PACKET ADDRESS
1075	010636	000207			RTS	PC	:RETURN

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1079  
1080  
1081  
1085  
1086  
1087 010640 000000  
1088  
1089 010642 000000  
1090 010644 000000  
1091 010646 000000  
1092 010650 000000  
1093

.....  
:PACKET FOR SPACE COMMAND  
.....  
:COMMAND WORD  
80\$: .WORD  
:NUMBER OF RECORDS TO BE SPACED OVER WORD  
90\$: .WORD  
      .WORD  
      .WORD  
SDELAY: .WORD 0                   :DELAY COUNTER  
      .EVEN



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1120  
1121  
1122  
1123  
1124  
1125  
1126  
1127 010652  
1128 010652  
1129 010656 005037 002226  
1130 010662 005037 002224  
1131 010666 010465 000000  
1132 010672 004737 016326  
1133 010676 103401  
1134 010700 000435  
1135 010702 016501 000002  
1136 010706 012702 000200  
1137 010712 032701 000100  
1138 010716 001402  
1139 010720 052702 000100  
1140 010724 020201  
1141 010726 001401  
1142 010730 000421  
1143 010732 062704 000010  
1144 010736 011403  
1145 010740 032763 000200 000012  
1146 010746 001402  
1147 010750 005237 002224  
1148 010754  
1149 010754 032763 000100 000012  
1150 010762 001402  
1151 010764 005237 002226

.SBTTL WRTCHR - WRITE CHARACTERISTICS COMMAND

```

:~+
:ROUTINE TO ISSUE A WRITE CHARACTERISTICS
:COMMAND SO THAT OTHER COMMANDS WILL BE ACCEPTED
:INPUT:
:
:R4 ADDRESS OF PACKET FROM TEST
:R5 FIRST DEVICE UNIBUS ADDRESS
:REQUIRES A CALL TO SOFINIT BE DONE PREVIOUSLY
:OUTPUT:
:
:R0 TSSR CONTENTS
:CARRY SET - WRITE CHARACTERISTICS COMMAND OK
:CLR - WRITE CHARACTERISTICS FAILED
:IMPLICIT OUTPUT:
:
:MESSAGE BUFFER AND OTHER BUFFERS ALL SET UP
:SOFTWARE SWITCHES SET AS FOLLOWS:
:EXTFEA = EXTENDED FEATURES PRESENT
:BENBSW = BUFFER ENABLE SWITCH ON OR OFF
:SIDE EFFECTS:
:-
    
```

```

WRTCHR::
:SAVREG
:SAVE THE GENERAL REGISTERS
:CLR BENBSW ;CLEAR BUFFER ENABLE SWITCH
:CLR EXTFEA ;CLEAR EXTENDED FEATURES SW SWITCH
10$: MOV R4,TSDB(R5) ;SEND OUT COMMAND
:JSR PC,CHKTSSR ;WAIT FOR SSR
:BCS 20$ ;BR, IF SSR IS SET AND OK
:BR 60$ ;BR IF TROUBLE CARRY = CLEAR
20$: MOV TSSR(R5),R1 ;READ TSSR
:MOV #SSR,R2 ;SET UP EXPECTED
:BIT #OFL,R1 ;WAS OFF LINE SET IN TSSR
:BEQ 25$ ;BR, IF NO OFL SET
:BIT #OFL,R2 ;MAKE THEM LOOK ALIKE
25$: CMP R2,R1 ;ARE THEY OK
:BEQ 40$ ;BR, IF EQUAL = OK
:BR 60$ ;TROUBLE EXIT
40$: ADD #8,R4 ;POINT TO WRT CHARA DATA PACKET
:MOV (R4),R3 ;GET ADDRESS OF MESSAGE BUFFER
:BIT #X2.EXTF,XST2(R3) ;EXTENDED FEATURES BIT SET?
:BEQ 45$ ;BR IF NO
:INC EXTFEA ;SET EXTENDED FEATURES SW SWITCH
45$: BIT #X2.BUFE,XST2(R3) ;BUFFER ENABLE SWITCH SET
:BEQ 50$ ;BR, IF SWITCH NOT SET
:INC BENBSW ;SET SOFTWARE SWITCH FOR ENABLED
    
```

1152 010770  
1153 010770 000261  
1154 010772 000401  
1155 010774 000241  
1156 010776 016500 000002  
1157 011002 000207  
1158  
1159

50\$:

SEC  
BR  
CLC  
MOV  
RTS

70\$

60\$:

70\$:

TSSR(R5),R0  
PC

;SET CARRY NO TROUBLE  
;EXIT  
;CARRY CLEAR = ERROR  
;RETURN TSSR CONTENTS  
;RETURN



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.SBTTL REWIND - POSITION TAPE (REWIND) COMMAND

```

+
: THIS ROUTINE WILL REWIND THE SELECTED TAPE.
: CAUTION: THE ROUTINE DOES NOT WAIT FOR BOT
: TO ARRIVE. ALSO THE CALLER MUST CHECK FOR
: SSR TO SET IN THE TSSR
    
```

: CALLING SEQUENCE:

```

DO A SOFT INIT
DO A WRITE CHARACTERISTICS
JSR PC,REWIND
    
```

: INPUT:

R5 FIRST DEVICE UNIBUS ADDRESS

: OUTPUT

R0 THE CONTENTS OF R4 IS PASSED TO R0

REWIND::

```

SAVREG ;SAVE R1-R5 UNTIL NEXT RETURN
MOV #RWPACK,R4 ;GET PACKET ADDRESS
MOV R4,TSDB(R5) ;SEND PACKET ADDRESS TO EXECUTE
MOV #360,R3 ;ENOUGH TIME FOR 2400' REEL TO REWIND
10$: JSR PC,WAITF ;WAIT FOR SSR TO SET
BCS 20$ ;LEAVE WHEN SSR IS SET
DELAY 250 ;WAIT FOR .25 SECONDS
MOV #250,.(PC)+
.WORD 0
MOV LSDLY,.(PC)+
.WORD 0
DEC -6(PC)
BNE -4
DEC -22(PC)
BNE -20
DEC R3 ;BUMP COUNTER DOWN
BNE 10$ ;KEEP GOING
CLC ;CLEAR CARRY TO SET ERROR
20$: MOV R4,R0 ;PASS THE PACKET ADDRESS
RTS PC ;RETURN

RWPACK: .=<. +10>&177770
.WORD 102010 ;POSTION COMMAND (REWIND)
.WORD 0 ;NOT USED
    
```

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.SBTTL CKRAM - COMPARE RAM TO I/O PACKET

```

: +
: ROUTINE TO READ THE FIRST 8 BYTES FROM RAM
: MEMORY AND COMPARE THIS DATA TO A COMMAND PACKET.
: INPUT:
:       R4      ADDRESS OF THE COMMAND PACKET
:       R5      FIRST DEVICE UNIBUS ADDRESS
: OUTPUT:
:       CARRY   SET - RAM MATCHES PACKET
:              CLR - RAM DOES NOT MATCH PACKET
: IMPLICIT OUTPUT:
:       THE TABLE RAMDATA IS FILLED WITH THE
:       DATA HELD IN RAM.
:       RAMSIZ IS SET TO 8. FOR PRAMPKT ROUTINE
: SIDE EFFECTS:
:       THE SUBSYSTEM IS LEFT IN MAINTENANCE MODE
: -
  
```

```

CKRAM::
  SAVREG                                ;SAVE THE GENERAL REGISTERS
  MOV #RAMDATA,R1                       ;ADDRESS TO SAVE THE RAM DATA
  MOV #RMPKTBEG,R2                      ;BYTE ADDRESS OF FIRST RAM DATA
  CLR R3                                 ;CLEAR THE ERROR FLAG
  JSR PC,CHKTSSR                         ;WAIT FOR SSR
  MOVB #0,TSDB(R5)                      ;SET MAINTENANCE MODE
  JSR PC,CHKTSSR                         ;WAIT FOR SSR TO SET
  MOV R2,TSDB(R5)                       ;SELECT NEXT RAM ADDRESS
  JSR PC,CHKTSSR                         ;WAIT FOR SSR TO SET
  MOVB TSBA(R5),(R1)                    ;READ THE RAM DATA
  CMPB (R1)+,(R4)+                      ;COMPARE TO EXPECTED
  BEQ 20$                                ;BRANCH IF OK
  INC R3                                 ;SET ERROR FLAG
  INC R2                                 ;ADDRESS OF NEXT RAM LOCATION
  CMP R2,#RMPKTEND                      ;REACHED END YET ?
  BLE 10$                                ;BRANCH TILL ALL READ
  TST R3                                 ;WAS AN ERROR FOUND ?
  BEQ 30$                                ;BRANCH IF NOT
  CLC                                    ;CLEAR CARRY TO SHOW ERROR
  BR 50$                                 ;AND EXIT
  SEC                                    ;SHOW GOOD COMPARE
  MOV #8.,RAMSIZ                        ;SETUP RAMSIZ FOR PRAMPKT ROUTINE
  RTS PC                                 ;RETURN
  
```

```

011104
011104
011110 012701 002240
011114 012702 000201
011120 005003
011122 004737 016326
011126 112765 000000 000000
011134 004737 016326 10$:
011140 010265 000000
011144 004737 016326
011150 116511 000000
011154 122124
011156 001401
011160 005203
011162 005202 20$:
011164 020227 000210
011170 003761
011172 005703
011174 001402
011176 000241
011200 000401
011202 000261 30$:
011204 012737 000010 002300 50$:
011212 000207
  
```

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.SBTTL CKRAM2 - COMPARE RAM TO I/O CHARACTERISTICS DATA

:+  
:ROUTINE TO READ THE FIRST 8 OR 10 BYTES FROM RAM  
:MEMORY AND COMPARE THIS DATA TO A CHARACTERISTICS DATA BLOCK.

:INPUT:

R4 ADDRESS OF THE CHARACTERISTICS DATA  
R5 FIRST DEVICE UNIBUS ADDRESS

:OUTPUT:

CARRY SET - RAM MATCHES PACKET  
CLR - RAM DOES NOT MATCH PACKET

:IMPLICIT OUTPUT:

THE TABLE RAMDATA IS FILLED WITH THE  
DATA HELD IN RAM.  
RAMSIZ IS SET TO 8. OR 10. FOR PRAMPKT ROUTINE

:SIDE EFFECTS:

THE SUBSYSTEM IS LEFT IN MAINTENANCE MODE

CKRAM2::

```

SAVREG          ;SAVE THE GENERAL REGISTERS
MOV #RAMDATA,R1 ;ADDRESS TO SAVE THE RAM DATA
MOV #RMCHBEG,R2 ;BYTE ADDRESS OF FIRST RAM DATA
CLR R3          ;CLEAR THE ERROR FLAG
JSR PC,CHKTSSR  ;WAIT FOR SSR
MOVB #0,TSDB(R5);SET MAINTENANCE MODE
JSR PC,CHKTSSR  ;WAIT FOR SSR TO SET
MOV R2,TSDB(R5) ;SELECT NEXT RAM ADDRESS
JSR PC,CHKTSSR  ;WAIT FOR SSR TO SET
MOVB TSBA(R5),(R1);READ THE RAM DATA
CMPB (R1)+,(R4)+;COMPARE TO EXPECTED
BEQ 20$        ;BRANCH IF OK
INC R3         ;SET ERROR FLAG
INC R2         ;ADDRESS OF NEXT RAM LOCATION
MOV #8.,RAMSIZ ;ASSUME EXTFEA NOT SET
TST EXTFEA     ;IS THE SOFTWARE EXTENDED FEATURES SET
BEQ 25$        ;BR, IF NOT SET
MOV #10.,RAMSIZ;SET RAMSIZ FOR EXTEND FEATURES
CMP R2,#RMCHEND;AT END OF EXTENDED BUFFER
BLE 10$        ;BR, IF NOT AT END YET
BR 27$         ;AT END BRANCH
CMP R2,#RMCHEND-2;REACHED END YET ?
BLE 10$        ;BRANCH TILL ALL READ
TST R3         ;WAS AN ERROR FOUND ?
BEQ 30$        ;BRANCH IF NOT
CLC           ;CLEAR CARRY TO SHOW ERROR
BR 50$         ;AND EXIT
SEC           ;SHOW GOOD COMPARE
    
```

```

011214
011214
011220 012701 002240
011224 012702 000167
011230 005003
011232 004737 016326
011236 112765 000000 000000
011244 004737 016326 10$:
011250 010265 000000
011254 004737 016326
011260 116511 000000
011264 122124
011266 001401
011270 005203
011272 005202
011274 012737 000010 002300
011302 005737 002224
011306 001407
011310 012737 000012 002300
011316 020227 000200
011322 003750
011324 000403
011326 020227 000176 25$:
011332 003744
011334 005703 27$:
011336 001402
011340 000241
011342 000401
011344 000261 30$:
    
```



TSV3 - GLOBAL AREAS    MACRO M1113 25-MAY-82 08:41    PAGE 32-1  
CKRAM2 - COMPARE RAM TO I/O CHARACTERISTICS DATA

I 5

SEQ 0060

1328 011346 000207  
1329

50\$:    RTS    PC

:RETURN

```

1331 .SBTTL CKMSG - COMPARE WRITE CHAR. MESSAGE BUFFERS
1332
1333
1334 :ROUTINE TO COMPARE A WRITE CHARACTERISTICS EXPD AND RECV
1335 :BUFFER. THE EXPECTED AND RECEIVED BUFFERS ARE STORED FOR
1336 :ERROR PRINT ROUTINES.
1337
1338 :INPUT:
1339
1340 R0 RECV MESSAGE BUFFER HIGH ORDER ADDRESS
1341 R1 RECV MESSAGE BUFFER LOW ORDER ADDRESS
1342 R2 EXPD MESSAGE BUFFER ADDRESS
1343
1344 :OUTPUT:
1345
1346 CARRY SET - MESSAGE BUFFERS MATCH
1347 CLR -MESSAGE BUFFERS DON'T MATCH
1348
1349 :IMPLICIT OUTPUT:
1350
1351 EXPMSG BUFFER IS SET TO EXPD DATA
1352 RECMMSG BUFFER IS SET TO RECV DATA
1353 RCVHIADD SET TO HIGH ORDER ADDRESS OF RECV
1354 RCVLOADD SET TO LOW ORDER ADDRESS OF RECV
1355
1356 011350 CKMSG::
1357 011350 SAVREG ;SAVE R1-R5 UNTIL NEXT RETURN
1358 011354 010037 002302 MOV R0,RCVHIADD ;SAVE RECV HIGH ADDRESS
1359 011360 010137 002304 MOV R1,RCVLOAD ;SAVE RECV LOW ADDRESS
1360 011364 005737 003130 TST KTENABLE ;TESTING ABOVE 28K?
1361 011370 001403 BEQ 10$ ;BR IF NO
1362 011372 004737 017306 JSR PC,SETMAP ;RETURN ADDRESS BIASED TO PAR6 IN R0
1363 011376 010001 MOV R0,R1 ;GET RETURNED ADDRESS BIASED TO PAR6
1364 011400 005004 10$: CLR R4 ;WORD IN BUFFER
1365 011402 005003 CLR R3 ;CLEAR ERROR SEEN FLAG
1366 011404 010205 MOV R2,R5 ;GET EXPD BUFFER ADDRESS
1367 011406 011264 002320 15$: MOV (R2),EXPMSG(R4) ;SAVE EXPD FOR ERROR REPORT
1368 011412 011164 002464 MOV (R1),RECMMSG(R4) ;SAVE RECV FOR ERROR REPORT
1369 011416 022221 CMP (R2)+,(R1)+ ;EXPD EQUAL RECV?
1370 011420 001401 BEQ 25$ ;BR IF YES
1371 011422 005203 INC R3 ;SET ERROR SEEN FLAG
1372 011424 062704 000002 25$: ADD #2,R4 ;POINT TO NEXT WORD ADDRESS
1373 011430 020427 000014 CMP R4,#14 ;DONE FIRST 7 WORDS?
1374 011434 003764 BLE 15$ ;BR IF NO
1375 011436 032765 000200 000012 BIT #X2.EXTF,XST2(R5) ;IS EXTENDED FEATURES SET IN EXPD?
1376 011444 001403 BEQ 50$ ;BR IF NO
1377 011446 020427 000016 CMP R4,#16 ;DONE EXTENDED FEATURES WORD?
1378 011452 003755 BLE 15$ ;BR IF NO
1379 011454 005703 50$: TST R3 ;ANY ERRORS SEEN?
1380 011456 001402 BEQ 55$ ;BR IF NO
1381 011460 000241 CLC ;SET FAILURE
1382 011462 000401 BR 60$
1383 011464 000261 55$: SEC ;SET SUCCESS
1384 011466 000207 60$: RTS PC ;RETURN
1385

```



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1414 011470  
1415 011470  
1416 011474 020327 000144  
1417 011500 003412  
1418 011502 012703 000144  
1419 011506  
011506 012746 011622  
011512 012746 000001  
011516 010600  
011520 104417  
011522 062706 000004  
1420 011526 010037 002302  
1421 011532 010137 002304  
1422 011536 005737 003130  
1423 011542 001403  
1424 011544 004737 017306  
1425 011550 010001  
1426 011552 005004  
1427 011554 005005  
1428 011556 111264 002320  
1429 011562 111164 002464  
1430 011566 122221  
1431 011570 001401  
1432 011572 005205  
1433 011574 062704 000001  
1434 011600 020403  
1435 011602 002001  
1436 011604 000764  
1437 011606 005705  
1438 011610 001402

```

.SBTTL CKMSG2 - COMPARE EXPD RECV MESSAGE BUFFERS
:
:ROUTINE TO COMPARE AN EXPECTED AND RECEIVED MESSAGE
:BUFFER. THE EXPECTED AND RECEIVED BUFFERS ARE STORED FOR
:ERROR PRINT ROUTINES.
:
:INPUT:
:
:      R0      RECV MESSAGE BUFFER HIGH ORDER ADDRESS
:      R1      RECV MESSAGE BUFFER LOW ORDER ADDRESS
:      R2      EXPD MESSAGE BUFFER ADDRESS
:      R3      NUMBER OF BYTES TO COMPARE
:
:OUTPUT:
:
:      CARRY   SET - MESSAGE BUFFERS MATCH
:             CLR - MESSAGE BUFFERS DON'T MATCH
:
:IMPLICIT OUTPUT:
:
:      EXPMSG  BUFFER IS SET TO EXPD DATA
:      RECMSG  BUFFER IS SET TO RECV DATA
:      RCVHIADD SET TO HIGH ORDER ADDRESS OF RECV
:      RCVLOADD SET TO LOW ORDER ADDRESS OF RECV
:
-
CKMSG2::
  SAVREG          ;SAVE R1-R5 UNTIL NEXT RETURN
  CMP             R3,#RECMSG-EXPMSG;@ad IS COUNT ABOVE MAX ALLOWED?
  BLE            5$ ;@ad BR IF NO
  MOV            #RECMSG-EXPMSG,R3;@ad
  PRINTF         #DEBUGMSG ;@ad
  MOV            #DEBUGMSG,-(SP)
  MOV            #1,-(SP)
  MOV            SP,R0
  TRAP           C$PNTF
  ADD            #4,SP
5$: MOV            R0,RCVHIADD ;SAVE RECV HIGH ADDRESS
  MOV            R1,RCVLOAD ;SAVE RECV LOW ADDRESS
  TST            KTENABLE ;TESTING ABOVE 28K?
  BEQ            10$ ;BR IF NO
  JSR            PC,SETMAP ;RETURN ADDRESS BIASED TO PAR6 IN R0
  MOV            R0,R1 ;GET RETURNED ADDRESS BIASED TO PAR6
10$: CLR          R4 ;WORD IN BUFFER
  CLR            R5 ;CLEAR ERROR SEEN FLAG
15$: MOVB         (R2),EXPMSG(R4) ;SAVE EXPD FOR ERROR REPORT
  MOVB          (R1),RECMSG(R4) ;SAVE RECV FOR ERROR REPORT
  CMPB          (R2)+,(R1)+ ;EXPD EQUAL RECV?
  BEQ            25$ ;BR IF YES
  INC            R5 ;SET ERROR SEEN FLAG
25$: ADD          #1,R4 ;POINT TO NEXT BYTE
  CMP            R4,R3 ;DONE ALL BYTES?
  BGE            50$ ;BR IF YES
  BR             15$ ;DO NEXT BYTE
50$: TST          R5 ;ANY ERRORS SEEN?
  BEQ            55$ ;BR IF NO
  
```

```
1439 011612 000241                    CLC                    ;SET FAILURE
1440 011614 000401                    BR                    60$                    ;
1441 011616 000261                    55$:                  SEC                    ;SET SUCCESS
1442 011620 000207                    60$:                  RTS                    ;RETURN
1443
1444 011622            120            122            117 DEBUGMSG:            .ASCIZ 'PROGRAM INTERNAL ERROR -CKMSG2 MESSAGE BUFFER EXCEEDED-';@@D
1445 011712            045            116            045 FERCM:            .ASCII /%N% ***/
1446 011723            040            040            124 ERCM:            .ASCIZ / TSSR ERROR CODE REC'D = /
1447 011756            056            056            056 SIMSG:            .ASCIZ /... AFTER DOING SOFT INIT/
1448 012011            124            105            123 TINERR:            .ASCIZ /TEST: .../
1449                                    .EVEN
```



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 1467 012024  
 012024  
 1468 012024 004737 006020  
 1469 012030 004737 017172  
 1470 012034  
 012034  
 012034 104423  
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 1481  
 1482  
 1483 012036  
 012036  
 1484 012036 004737 006020  
 1485 012042 012700 000004  
 1486 012046 004737 007364  
 1487 012052  
 012052  
 012052 104423  
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 1499  
 1500 012054  
 012054

```

: +
: PRINT ROUTINE TO FATAL SOFT INIT ERRORS
: INPUT:
:       R1      CONTENTS OF TSSR AT ERROR
: SIDE EFFECTS:
:       EXECUTES DROP UNIT TO CEASE TESTING
: -

BGNMSG SFIMSG
SFIMSG:: JSR    PC,PRITSSR    ;PRINT CONTENTS OF TSSR REGISTER
          JSR    PC,CKDROP   ;DROP UNIT, IF ALLOWED
          ENDMSG
L10003:  TRAP   C$MSG

: +
: PRINT ROUTINE TO PRINT THE CONTENTS OF
: TSSR AND A COMMAND PACKET OTHER THAN GET STATUS COMMAND PACKET.
: INPUTS:
:       R1      TSSR CONTENTS
:       R4      ADDRESS OF COMMAND PACKET
: -

BGNMSG PKTSSR
PKTSSR:: JSR    PC,PRITSSR    ;PRINT THE CONTENTS OF TSSR REGISTER
          MOV    #4,R0       ;NO. OF WORDS IN PACKET
          JSR    PC,PRIPKT   ;PRINT THE CONTENTS OF COMMAND PACKET
          ENDMSG
L10004:  TRAP   C$MSG

: +
: PRINT ROUTINE TO PRINT THE CONTENTS OF
: TSSR AND A GET STATUS COMMAND PACKET.
: INPUTS:
:       R1      TSSR CONTENTS
:       R4      ADDRESS OF COMMAND PACKET
: -

BGNMSG PKTGETS
PKTGETS::
  
```

```

1501 012054 004737 006020      JSR    PC,PRITSSR      ;PRINT THE CONTENTS OF TSSR REGISTER
1502 012060 012700 000002      MOV    #2,R0          ;NO. OF WORDS IN GET STATUS PACKET
1503 012064 004737 007364      JSR    PC,PRIPKT      ;PRINT THE CONTENTS OF COMMAND PACKET
1504 012070
      012070
      012070 104423      L10005:  TRAP    C$MSG
  
```

```

1505
1506
1507
1508      ;+
1509      ;PRINT TSSR ERRORS FOR INITIALIZATION TESTS
1510
1511      ;INPUTS:
1512
1513      R1      TSSR CONTENTS
1514      R4      ADDRESS OF COMMAND PACKET
1515      ;-
  
```

```

1516 012072
      012072
1517 012072 004737 006020      SFFMSG::  BGNMSG  SFFMSG
1518 012076
      012076 104423      L10006:  JSR    PC,PRITSSR      ;PRINT CONTENTS OF TSSR REGISTER
      012076
      012076
      TRAP    C$MSG
  
```

```

1519
1520
1521      .SBTTL  PKTMES - PRINT TSSR AND MESSAGE BUFFER
1522
1523      ;+
1524      ;PRINT ROUTINE TO PRINT THE CONTENTS OF TSSR AND MESSAGE
1525      ;BUFFER FOR ERROR REPORTS
1526
1527      ;INPUTS:
1528
1529      R1      CONTENTS OF TSSR
1530      R2      LOW ORDER MESSAGE BUFFER
1531      R3      HIGH ORDER MESSAGE BUFFER ADDRESS
1532      NOTE: R3 IS IGNORED IF KTENABLE FLAG IS CLEAR
1533      ;-
  
```

```

1534 012100
      012100
1535 012100 004737 006020      PKTMES::  BGNMSG  PKTMES
1536 012104 010200
1537 012106 010301
1538 012110 004737 014232      JSR    PC,PRITSSR      ;PRINT CONTENTS OF TSSR
1539 012114
      012114
      012114 104423      MOV    R2,R0          ;LOW ORDER ADDRESS
      MOV    R3,R1      ;HIGH ORDER ADDRESS
      JSR    PC,PRMESS   ;PRINT THE MESSAGE BUFFER
      ENDMSG
      L10007:  TRAP    C$MSG
  
```

1540



```

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1553
1554 012116
      012116
1555 012116 004737 010270
1556 012122 016501 000002
1557 012126 004737 006C20
1558 012132
      012132
      012132 104423
1559
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1572
1573 012134
      012134
1574 012134 012700 000007
1575 012140 005737 002224
1576 012144 001402
1577 012146 012700 000010
1578 012152 004737 014542
1579 012156
      012156
      012156 104423
1580
1581
  
```

```

      .SBTTL  ADDSSR - PRINT TEST ADDRESS AND TSSR
      :+
      :PRINT ROUTINE TO PRINT THE CONTENTS OF
      :TSSR AND A MEMORY TEST ADDRESS
      :INPUTS:
      :
      :       R5      FIRST DEVICE UNIBUS ADDRESS
      :       ERRHI   HIGH ORDER MEMORY TEST ADDRESS
      :       ERRLO   LOW ORDER MEMORY TEST ADDRESS
      :-
      BGNMSG  ADDSSR
ADDSSR::
      JSR    PC,PRITADD      :PRINT MEMORY TEST ADDRESS
      MOV    TSSR(R5),R1     :GET CURRENT TSSR
      JSR    PC,PRITSSR     :PRINT THE CONTENTS OF TSSR REGISTER
      ENDMSG
L10010:
      TRAP   C$MSG
  
```

```

      .SBTTL  MSGEXP - PRINT WRITE CHAR. EXPD-RCV MESSAGE BUFFERS
      :+
      :PRINT ROUTINE TO PRINT WRITE CHARACTERISTIC MESSAGE BUFFER
      :IMPLICIT INPUTS:
      :
      :       EXPMSG  - EXPECTED MESSAGE BUFFER
      :       RECMSG  - RECEIVED MESSAGE BUFFER
      :       RCVHIADD- RECEIVED MESSAGE BUFFER HIGH ORDER ADDRESS
      :       RCVLOADD- RECEIVED MESSAGE BUFFER LOW ORDER ADDRESS
      :-
      BGNMSG  MSGEXP
MSGEXP::
      MOV    #7,R0          :ASSUME NO EXT FEATURES
      TST    EXTFEA         :EXT FEATURES SET?
      BEQ    $$             :BR IF NO
      MOV    #8.,R0        :EXT FEATURE BUFFER IS 8 WORDS
      JSR    PC,PRMSGEXP   :PRINT EXPD/RCV MESSAGE BUFFERS
      ENDMSG
      $$:
L10011:
      TRAP   C$MSG
  
```

```

1583
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1589
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1591
1592
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1594
1595 012160
      012160
1596 012160
      012160 010146
      012162 012746 012232
      012166 012746 000002
      012172 010600
      012174 104415
      012176 062706 000006
1597 012202
      012202 012746 012301
      012206 012746 000001
      012212 010600
      012214 104415
      012216 062706 000004
1598 012222 010100
1599 012224 004737 015112
1600 012230
      012230
      012230 104423
1601 012232 045 116
1602 012301 045 116
1603
1604
  
```

```

.SBTTL FIFEXP - PRINT FIFO EXP/REC DATA
:
:PRINT ROUTINE TO PRINT FIFO EXP/REC DATA
      R1 - BYTE COUNT
:IMPLICIT INPUTS:
      EXPMSG - EXPECTED MESSAGE BUFFER (CONTAINS FIFO DATA ONLY)
      RECMSG - RECEIVED MESSAGE BUFFER (CONTAINS FIFO DATA ONLY)
:
      BGNMSG FIFEXP
FIFEXP::
PRINTX #FIF1MSG,R1 ;PRINT BYTES TRANSFERRED
MOV R1,-(SP)
MOV #FIF1MSG,-(SP)
MOV #2,-(SP)
MOV SP,R0
TRAP C$PNTX
ADD #6,SP
PRINTX #FIF2MSG ;PRINT HEADER MSG
MOV #FIF2MSG,-(SP)
MOV #1,-(SP)
MOV SP,R0
TRAP C$PNTX
ADD #4,SP
MOV R1,R0 ;GET BYTE COUNT
JSR PC,PRBYTEXP ;PRINT FIFO BYTES IN ERROR
ENDMSG
L10012:
TRAP C$MSG
      .ASCIZ '%N% NUMBER OF BYTES TRANSFERRED = %u2'
      .ASCIZ '%N% FIFO DATA BYTES IN ERROR:'
      .EVEN
  
```



1606  
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1619 012340  
012340  
1620 012340 012701 012402  
1621 012344 012100  
1622 012346 001410  
1623 012350  
012350 010046  
012352 012746 000001  
012356 010600  
012360 104415  
012362 062706 000004  
1624 012366 000766  
1625 012370 012700 000012  
1626 012374 004737 014542  
1627 012400  
012400  
012400 104423  
1628  
1629 012402 012420 012462 012553  
1630 012420 045 116 045  
1631 012462 045 116 045  
1632 012553 045 116 045  
1633 012644 045 116 045  
1634 012735 045 116 045  
1635 012777 045 116 045  
1636  
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1652 013054  
013054  
1653 013054 012701 013116

```
.SBTTL MSGSTAT - PRINT STATUS HEADER AND MESSAGE BUFFERS
:+
:PRINT ROUTINE TO PRINT MESSAGE BUFFER EXPD/RECV
:IMPLICIT INPUTS:
:
:EXPMSG - EXPECTED MESSAGE BUFFER
:RECMSG - RECEIVED MESSAGE BUFFER
:RCVHIADD- RECEIVED MESSAGE BUFFER HIGH ORDER ADDRESS
:RCVLOADD- RECEIVED MESSAGE BUFFER LOW ORDER ADDRESS
:-
BGNMSG MSGSTAT
MSGSTAT::
MOV #STATCOD,R1 ;ASCII ADDRESS TABLE
10$: MOV (R1)+,R0 ;DONE ALL MSG LINES?
BEQ 20$ ;BR IF YES
PRINTX R0 ;PRINT STATUS BIT NAMES
MOV R0,-(SP)
MOV #1,-(SP)
MOV SP,R0
TRAP C$PNTX
ADD #4,SP
BR 10$ ;DO ANOTHER MSG LINE
20$: MOV #10,R0 ;NUMBER OF WORDS IN A READ STATUS BUFFER
JSR PC,PRMSGEXP ;PRINT EXPD/RECV MESSAGE BUFFERS
ENDMSG
L10013: TRAP C$MSG
STATCOD: .WORD 1$,2$,3$,4$,5$,6$,0
1$: .ASCIIZ '%NZA Tape Bus Signals in Word #8:'
2$: .ASCIIZ '%NZA PARERR<15> IEOT <12> IFMK <9> IRDY<6> IRWD<2>'
3$: .ASCIIZ '%NZA IRESV2<14> IIDENT<11> IHER <8> IONL<5> IFBY<1>'
4$: .ASCIIZ '%NZA IRESV1<13> ICER <10> ISPEED<7> ILDP<4> IFPT<0>'
5$: .ASCIIZ '%NZA Tape Bus Signals in Word #9:'
6$: .ASCIIZ '%NZA DATMIS<7> ILW<6> OUTRDY<5> INRDY<4>'
.EVEN
```

```
.SBTTL MSGLOOP - PRINT LOOPBACK HEADER AND MESSAGE BUFFERS
:+
:PRINT ROUTINE TO PRINT MESSAGE BUFFER EXPD/RECV
:IMPLICIT INPUTS:
:
:EXPMSG - EXPECTED MESSAGE BUFFER
:RECMSG - RECEIVED MESSAGE BUFFER
:RCVHIADD- RECEIVED MESSAGE BUFFER HIGH ORDER ADDRESS
:RCVLOADD- RECEIVED MESSAGE BUFFER LOW ORDER ADDRESS
:-
BGNMSG MSGLOOP
MSGLOOP::
MOV #LOOPCOD,R1 ;ASCII ADDRESS TABLE
```

```

1654 013060 012100          10$:  MOV      (R1)+,R0      ;DONE ALL MSG LINES?
1655 013062 001410          BEQ      20$      ;BR IF YES
1656 013064          PRINTX  R0      ;PRINT STATUS BIT NAMES
      013064 010046          MOV      R0,-(SP)
      013066 012746 000001  MOV      #1,-(SP)
      013072 010600          MOV      SP,R0
      013074 104415          TRAP    C$PNTX
      013076 062706 000004  ADD      #4,SP
1657 013102 000766          BR      10$      ;DO ANOTHER MSG LINE
1658 013104 012700 000012  20$:  MOV      #10.,R0    ;NUMBER OF WORDS IN A READ STATUS BUFFER
1659 013110 004737 014542  JSR      PC,PRMSGEXP ;PRINT EXPD/RCV MESSAGE BUFFERS
1660 013114          ENDMMSG
      013114          L10014:
      013114 104423          TRAP    C$MSG
1661
1662 013116 013136 013211 013310 LOOPCOD: .WORD 1$,2$,3$,4$,5$,6$,7$,0
1663 013136          045 116 045 1$: .ASCIZ '%N% Tape Bus Loopback Signals in Word #8:'
1664 013211          045 116 045 2$: .ASCIZ '%N% PARERR<15> IRESV2<14>'
1665 013310          045 116 045 3$: .ASCIZ '%N% IHISP=>IEOT<12> IWRT=>IIDENT<11> IREV =>ICER <10>'
1666 013407          045 116 045 4$: .ASCIZ '%N% IWFM =>IFMK<09> IEDIT=>IHER <08> IFAD =>ISPEED<07>'
1667 013506          045 116 045 5$: .ASCIZ '%N% ITAD0=>IRDY<06> ITAD1=>IONL <05> IERASE=>ILDPA <04>'
1668 013605          045 116 045 6$: .ASCIZ '%N% IREW =>IDBY<03> IRWU =>IRWD <02> IFEN =>IFBY <01>'
1669 013704          045 116 045 7$: .ASCIZ '%N% IGO =>IFPT<00>'
1670
1671          .EVEN
  
```



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 1686 013732  
 013732  
 1687 013732 012700 000012  
 1688 013736 004737 014542  
 1689 013742  
 013742  
 013742 104423  
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 1707 013744  
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 1708 013744 004737 010154  
 1709 013750 013701 002230  
 1710 013754 013702 002232  
 1711 013760 004737 007736  
 1712 013764  
 013764  
 013764 104423  
 1713

```

.SBTTL MSGSUB - PRINT WRITE SUBSYSTEM MESSAGE BUFFER
:
:PRINT ROUTINE TO PRINT MESSAGE BUFFER EXPD/RCV
:
:IMPLICIT INPUTS:
:
:   EXPMSG - EXPECTED MESSAGE BUFFER
:   RECMSG - RECEIVED MESSAGE BUFFER
:   RCVHIADD- RECEIVED MESSAGE BUFFER HIGH ORDER ADDRESS
:   RCVLOADD- RECEIVED MESSAGE BUFFER LOW ORDER ADDRESS
:
:
:   BGNMSG  MSGSUB
MSGSUB::
:   MOV     #10,R0      ;SIZE OF WRITE SUBSYSTEM BUFFER
:   JSR     PC,PRMSGEXP ;PRINT EXPD/RCV MESSAGE BUFFERS
:   ENDMSG
L10015:
:   TRAP   C$MSG
  
```

```

.SBTTL MEMADD - PRINT MEMORY ADDRESS DATA ERROR
:
:PRINT ROUTINE TO PRINT MEMORY ADDRESS DATA COMPARE ERROR
:
:IMPLICIT INPUTS:
:
:   ERRHI - MEMORY ERROR HIGH ORDER ADDRESS
:   ERRLO - MEMORY ERROR LOW ORDER ADDRESS
:   EXP   - EXPECTED DATA
:   RECV  - RECEIVED DATA
:
:
:   BGNMSG  MEMADD
MEMADD::
:   JSR     PC,PRIADD  ;PRINT MEMORY ADDRESS IN ERROR
:   MOV     EXPD,R1    ;GET EXPD DATA
:   MOV     RECV,R2    ;GET RECEIVED DATA
:   JSR     PC,PRIXOR  ;PRINT EXPD/RCV
:   ENDMSG
L10016:
:   TRAP   C$MSG
  
```





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1765

.EVEN

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1784 014232
1785 014232
1786 014236 010005
1787 014240 005737 003130
1788 014244 001001
1789 014246 005001
1790 014250 010103
1791 014252 006100
1792 014254 006101
1793 014256
    014256 010546
    014260 010146
    014262 012746 014410
    014266 012746 000003
    014272 010600
    014274 104415
    014276 062706 000010
1794 014302
    014302 012746 014455
    014306 012746 000001
    014312 010600
    014314 104415
    014316 062706 000004
1795 014322 005004
1796 014324 010501
1797 014326 010300
1798 014330 001403
1799 014332 004737 017306
1800 014336 010005
1801 014340
    014340 012546
    014342 010446
    014344 012746 014513
    014350 012746 000003
    014354 010600
    014356 104415
    014360 062706 000010
1802 014364 005204
1803 014366 020427 000007
1804 014372 003005
    
```

```

.SBTTL PRMESS - PRINT CONTENTS OF MESSAGE BUFFER
:
: THIS ROUTINE PRINTS THE CONTENTS OF
: THE 7 OR 8 WORD MESSAGE BUFFER RETURNED BY THE
: TSV-05.
:
: INPUT:
:
: R0 LOW ORDER ADDRESS OF MESSAGE BUFFER
: R1 HIGH ORDER ADDRESS OF MESSAGE BUFFER
: NOTE: R1 IS IGNORED IF KENABLE FLAG IS CLEAR
:
: THIS ROUTINE IS NORMALLY CALLED FROM A PRINT ROUTINE
:
: -
PRMESS:
    SAVREG                :SAVE THE REGISTERS
    MOV R0,R5              :SAVE LOW ORDER ADDRESS
    TST KENABLE           :ADDRESS ABOVE 28K?
    BNE 10$               :BR IF YES
    CLR R1                :SET HIGH ORDER ADDRESS TO 0
    MOV R1,R3             :SAVE HIGH ORDER ADDRESS
    ROL R0                 :SHIFT BIT15 TO C BIT
    ROL R1                 :SHIFT TO HIGH ORDER FOR PRINTOUT
    PRINTX #PROASC,R1,R5  :PRINT MESSAGE BUFFER ADDRESS
    MOV R5,-(SP)
    MOV R1,-(SP)
    MOV #PROASC,-(SP)
    MOV #3,-(SP)
    MOV SP,R0
    TRAP C$PNTX
    ADD #10,SP
    PRINTX #PR1ASC        :PRINT HEADER FOR CONTENTS
    MOV #PR1ASC,-(SP)
    MOV #1,-(SP)
    MOV SP,R0
    TRAP C$PNTX
    ADD #4,SP
    CLR R4                :NUMBER OF THE NEXT WORD
    MOV R5,R1             :COPY LOW ORDER ADDRESS
    MOV R3,R0             :COPY HIGH ORDER ADDRESS
    BEQ 20$              :BR IF NOT ABOVE 28K
    JSR PC,SETMAP        :SETUP PAR ADDRESS IN R0
    MOV R0,R5            :GET PAR FORMAT ADDRESS ABOVE 28K
    PRINTX #PRASC,R4,(R5)+ :PRINT THE CONTENTS OF MEMORY BUFFER
    MOV (R5)+,-(SP)
    MOV R4,-(SP)
    MOV #PRASC,-(SP)
    MOV #3,-(SP)
    MOV SP,R0
    TRAP C$PNTX
    ADD #10,SP
    INC R4                :NUMBER OF THE NEXT
    CMP R4,#7            :DONE ALL YET?
    BGT 50$              :BRANCH IF ALL DONE
    
```



1805	014374	002761				BLT	20\$	:PRINT FIRST 7 WORDS
1806	014376	032763	000200	000012		RIT	#X2.EXTF,XST2(R3)	:EXTENDED FEATUTES ON ?
1807	014404	001355				BNE	20\$	:PRINT EXTENDED STATUS WORD
1808	014406	000207			50\$:	RTS	PC	:RETURN
1809								
1810	014410	045	116	045	PROASC:	.ASCIZ	'%N% Message Buffer Address = %01%05'	
1811	014455	045	116	045	PR1ASC:	.ASCIZ	'%N% Message Buffer Contents:'	
1812	014513	045	116	045	PRASC:	.ASCIZ	'%N% Word%D1%A: %0'	
1813						.EVEN		

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 1829 014542  
 1830 014542  
 1831 014546 010005  
 1832 014550 013700 002304  
 1833 014554 010004  
 1834 014556 013701 002302  
 1835 014562 006100  
 1836 014564 006101  
 1837 014566  
 014566 010446  
 014570 010146  
 014572 012746 014722  
 014576 012746 000003  
 014602 010600  
 014604 104415  
 014606 062706 000010  
 1838 014612  
 014612 012746 014767  
 014616 012746 000001  
 014622 010600  
 014624 104415  
 014626 062706 000004  
 1839 014632 005004  
 1840 014634 012701 002320  
 1841 014640 012702 002464  
 1842 014644 011100  
 1843 014646 011203  
 1844 014650  
 1845 014660  
 014660 010346  
 014662 012246  
 014664 012146  
 014666 010446  
 014670 012746 015025  
 014674 012746 000005  
 014700 010600  
 014702 104415  
 014704 062706 000014  
 1846 014710 005204  
 1847 014712 020405  
 1848 014714 002001  
 1849 014716 000752  
 1850 014720 000207

```

.SBTTL PRMSGEXP - PRINT EXPD/RCV MESSAGE BUFFERS
:ROUTINE TO PRINT EXPECTED AND RECEIVED MESSAGE BUFFERS
    RO      - NUMBER OF WORDS IN BUFFER
:IMPLICIT INPUTS:
    EXPMSG  - EXPECTED MESSAGE BUFFER
    RECMSG  - RECEIVED MESSAGE BUFFER
    RCVHIADD- RECEIVED MESSAGE BUFFER HIGH ORDER ADDRESS
    RCVLOADD- RECEIVED MESSAGE BUFFER LOW ORDER ADDRESS
PRMSGEXP::
    SAVREG      ;SAVE R1-R5 UNTIL NEXT RETURN
    MOV         RO,R5      ;SAVE NUMBER OF WORDS
    MOV         RCVLOADD,RO ;GET RECV LOW ADDRESS
    MOV         RO,R4      ;COPY LOW ADDRESS
    MOV         RCVHIADD,R1 ;GET RECV HIGH ADDRESS
    ROL         RO         ;SHIFT BIT15 TO C BIT
    ROL         R1         ;SHIFT TO HIGH ORDER FOR PRINTOUT
    PRINTX     #PRMSG0,R1,R4 ;PRINT MESSAGE BUFFER ADDRESS
    MOV         R4,-(SP)
    MOV         R1,-(SP)
    MOV         #PRMSG0,-(SP)
    MOV         #3,-(SP)
    MOV         SP,R0
    TRAP       C$PNTX
    ADD         #10,SP
    PRINTX     #PRMSG1      ;PRINT HEADER FOR CONTENTS
    MOV         #PRMSG1,-(SP)
    MOV         #1,-(SP)
    MOV         SP,R0
    TRAP       C$PNTX
    ADD         #4,SP
    CLR         R4          ;NUMBER OF THE CURRENT WORD
    MOV         #EXPMSG,R1 ;GET EXPD BUFFER ADDRESS
    MOV         #RECMSG,R2 ;GET RECV BUFFER ADDRESS
    20$:      MOV         (R1),R0 ;GET EXPD
    MOV         (R2),R3      ;GET RECV
    XOR         R0,R3        ;XOR EXPD/RCV
    PRINTX     #PRMSG2,R4,(R1)+,(R2)+,R3
    MOV         R3,-(SP)
    MOV         (R2)+,-(SP)
    MOV         (R1)+,-(SP)
    MOV         R4,-(SP)
    MOV         #PRMSG2,-(SP)
    MOV         #5,-(SP)
    MOV         SP,R0
    TRAP       C$PNTX
    ADD         #14,SP
    INC         R4          ;NUMBER OF THE NEXT
    CMP         R4,R5      ;DONE ALL YET?
    BGE         50$        ;BR IF YES
    BR          20$        ;DO ANOTHER
    50$:      RTS         PC ;RETURN
    
```



1851					
1852	014722	045	116	045	PRMSG0: .ASCIZ '%N% Message Buffer Address = %01%05'
1853	014767	045	116	045	PRMSG1: .ASCIZ '%N% Message Buffer Contents:'
1854	015025	045	116	045	PRMSG2: .ASCIZ '%N% WORD #%D2% EXPD: %06% RECV: %06% XOR: %06'
1855					.EVEN
1856					

```

1858 .SBTTL PRBYTEXP - PRINT ERROR BYTES IN EXP/REC MESSAGE BUFFER
1859
1860
1861 :+
1862 :ROUTINE TO PRINT ERROR BYTES IN MESSAGE BUFFERS
1863 :ONLY THE FIRST 8 ERRORS ENCOUNTERED ARE PRINTED DUE TO SCREEN SPACE
1864
1865 :RO - NUMBER OF BYTES IN BUFFER
1866
1867 :IMPLICIT INPUTS:
1868
1869 :EXPMSG - EXPECTED MESSAGE BUFFER
1870 :RECMSG - RECEIVED MESSAGE BUFFER
1871
1872 PRBYTEXP::
1873 SAVREG ;SAVE R1-R5 UNTIL NEXT RETURN
1874 MOV R0,R5 ;SAVE NUMBER OF BYTES
1875 CLR PRMNO ;INIT ERROR COUNT
1876 CLR R4 ;NUMBER OF THE CURRENT BYTE
1877 MOV #EXPMSG,R1 ;GET EXPD BUFFER ADDRESS
1878 MOV #RECMSG,R2 ;GET RECV BUFFER ADDRESS
1879 20$: MOV (R1),R0 ;GET EXPD BYTE
1880 BIC #^C<377>,R0 ;CLEAR UPPER BYTE
1881 MOV R0,PRBEXP ;SAVE FOR ERROR REPORT
1882 MOV (R2),R3 ;GET RECV BYTE
1883 BIC #^C<377>,R3 ;CLEAR UPPER BYTE
1884 MOV R3,PRBREC ;FOR ERROR REPORT
1885 XOR R0,R3 ;XOR EXPD/RECV
1886 CMPB (R1)+,(R2)+ ;EXPD = RECV?
1887 BEQ 30$ ;BR IF YES
1888 INC PRMNO ;UPDATE ERROR COUNT
1889 CMP PRMNO,#8. ;PRINTED 8?
1890 BHI 30$ ;BR IF YES
1891 27$: PRINTX #PRBMSG,R4,PRBEXP,PRBREC,R3
1892 MOV R3,-(SP)
1893 MOV PRBREC,-(SP)
1894 MOV PRBEXP,-(SP)
1895 MOV R4,-(SP)
1896 MOV #PRBMSG,-(SP)
1897 MOV #5,-(SP)
1898 MOV SP,R0
1899 TRAP C$PNTX
1900 ADD #14,SP
1901 FORCEEXIT 50$ ;aod
1902 BR 35$ ;aod
1903 30$:
1904 FORCERROR 27$,NOTSSR ;aod
1905 35$:
1906 INC R4 ;NUMBER OF THE NEXT
1907 CMP R4,R5 ;DONE ALL YET?
1908 BGE 50$ ;BR IF YES
1909 BR 20$ ;DO ANOTHER
1910 50$: PRINTX #PRBTOT,PRMNO ;PRINT TOTAL ERROR COUNT
1911 MOV PRMNO,-(SP)
1912 MOV #PRBTOT,-(SP)
1913 MOV #2,-(SP)
1914 MOV SP,R0
1915 TRAP C$PNTX
  
```

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1871 015112
1872 015112
1873 015116 010005
1874 015120 005037 002316
1875 015124 005004
1876 015126 012701 002320
1877 015132 012702 002464
1878 015136 111100
1879 015140 042700 177400
1880 015144 110037 015460
1881 015150 111203
1882 015152 042703 177400
1883 015156 110337 015462
1884 015162
1885 015172 122122
1886 015174 001431
1887 015176 005237 002316
1888 015202 023727 002316 000010
1889 015210 101023
1890 015212
1891 015212 010346
1892 015214 013746 015462
1893 015220 013746 015460
1894 015224 010446
1895 015226 012746 015326
1896 015232 012746 000005
1897 015236 010600
1898 015240 104415
1899 015242 062706 000014
1891 015246
1892 015256 000404
1893 015260
1894 015260
1895 015270
1896 015270 005204
1897 015272 020405
1898 015274 002001
1899 015276 000717
1900 015300
1901 015300 013746 002316
1902 015304 012746 015413
1903 015310 012746 000002
1904 015314 010600
1905 015316 104415
  
```





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1922 015464  
015464  
1923 015464 004737 007736  
1924 015470  
015470  
015470 104423  
1925  
1926

```
.SBTTL EXPREC - PRINT EXPD/RECV WORD DATA
:+
:PRINT ROUTINE TO DISPLAY EXPD/RECV DATA
:INPUTS:
:      R1      RECEIVED DATA
:      R2      EXPECTED DATA
:-

      BGNMSG EXPREC
EXPREC:: JSR PC,PRIXOR          :PRINT THE DATA
          ENDMSG
L10017: TRAP C$MSG
```



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1941 015472  
015472  
1942 015472 004737 007606  
1943 015476  
015476  
015476 104423  
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1968 015500  
015500  
1969 015500 004737 013766  
1970 015504  
015504  
015504 104423  
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.SBTTL EXPBREC - PRINT EXPD/RECV BYTE DATA

:+  
:PRINT ROUTINE TO DISPLAY BYTE EXPD/RECV DATA

:INPUTS:

: R1 RECEIVED DATA BYTE  
: R2 EXPECTED DATA BYTE

:  
: BGNMSG EXPBREC  
EXPBREC:: JSR PC,PRIBXOR ;PRINT THE DATA  
: ENDMSG  
L10020: TRAP CSMSG

.SBTTL RAMERR - PRINT RAM AND PACKET DATA

:+  
:PRINT ROUTINE TO DISPLAY RAM/PACKET DATA

:INPUTS:

: R4 POINTER TO COMMAND PACKET

:IMPLICIT INPUTS:

: RAMDATA DATA AS READ FROM THE RAM  
: RAMSIZ NUMBER OF BYTES IN PACKET  
: IF RAMSIZ=0 THEN DEFAULT TO 8.

:IMPLICIT OUTPUTS:

: RAMSIZ SET TO 0

:  
: BGNMSG RAMERR  
RAMERR:: JSR PC,PRAMPKT ;PRINT RAM/PACKET DATA  
: ENDMSG  
L10021: TRAP CSMSG

.SBTTL RAMTADD - PRINT TEST ADDRESS, RAM AND PACKET DATA

:+  
:PRINT ROUTINE TO DISPLAY RAM/PACKET DATA

:INPUTS:

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 1994  
 1995 015506  
 015506  
 1996 015506 004737 010270  
 1997 015512 004737 013766  
 1998 015516  
 015516  
 015516 104423  
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 2013 015520  
 015520  
 2014 015520 042701 177400  
 2015 015524 042702 177400  
 2016 015530 004737 010062  
 2017 015534 004737 007736  
 2018 015540  
 015540  
 015540 104423  
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```

        R4      POINTER TO COMMAND PACKET
:IMPLICIT INPUTS:
        RAMDATA  DATA AS READ FROM THE RAM
        RAMSIZ   NUMBER OF BYTES IN PACKET
                IF RAMSIZ=0 THEN DEFAULT TO 8.
        ERRHI    HIGH ORDER TEST ADDRESS
        ERRLO    LOW ORDER TEST ADDRESS
:IMPLICIT OUTPUTS:
        RAMSIZ   SET TO 0
:--
        BGNMSG  RAMTADD
RAMTADD::  JSR    PC,PRITADD      ;PRINT TEST ADDRESS
          JSR    PC,PRAMPKT    ;PRINT RAM/PACKET DATA
          ENDMSG
L10022:   TRAP   C$MSG
:
        .SBTTL  RAMEXP - PRINT RAM EXPD/RECV DATA
:++
:PRINT ROUTINE TO DISPLAY EXPD/RECV DATA
:INPUTS:
        R1      RECEIVED DATA
        R2      EXPECTED DATA
        R4      CONTROLLER RAM ADDRESS
:--
        BGNMSG  RAMEXP
RAMEXP::  BIC    #*C<377>,R1    ;SAVE EXPD RAM DATA BYTE
          BIC    #*C<377>,R2    ;SAVE EXPD RAM DATA BYTE
          JSR    PC,PRIRAM      ;PRINT THE RAM ADDRESS
          JSR    PC,PRIXOR      ;PRINT THE DATA
          ENDMSG
L10023:   TRAP   C$MSG
:
        .SBTTL  TIMEXP - PRINT TIMER A,B AND EXP/REC
:++
:PRINT ROUTINE TO DISPLAY EXPD/RECV DATA
:AND TIMER A,B HEADER MESSAGE
:INPUTS:
        R1      RECEIVED DATA
        R2      EXPECTED DATA
    
```



```

2030
2031      :-
2032 015542      BGNMSG  TIMEXP
015542      TIMEXP::
2033 015542      PRINTX  #TIMSGO      ;PRINT HEADER
015542 012746 015570      MOV      #TIMSGO,-(SP)
015546 012746 000001      MOV      #1,-(SP)
015552 010600      MOV      SP,R0
015554 104415      TRAP    C$PNTX
015556 062706 000004      ADD      #4,SP
2034 015562 004737 007736      JSR     PC,PRIXOR      ;PRINT THE DATA
2035 015566      ENDMSG
015566      L10024:
015566 104423      TRAP    C$MSG
2036
2037
2038 015570      045      116      045  TIMSGO: .ASCIZ  '%N% TIMER A STATUS IS IN BIT 3%N% TIMER B STATUS IS IN BIT 2'
2039      .EVEN
    
```





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.SBTTL GLOBAL SUBROUTINES SECTION

:++  
: THE GLOBAL SUBROUTINES SECTION CONTAINS THE SUBROUTINES  
: THAT ARE USED IN MORE THAN ONE TEST.  
:--

2072 .SBTTL SOFINIT - SOFT INITIALIZE OF CONTROLLER

2073  
 2074  
 2075  
 2076 :ROUTINE TO DO A SOFT INITIALIZE OF THE CONTROLLER  
 2077 :BY WRITING INTO THE TSSR REGISTER. AFTER THE INIT,  
 2078 :THE TSSR REGISTER IS TESTED FOR ERRORS. ANY ERRORS  
 2079 :DETECTED SHOULD BE TREATED AS DEVICE FATAL ERRORS.

2080  
 2081 :INPUTS:  
 2082  
 2083 R5 ADDRESS OF FIRST REGISTER

2084  
 2085 :OUTPUTS:  
 2086  
 2087 R0 CCNTENTS OF TSSR, IF ERROR  
 2088 CARRY SET IF INIT WAS OKAY  
 2089 CLEAR IF FATAL ERROR

2090  
 2091 :CALLING SEQUENCE:  
 2092  
 2093 MOV #ADDRESS,R5  
 2094 JSR PC,SOFINIT  
 2095 BCS CONTINUE  
 2096 ERRDF ;REPORT FATAL ERROR  
 2097  
 2098  
 2099

2100	015764				SOFINIT::	SAVREG		: SAVE THE REGISTERS
2101	015764					MOV #0,TSSR(R5)		: DO THE INIT.
2102	015770	012765	000000	000002		JSR PC,WAITF		: WAIT FOR SSR
2103	015776	004737	016240			MOV TSSR(R5),R0		:GET THE TSSR REGISTER
2104	016002	016500	000002			MOV R0,R4		:TSSR CONTENTS
2105	016006	010004				BIC #^C<HIADDR!OFL>,R4		:R4 HAS EXPECTED CONTENTS
2106	016010	042704	176277			BIS #SSR!NBA,R4		:ONLY EXPECTED BITS SET ?
2107	016014	052704	002200			CMP R4,R0		:BRANCH IF OKAY
2108	016020	020400				BEQ \$\$		:CLEAR THE CARRY FOR ERROR
2109	016022	001402				CLC		:GO TO EXIT
2110	016024	000241				BR 10\$		:SET THE CARRY BIT
2111	016026	000401			5\$:	SEC		:RETURN TO CALLER
2112	016030	000261			10\$:	RTS	PC	
2113	016032	000207						



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.SBTTL    CHKAMB    - CHECK TSSR FOR AMBIGUITY

```

: +
: THIS ROUTINE TESTS THE CONTENTS OF THE TSSR REGISTER
: FOR AMBIGUITY
: INPUT:
:         RO         CONTENTS OF TSSR
: OUTPUT:
:         RO         CONTENTS OF TSSR
:         CARRY      SET - NO AMBIGUITY
:                   CLR - AMBIGUOUS CONTENTS
: -
  
```

```

CHKAMB:
        SAVREG                :SAVE THE GENERAL REGISTERS
        MOV    RO,R4           :CONTENTS OF TSSR
        BIT    #SC,RO          :IS BIT 15 SET ?
        BNE    5$              :BRANCH IF YES
        BIT    #^C<NBA!OFL!SSR!HIADDR>,RO :ANY OTHER BITS SET ?
        BNE    40$             :MUST BE AN ERROR
        BR    45$              :RETURN WITH SUCCESS
5$:     BIT    #SSR,RO         :IS READY BIT SET ?
        BNE    10$            :BRANCH IF READY BIT IS SET.
        BIT    #BIT5,RO        :IS FATAL ERROR BIT SET ?
        BEQ    40$             :ERROR IF NOT
        BIC    #^CTERCLS,R4    :CLEAR ALL BUT TERMINATION CODE
        CMP    R4,#16          :ALL THREE BITS MUST BE SET
        BNE    40$             :ERROR IF NOT SET
        BR    45$              :OK IF ALL ARE SET
10$:    BIT    #BIT5,RO        :IS FATAL ERROR BIT SET ?
        BEQ    45$             :ERROR IF BIT IS SET WITH SSR
        BIT    #BIT2!BIT1,RO   :IS THIS A FUNCTION REJECT
        BNE    45$             :BR, IF TSSR IS OK
40$:    CLC                    :AMBIGUOUS CONTENTS
        BR    50$
45$:    SEC                    :SHOW SUCCESS - NO AMBIGUITY
50$:    RTS    PC              :RETURN TO CALLER
  
```

016034  
016034    100000  
016040    010004    100000  
016042    032700    100000  
016046    001004  
016050    032700    174077  
016054    001023  
016056    000424  
016060    032700    000200  
016064    001011  
016066    032700    000040  
016072    001414  
016074    042704    177761  
016100    020427    000016  
016104    001007  
016106    000410  
016110    032700    000040  
016114    001405  
016116    032700    000006  
016122    001002  
016124    000241  
016126    000401  
016130    000261  
016132    000207

```

2161          .SBTTL ENAINT,DSBINT - ENABLE/DISABLE INTERRUPTS
2162          :
2163          : DEFAULT DISPLAY INTERRUPT HANDLERS.
2164          : IF DISPLAY TIME-OUT, REPORT DEV FATAL, AND ABORT PASS.
2165          : OTHERWISE, SAVE DPU REGISTERS AND DISMISS.
2166          :
2167          :
2168          : BIT DEFINITIONS FOR "INTMASK" AND "INTFLAG" BYTES:
2169          :
2170          000200          IOKCKIN=BIT7      ; DON'T CHECK FOR BAD INTERRUPTS -- TEST WILL.
2171          000001          IOKSTP=BIT0       ; EXPECT "STOP" INTERRUPT.
2172          :
2173          : INTERRUPT MASK -- SAYS EXPECTING INTERRUPTS
2174          016134          000          INTMASK: .BYTE 0
2175          : INTERRUPT FLAG -- SAYS WE GOT ONE (IF POSITIVE)
2176          016135          000          INTFLAG: .BYTE 0
2177          :
2178          : SAVED INTERRUPT VECTOR:
2179          016136          000000          INTVEC: .WORD 0
2180          : SAVE CPU PC
2181          016140          000000          INTCPC: .WORD 0
2182          :
2183          : SUBROUTINE TO ENABLE INTERRUPTS:
2184          016142          010046          ENAINT: MOV      RO,-(SP)          ;SAVE RO
2185          016144          013700          002206          MOV      IVEC,RO          ;GET POINTER TO VECTORS
2186          016150          012720          016206          MOV      #INTR,(RO)+          ;SET UP INTERRUPT VECTOR
2187          016154          012720          000340          MOV      #PRI07,(RO)+
2188          016160          012600          MOV      (SP)+,RO          ;RESTORE RO
2189          016162          011646          MOV      (SP),-(SP)
2190          016164          012766          000000          000002          MOV      #0,2(SP)          ;SET CPU TO LEVEL 0
2191          016172          000002          RTI
2192          :
2193          : SUBROUTINE TO DISABLE INTERRUPTS (RAISE PRIORITY TO LEVEL 7)
2194          016174          011646          DSBINT: MOV      (SP),-(SP)
2195          016176          012766          000340          000002          MOV      #PRI07,2(SP)
2196          016204          000002          RTI
2197

```



```
2199                            .SBTTL    INTR    - INTERRUPT HANDLERS
2200
2201 016206                    BGNSRV    INTR                    ;DEFINE INTERRUPT ENTRY
     016206                    INTR::
2202 016206    012737    000001    002222    MOV        #1,INTRECV        ;SET FLAG TO SHOW INTERRUPT RECEIVED
2203 016214    105037    016135                    CLRB       INTFLAG        ;CLEAR FLAG TO SAY WE GOT INTERRUPT
2204 016220    132737    000001    016134    BITB       #IOKSTP,INTMASK ;EXPECTING STOP INTERRUPT?
2205 016226    001003                    BNE        1$               ;BR IF YES
2206 016230    152737    000001    016135    BISB       #IOKSTP,INTFLAG ;NO. SET THE ERROR FLAG.
2207
2208                            ;SAVE REGISTERS, MSG BUFFER, ETC.
2209 016236                    1$:
2210 016236                    ENDSRV
     016236                    L10026:
     016236    000002                    RTI
2211
2212
```

```

2214 .SBTTL WAITF - WAIT FOR SUBSYSTEM READY
2215
2216 : SUBROUTINE TO WAIT FOR THE SUBSYSTEM READY FLAG
2217 :
2218 : INPUTS:
2219
2220 R5 ADDRESS OF FIRST DEVICE REGISTER
2221
2222 : OUTPUTS:
2223
2224 R0 CONTENTS OF LAST TSSR READ
2225 CARRY SET - READY BIT SET
2226 CLR - TIMEOUT WAITING FOR READY
2227
2228 016240 000401 WAITF:: BR 1$ ;NOP WHEN SUPER FIXED
2229 016242 104422 BREAK ; DO A SUPVSR BREAK FIRST.
016242 104422 TRAP CSBRK
2230 016244 012746 003000 1$: MOV #3000,-(SP) ;300 MSEC TIMER
2231 016250 016500 000002 2$: MOV TSSR(R5),R0 ;READ THE TSSR REGISTER
2232 016254 105700 TSTB R0 ;TEST FOR READY BIT SET
2233
2234 016256 100420 BMI 3$ ; EXIT ON STOP FLAG.
2235 016260 DELAY 1 ; WAIT 100 USEC
016260 012727 000001 MOV #1,(PC)+
016264 000000 .WORD 0
016266 013727 002116 MOV LSDLY,(PC)+
016272 000000 .WORD 0
016274 005367 177772 DEC -6(PC)
016300 001375 BNE -4
016302 005367 177756 DEC -22(PC)
016306 001367 BNE -20
2236 016310 005316 DEC (SP) ;REDUCE DELAY COUNT
2237 016312 001356 BNE 2$ ;RETRY UNTIL TIMER EXPIRES
2238 016314 000241 CLC ; C = 0, CONTROLLER STILL RUNNING...
2239 016316 000401 BR 4$ ;...OR HUNG-UP AFTER 300 MSEC.
2240 016320 000261 3$: SEC ; C = 1, CONTROLLER IS STOPPED.
2241 016322 005326 4$: DEC (SP)+ ;RESTORE STACK WITHOUT CHANGING CARRY BIT
2242 016324 000207 RTS PC
    
```



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.SBTTL CHKTSSR - CHECK TSSR FOR READY

```

: +
: THIS ROUTINE WAITS FOR READY IN THE TSSR
: AND TESTS FOR AMBIGUOUS BIT SETTINGS IN TSSR.
: INPUT:
:       R5      ADDRESS OF CSR REGISTERS
: OUTPUT:
:       R0      CONTENTS OF TSSR
:       CARRY   SET - OKAY
:              CLR - NOT READY AMBIGUOUS, OR SC SET
: -
  
```

CHKTSSR:

```

      JSR      PC, WAITF      ;WAIT FOR READY
      BCC     20$            ;BRANCH IF TIME OUT
      JSR     PC, CHKAMB     ;TSSR AMBIGUOUS?
      BCC     10$            ;BR IF YES
      BIT     #SC, R0        ;SPECIAL CONDITION SET?
      BEQ     15$            ;BR IF NO
      BIT     #<SCE!BIE!RMR!NXM>, R0 ;ANY ERROR BITS SET?
      BEQ     15$            ;BR IF NO
10$:   CLC
      BR      20$
15$:   SEC
20$:   RTS      PC          ;SET SUCCESS
                                ;RETURN TO CALLER
  
```

016326 004737 016240  
 016332 103014  
 016334 004737 016034  
 016340 103006  
 016342 032700 100000  
 016346 001405  
 016350 032700 074000  
 016354 001402  
 016356 000241  
 016360 000401  
 016362 000261  
 016364 000207

```

2277          .SBTTL XNXM - CHECK FOR NONEXISTENT MEMORY
2278
2279          :+
2280          : ROUTINE TO TEST FOR A NEXM IN THE RANGE (R1) THRU (R2).
2281          : ON RETURN, IF 'C' = 1, (R1) = NEXM ADDRESS.
2282          : 'C' = 0, ALL ADDRESSES OK.
2283
2284          : CALL: MOV ADR1,R1
2285          :         MOV ADR2,R2
2286          :         JSR PC,NXM
2287          :         RETURN          ;TEST 'C' AND PROCEED.
2288 016366 012737 016420 000004 XNXM: MOV #2$,@#4          ; SET BUSERR VECTOR.
2289 016374 012737 000200 000006   MOV #PRI04,@#6
2290 016402 005003   CLR R3          ;FLAG.
2291 016404 005711 1$: TST (R1)          ;TEST THE ADDRESS(ES).
2292
2293 016406 020102   CMP R1,R2          ;IF ANY TRAP, CONTINUE AT 2$.
2294 016410 001407   BEQ 3$          ;OTHERWISE, CONTINUE HERE.
2295 016412 062701 000002   ADD #2,R1          ;BR IF FINISHED (NO NEXM'S).
2296 016416 000772   BR 1$          ;SET NEXT ADDRESS...
2297
2298 016420 005103   COM R3          ;...AND CONTINUE.
2299 016422 012716 016430 2$: MOV #3$, (SP)          ;GOT ONE, SET FLAG...
2300 016426 000002   RTI          ;...AND DISMISS INTERRUPT...
2301 016430 012700 000004 3$: CLRVEC #4          ;...AND GIVE BACK THE VECTOR.
2302 016434 104436   MOV #4,R0
2303 016436 005703   TRAP C$CVEC
2304 016440 001401   TST R3          ;DID WE CATCH ONE ??
2305 016442 000261   BEQ .+4          ;NO, 'C' = 0, SKIP NEXT.
2306 016444 000207   SEC          ;YES, 'C' = 1, (R1) = NEXM ADDR.
2307   RTS PC

```

```

2308
2309          .SBTTL TSTLOOP - CHECK ITERATION COUNT
2310
2311          :+
2312          : SUBROUTINE TO EXECUTE TEST ITERATIONS.
2313          : EXIT WITH 'C' SET IF LOOPS ALLOWED AND LOOP COUNT NON-ZERO.
2314          : LOOP COUNTER IS SET BY 'BEGIN.TEST' MACRO.
2315
2316          : CALL: LOOPTO ARG
2317
2318 016446 005737 002166 TSTLOOP:: TST NOITS          ; ITERATIONS INHIBITED?
2319 016446 001006   BNE 1$          ; YES.
2320 016452 005737 002202   TST QVP          ; NO.
2321 016454 100403   BMI 1$          ;LOOPS DISALLOWED IN QUICK PASS.
2322 016460 005337 002214   DEC LOOPCNT          ; BUMP LOOP COUNTER.
2323 016462 001002   BNE 2$
2324 016466 000241 1$: CLC          ;LOOP DISALLOWED, OR DONE.
2325 016470 000401   BR 3$
2326 016472 000261 2$: SEC          ;LOOP ENABLED.
2327 016474 000207 3$: RTS PC
2328 016476 000207

```



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016500 010046  
016502 005037 003150  
016506 005037 016746  
016512 005037 005766  
016516 105037 016134  
016522 013700 002200  
016526 006300  
016530 005737 003110  
016534 001430  
016536 100010  
016540 052760 160000 003172  
016546  
016546 104455  
016550 000001  
016552 003734  
016554 005732  
016556 000407  
016560 052760 160001 003172 3\$:  
016566  
016566 104455  
016570 000002  
016572 004331  
016574 000000  
016576 012737 177777 003106 2\$:  
016604  
016604 013700 002200  
016610 104451  
016612

```

.SBTTL TSTSETUP - PRINT TEST NAME AND INIT ERROR COUNTS
:PRINT THE NUMBER AND NAME OF EACH TEST AS WE GO ALONG.
:INCREMENT "TESTK" TO INDICATE THE NUMBER OF TESTS
:IN THE CURRENT RUN SEQUENCE.
:CLEAR THE ERROR COUNTER AND SIGNATURE EXTENSION FLAGS.
:INPUT:
:      R0      POINTER TO TEST ID ASCIZ STRING
:OUTPUT:
:      R5      ADDRESS OF FIRST DEVICE REGISTER
:IMPLICIT OUTPUTS:
:      TSTCNT  UPDATED TO COUNT TESTS PERFORMED SINCE START OR RESTART
:SIDE EFFECTS:
:      INTERRUPT LEVEL IS RASIED TO LEVEL OF
:      THE DEVICE UNDER TEST
:-
TSTSETUP::
MOV      R0,-(SP)      ;SAVE THE TEST ID MESSAGE
CLR      SIFLAG        ;CLEAR "SOFT INIT" FLAG
CLR      ERRK          ;CLEAR LOCAL ERROR COUNTER.
CLR      EXTA          ;CLEAR ERROR EXTENSION FLAG.
CLRB     INTMASK       ;CLEAR INTERRUPT MASK (CHECK ERROR)
MOV      UNITN,R0      ;GET THE UNIT NUMBER,
ASL      R0            ;... AND MAKE IT A WORD OFFSET.
TST      NODEV         ;DID STARTUP FIND THE DEVICE?
BEQ      4$            ;BR IF YES
BPL      3$            ;BR IF NOT IDLE
BIS      #160000,ERTABL(R0) ; FLAG ERROR IN THE ERROR TABLE
ERRDF   1,NXR,NXRERR   ; NO DEVICE HERE -- PRINT IT
TRAP    C$ERDF
.WORD   1
.WORD   NXR
.WORD   NXRERR
BR      2$
BIS      #160001,ERTABL(R0) ; FLAG ERROR IN THE ERROR TABLE
ERRDF   2,NOINIT      ; DEVICE NOT IDLE
TRAP    C$ERDF
.WORD   2
.WORD   NOINIT
.WORD   0
MOV      #-1,DUFLG     ; DROP THE UNIT
DODU    UNITN
MOV      UNITN,R0
TRAP    C$DODU
DOCLN   ; ABORT THE PASS

```

2377	016612	104444			TRAP	CSDCLN		
2378	016614	000423			BR	5\$		
2379	016616			4\$:	RFLAGS	R0		; GET THE OPERATOR FLAGS.
	016616	104421			TRAP	CSRFLA		
2380	016620	032700	001000		BIT	#PNT,R0		; PRINT THE TEST NUMBERS?
2381	016624	001412			BEG	1\$		; BR IF NO
2382	016626	011600			MOV	(SP),R0		;GET THE ID MESSAGE
2383	016630				PRINTF	#TNAM,R0		;DISPLAY THE TEST ID
	016630	010046			MOV	R0,-(SP)		
	016632	012746	016674		MOV	#TNAM,-(SP)		
	016636	012746	000002		MOV	#2,-(SP)		
	016642	010600			MOV	SP,R0		
	016644	104417			TRAP	CSPNTF		
2384	016646	062706	000006		ADD	#6,SP		; BUMP TEST COUNTER.
2385	016652	005237	002212	1\$:	INC	TSTCNT		;PRIORITY THAT OF DEVICE
	016656				SETPRI	IPRI		
	016656	013700	002210		MOV	IPRI,R0		
	016662	104441			TRAP	CSSPRI		
2386	016664	005726		5\$:	TST	(SP)+		;FIX UP THE STACK
2387	016666	013705	002204		MOV	CSRADDR,R5		; ADDRESS OF TSV REGISTERS ON UNIBUS
2388	016672	000207			RTS	PC		
2389	016674	045	123	045	TNAM:	.ASCIZ	'%S%T%A Test'	
2390						.EVEN		



```

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2397 016710
      016710 104421
2398 016712 030027 020000
2399 016716 001412
2400 016720
      016720 013746 016746
      016724 012746 016750
      016730 012746 000002
      016734 010600
      016736 104417
      016740 062706 000006
2401 016744 000207
2402
2403 016746 000000
2404 016750      045      101      040
2405 016767      105      122      122
2406
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2412 017034 005237 016746
2413 017040 010046
2414 017042 013700 002200
2415 017046 006300
2416 017050 062700 003172
2417 017054 005210
2418 017056 032710 007777
2419 017062 001001
2420 017064 005310
2421 017066 012600
2422 017070 000207
2423
2424 017072 010046
2425 017074 013700 002200
2426 017100 006300
2427 017102 016000 003172
2428 017106 042700 170000
2429 017112 020037 002172
2430 017116 103004
2431 017120 023737 016746 002170
2432 017126 103417
2433 017130
      017130 104421
2434 017132 032700 000040
2435 017136 001013
2436 017140 012737 177777 003106
2437 017146
      017146 104455
      017150 000004
      017152 016767
    
```

```

.SBTTL TSTEND - PRINT ERRORS RECEIVED
:
: AT END OF EACH TEST, PRINT THE NUMBER OF ERRORS RECEIVED
: IF NORMAL ERROR REPORTING IS DISABLED (FLA:IER).
:
    
```

```

TSTEND: RFLAGS R0
        TRAP   CSRFLA
        BIT    R0,#IER
        BEQ    1$
        PRINTF #ESUM,ERRK
        MOV    ERRK,-(SP)
        MOV    #ESUM,-(SP)
        MOV    #2,-(SP)
        MOV    SP,R0
        TRAP   C$PNTF
        ADD    #6,SP
1$:     RTS    PC
    
```

```

ERRK:   0
ESUM:   .ASCIZ /%A %D%A ERRORS/
EMAXDU: .ASCIZ /ERROR LIMIT REACHED -- DROPPING UNIT/
        .EVEN
    
```

```

.SBTTL INCERK - INCREMENT LOCAL ERROR COUNT
:
: +
: ROUTINES TO INCREMENT LOCAL ERROR COUNT AND CHECK FOR LIMIT:
: -
    
```

```

INCERK: INC    ERRK
        MOV    R0,-(SP)
        MOV    UNITN,R0
        ASL    R0
        ADD    #ERTABL,R0
        INC    (R0)
        BIT    #7777,(R0)
        BNE    1$
        DEC    (R0)
1$:     MOV    (SP)+,R0
        RTS    PC
    
```

```

CKEMAX: MOV    R0,-(SP)
        MOV    UNITN,R0
        ASL    R0
        MOV    ERTABL(R0),R0
        BIC    #170000,R0
        CMP    R0,GERRMAX
        BHIS   1$
        CMP    ERRK,LERRMAX
        BLO    2$
1$:     RFLAGS R0
        TRAP   CSRFLA
        BIT    #IDU,R0
        BNE    2$
        MOV    #-1,DUFLG
        ERRDF  4,EMAXDU
        TRAP   C$ERDF
        .WORD  4
        .WORD  EMAXDU
    
```

```

: INCREMENT LOCAL ERROR COUNT
: SAVE R0
: GET UNIT NUMBER,
: ... AND MAKE IT A WORD OFFSET.
: R0 GETS ADDRESS OF ERROR TABLE ENTRY.
: INCREMENT THE DEVICE ERROR COUNT
: DID WE OVERFLOW THE FIELD?
: BR IF NO.
: YES -- BACK IT UP TO 7777.
: RESTORE R0
: RETURN TO CALLER.

: SAVE R0
: GET UNIT NUMBER
: ... AND MAKE IT A WORD OFFSET
: GET ERROR TABLE ENTRY
: EXTRACT ERROR COUNT FIELD
: IS GLOBAL LIMIT EXCEEDED FOR THIS UNIT?
: BR IF YES
: IS LOCAL LIMIT EXCEEDED FOR THIS TEST?
: BR IF NO
: GET OPERATOR FLAGS

: IS DROPPING INHIBITED?
: BR IF YES.
: NO -- DROP THE UNIT
    
```

2438 017154 000000  
017156  
017156 013700 002200  
017162 104451  
2439 017164  
017164 104444  
2440 017166 012600  
2441 017170 000207  
2442

2\$:

.WORD 0  
DODU UNITN  
MOV UNITN,RO  
TRAP CSDODU  
DOCLN  
TRAP CSDCLN  
MOV (SP)+,RO  
RTS PC

: RESTORE RO  
: RETURN TO CALLER



2444  
2445  
2446  
2447  
2448 017172 010046  
2449 017174  
2450 017204  
2451 017204 104421  
2452 017206 032700 000040  
2453 017212 001010  
2454 017214 011600  
2455 017216 012737 177777 003106  
2456 017224  
2457 017224 013706 002200  
2458 017230 104451  
2459 017232  
2460 017232 104444  
2461 017234 012600  
2462 017236 000207  
2463  
2464  
2465  
2466  
2467 017240  
2468 017240 004737 015764  
2469 017244 000207  
2470  
2471  
2472

```
.SBTTL CKDROP - CHECK IF UNIT SHOULD BE DROPPED
:
: + CHECK IF UNIT SHOULD BE DROPPED
: -
CKDROP: MOV     R0,-(SP)
        FORCERROR R0      1$,NOTSSR
        RFLAGS   R0
        TRAP     CSRFLA
        BIT      #IDU,R0
        BNE     1$
        MOV     (SP),R0
        MOV     #-1,DUFLG
        DODU    UNITN
        MOV     UNITN,R0
        TRAP    CSDODU
        DOCLN
        TRAP    CSDCLN
        MOV     (SP)+,R0
        RTS     PC
;ABORT THE PASS
```

```
.SBTTL CONFIG - DETERMINE CONFIGURATION OF SYSTEM
:
: SUBROUTINE - DETERMINE CONFIGURATION OF TSV05 SYSTEM.
:
CONFIG: JSR     PC,SOFINIT
        RTS     PC
```

```
2474 .SBTTL KTON,KTOFF - ENABLE/DISABLE MEMORY MANAGEMENT
2475 :
2476 : SUBROUTINE - ENABLE MEM MGT.
2477 :
2478 017246 005737 003126 KTON: TST KFLG : GOT KT?
2479 017252 001403 BEQ 1$ : NO.
2480 017254 012737 000001 177572 MOV #1,SRO : YES. ENABLE KT11.
2481 017262 000207 1$: RTS PC
2482
2483
2484
2485 :
2486 : SUBROUTINE - DISABLE MEM MGT.
2487 :
2488 017264 005737 003126 KTOFF: TST KFLG : GOT KT11?
2489 017270 001405 BEQ 1$ : NO.
2490 017272 000240 NOP
2491 017274 000240 NOP
2492 017276 012737 000000 177572 MOV #0,SRO : DISABLE KT.
2493 017304 000207 1$: RTS PC
2494
2495
```



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.SBTTL SETMAP - SETUP PAR6 MAPPING

```

    :+
    :THIS ROUTINE SETS UP KERNEL PAR6 TP HANDLE
    :AN 18 BIT ADDRESS. THE OFFSET INTO THE PAGE
    :IS RETURNED BIASED TO PAR6.
    
```

:INPUTS:

```

    R0    HIGH ORDER ADDRESS BITS
    R1    LOW ORDER ADDRESS BITS
    
```

:OUTPUTS:

```

    R0    OFFSET INTO BLOCK WITH PAR6 BIAS (I.E. THE ADDRESS)
    CARRY SET IF SUCCESS
          CLR IF ERROR
    
```

SETMAP:

```

    SAVREG          ;SAVE R1-R4 UNTIL NEXT RETURN
    TST             ;SYSTEM HAVE ABOVE 28K?
    BEQ             ;BR IF NO
    MOV             ;SAVE LOW ORDER BITS
    .REPT           6
    ASR             ;CONVERT WORD ADDRESS TO 32W BLOCKS
    ROR             ;MAKE IT DOUBLE PRECISION
    .ENDR
    BIC             ;ALINE FOR LOWER 4K BOUNDARY
    CMP             ;HIGHER THAN EXISTING MEMORY?
    BHIS            ;BR IF YES
    MOV             ;SETUP MAPPING REGISTER PAR6
    BIC             ;SETUP DISPLACEMENT IN PAGE
    ADD             ;ADD IN PAR6 BIAS
    MOV             ;RETURN IN R0
    SEC             ;SET SUCCESS
    BR              15$
    10$:           CLC
    15$:           RTS    PC
    
```

```

    017306
    017306
    005737 003126
    001433
    010102
    000006
    017352 042701 000177
    017356 020137 003126
    017362 103011
    017364 010137 172354
    017370 042702 160000
    017374 062702 140000
    017400 010200
    017402 000261
    017404 000401
    017406 000241
    017410 000207
    
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017412  
 017412  
 017416 004737 017264  
 017422 010003  
 017424 013701 003120  
 017430 013702 003122  
 017434 010321  
 10\$: 017436 005302  
 017440 003375  
 017442 005737 003126  
 017446 001477  
 017450 004737 017246  
 017454 005000  
 017456 013701 003146  
 000006  
 017526 004737 017306  
 017532 010320  
 017534 020027 160000  
 017540 103774  
 017542 162700 020000  
 017546 062737 000200 172354  
 017554 023737 172354 003126  
 017562 001427  
 017564 005737 003140  
 017570 001407  
 017572 013704 177572  
 017576 042704 177761  
 017602 022704 000016  
 017606 001415  
 017610 005737 003142  
 017614 001410  
 017616 023727 172354 007600  
 017624 103001  
 017626 000403  
 017630 012737 000020 172516  
 017636 000137 017532  
 017642 004737 017264  
 000207

```

.SBTTL FILLMEM - FILL MEMORY WITH BACKGROUND PATTERN
+
FILL MEMORY WITH A BACKGROUND PATTERN
:
INPUTS:
:
R0 = BACKGROUND PATTERN
FREE = FIRST LOCATION AVAILABLE TO DIAGNOSTIC
KTFLG = SET TO HIGHEST MEMORY LOCATION IF > 28K.
:
OUTPUTS:
:
NONE
-
FILLMEM:
:
SAVREG ;SAVE R1-R5 UNTIL NEXT RETURN
JSR PC,KTOFF ;DISABLE KT.
MOV R0,R3 ;COPY TEST PATTERN
MOV FREE,R1 ;GET FIRST FREE LOCATION
MOV FRESIZ,R2 ;SIZE OF FREE SPACE BELOW 28K.
10$: MOV R3,(R1)+ ;STORE A BACKGROUND WORD
DEC R2 ;DONE ALL MEMORY IN FREE SPACE?
BGT 10$ ;BR IF NO
TST KTFLG ; GOT KT?
BEQ 55$ ; NO. GET OUT.
JSR PC,KTON ; YES. ENABLE KT.
CLR R0 ;HIGH ORDER ADDRESS START
MOV PST32W,R1 ;GET >28K START ADDRESS (IN 32W BLOCKS)
.REPT 6
CLC ;CLEAR C BIT
ROL R1 ;CONVERT BLOCKS TO WORDS
ROL R0 ;MAKE IT DOUBLE PRECISION
.ENDR
30$: JSR PC,SETMAP ;SETUP PAR6 MAPPING REGISTER
MOV R3,(R0)+ ;STORE TEST PATTERN IN >28K ADDRESS
CMP R0,#160000 ;END OF PAR6 MAPPING AREA?
BLO 30$ ;BR IF NO
SUB #20000,R0 ;BACKUP INTO PAR6 MAPPING BEGIN
ADD #200,@#KIPAR6 ;POINT TO NEXT 4K BLOCK >28K.
CMP @#KIPAR6,KTFLG ;END OF MEMORY?
BEQ 50$ ;BR IF YES
TST T23A ;11/23A?
BEQ 35$ ;NO KEEP GOING
MOV SR0,R4 ;GET SR0 CONTENTS
BIC #177761,R4 ;CLEAR ALL BUT PAGE NUMBER
CMP #16,R4 ;SEE IF PAGE 7
BEQ 50$ ;EXIT IF THERE
35$: TST T23B ;11/23B?
BEQ 45$ ;NO KEEP GOING
CMP @#KIPAR6,#7600 ;REACHED 18 BITS?
BHS 40$ ;YES
BR 45$ ;NO KEEP GOING
40$: MOV #20,SR3 ;SET 22 BIT RELOCATION
45$: JMP 30$ ;KEEP GOING ON ETC.
50$: JSR PC,KTOFF ;DISABLE KT.
55$: RTS PC
    
```



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017650  
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004737 017264  
013701 003120  
013702 003122  
020311  
001411  
010137 002236  
005037 002234  
010337 002230  
011137 002232  
000474  
005721  
005302  
003362  
005737 003126  
001472  
004737 017246  
005000  
013701 003146  
000006  
042701 000177  
010046  
010146  
004737 017306  
010004  
012601  
012600  
020314  
001411  
010037 002234

```

.SBTTL CMPMEM - COMPARE MEMORY TO BACKGROUND PATTERN
+
COMPARE MEMORY WITH A BACKGROUND PATTERN
INPUTS:
    RO = BACKGROUND PATTERN
    FREE = FIRST LOCATION AVAILABLE TO DIAGNOSTIC
    KTFLG = SET TO HIGHEST MEMORY LOCATION IF > 28K.
OUTPUTS:
    CARRY - SET IF NO ERROR
    CARRY - CLR IF ERROR
IMPLICIT OUTPUTS:
    ERRHI - ERROR HIGH ADDRESS
    ERRLO - ERROR LOW ADDRESS
    EXPD  - EXPECTED DATA
    RECV  - RECEIVED DATA
-
CMPMEM:
    SAVREG
    MOV RO,R3 ;SAVE R1-R5 UNTIL NEXT RETURN
    JSR PC,KTOFF ;COPY TEST PATTERN
    MOV FREE,R1 ;DISABLE KT.
    MOV FRESIZ,R2 ;GET FIRST FREE LOCATION
    CMP R3,(R1) ;SIZE OF FREE SPACE BELOW 28K.
    BEQ 15$ ;FREE SPACE LOCATION EQUAL TO EXPD?
    MOV R1,ERRLO ;BR IF YES
    CLR ERRHI ;SAVE ADDRESS IN ERROR
    MOV R3,EXPD ;NO HIGH ADDRESS
    BR 50$ ;SAVE EXPD FOR ERROR REPORT
    ;SAVE RECV FOR ERROR REPORT
    TST (R1)+ ;POINT TO NEXT ADDRESS
    DEC R2 ;DONE ALL MEMORY IN FREE SPACE?
    BGT 10$ ;BR IF NO
    TST KTFLG ;GOT KT?
    BEQ 55$ ;NO. GET OUT.
    JSR PC,KTON ;YES. ENABLE KT.
    CLR RO ;HIGH ORDER ADDRESS START
    MOV PST32W,R1 ;GET >28K START ADDRESS (IN 32W BLOCKS)
    .REPT 6
    ROL R1 ;CONVERT BLOCKS TO WORDS
    ROL RO ;MAKE IT DOUBLE PRECISION
    .ENDR
    BIC #177,R1 ;ALINE 4K BOUNDARY
    MOV RO,-(SP) ;SAVE HIGH ORDER
    MOV R1,-(SP) ;SAVE LOW ORDER
    JSR PC,SETMAP ;SETUP PAR6 MAPPING REGISTER
    MOV RO,R4 ;COPY ADDRESS BIASED TO PAR6
    MOV (SP)+,R1 ;RESTORE LOW ORDER IN NON PAR6 FORMAT
    MOV (SP)+,RO ;RESTORE HIGH ORDER IN NON PAR6 FORMAT
    CMP R3,(R4) ;ABOVE 28K LOCATION EQUAL EXPD?
    BEQ 32$ ;BR IF YES
    MOV RO,ERRHI ;SAVE HIGH ORDER IN ERROR
    
```

10\$:  
15\$:  
30\$:



2655	020030	010137	002236		MOV	R1,ERRLO	:SAVE LOW ORDER IN ERROR
2656	020034	010337	002230		MOV	R3,EXPD	:SAVE EXPD FOR ERROR REPORT
2657	020040	011437	002232		MOV	(R4),RECV	:SAVE RECV FOR ERROR REPORT
2658	020044	000421			BR	50\$	:
2659	020046	062701	000002	32\$:	ADD	#2,R1	:UPDATE NON PAR6 ADDRESS
2660	020052	005500			ADC	R0	:MAKE IT DOUBLE PRECISION ADD
2661	020054	062704	000002		ADD	#2,R4	:UPDATE PAR FORMAT ADDRESS
2662	020060	020427	160000		CMP	R4,#160000	:END OF PAR6 MAPPING AREA?
2663	020064	103755			BLO	30\$	:BR IF NO
2664	020066	162704	020000		SUB	#20000,R4	:BACKUP INTO PAR6 MAPPING BEGIN
2665	020072	062737	000200	172354	ADD	#200,@#KIPAR6	:POINT TO NEXT 4K BLOCK >28K.
2666	020100	023737	172354	003126	CMP	@#KIPAR6,KTFLG	:END OF MEMORY?
2667	020106	101744			BLOS	30\$	:BR IF NO
2668	020110	004737	017264	50\$:	JSR	PC,KTOFF	:TURN OFF MEMORY MAPPING
2669	020114	000241			CLC		:SET FAILURE
2670	020116	000403			BR	60\$	:
2671	020120	004737	017264	55\$:	JSR	PC,KTOFF	:TURN OFF MEMORY MAPPING
2672	020124	000261			SEC		:SET SUCCESS
2673	020126	000207		60\$:	RTS	PC	
2674							

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2696 020130  
2697 020130 010446  
2698 020132 010346  
2699 020134 010246  
2700 020136 010146  
2701 020140 010546  
2702 020142 016605 000012  
2703 020146 004736  
2704 020150 012601  
2705 020152 012602  
2706 020154 012603  
2707 020156 012604  
2708 020160 012605  
2709 020162 000207  
2710

.SBTTL REGSAV - SAVE R1-R5 ON STACK

:+  
:ROUTINE TO  
:SAVE R1 THROUGH R5 ON THE STACK  
:CALLING SEQUENCE:  
: JSR R5,REGSAV  
:THIS IS A COOROUTINE WHICH TRANSFER CONTROL BACK TO  
:THE CALLING ROUTINE. AT THE END OF THE CALLING ROUTINE,  
:THE RTS PC RETURNS CONTROL TO THIS ROUTINE TO RESTORE  
:REGISTERS.  
:THIS ROUTINE SHOULD ONLY BE CALLED FROM ROUTINES WHICH ARE  
:CALLED VIA A JSR PC INSTRUCTION  
:-

REGSAV:  
MOV R4,-(SP)  
MOV R3,-(SP)  
MOV R2,-(SP)  
MOV R1,-(SP)  
MOV R5,-(SP)  
MOV 10.(SP),R5  
JSR PC,@(SP)+  
MOV (SP)+,R1  
MOV (SP)+,R2  
MOV (SP)+,R3  
MOV (SP)+,R4  
MOV (SP)+,R5  
RTS PC



```

2712 .SBTTL GETPAT - GET 8 BIT PATTERN FROM OPERATOR
2713
2714 :+
2715 :ROUTINE TO REQUEST AN 8 BIT DATA PATTERN FROM THE OPERATOR
2716
2717 :INPUTS:
2718
2719 NONE.
2720
2721 :OUTPUTS:
2722
2723 RO OCTAL NUMBER FROM THE OPERATOR
2724
2725 :CALLING SEQUENCE:
2726
2727 JSR PC,GETPAT
2728
2729 :-
2730
2731 GETPAT::
2732 1$: SAVREG ;SAVE THE GENERAL REGISTERS
2733 020164 GMANID DATASC,PATDAT,0,377,0,377,NO
2734 020170 104443 TRAP CS$GMAN
2735 020172 000406 BR 10000$
2736 020174 020220 .WORD PATDAT
2737 020176 000022 .WORD T$CODE
2738 020200 020222 .WORD DATASC
2739 020202 000377 .WORD 377
2740 020204 000000 .WORD T$L$OLIM
2741 020206 000377 .WORD T$H$ILIM
2742 10000$: BNCOMPLETE 1$ ;RETRY IF ERROR
2743 020210 103367 BCC 1$
2744 020212 013700 020220 MOV PATDAT,RO ;DATA PATTERN FROM OPERATOR
2745 020216 000207 RTS PC ;RETURN TO CALLER
2746
2747 :+
2748 :LOCAL DATA AREA
2749 :-
2750
2751 PATDAT: .WORD 0 ;TEMPORARY STORAGE FOR DATA
2752 DATASC: .ASCIZ 'ENTER DATA PATTERN'
2753 .EVEN
  
```

```

2746 .SBTTL GETSEL - ISSUE MENU AND GET OPERATOR RESPONSE
2747
2748
2749 :ROUTINE TO ISSUE A MENU AND GET
2750 :THE OPERATOR'S RESPONSE.
2751
2752 :INPUTS:
2753
2754 :      R0      ADDRESS OF ASCIZ STRING OF MENU
2755 :      R1      MAXIMUM ALLOWABLE OPERATOR RESPONSE
2756
2757 :OUTPUTS:
2758
2759 :      R0      NUMBER OF THE OPERATOR'S SELECTION
2760
2761 :-
2762
2763 GETSEL::
2764     SAVREG                                ;SAVE GENERAL REGISTERS
2765     MOV      R0,R2                        ;SAVE THE MENU ADDRESS
2766     MOV      R2,R3                        ;START OF MENU STRING
2767     TST      (R3)                          ;END OF ASCII ?
2768     BEQ      3$                          ;BRANCH IF ALL LINES DISPLAYED
2769     PRINTF   #SELASC,(R3)+                ;DISPLAY THE MENU
2770     MOV      (R3)+,-(SP)
2771     MOV      #SELASC,-(SP)
2772     MOV      #2,-(SP)
2773     MOV      SP,R0
2774     TRAP     C$PNTF
2775     ADD      #6,SP
2776     BR      2$
2777     3$:      G$MANID  MENASC,MENRES,D,-1,0,-1,NO
2778     TRAP     C$G$MAN
2779     BR      10001$
2780     .WORD    MENRES
2781     .WORD    T$CODE
2782     .WORD    MENASC
2783     .WORD    -1
2784     .WORD    T$LOLIM
2785     .WORD    T$HILIM
2786     10001$: BNCOMPLETE      1%          ;RETRY IF ERROR
2787     BCC      1$
2788     MOV      MENRES,R0                    ;GET THE OPERATOR'S REPLY
2789     CMP      R0,R1                        ;COMPARE TO MAXIMUM ALLOWED
2790     BLOS     5$                          ;BRANCH IF OK
2791     PRINTF   #MENERR                      ;DISPLAY ERROR MESSAGE
2792     MOV      #MENERR,-(SP)
2793     MOV      #1,-(SP)
2794     MOV      SP,R0
2795     TRAP     C$PNTF
2796     ADD      #4,SP
2797     BR      1$
2798     5$:      RTS      PC                  ;RETRY
2799     ;RETURN TO CALLER
2800     045     MENERR: .ASCIZ  '%N% *** Menu Selection Too Large ***'
2801     045     SELASC: .ASCIZ  '%N%T'
2802     164     MENASC: .ASCIZ  'Enter Menu Selection: '
  
```



2782  
2783 020466 000000

MENRES: .EVEN    .WORD 0

```

2785                                    .SBTTL  CHKMAN - CHECK MANUAL INTERVENTION LEGALITY
2786
2787
2788                                    :ROUTINE TO TEST FOR MANUAL INTERVENTION LEGALITY.
2789
2790                                    :INPUT:
2791
2792                                    :      NONE.
2793
2794                                    :OUTPUT:
2795
2796                                    :      CARRY   0        MANUAL INTERVENTION NOT ALLOWED
2797                                    :      1        MANUAL INTERVENTION IS OK
2798
2799                                    :SIDE EFFECTS:
2800
2801                                    :      A MESSAGE IS DISPLAYED WARNING THAT TEST IS
2802                                    :      NOT EXECUTED IF MANUAL INTERVENTION IS NOT
2803                                    :      ALLOWED.
2804
2805                                    :-
2806
2807 020470                            CHKMAN::
2808 020470                            :SAVREG                            :SAVE THE REGISTERS
2809 020474                            MANUAL                            :SEE IF MANUAL INTERVENTION OK
                                  TRAP       CSMANI
2810 020476                            BCOMPLETE 1$                    :BRANCH IF ALLOWED
                                  1$
2811 020500                            PRINTF #NOMAN                    :PRINT THE WARNING MESSAGE
                                  MOV       #NOMAN,-(SP)
                                  MOV       #1,-(SP)
                                  MOV       SP,R0
                                  TRAP       CSPNTF
                                  ADD       #4,SP
2812 020520                            CLC                            :CLEAR CARRY FOR ERROR
2813 020522                            1$:   RTS       PC                    :RETURN
2814
2815 020524                            045       116       045  NOMAN: .ASCIZ  'XNZA *** Manual Intervention not Allowed - Test Aborted ***'
2816                                    .even
  
```



```

2818                                     .SBTTL  ENVIRN  - SETUP FREE DIAGNOSTIC SPACE
2819                                     :
2820                                     : SUBROUTINE TO SET-UP VARIOUS ENVIRONMENTAL PARAMETERS.
2821                                     :
2822 020620                               ENVIRN: MEMORY  R0
2823 020620 104431                       TRAP          C$MEM
2824 020622 010037 003120                MOV          R0,FREE          ; GET 1ST FREE ADDRESS...
2825 020626 062737 000002 003120        ADD          #2,FREE
2826 020634 011037 003122                MOV          (R0),FRESIZ     ;...AND WORD COUNT.
2827 020640 162737 000004 003122        SUB          #4,FRESIZ
2828 020646 013702 002012                MOV          L$UNIT,R2      ; GET NUMBER OF UNITS
2829 020652 162737 000007 003122 10$:  SUB          #7,FRESIZ     ; TAKE AWAY 7 WORDS PER UNIT
2830 020660 005302                       DEC          R2
2831 020662 001373                       BNE          10$
2832 020664 013700 003120                MOV          FREE,R0        ;GET FIRST FREE ADDRESS
2833 020670 063700 003122                ADD          FRESIZ,R0      ;POINT TO LAST FREE ADDRESS
2834 020674 162700 000002                SUB          #2,R0         ;BACKUP 1 WORD
2835 020700 010037 003124                MOV          R0,FREEHI     ;STORE LAST FREE ADDRESS
2836 020704 000240                       NOP
2837 020706 012701 177520                MOV          #BDVPCR,R1    ;GET BDV11 PCR ADDRESS
2838 020712 010102                       MOV          R1,R2         ;COPY TO R2
2839 020714 062702 000002                ADD          #2,R2         ;SET THE RANGE
2840 020720 004737 016366                JSR          PC,XNXM       ;SEE IF WE HAVE ONE
2841 020724 103001                       BCC          15$          ;OK TO SET FLAGS
2842 020726 000445                       BR           40$          ;RETURN WITH FLAGS CLEAR
2843 020730 013701 177520 15$:          MOV          BDVPCR,R1     ;SAVE PCR CONTENTS
2844 020734 062701 000001                ADD          #1,R1         ;ADD ONE TO IT
2845 020740 012702 177520                MOV          #BDVPCR,R2    ;GET BDV11 PCR ADDRESS
2846 020744 005212                       INC          (R2)         ;TRY TO WRITE TO IT
2847 020746 013703 177520                MOV          BDVPCR,R3     ;GET RESULTS
2848 020752 020103                       CMP          R1,R3        ;DID IT CHANGE?
2849 020754 001017                       BNE          20$          ;NO, MUST BE 11/23B
2850 020756 005237 003140                INC          T23A         ;SET THE FLAG
2851 020762 042737 170000 002120        BIC          #170000,L$HIME ;SUPERVISOR COULD BE WRONG
2852 020770 000240                       NOP
2853 020772                               PRINTF      #M8186
2854 020772 012746 005550                MOV          #M8186,-(SP)  ;TELL THE SYSTEM TYPE
2855 020776 012746 000001                MOV          #1,-(SP)
2856 021002 010600                       MOV          SP,R0
2857 021004 104417                       TRAP        C$PNTF
2858 021006 062706 000004                ADD          #4,SP
2859 021012 000413                       BR           40$          ;RETURN
2860 021014 005237 003142 20$:          INC          T23B         ;SET THE FLAG
2861 021020 000240                       NOP
2862 021022                               PRINTF      #M8189
2863 021022 012746 005641                MOV          #M8189,-(SP)  ;TELL THE SYSTEM TYPE
2864 021026 012746 000001                MOV          #1,-(SP)
2865 021032 010600                       MOV          SP,R0
2866 021034 104417                       TRAP        C$PNTF
2867 021036 062706 000004                ADD          #4,SP
2868 021042 000207 40$:                RTS          PC           ;RETURN

```

```

2860                                     .SBTTL  KTINIT - SETUP KT11 MEMORY MANAGEMENT REGISTERS
2861                                     :+
2862                                     :ROUTINE TO INIT KT-11
2863                                     :-
2864
2865
2866
2867 021044      KTINIT:
2868 021044      005037 003126      CLR      KTFLG      ; INIT >28K MEMORY FLAG
2869 021050      005037 003130      CLR      KTENABLE  ; INIT TEST >28K FLAG
2870 021054      023727 002120 001577  CMP      LSHIME,#1577 ; GOT ENOUGH MEMORY (>28K)?
2871 021062      101444      9$      BLOS     9$         ; NO.
2872 021064      013700 000004      MOV      @#ERRVEC,R0 ; SAVE OLD ERR VEC PTR.
2873 021070      012737 021162 000004  MOV      #2$,@#ERRVEC ; SET ERR VEC PTR.
2874 021076      005737 177572      TST      @#SRO      ; GOT KT11?
2875 021102      000240      NOP      ; (TRAP IF NO).
2876 021104      013737 002120 003126  MOV      LSHIME,KTFLG ; YES. SET KT FLAG.
2877 021112      042737 000177 003126  BIC      #177,KTFLG
2878 021120      010037 000004      MOV      R0,@#ERRVEC ; RESTORE OLD ERR VEC PTR.
2879 021124      005000      CLR      R0         ; R0 = AR DATA.
2880 021126      012701 172340      MOV      #KIPAR0,R1 ; R1 = KI REGS PTR.
2881 021132      012761 077406 177740 1$:  MOV      #77406,-40(R1) ; SET DESCRIPTOR REG.
2882 021140      010021      MOV      R0,(R1)+  ; SET KIPAR REG.
2883 021142      062700 000200      ADD      #200,R0    ; BUMP AR DATA BY "4k".
2884 021146      020027 002000      CMP      R0,#2000  ; AT "I/O"?
2885 021152      001367      BNE     1$         ; NO.
2886 021154      012741 177600      MOV      #177600,-(R1) ; YES. SET KTPAR7 FOR I/O.
2887 021160      000405      BR      9$
2888
2889 021162      012716 021170      2$:  MOV      #6$,(SP)  ; SET LIP RETURN
2890 021166      000002      RTI     ; RTI TO NEXT LOCATION
2891
2892 021170      010037 000004      6$:  MOV      R0,@#ERRVEC ; RESTORE OLD ERR VEC PTR.
2893
2894 021174      000207      9$:  RTS      PC
2895
    
```



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2910 021176
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2912 021176 005737 002224
2913 021202 001020
2914 021204 012737 100206 021250
2915 021212 012737 021260 021252
2916 021220 012737 000006 021256
2917 021226 012737 100010 021260
2918 021234 012704 021250
2919 021240 004737 010652
2920 021244 000207
2921
2922
2923
2924
2925 021250
2926
2927 021250 000000
2928 021252 000000
2929 021254 000000
2930 021256 000000
2931
2932
2933
2934
2935 021260 000000
2936 021262 000000
2937 021264 000000
2938
2939
    
```

:+  
 : SUBROUTINE TO SET EXTENDED FEATURES SWITCH  
 : Requires that SOFINIT and WRTCHR have been done previous to call.  
 :-  
 : INPUTS:  
 : R5 CURRENT UNIT NUMBER  
 : OUTPUTS:  
 : The Extended Features Switch is set.  
 :-  
 INVERT::  
 TST EXTFEA ; IS SWITCH SET?  
 BNE 1\$ ; YES,EXIT STAGE RIGHT!(or the next one outa town!)  
 MOV #100206,CMDPKT ; WRT SUB-SYS MEM CMD  
 MOV #WSMBK,CMDPKT+2 ; MSG BUF ADDR  
 MOV #6,CMDPKT+6 ; BYTE COUNT  
 MOV #100010,WSMBK ; INVERT THE SWITCH  
 MOV #CMDPKT,R4 ; SET CMDPKT INTO R4  
 JSR PC,WRTCHR ; DO IT  
 RTS PC ; RETURN  
 1\$:  
 : COMMAND PACKET.  
 . = <.+3>&177774 ;MUST BE ON MOD 4 BOUNDRY.  
 CMDPKT:: 0 ;1ST WORD IS TS05 COMMAND.  
 0 ;2ND WORD IS THE BUFFER LOW ADDRESS.  
 0 ;3RD WORD IS THE BUFFER HIGH ADDRESS.  
 0 ;4TH WORD IS THE BYTE/RECORD/FILE COUNT.  
 : WRITE SUB-SYSTEM MEMORY CHARACTERISTIC BLOCK.  
 WSMBK:: 0 ;1ST WORD:: SEL 0  
 0 ;2ND WORD:: SEL 2  
 0 ;3RD WORD:: SEL 4  
 .EVEN

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2951 021266
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2953 021266
2954 021272 005037 003132
2955 021276 005037 003134
2956 021302 005037 003136
2957 021306 005737 003142
2958 021312 001407
2959 021314 023727 002120 007777
2960 021322 103406
2961 021324 004737 021442
2962 021330 000427
2963 021332 005737 003140 1$:
2964 021336 001413
2965 021340 023727 002120 005777 2$:
2966 021346 101023
2967 021350 023727 002120 003777
2968 021356 103403
2969 021360 004737 021442
2970 021364 000411
2971 021366 023727 002120 001577 4$:
2972 021374 103410
2973 021376 004737 021442
2974 021402 062737 000077 003136
2975 021410 005237 003132 13$:
2976 021414 000411
2977 021416 000410 14$:
2978 021420
    021420 012746 005454
    021424 012746 000001
    021430 010600
    021432 104417
    021434 062706 000004
2979 021440 000207 15$:
    021440 000207

    SAVREG
    CLR NXMFLG
    CLR NXMLO
    CLR NXMHI
    TST T23B
    BEQ 1$
    CMP L$HIME,#7777
    BLO 2$
    JSR PC,NXMTST
    BR 13$
    TST T23A
    BEQ 4$
    CMP L$HIME,#5777
    BHI 14$
    CMP L$HIME,#3777
    BLO 4$
    JSR PC,NXMTST
    BR 13$
    CMP L$HIME,#1577
    BLO 14$
    JSR PC,NXMTST
    ADD #77,NXMHI
    INC NXMFLG
    BR 15$
    PRINTF #NOMEM
    MOV #NOMEM,-(SP)
    MOV #1,-(SP)
    MOV SP,R0
    TRAP C$PNTF
    ADD #4,SP
    RTS PC

    :+
    SUBROUTINE TO CHECK WETHER OR NOT WE'LL TEST NXM
    :INPUTS:
    :OUTPUTS:
    The NXMFLG is set if we can test.
    The NXMLO and NXMHI addresses are setup.

    MEMCK::
    :SAVE THE REGISTERS
    :CLEAR THE FLAG
    :CLEAR THE TEST ADDRESS LO
    :CLEAR THE TEST ADDRESS HI
    :IS IT A 11/23B?
    :NO
    : GREATER THAN 128K
    : NO
    :SETUP THE ADDRESS
    :SET THE FLAG AND EXIT
    :IS IT A 11/23A?
    :NO
    :GREATER THAN 96K
    :YES,23A/23B WITH 128K MEMORY
    :GREATER THAN 64K BUT LESS THAN 92K?
    :NO, CHECK 24K
    :SETUP THE ADDRESS.
    :SET THE FLAG AND EXIT
    :GREATER THAN 24K BUT LESS THAN 64K?
    :NO, TELL THEM AND EXIT WITH FLAG CLEAR
    :SETUP THE ADDRESS
    :FOOL THE 11/02 & 11/03
    :SET THE FLAG
    :EXIT
    :NOP FOR PRINTOUT
    :TELL THEM & EXIT ***NO PRINT*****

    :RETURN
    
```

```

2982
2983
2984
2985
2986
2987
2988
2989 021442 013701 002120
2990 021446 062701 000200
2991 021452 042701 000177
2992 021456 010102

    :+
    SUBROUTINE TO SETUP THE NXM ADDRESS FOR TESTING
    :OUTPUTS:NXMLO,NXMHI
    :SETUP WITH NXM ADDRESS

    :
    :
    :
    NXMTST: MOV L$HIME,R1
    ADD #200,R1
    BIC #177,R1
    MOV R1,R2

    :GET TOP OF MEMORY
    :MAKE IT I/O BLOCK OR OTHER NXM
    :RESAVE RESULTS
    
```



2993		000006		.REPT	6	
2994				ASL	R1	:PUT IN PLACE FOR XFER
2995				.ENDR		
2996	021474	010137	003134	MOV	R1,NXMLO	:SAVE TEST ADDRESS LOW
2997		000012		.REPT	10.	
2998				ASR	R2	:PUT IN PLACE FOR XFER
2999				.ENDR		
3000	021524	042702	1777C0	BIC	#177700,R2	:DON'T WANT ILA!
3001	021530	010237	003136	MOV	R2,NXMHI	:SAVE TEST ADDRESS HIGH
3002	021534	000207		RTS	PC	:RETURN
3003						
3004						
3005						
3006						
3007	021536			ENDMOD		

7  
8  
9 021536  
021536  
10  
16

.TITLE TSV4 - MISCELLANEOUS SECTIONS  
BGNMOD TSV4  
TSV4::



18  
19 021536  
   021536  
20 021536  
21 021546  
22

177777 177777 177777

          .SBTTL PROTECTION TABLE  
          BGNPROT  
LSPROT::  
          .WORD -1, -1, -1, -1  
          ENDPROT

;NO DEVICE PROTECTION REQUIRED.

.SBTTL INITIALIZE SECTION

```

    :++
    :THE INITIALIZE SECTION CONTAINS THE CODING THAT IS PERFORMED
    :AT THE BEGINNING OF EACH PASS.
    :
    :IF "START" OR "RESTART", SET QUICK-PASS FLAG AND BUS-INIT.
    :IF "CONTINUE", NOTHING IS REQUIRED.
    :
    :--
    :+
    :INSERT TEMPORARY JUMP TO ODT
    :--
    
```

BGNINIT

L\$INIT::

40\$:

```

    CLR     EXTFEA
    CLR     NXMFLG
    MOV     #EPR1,EPR1SW      ;SET UP PRIMARY MESSAGE FOR REPLACEMENT
    CLR     SIFLAG           ;CLEAR "SOFT INIT" FLAG
    CLR     KTENABLE         ;CLEAR TEST ABOVE 28K FLAG
    CLR     RAMSIZ           ;CLEAR RAM SIZE FOR RAMERR ROUTINE
    READEF #EF.CONTINUE
    MOV     #EF.CONTINUE,R0
    TRAP   CSREFG
    BNCOMPLETE 1$
    BCC     1$
    CMP     UNITN,L$UNIT      ;UNIT IN RANGE?
    BHIS   4$                ;BR IF NO.
    TST    DUFLG             ;DROPPED UNIT?
    BMI    NXTU              ;BR IF YES
    MOV     UNITN,R1
    ASL    R1
    TST    ERTABL(R1)
    BEQ    SETU
    BIT    #BIT14,ERTABL(R1) ;DROPPED?
    BNE    NXTU
    EXIT   INIT              ;DO NOTHING IF "CONTINUE".
    TRAP   C$EXIT
    .WORD  L10030-.
    READEF #EF.NEW
    MOV     #EF.NEW,R0
    TRAP   CSREFG
    BNCOMPLETE NXTU         ;TAKE NEXT UNIT IF NOT NEW PASS.
    BCC    NXTU
    READEF #EF.START
    MOV     #EF.START,R0
    TRAP   CSREFG
    BCOMPLETE 2$
    BCS    2$
    READEF #EF.RESTART
    MOV     #EF.RESTART,R0
    TRAP   CSREFG
    BNCOMPLETE 31$
    BCC    31$
    
```

1\$:

2\$:

```

    BRESET
    TRAP   C$RESET          ;1ST PASS, BUS-INIT...
                                ;BUS RESET.
    
```

```

24
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37 021546
   021546
38 021546 005037 002224
39 021552 005037 003132
40 021556 012737 006354 002176
41 021564 005037 003150
42 021570 005037 003130
43 021574 005037 002300
44 021600
   021600 012700 000036
   021604 104447
45 021606
   021606 103023
46 021610 023737 002200 002012
47 021616 103070
48 021620 005737 003106
49 021624 100472
50 021626 013701 002200
51 021632 006301
52 021634 005761 003172
53 021640 001516
54 021642 032761 040000 003172
55 021650 001060
56 021652
   021652 104432
   021654 000416
57 021656
   021656 012700 000035
   021662 104447
58 021664
   021664 103052
59 021666
   021666 012700 000040
   021672 104447
60 021674
   021674 103404
61 021676
   021676 012700 000037
   021702 104447
62 021704
   021704 103031
63 021706
64 021706
   021706 104433
    
```



```

65 021710 005037 002212          CLR      TSTCNT          :NUMBER OF TESTS RUN IN PASS
66 021714 005037 002220          CLR      FATFLG         :CLEAR FATAL ERROR COUNT
67 021720 005037 003140          CLR      T23A           :CLEAR 11/23A FLAG
68 021724 005037 003142          CLR      T23B           :CLEAR 11/23B FLAG
69                                     :
70                                     :
71                                     :
72 021730 005037 003374          CLR      SKIPT          :CLEAR THE SUBTEST "SKIPPER"
73 021734                                     20$:
74 021734 012737 177777 002202  MOV      #-1,QVP        :...QUICK VERIFY...
75 021742 004737 020620          JSR      PC,ENVIRN      :SET ENVIRONMENT.
76 021746 004737 021044          JSR      PC,KTINIT     :INITIALIZE KT MEMORY MANAGEMENT
77 021752 012700 003172          MOV      #ERTABL,RO
78 021756 005020 30$:          CLR      (RO)+         :CLEAR THE ERROR TABLE
79 021760 020027 003372          CMP      RO,#ERTABE
80 021764 103774          BLO     30$
81 021766 000404          BR      4$
82 021770 005037 002202 31$:          CLR      QVP
83 021774 000137 022044          JMP      PASRPT        :GO REPORT THE STATUS
84
85 022000 4$:
86 022000 012737 177777 002200  NEWPAS: MOV      #-1,UNITN      :INIT UNIT NUMBER...
87 022006 005037 002216          CLR      DEVCNT        :CLEAR COUNT OF DEVICES RUNNING
88 022012 104422          NXTU:  BREAK
89 022014 005237 002200          TRAP    CSBRK
90 022020 023737 002200 002012  INC      UNITN
91 022026 103423          CMP     UNITN,LSUNIT   :...AND SET NEXT UNIT NUMBER.
92 022030 012737 177777 003106  BLO     SETU
93 022036 000401          MOV     #-1,DUFLG
94 022040          BR      11$
95 022042 104444          DOCLN  CSDCLN         :ABORT, NO MORE UNITS.
96 022044 000240          TRAP   CSDCLN
97 022044 023727 002012 000001 11$:          NOP
98 022052 101752          PASRPT: CMP     LSUNIT,#1     :HOW MANY UNITS SELECTED?
99 022054 005737 002216          BLOS   NEWPAS         :BR IF ONLY 1
100 022060 001747          TST    DEVCNT         :ARE ANY STILL RUNNING?
101 022062          BEQ   NEWPAS         :BR IF NO
102 022064 104421          RFLAGS RO
103 022070 032700 000100          TRAP   CSRFLA
104          BIT   #ISR,RO
105          BNE  NEWPAS         :SHOULD WE PRINT STATISTICS
106          :BR IF NO
107 022072          DORPT
108 022072 104424          TRAP   CSDRPT
109 022074 000741          BR     NEWPAS
110          10$:
111 022076          SETU: GPHARD UNITN,RO   :GET UNIT N P-TABLE POINTER.
112 022076 013700 002200          MOV    UNITN,RO
113 022102 104442          TRAP   CSGPHRD
114 022104          BNCOMPLETE NXTU     :BR IF UNIT NOT AVAILABLE.
115 022104 103342          BCC   NXTU
116 022106 005037 003106          CLR   DUFLG          :CLEAR "DROPPED" FLAG.
117 022112 005237 002216          INC   DEVCNT
118 022116 012001          MOV   (RO)+,R1
119 022120 010137 002204          MOV   R1,CSRADDR     :GET 1ST REGISTER ADDRESS.
                                     :ADDRESS OF REGISTERS OF UNIT UNDER TEST

```

```

115
116 022124 012001      MOV      (R0)+,R1      ;GET VECTOR ADDRESS.
117                   ;MOV      (R0),R2      ;GET INTERRUPT PRIORITY
118                   ;MOV      R2,IPRI    ;SET INTERRUPT PRIORITY.
119 022126 010137 002206  MOV      R1,IVEC      ;SET INTERRUPT VECTOR POINTER...
120 022132 012721 016206  MOV      #INTR,(R1)+  ;...VECTOR...
121 022136 013721 002210  MOV      IPRI,(R1)+   ;...AND PRIORITY.
122
123 022142             1$:
124                   ;
125                   ; TST      QVP      ;1ST PASS ??
126                   ; BEQ      5$      ;NO, SKIP THE PASS 1 STUFF.
127
128                   ;
129                   ;1ST PASS, CHECK THAT DEVICE ADDRESSES ARE VALID, AND
130                   ;THAT THE DISPLAY STATUS IS PROPERLY INITIALIZED.
131
131 022142 013701 002200      MOV      UNITN,R1
132 022146 006301            ASL      R1
133 022150 052761 100000 003172  BIS      #BIT15,ERTABL(R1) ;SAY DEVICE RUNNING
134 022156 005037 005766      CLR      EXTA          ;CLEAR ERROR EXTENSION FLAG.
135 022162 023727 002012 000001  CMP      LSUNIT,#1     ;ARE WE TESTING MULTIPLE UNITS?
136 022170 101416            BLOS    10$           ;BR IF NO.
137 022172            RFLAGS  R0          ;YES -- GET OPERATOR FLAGS.
138 022172 104421            TRAP   CSRFLA
138 022174 032700 001000      BIT      #PNT,R0      ;SHOULD WE PRINT UNIT #?
139 022200 001412            BEQ      10$           ;BR IF NOT.
140 022202            PRINTF #PUNIT,UNITN ;PRINT THE UNIT #
141 022202 013746 002200      MOV      UNITN,-(SP)
142 022206 012746 022274      MOV      #PUNIT,-(SP)
143 022212 012746 000002      MOV      #2,-(SP)
144 022216 010600            MOV      SP,R0
145 022220 104417            TRAP   C$PNTF
146 022222 062706 000006      ADD      #6,SP
147
141 022226             10$:
142 022226 005037 003110      CLR      NODEV
143 022232 013701 002204      MOV      CSRADDR,R1  ;ADDRESS OF FIRST REGISTER
144 022236 010102            MOV      R1,R2        ;START OF REGISTERS
145 022240 062702 000002      ADD      #TSSR,R2    ;ADDRESS OF TSSR REGISTER
146 022244 004737 016366      JSR      PC,XNXM     ;TEST BOTH CONTROLLER REGISTERS...
147 022250 103005            BCC     2$           ;...AND BR IF ALL OK.
148 022252 010137 003110      MOV      R1,NODEV    ;FLAG DEVICE AS NON-EXISTENT
149 022256 012737 177777 003106  MOV      #-1,DUFLG   ;DROP THIS UNIT.
150 022264
151
152                   ;
153                   ;FINALLY, SET CPU PRIORITY AND WE'RE DONE.
154
154 022264             5$:
155 022264 012700 000000      SETPRI  #PRI00        ;ENABLE INTERRUPTS.
156 022270 104441            MOV      #PRI00,R0
157 022272            TRAP   C$SPRI
158 022272            ENDINIT
159
155 022272            L10030:
160 022272 104411            TRAP   C$INIT
161
156
157 022274 045 116 045 PUNIT: .ASCIZ /%N%N%A***** TESTING UNIT %D2%A *****/
158 .EVEN

```



```

160
161
162
163
164
165
166
167 022342
    022342
168 022342 010001
169 022344 006301
170 022346 052761 100000 003172
171 022354 042761 040000 003172
172 022362
    022362 010046
    022364 012746 022410
    022370 012746 000002
    022374 010600
    022376 104417
    022400 062706 000006
173 022404
    022404 000167
    022406 000026
174 022410 045 116 045 1$:
175
176
177 022436
    022436
    022436- 104452
178
179
180
181
182
183
184
185
186
187
188
189 022440
    022440
190 022440 012737 177777 003106
191 022446 010001
192 022450 006301
193 022452 052761 140000 003172
194 022460 000240 000240 000240
195 022466
    022466 010046
    022470 012746 022514
    022474 012746 000002
    022500 010600
    022502 104417
    022504 062706 000006
196 022510
    022510 000167
    022512 000030

```

.SBTTL ADD AND DROP UNITS SECTIONS

```

:++
: THE ADD-UNIT SECTION CONTAINS THE CODING THAT CAUSES A DEVICE
: TO BE (A) ADDED TO THE TEST LIST FOR THE FIRST TIME,
: OR (B) RE-INSERTED IF IT HAD BEEN PREVIOUSLY DROPPED.
:--

```

```

      BGNU
LSAU::
      MOV      R0,R1          ; GET UNIT TO BE ADDED (R0)
      ASL      R1             ; MAKE IT A WORD INDEX
      BIS      #100000,ERTABL(R1) ; SET THE "ACTIVE" BIT
      BIC      #40000,ERTABL(R1) ; CLEAR THE "DROPPED" BIT
      PRINTF   #1$,R0
      MOV      R0,-(SP)
      MOV      #1$,-(SP)
      MOV      #2,-(SP)
      MOV      SP,R0
      TRAP     C$PNTF
      ADD      #6,SP
      EXIT     AU
      .WORD    JSJMP
      .WORD    L10031-2-.
      .ASCIZ   /%NZA UNIT %DZA ADDED/
      .EVEN

```

```

      ENDAU          ; UNUSED.
L10031:
      TRAP     C$AU

```

```

:++
: THE DROP-UNIT SECTION CONTAINS THE CODING THAT CAUSES A DEVICE
: TO BE REMOVED FROM THE TEST LIST.
:
: SUPVSR DOES THE "DROPPING". THIS IS JUST TO TELL THE MAN.
: "DROPPED" UNITS ARE RE-SELECTED ON OPERATOR "STA" OR "ADD"
: COMMAND, OTHERWISE REMAIN INACTIVE. THE "DISPLAY" COMMAND
: WILL PRINT ALL DROPPED UNITS, AND THE P-TABLES OF THOSE
: WHICH ARE STILL ACTIVE.
: UPON ENTRY, R0 CONTAINS THE UNIT TO BE DROPPED.

```

```

      BGNU
LSDU::
      MOV      #-1,DUFLG
      MOV      R0,R1
      ASL      R1
      BIS      #140000,ERTABL(R1) ; SAY DROPPED
      240,240,240 ; ??????????
      PRINTF   #1$,R0
      MOV      R0,-(SP)
      MOV      #1$,-(SP)
      MOV      #2,-(SP)
      MOV      SP,R0
      TRAP     C$PNTF
      ADD      #6,SP
      EXIT     DU
      .WORD    JSJMP
      .WORD    L10032-2-.

```

197	022514	045	116	045	1\$:	.ASCIZ	/%NZA	UNIT	%DZA	DROPPED/	
198						.EVEN					
199	022544					ENDDU					
	022544				L10032:						
	022544	104453				TRAP	C\$DU				
200						;++					
201						: AUTO-DROP CODE SECTION.					
202						:--					
203	022546					BGNAUTO					
	022546				LSAUTO::						
204	022546	013705	002204			MOV	CSRADDR,R5			:POINT TO DEVICE REGISTER	
205	022552	012703	000550			MOV	#360.,R3			:ENOUGH TIME FOR 2400' REEL TO REWIND	
206	022556	004737	016240		10\$:	JSR	PC,WAITF			:WAIT FOR SSR TO SET	
207	022562	103420				BCS	20\$			:LEAVE WHEN SSR IS SET	
208	022564					DELAY	250.			:WAIT FOR .25 SECONDS	
	022564	012727	000372			MOV	#250.,(PC)+				
	022570	000000				.WORD	0				
	022572	013727	002116			MOV	L\$DLY,(PC)+				
	022576	000000				.WORD	0				
	022600	005367	177772			DEC	-6(PC)				
	022604	001375				BNE	.-4				
	022606	005367	177756			DEC	-22(PC)				
	022612	001367				BNE	.-20				
209	022614	005303				DEC	R3			:BUMP COUNTER DOWN	
210	022616	001357				BNE	10\$			:KEEP GOING	
211	022620	004737	017172			JSR	PC,CKDROP			:TRY AND DROP UNIT	
212	022624				20\$:						
213	022624					ENDAUTO				: UNUSED.	
	022624				L10033:						
	022624	104461				TRAP	C\$AUTO				



```

215                                     .SBTTL CLEAN-UP AND REPORT CODING SECTIONS
216
217
218                                     :++
219                                     : THE CLEANUP CODING SECTION CONTAINS THE CODING THAT IS
220                                     : EXECUTED AT THE END OF EACH PASS (OR SUB-PASS).
221                                     : USE TO RETURN DEVICE UNDER TEST TO A NEUTRAL STATE.
222                                     :--
222 022626                                BGNCLN
222 022626                                L$CLEAN::
223 022626 013705 002204                    MOV     CSRADDR,R5                ;POINT TO DEVICE REGISTER
224 022632 005737 003106                    TST     DUFLG                    ;'DROPPED' FLAG IS SET ON...
225 022636 100405                            BMI     1$                       ;...AND GROSS CONTROLLER FAULT...
226                                     ;...DON'T TRY TO XCT CLEANUP CODE.
227
228 022640 012765 000000 000002            MOV     #0,TSSR(R5)              ;DO SOFT INIT
229 022646 004737 016240                    JSR     PC,WAITF
230 022652
231 022652                                1$:
231 022652                                2$:
231 022652                                L10034:
231 022652 104412                            TRAP   C$CLEAN
232
233                                     :++
234                                     : THE REPORT CODING SECTION CONTAINS THE
235                                     : 'PRINTS' CALLS THAT GENERATE STATISTICAL REPORTS.
236                                     :--
236 022654                                BGNRPT
236 022654                                L$RPT::
237 022654                                PRINTS #DEVSUM
237 022654 012746 023116                    MOV     #DEVSUM,-(SP)
237 022660 012746 000001                    MOV     #1,-(SP)
237 022664 010600                            MOV     SP,R0
237 022666 104416                            TRAP   C$PNTS
237 022670 062706 000004                    ADD     #4,SP
238 022674 010246                            MOV     R2,-(SP)
239 022676 010346                            MOV     R3,-(SP)
240 022700 010446                            MOV     R4,-(SP)
241 022702 012704 003172                    MOV     #ERTABL,R4              ; GET START OF ERROR TABLE.
242 022706 005003                            CLR     R3                      ; CLEAR UNIT NUMBER
243 022710 011402                                1$:
243 022710 011402                            MOV     (R4),R2                ; GET ERROR TABLE ENTRY & TEST IT.
244 022712 001467                            BEQ     4$                      ; ZERO IF UNIT NOT RUN
245 022714 100066                            BPL     4$
246 022716 032702 040000                    BIT     #BIT14,R2              ; WAS UNIT DROPPED?
247 022722 001015                            BNE     2$                      ; BR IF YES
248 022724 042702 170000                    BIC     #^C7777,R2            ; GET ERROR COUNT FIELD
249 022730                                PRINTS #DEVONL,R3,R2          ; PRINT
249 022730 010246                            MOV     R2,-(SP)
249 022732 010346                            MOV     R3,-(SP)
249 022734 012746 023153                    MOV     #DEVONL,-(SP)
249 022740 012746 000003                    MOV     #3,-(SP)
249 022744 010600                            MOV     SP,R0
249 022746 104416                            TRAP   C$PNTS
249 022750 062706 000010                    ADD     #10,SP
250 022754 000446                            BR      4$
251 022756 020227 160000                                2$:
251 022756 020227 160000                    CMP     R2,#160000            ; WAS UNIT NON-EXISTENT?
252 022762 001012                            BNE     3$                      ; BR IF NO
253 022764                                PRINTS #DEVNXR,R3
253 022764 010346                            MOV     R3,-(SP)
253 022766 012746 023223                    MOV     #DEVNXR,-(SP)
    
```

```

022772 012746 000002      MOV      #2,-(SP)
022776 010600      MOV      SP,R0
023000 104416      TRAP     C$PNTS
023002 062706 000006      ADD      #6,SP
254 023006 000431      BR       4$
255 023010 020227 160001      3$:     CMP      R2,#160001      ; WAS UNIT NOT READY AT STARTUP?
256 023014 001012      BNE     30$      ; BR IF NO.
257 023016      PRINTS  #DEVNRD,R3
023016 010346      MOV      R3,-(SP)
023020 012746 023305      MOV      #DEVNR,-(SP)
023024 012746 000002      MOV      #2,-(SP)
023030 010600      MOV      SP,R0
023032 104416      TRAP     C$PNTS
023034 062706 000006      ADD      #6,SP
258 023040 000414      BR       4$
259 023042 042702 170000      30$:    BIC      #^C7777,R2
260 023046      PRINTS  #DEVDRD,R3,R2
023046 010246      MOV      R2,-(SP)
023050 010346      MOV      R3,-(SP)
023052 012746 023366      MOV      #DEVDRD,-(SP)
023056 012746 000003      MOV      #3,-(SP)
023062 010600      MOV      SP,R0
023064 104416      TRAP     C$PNTS
023066 062706 000010      ADD      #10,SP
261 023072 062704 000002      4$:     ADD      #2,R4
262 023076 005203      INC      R3
263 023100 020427 003372      CMP      R4,#ERTABE
264 023104 103701      BLO     1$
265 023106 012604      MOV      (SP)+,R4
266 023110 012603      MOV      (SP)+,R3
267 023112 012602      MOV      (SP)+,R2
268 023114      ENDRPT      ; UNUSED.
023114      L10035:
023114 104425      TRAP     C$RPT
269
270
271 023116      045      116      045  DEVSUM: .ASCIZ  /%N%ADEVICE STATUS SUMMARY:%N/
272 023153      045      101      040  DEVONL: .ASCIZ  /%A UNIT %D3%A ONLINE, ERRORS = %D%N/
273 023223      045      101      040  DEVNXR: .ASCIZ  /%A UNIT %D3%A DROPPED, NON-EXISTENT REGISTER%N/
274 023305      045      101      040  DEVNRD: .ASCIZ  /%A UNIT %D3%A DROPPED, NOT READY AT STARTUP%N/
275 023366      045      101      040  DEVDRD: .ASCIZ  /%A UNIT %D3%A DROPPED, ERRORS = %D%N/
276      .EVEN
277
278 023436      ENDMOD
279
280

```



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24

.TITLE TSV5A - HARDWARE TESTS

TSV5:: BGNMOD TSV5

023436  
023436





```
83 023500 052702 002200      BIS      #SSR!NBA,R2      ;READY AND NEW DATA SHOULD BE SET
84 023504 020102              CMP      R1,R2        ;COMPARE EXPECTED TO RECEIVED
85 023506 001405              BEQ     10$           ;BRANCH IF COMPARE
89 023510              ERRDF  ERRNO,SFHERR,SFFMSG ;REPORT A FATAL ERROR
    023510 104455              TRAP    C$ERDF
    023512 000145              .WORD  101
    023514 003701              .WORD  SFHERR
    023516 012072              .WORD  SFFMSG
90 023520 005203              INC     R3            ;SET THE FATAL ERROR FLAG
91 023522              10$:
92 023522              ENDSUB          ;////////////////// END SUBTEST ////////////////////
    023522              L10037:
93 023522 104403              TRAP    C$ESUB
```

```
95 023524 005703          TST      R3          ;DID WE HAVE FATAL ERROR ?
96 023526 001402          BEQ      20$         ;BRANCH IF NOT
97 023530 004737 017172   JSR      PC,CKDROP  ;GO DROP THIS UNIT, IF ALLOWED
98 023534 005003          CLR      R3          ;RESET FATAL ERROR FLAG
99
100
101 023536          BGNSUB          ;//////////////// BEGIN SUBTEST //////////////////
    023536          T1.2:          TRAP      CSBSUB
    023536 104402
102
103 023540 005065 000002   CLR      TSSR(R5)   ;WRITE TO ISSUE A SOFT RESET
104 023544 004737 016240   JSR      PC,WAITF   ;WAIT FOR READY TO SET
105 023550 016501 000002   MOV      TSSR(R5),R1 ;GET REGISTER TSSR DATA
106 023554 010102          MOV      R1,R2      ;CONTENTS OF TSSR
107 023556 042702 176277   BIC      #^C<HIADDR!OFL>,R2 ;THESE BITS MAY BE SET
108 023562 052702 002200   BIS      #SSR!NBA,R2 ;READY AND NEW DATA SHOULD BE SET
109 023566 020102          CMP      R1,R2      ;COMPARE EXPECTED TO RECEIVED
110 023570 001405          BEQ      10$        ;BRANCH IF COMPARE
114 023572          ERRDF  ERRNO,SFIERR,SFFMSG ;REPORT A FATAL ERROR
    023572 104455          TRAP      CSERDF
    023574 000146          .WORD    102
    023576 003646          .WORD    SFIERR
    023600 012072          .WORD    SFFMSG
115 023602 005203          INC      R3          ;SET THE ERROR FLAG
116 023604          10$:          ENDSUB          ;//////////////// END SUBTEST //////////////////
117 023604          L10040:        TRAP      CSESUB
    023604 104403
118
119
120 023606 005703          TST      R3          ;FATAL ERROR DETECTED ?
121 023610 001402          BEQ      20$         ;BRANCH IF NOT
122 023612 004737 017172   JSR      PC,CKDROP  ;SEE IF TIME TO DROP UNIT
123 023616 004737 016446   JSR      PC,TSTLOOP ;SHOULD WE DO ITERATIONS ?
124 023622 103002          BCC      40$        ;BRANCH IF NOT
125 023624 000137 023454   JMP      T1LOOP     ;LOOP UNTIL COUNT EXPIRED
126 023630          40$:          EXIT      TST      ;ALL DONE THIS TEST
    023630 104432          TRAP      CSEXIT
    023632 000022          .WORD    L10036-.
127
128
129          ;+
130          ;LOCAL TEXT MESSAGES FOR TEST
131          ;-
132 023634          111      156      151  TST1ID: .ASCIZ 'Initialization'
133          .EVEN
134 023654          ENDTST
    023654          L10036:        TRAP      CSETST
    023654 104401
135
136
```















024454	104455						TRAP	C\$ERDF
024456	000456						.WORD	302
024460	024746						.WORD	T3SSR
024462	015464						.WORD	EXPREC
325	024464	005203		INC	R3			
326	024466	005237	002220	INC	FATFLG			
327	024472			15\$:	CKLOOP			
	024472	104406						
328	024474	005737	002220	TST	FATFLG		TRAP	C\$CLP1
329	024500	001402		BEQ	20\$			
330	024502	004737	017172	JSR	PC,CKDROP			
331	024506	010402		20\$:	MOV	R4,R2		
332	024510	042702	177774	BIC	#^C<BIT0!BIT1>,R2			
333	024514	000302		SWAB	R2			
334	024516	052702	002200	BIS	#SSR!NBA,R2			
335	024522	016501	000002	MOV	TSSR(R5),R1			
336	024526	032701	000100	BIT	#OFL,R1			
337	024532	001402		BEQ	25\$			
338	024534	052702	000100	25\$:	BIS	#OFL,R2		
339	024540	020201		CMP	R2,R1			
340	024542	001405		BEQ	30\$			
344	024544			ERRHRD	ERRNO,T3TSSR,EXPREC			
	024544	104456						
	024546	000457					TRAP	C\$ERHRD
	024550	024702					.WORD	303
	024552	015464					.WORD	T3TSSR
345	024554	005203		30\$:	INC	R3	.WORD	EXPREC
346	024556			CKLOOP				
	024556	104406						
347	024560	016501	000000	MOV	TSBA(R5),R1		TRAP	C\$CLP1
348	024564	005002		CLR	R2			
349	024566	150402		BISB	R4,R2			
350	024570	000302		SWAB	R2			
351	024572	150402		BISB	R4,R2			
352	024574	020102		CMP	R1,R2			
353	024576	001405		BEQ	35\$			
357	024600			ERRHRD	ERRNO,T3TSBA,EXPREC			
	024600	104456						
	024602	000460					TRAP	C\$ERHRD
	024604	024636					.WORD	304
	024606	015464					.WORD	T3TSBA
358	024610	005203		35\$:	INC	R3	.WORD	EXPREC
359								
360	024612			ENDSEG				
	024612							
	024612	104405						
361								
362	024614	105204		INCB	R4			
363	024616	001270		BNE	5\$			
364	024620	004737	016446	JSR	PC,TSTLOOP			
365	024624	103002		BCC	40\$			
366	024626	000137	024372	40\$:	JMP	T3LOOP		
367	024632			EXIT	TST			
	024632	104432						
	024634	000210					TRAP	C\$EXIT
368							.WORD	L10042-
369				:+				

```
370          ;LOCAL TEXT MESSAGES FOR TEST
371          ;=
372
373 024636    124    123    102 T3TSBA: .ASCIZ 'TSBA Incorrect After TSDB Low Write'
374 024702    124    123    123 T3TSSR: .ASCIZ 'TSSR Incorrect After TSDB Low Write'
375 024746    116    157    040 T3SSR:  .ASCIZ 'No Sub-System Ready After TSDB Low Write'
376 025017    127    162    141 TST3ID: .ASCIZ 'Wrap Data - Low Byte'
377          .EVEN
378 025044          .ENDTST
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025044
025044 104401
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L10042: TRAP CSETST



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025046 104402  
025050 012700 026356  
025054 004737 016500  
025060 012737 000005 002214

.SBTTL TEST 4: RAM TEST

THIS TEST VERIFIES THAT ALL LOCATIONS OF THE RAM ON THE M7196 CAN PROPERLY STORE AND READ BACK ALL DATA PATTERNS, AND THAT EACH RAM LOCATION IS UNIQUELY ADDRESSED (I.E., THAT ONE AND ONLY ONE LOCATION IS ACCESSED BY ANY PARTICULAR ADDRESS). THESE TESTS ARE PERFORMED BY THREE SUBTESTS, DESCRIBED BELOW. A BYPRODUCT OF THESE TESTS IS A VERIFICATION OF TWO REGISTERS IN THE 2901 AND THE CAPABILITY OF THE 2901 TO CORRECTLY PERFORM AN ADD.

TEST 4 SUBTEST 1: -

THIS SUBTEST VERIFIES EACH RAM LOCATION BY FIRST PLACING THE M7196 INTO MAINTENANCE MODE BY WRITING INTO THE LOW BYTE OF TSDB AND THEN PERFORMING THE FOLLOWING SEQUENCE FOR EACH ADDRESS 0-7777 (OCTAL):

1. THE ADDRESS TO BE TESTED IS LOADED INTO THE TSDB (VIA A WORD WRITE).
2. THE ADDRESSED RAM LOCATION IS WRITTEN, THEN READ INTO THE LOW BYTE OF TSBA, BY WRITING A DATA BYTE INTO THE LOW BYTE OF TSDB.
3. THE LOW BYTE OF TSBA IS CHECKED TO SEE IF IT CONTAINS THE DATA PATTERN ORIGINALLY WRITTEN; A DISCREPANCY IS REPORTED AS AN ERROR.
4. THE ADDRESS OF THE LOCATION BEING TESTED IS AGAIN WRITTEN INTO TSDB (WORD WRITE), TO CAUSE THE LOCATION UNDER TEST TO AGAIN BE READ INTO THE LOW BYTE OF TSBA. THE LOW BYTE OF TSBA IS AGAIN CHECKED AND DISCREPANCIES REPORTED.
5. THE HIGH BYTE OF TSBA IS CHECKED; IT SHOULD CONTAIN THE SUM OF THE HIGH AND LOW BYTES LAST WRITTEN INTO TSDB AS A WORD. A DISCREPANCY IS REPORTED AS A 2901 PROBLEM.
6. THE CONTENT OF TSSR IS CHECKED; SETTING OF THE SC BIT IS IGNORED. OTHER DISCREPANCIES IN TSSR ARE REPORTED.

BGNTST

T4::

BGNSUB

:/: BEGIN SUBTEST /:

T4.1:

TRAP CSBSUB

MOV #TST4ID,R0  
JSR PC,TSTSETUP  
MOV #5,LOOPCNT

:ASCII MESSAGE TO IDENTIFY TEST  
:DO INITIAL TEST SETUP  
:PERFORM 5 ITERATIONS





```

489 025250 104406 000001          MOVB   TSBAH(R5),R1      ;HIGH BYTE READ OF TSBA   TRAP   C$CLP1
490 025252 116501          MOV    R4,R2           ;DATA PATTERN WRITTEN
491 025256 010402          SWAB  R2              ;HIGH TO LOW
492 025260 000302          ADD   R4,R2           ;TOTAL OF BYTES IN LOW BYTE
493 025262 060402          CMPB  R1,R2           ;SUM OF BYTES WRITTEN TO TSDB = TSBAH
494 025264 120102          BEQ   50$             ;BR IF OK, THEY SHOULD BE
498 025270 001404          ERRHRD ERRNO,M2901,EXPREC ;2901 PROBLEM ADDER
      025270 104456          TRAP  C$SERHRD
      025272 000624          .WORD 404
      025274 026124          .WORD M2901
      025276 015454          .WORD EXPREC
499 025300 104406          50$: CKLOOP          ;SCOPE LOOP
500 025302 005304          DEC   R4              TRAP   C$CLP1
501 025304 002342          BGE   40$             ;DROP DATA COUNTER (PATTERN)
502                                     ;NOT AT LOC. ZERO YET
503 025306          ENDSUB              ;////////////////// END SUBTEST ////////////////////
      025306          L10044:
504 025306 104403          TRAP  C$ESUB
    
```





```

559 025460          ERRHRD  ERRNO,TSBAM3,EXPREC      ;CHARACTERISTICS DATA NOT CORRECT
      025460 104456          TRAP  C$ERHRD
      025462 000627          .WORD  407
      025464 026276          .WORD  TSBAM3
      025466 015464          .WORD  EXPREC
560 025470 012702 000377      43$:  MOV  #000377,R2      ;SET ALL ONES WORD
561 025474 010465 000000      MOV  R4,TSDB(R5)      ;LOAD UP RAM ADDRESS POINTER
562 025500 004737 016326      JSR  PC,CHKTSSR      ;WAIT FOR READY, NON-AMBIGUOUS
563 025504 110265 000000      MOV  R2,TSDB(R5)      ;WRITE DATA INTO RAM
564 025510 004737 016326      JSR  PC,CHKTSSR      ;WAIT FOR READY, NON-AMBIGUOUS
565 025514 016501 000000      MOV  TSBA(R5),R1      ;READ RAM CONTENTS BACK
566 025520 120102          CMPB R1,R2          ;CHECK WITH DATA WRITTEN
567 025522 001404          BEQ  45$            ;BR IF OK, DATA IN = DATA OUT
571 025524          ERRHRD  ERRNO,TSBAM2,EXPREC      ;WRITTEN DATA NOT = TO READ
      025524 104456          TRAP  C$ERHRD
      025526 000630          .WORD  408
      025530 026214          .WORD  TSBAM2
      025532 015464          .WORD  EXPREC
572 025534          45$:  CKLOOP      ;SCOPE LOOP
      025534 104406          TRAP  C$CLP1
573 025536 004737 016326      JSR  PC,CHKTSSR      ;WAIT FOR READY, NON-AMBIGUOUS
574 025542 010465 000000      MOV  R4,TSDB(R5)      ;WORD WRITE TO SET UP ADDRESS
575 025546 004737 016326      JSR  PC,CHKTSSR      ;WAIT FOR READY, NON-AMBIGUOUS
576 025552 116501 000001      MOV  TSBAH(R5),R1      ;HIGH BYTE READ OF TSBA
577 025556 010403          MOV  R4,R3          ;DATA PATTERN WRITTEN
578 025560 000303          SWAB R3          ;HIGH TO LOW
579 025562 060403          ADD  R4,R3          ;TOTAL OF BYTES IN LOW BYTE
580 025564 120103          CMPB R1,R3          ;SUM OF BYTES WRITTEN TO TSDB = TSBAH
581 025566 001404          BEQ  50$            ;BR IF OK, THEY SHOULD BE
585 025570          ERRHRD  ERRNO,M2901,EXPREC      ;2901 PROBLEM ADDER
      025570 104456          TRAP  C$ERHRD
      025572 000631          .WORD  409
      025574 026124          .WORD  M2901
      025576 015464          .WORD  EXPREC
586 025600          50$:  CKLOOP      ;SCOPE LOOP
      025600 104406          TRAP  C$CLP1
587 025602 005304          DEC  R4          ;DROP RAM ADDRESS POINTER
588 025604 002312          BGE  40$          ;NOT AT LOC. ZERO YET
589
590 025606          ENDSUB      ;////////////////// END SUBTEST ////////////////////
      025606          L10045:
      025606 104403          TRAP  C$ESUB
591
    
```





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641 025760 016501 000000      MOV      TSBA(R5),R1      ;PICK UP RAM CONTENTS
642 025764 120102              CMPB     R1,R2           ;IS MEMORY STILL ALL ONES
643 025766 001404              BEQ     43$             ;BR, IF OK (ALL ONES)
647 025770              ERRHRD  ERRNO,TSBAM3,EXPREC ;MEMORY CHANGED AFTER ALL ONES WRITE
                                TRAP      C$ERHRD
                                .WORD    412
                                .WORD    TSBAM3
                                .WORD    EXPREC
648 026000 005002              43$:   CLR      R2           ;SET UP NEW EXPECTED
649 026002 010465 000000      MOV     R4,TSDB(R5)     ;LOAD UP RAM ADDRESS POINTER
650 026006 004737 016326      JSR    PC,CHKTSSR      ;WAIT FOR READY, NON-AMBIGUOUS
651 026012 110265 000000      MOVB   R2,TSDB(R5)     ;WRITE DATA INTO RAM
652 026016 004737 016326      JSR    PC,CHKTSSR      ;WAIT FOR READY, NON-AMBIGUOUS
653 026022 016501 000000      MOV     TSBA(R5),R1     ;READ RAM CONTENTS BACK
654 026026 120102              CMPB   R1,R2           ;CHECK WITH DATA WRITTEN
655 026030 001404              BEQ    45$             ;BR IF OK, DATA IN = DATA OUT
659 026032              ERRHRD  ERRNO,TSBAM2,EXPREC ;WRITTEN DATA NOT = TO READ
                                TRAP      C$ERHRD
                                .WORD    413
                                .WORD    TSBAM2
                                .WORD    EXPREC
660 026042              45$:   CKLOOP          ;SCOPE LOOP
                                TRAP      C$CLP1
661 026044 004737 016326      JSR    PC,CHKTSSR      ;WAIT FOR READY, NON-AMBIGUOUS
662 026050 116501 000001      MOVB   TSBAH(R5),R1    ;HIGH BYTE READ OF TSBA
663 026054 010203              MOV     R2,R3           ;DATA PATTERN WRITTEN
664 026056 000303              SWAB   R3              ;HIGH TO LOW
665 026060 060203              ADD    R2,R3           ;TOTAL OF BYTES IN LOW BYTE
666 026062 120103              CMPB   R1,R3           ;SUM OF BYTES WRITTEN TO TSDB = TSBAH
667 026064 001404              BEQ    50$             ;BR IF OK, THEY SHOULD BE
671 026066              ERRHRD  ERRNO,M2901,EXPREC ;2901 PROBLEM ADDER
                                TRAP      C$ERHRD
                                .WORD    414
                                .WORD    M2901
                                .WORD    EXPREC
672 026076              50$:   CKLOOP          ;SCOPE LOOP
                                TRAP      C$CLP1
673 026100 005304              DEC    R4              ;DROP RAM ADDRESS POINTER
674 026102 001315              BNE    40$            ;NOT AT LOC. ZERO YET
675
676 026104              ENDSUB                ;////////////////////// END SUBTEST ////////////////////////
                                L10046:
                                TRAP      C$ESUB
677
678 026106 004737 016446      JSR    PC,TSTLOOP      ;DO WE NEED TO ITERATE TEST ?
679 026112 103002              BCC    63$            ;BRANCH IF NOT
680 026114 000137 025066      JMP    T4LOOP          ;EXECUTE AGAIN
681 026120              63$:   EXIT      TST      ;ALL DONE THIS TEST
                                TRAP      C$EXIT
                                .WORD    L10043-.
682
683      ;+
684      ;LOCAL TEXT MESSAGES FOR TEST
685      ;-
686 026124      040      124      123  M2901: .ASCIZ ' TSBA High Byte Not Sum of Last TSDB Write (2901 Error)'
687 026214      040      127      162  TSBAM2: .ASCIZ ' Write to TSDB Not Equal to Read of TSBA Low Byte'
688 026276      127      162      151  TSBAM3: .ASCIZ 'Write To RAM Location Modified Another Location'

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689 026356 122 101 115 TST4ID: .ASCIZ 'RAM Verification'  
690 .EVEN  
691 026400 .ENDTST  
026400  
692 026400 104401

L10043: TRAP CSETST



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.SBTTL TEST 5: SECOND INITIALIZATION TEST

THIS TEST VERIFIES THE SAME ELEMENTS AS DID INITIALIZATION TEST #1 AND ALSO CHECKS THAT CERTAIN PARTS OF RAM IS CLEARED TO ZERO AND THAT 2901 REGISTERS 10 AND 11 ARE ALSO CLEARED TO ZERO. THIS IS A CONFIDENCE CHECK OF A PART OF THE SELF-TEST SEQUENCE (I.E., THAT IT IS REALLY BEING EXECUTED). FOR EACH OF TWO SUBTESTS (ONE FOR INITIALIZING VIA A BUS INIT, THE OTHER FOR INITIALIZING BY WRITING INTO THE TSSR), THE FOLLOWING SEQUENCE IS PERFORMED:

1. EACH RAM LOCATION AND 2901 REGISTERS 10 AND 11 ARE SET TO ALL 1'S BY USING WRITES INTO THE TSDB REGISTER (LOW BYTE AND MAINTENANCE MODE WORD WRITES).
2. THE CONTROLLER IS INITIALIZED AND THE VARIOUS CHECKS ON THE TSSR DESCRIBED IN INITIALIZATION TEST #1 ARE PERFORMED.
3. #1'S (377 OCTAL) ARE WRITTEN INTO THE LOW BYTE OF TSDB, WHICH SHOULD CAUSE RAM LOCATION 0 TO BE WRITTEN TO ALL 1'S SINCE 2901 REGISTERS 10 AND 11, SPECIFYING THE RAM ADDRESS, SHOULD BE 0. RAM LOCATION 0 IS VERIFIED BY WRITING A WORD OF ZEROS INTO THE TSDB. THE RESULTING LOW BYTE OF TSBA SHOULD CONTAIN ALL 1'S.
4. THE ENTIRE RAM IS SCANNED. LOCATION 0 SHOULD CONTAIN ALL 1'S AND THE REMAINING LOCATIONS, EXCEPT FOR THE MESSAGE BUFFER IMAGE AREA, SHOULD CONTAIN 0. DISCREPANCIES ARE REPORTED. AN ERROR AT THIS POINT IS MOST LIKELY DUE TO A ROM, PIPELINE OR SEQUENCER PROBLEM OR A TIMING PROBLEM.

```

731 026402          BGNTST
      026402
736 026402 012700 027354          MOV   #TST5ID,R0          T5::
737 026406 004737 016500          JSR   PC,TSTSETUP      :ASCII MESSAGE TO IDENTIFY TEST
738 026412 012737 000024 002214  MOV   #20.,LOOPCNT    :DO INITIAL TEST SETUP
739 026420          T5LOOP:    :PERFORM 20 ITERATIONS
740 026420 005037 002220          CLR   FATFLG          :CLEAR THE FATAL ERROR FLAG
741
742 026424          BGNSUB      :////////// BEGIN SUBTEST //////////
      026424
      026424 104402          T5.1:
743                                     TRAP   C$BSUB
744 026426 004737 015764          JSR   PC,SOFINIT      :DO A SOFT TO START
745 026432 103404          BCS   10$            :BRANCH IF O.K.
749 026434          ERRDF      ERRNO,SFIERR,SFIMSG :REPORT ERROR AND DROP DRIVE
      026434 104455          TRAP   C$ERDF
      026436 000765          .WORD  501
      026440 003646          .WORD  SFIERR
      026442 012024          .WORD  SFIMSG
750 026444 012702 177777 10$: MOV   #-1,R2          :ALL ONE DATA PATTERN
    
```

751	026450	005004		CLR	R4		:STARTING RAM ADDRESS	
752	026452	004737	016326	JSR	PC,CHKTSSR		:WAIT FOR READY, NON-AMBIGUOUS	
753	026456	105065	000000	15\$: CLR	TSDB(R5)		:SET MAINTENANCE MODE	
754	026462	004737	016326	JSR	PC,CHKTSSR		:WAIT FOR READY, NON-AMBIGUOUS	
755	026466	010465	000000	MOV	R4,TSDB(R5)		:SET THE NEXT RAM ADDRESS	
756	026472	004737	016326	JSR	PC,CHKTSSR		:WAIT FOR READY, NON-AMBIGUOUS	
757	026476	110265	000000	MOVB	R2,TSDB(R5)		:LOAD TEST DATA	
758	026502	005204		INC	R4		:NEXT ADDRESS TO TEST	
759	026504	020427	007777	CMP	R4,#7777		:COMPARE TO LAST ADDRESS	
760	026510	003762		BLE	15\$		:BRANCH TILL ALL DATA WRITTEN	
761	026512			BRESET			:ISSUE A BUS RESET	
	026512	104433						TRAP C\$RESET
762	026514	004737	016326	JSR	PC,CHKTSSR		:WAIT FOR READY, NON-AMBIGUOUS	
763	026520	016501	000002	MOV	TSSR(R5),R1		:GET THE CONTENTS OF TSSR	
764	026524	010102		MOV	R1,R2		:CONTENTS OF TSSR	
765	026526	042702	176277	BIC	#^C<HIADDR!OFL>,R2		:THESE BITS MAY BE SET	
766	026532	052702	002200	BIS	#SSR!NBA,R2		:READY AND NEW DATA SHOULD BE SET	
767	026536	020102		CMP	R1,R2		:COMPARE EXPECTED TO RECEIVED	
768	026540	001406		BEQ	20\$		:BRANCH IF COMPARE	
772	026542			ERRDF	ERRNO,SFHERR,SFFMSG		:REPORT A FATAL ERROR	
	026542	104455						TRAP C\$ERDF
	026544	000766						.WORD 502
	026546	003701						.WORD SFHERR
	026550	012072						.WORD SFFMSG
773	026552	005237	002220	INC	FATFLG		:SET FATAL ERROR FLAG	
774	026556			20\$: CKLOOP			:LOOP ON ERROR IF FLAG SET	
	026556	104406						TRAP C\$CLP1
775	026560			ESCAPE	SUB		:EXIT IF FATAL ERROR DETECTED	
	026560	104410						TRAP C\$ESCAPE
	026562	000170						.WORD L10050-
776	026564	004737	016326	JSR	PC,CHKTSSR		:WAIT FOR SSR TO SET	
777	026570	105065	000000	CLRB	TSDB(R5)		:PUT BACK INTO MAINTENANCE MODE	
778	026574	004737	016326	JSR	PC,CHKTSSR		:WAIT FOR READY, NON-AMBIGUOUS	
779	026600	005065	000000	CLR	TSDB(R5)		:SET ADDRESS BACK TO 0000	
780	026604	012702	000377	MOV	#377,R2			
781	026610	004737	016326	JSR	PC,CHKTSSR		:WAIT FOR READY, NON-AMBIGUOUS	
782	026614	110265	000000	MOVB	R2,TSDB(R5)		:SHOULD POINT TO RAM 0	
783	026620	004737	016326	JSR	PC,CHKTSSR		:WAIT FOR READY, NON-AMBIGUOUS	
784	026624	005065	000000	CLR	TSDB(R5)		:SELECT LOCATION 0	
785	026630	004737	016326	JSR	PC,CHKTSSR		:WAIT FOR READY, NON-AMBIGUOUS	
786	026634	116501	000000	MOVB	TSBA(R5),R1		:READ RAM LOCATION SPECIFIED	
787	026640	120102		CMPB	R1,R2		:LOCATION SHOULD BE 377 OCTAL	
788	026642	001406		BEQ	25\$		:BR IF OK	
789	026644			ERRDF	ERRNO,TSADDR,EXPREC		:WASN'T POINTING TO CORRECT LOC.	
	026644	104455						TRAP C\$ERDF
	026646	000766						.WORD 502
	026650	027442						.WORD TSADDR
	026652	015464						.WORD EXPREC
790	026654	005237	002220	INC	FATFLG		:SET THE FATAL ERROR FLAG	
791	026660			25\$: CKLOOP			:SCOPE LOOP	
	026660	104406						TRAP C\$CLP1
792	026662			ESCAPE	SUB		:NO MORE CHECKS IF FATAL ERROR	
	026662	104410						TRAP C\$ESCAPE
	026664	000066						.WORD L10050-
793	026666	012704	000310	MOV	#310,R4		:START WITH LOC 310	
794	026672	005002		CLR	R2		:MEMORY EXPECTED SHOULD BE 000000	
795	026674	004737	016326	JSR	PC,CHKTSSR		:WAIT FOR READY, NON-AMBIGUOUS	



796	026700	010465	000000	30\$:	MOV	R4,TSDB(R5)	:SELECT LOCATION SPECIFIED		
797	026704	004737	016326		JSR	PC,CHKTSSR	:WAIT FOR READY, NON-AMBIGUOUS		
798	026710	116501	000000		MOVB	TSBA(R5),R1	:READ LOC CONTENTS		
799	026714	120102			CMPB	R1,R2	:CHECK MEMORY FOR 000000		
800	026716	001406			BEQ	40\$	:BRANCH IF DATA OKAY		
801	026720				ERRDF	ERRNO,T5MEM,SFFMSG	:MEMORY NOT ZERO AFTER INIT.		
	026720	104455						TRAP	C\$ERDF
	026722	000766						.WORD	502
	026724	027404						.WORD	T5MEM
	026726	012072						.WORD	SFFMSG
802	026730	005237	002220		INC	FATFLG	:SET THE FATAL ERROR FLAG		
803	026734			40\$:	CKLOOP				
	026734	104406							
804	026736				ESCAPE	SUB	:EXIT ON FATAL ERROR	TRAP	C\$CLP1
	026736	104410							
	026740	000012						TRAP	C\$ESCAPE
805	026742	005204			INC	R4	:LOOK AT NEXT RAM LOC.	.WORD	L10050-
806	026744	020427	000400		CMP	R4,#400	:AT TOP OF RAM ADDRESS SPACE		
807	026750	001353			BNE	30\$	:BRANCH TILL ALL MEMORY TESTED		
808									
809	026752				ENDSUB		:////////// END SUBTEST //////////		
	026752								
	026752	104403						L10050:	
810								TRAP	C\$ESUB
811	026754	005737	002220		TST	FATFLG	:IS FATAL ERROR FLAG SET ?		
812	026760	001404			BEQ	50\$	:BRANCH IF NOT		
813	026762	004737	017172		JSR	PC,CKDROP	:NO LOOP, TRY TO DROP DEVICE		
814	026766	005037	002220		CLR	FATFLG	:CLEAR THE FATAL ERROR FLAG		
815	026772			50\$:					

```

817 026772          BGNSUB          ://////////////// BEGIN SUBTEST //////////////////
      026772          T5.2:
      026772 104402          TRAP      C$BSUB
818
819 026774 004737 015764      JSR      PC,SOFINIT      :DO A SOFT TO START
820 027000 103404          BCS      10$          :BRANCH IF O.K.
824 027002          ERRDF      ERRNO,SFIERR,SFIMSG      :REPORT ERROR AND DROP DRIVE
      027002 104455          TRAP      C$ERDF
      027004 000767          .WORD    503
      027006 003646          .WORD    SFIERR
      027010 012024          .WORD    SFIMSG
825 027012 012702 177777      10$:  MOV      #-1,R2          :ALL ONE DATA PATTERN
826 027016 005004          CLR      R4          :STARTING RAM ADDRESS
827 027020 004737 016326      JSR      PC,CHKTSSR      :WAIT FOR READY, NON-AMBIGUOUS
828 027024 105065 000000      15$:  CLRB     TSDB(R5)      :SET MAINTENANCE MODE
829 027030 004737 016326      JSR      PC,CHKTSSR      :WAIT FOR READY, NON-AMBIGUOUS
830 027034 010465 000000      MOV      R4,TSDB(R5)      :SET THE NEXT RAM ADDRESS
831 027040 004737 016326      JSR      PC,CHKTSSR      :WAIT FOR READY, NON-AMBIGUOUS
832 027044 110265 000000      MOVB     R2,TSDB(R5)      :LOAD TEST DATA
833 027050 005204          INC      R4          :NEXT ADDRESS TO TEST
834 027052 020427 007777      CMP      R4,#7777        :COMPARE TO LAST ADDRESS
835 027056 003762          BLE     15$          :BRANCH TILL ALL DATA WRITTEN
836 027060 005065 000002      CLR      TSSR(R5)        :ISSUE A SOFT RESET
837 027064 004737 016326      JSR      PC,CHKTSSR      :WAIT FOR READY, NON-AMBIGUOUS
838 027070 016501 000002      MOV      TSSR(R5),R1      :GET THE CONTENTS OF TSSR
839 027074 010102          MOV      R1,R2          :CONTENTS OF TSSR
840 027076 042702 176277      BIC      #^C<HIADDR!OFL>,R2 :THESE BITS MAY BE SET
841 027102 052702 002200      BIS      #SSR!NBA,R2      :READY AND NEW DATA SHOULD BE SET
842 027106 020102          CMP      R1,R2          :COMPARE EXPECTED TO RECEIVED
843 027110 001406          BEQ     20$          :BRANCH IF COMPARE
847 027112          ERRDF      ERRNO,SFHERR,SFFMSG      :REPORT A FATAL ERROR
      027112 104455          TRAP      C$ERDF
      027114 000770          .WORD    504
      027116 003701          .WORD    SFHERR
      027120 012072          .WORD    SFFMSG
848 027122 005237 002220      20$:  INC      FATFLG          :SET FATAL ERROR FLAG
849 027126          CKLOOP          :LOOP ON ERROR IF FLAG SET
      027126 104406          TRAP      C$CLP1
850 027130          ESCAPE     SUB          :EXIT IF FATAL ERROR DETECTED
      027130 104410          TRAP      C$ESCAPE
      027132 000170          .WORD    L10051-.
851 027134 004737 016326      JSR      PC,CHKTSSR      :WAIT FOR SSR TO SET
852 027140 105065 000000      CLRB     TSDB(R5)        :PUT BACK INTO MAINTENANCE MODE
853 027144 004737 016326      JSR      PC,CHKTSSR      :WAIT FOR READY, NON-AMBIGUOUS
854 027150 005065 000000      CLR      TSDB(R5)        :SET ADDRESS BACK TO 0000
855 027154 012702 000377      MOV      #377,R2
856 027160 004737 016326      JSR      PC,CHKTSSR      :WAIT FOR READY, NON-AMBIGUOUS
857 027164 110265 000000      MOVB     R2,TSDB(R5)      :SHOULD POINT TO RAM 0
858 027170 004737 016326      JSR      PC,CHKTSSR      :WAIT FOR READY, NON-AMBIGUOUS
859 027174 005065 000000      CLR      TSDB(R5)        :SELECT LOCATION 0
860 027200 004737 016326      JSR      PC,CHKTSSR      :WAIT FOR READY, NON-AMBIGUOUS
861 027204 116501 000000      MOVB     TSBA(R5),R1      :READ RAM LOCATION SPECIFIED
862 027210 120102          CMPB     R1,R2          :LOCATION SHOULD BE 377 OCTAL
863 027212 001406          BEQ     25$          :BR IF OK
864 027214          ERRDF      ERRNO,T5ADDR,EXPREC      :WASN'T POINTING TO CORRECT LOC.
      027214 104455          TRAP      C$ERDF
      027216 000770          .WORD    504
  
```



```

027220 027442
027222 015464
865 027224 005237 002220      INC      FATFLG      ;SET THE FATAL ERROR FLAG
866 027230      25$:      CKLOOP      ;SCOPE LOOP
027230 104406
867 027232      ESCAPE      SUB      ;NO MORE CHECKS IF FATAL ERROR
027232 104410
027234 000066
868 027236 012704 000310      MOV      #310,R4      ;START WITH LOC 310
869 027242 005002      CLR      R2          ;MEMORY EXPECTED SHOULD BE 000000
870 027244 004737 016326      JSR      PC,CHKTSSR   ;WAIT FOR READY, NON-AMBIGUOUS
871 027250 010465 000000      30$:      MOV      R4,TSDB(R5) ;SELECT LOCATION SPECIFIED
872 027254 004737 016326      JSR      PC,CHKTSSR   ;WAIT FOR READY, NON-AMBIGUOUS
873 027260 116501 000000      MOVB     TSBA(R5),R1  ;READ LOC CONTENTS
874 027264 120102      CMPB     R1,R2        ;CHECK MEMORY FOR 000000
875 027266 001406      BEQ      40$         ;BRANCH IF DATA OKAY
876 027270      ERRDF     ERRNO,TSMEM,SFFMSG ;MEMORY NOT ZERO AFTER INIT.
027270 104455
027272 000770
027274 027404
027276 012072
877 027300 005237 002220      40$:      INC      FATFLG      ;SET THE FATAL ERROR FLAG
878 027304      CKLOOP
027304 104406
879 027306      ESCAPE      SUB      ;EXIT ON FATAL ERROR
027306 104410
027310 000012
880 027312 005204
881 027314 020427 000400      INC      R4          ;LOOK AT NEXT RAM LOC.
882 027320 001353      CMP      R4,#400     ;AT TOP OF RAM ADDRESS SPACE
883
884 027322      BNE      30$         ;BRANCH TILL ALL MEMORY TESTED
027322
027322 104403
885
886 027324 005737 002220      ENDSUB      ;////////////////// END SUBTEST ////////////////////
887 027330 001402
888 027332 004737 017172
889 027336 004737 016446
890 027342 103002
891 027344 000137 026420
892 027350      50$:      TST      FATFLG      ;IS FATAL ERROR FLAG SET ?
027350 104432      BEQ      50$         ;BRANCH IF NOT
027352 000132      JSR      PC,CKDROP   ;NO LOOP, TRY TO DROP DEVICE
893
894
895
896
897
898 027354 105 170 164 TST5ID: .ASCIZ 'Extended Initialization'
899 027404 111 156 143 TSMEM: .ASCIZ 'Incorrect RAM Data After Init'
900 027442 111 156 143 TSADDR: .ASCIZ 'Incorrect RAM Address After Init'
901
902 027504
027504
027504 104401
903
      60$:      JMP      T5LOOP     ;LOOP UNTIL COUNT EXPIRED
      EXIT      TST      ;ALL DONE THIS TEST
      TRAP     C$EXIT
      .WORD    L10047-.

;+
;LOCAL TEXT MESSAGES FOR TEST
;-

L10047:      TRAP     C$SETST

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.SBTTL TEST 6: COMMAND REJECT

THIS TEST VERIFIES THAT ALL COMMANDS OTHER THAN WRITE CHARACTERISTICS ARE REJECTED DUE TO THE NEED BUFFER ADDRESS (NBA) BIT BEING SET IN TSSR, AND THAT THE TSBA AND TSSR REGISTERS ARE LEFT IN THE PROPER STATE AFTER EACH COMMAND IS REJECTED. THIS TEST CHECKS MICROPROCESSOR SEQUENCING, BASIC COMMAND DECODING AND DATI DMA HANDLING. THIS TEST CONTAINS TWO SUBTESTS: SUBTEST 1 SEQUENCES THROUGH ALL COMMAND WORDS (OTHER THAN WRITE CHARACTERISTICS) WITH THE INTERRUPT ENABLE (IE) BIT CLEAR AND VERIFIES THAT AN INTERRUPT IS NOT GENERATED BY THE REJECTED COMMAND; SUBTEST 2 PERFORMS SIMILARLY TO SUBTEST 1 BUT SETS THE IE BIT IN EACH COMMAND WORD AND VERIFIES THAT AN INTERRUPT IS GENERATED WHEN THE COMMAND IS REJECTED. SUBTEST 1 SETS UP THE INTERRUPT SERVICE ROUTINE TO FLAG UNEXPECTED INTERRUPTS. THE COMMAND WORD IN THE COMMAND BUFFER IS INITIALIZED TO 100000 (OCTAL) AND THE REMAINING THREE WORDS IN THE COMMAND BUFFER ARE SET TO KNOWN UNIQUE PATTERNS. THEN THE FOLLOWING SEQUENCE IS PERFORMED:

1. INITIALIZE THE CONTROLLER BY WRITING INTO THE TSSR; PROPER INITIAL CONDITIONS ARE VERIFIED.
2. TSDB IS WRITTEN WITH ADDRESS OF THE COMMAND BUFFER TO START PROCESSING.
3. THE PROGRAM WAITS FOR SSR TO SET; IF SSR DOES NOT SET, AN ERROR REPORT IS ISSUED AND THE TEST IS ABORTED.
4. THE CONTENTS OF TSSR ARE CHECKED. TSSR IS CORRECT IF IT CONTAINS EITHER OCTAL 102206 OR 102306 (BIT 6 DEPENDS UPON THE STATE OF THE TAPE TRANSPORT).
5. THE CONTENTS OF TSBA ARE CHECKED. TSBA SHOULD CONTAIN THE INITIAL COMMAND BUFFER ADDRESS (LOADED IN STEP 2) PLUS 10 (OCTAL); I.E., TSBA SHOULD POINT TO THE WORD JUST AFTER THE COMMAND PACKET (NOTE THAT 4 COMMAND PACKET WORDS ARE ALWAYS FETCHED).
6. USING THE MAINTENANCE MODE WRAPAROUND FUNCTIONS, THE COMMAND IMAGE BLOCK IN THE M7196'S RAM (LOCATIONS 201-210 (OCTAL)) ARE CHECKED; THE IMAGE SHOULD CONTAIN A COPY OF THE FOUR COMMAND PACKET WORDS AS SET UP IN CPU MEMORY.
7. THE COMMAND WORD IN THE COMMAND BUFFER IS INCREMENTED TO THE NEXT PATTERN NOT CONTAINING WRITE CHARACTERISTICS OR IE. THE REMAINING THREE WORD OF THE COMMAND BUFFER ARE SEQUENCED WITH PSEUDO-RANDOM DATA. IF THE COMMAND WORD HAS NOT REACHED ITS MAXIMUM VALUE (177777+1), THE TEST SEQUENCE IS REPEATED.

SUBTEST 2 IS IDENTICAL TO SUBTEST 1, EXCEPT THAT THE PROGRAM













1152 030424 125252  
1153 030426 052525

.WORD 125252  
.WORD 052525

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:+  
:LOCAL TEXT MESSAGES FOR TEST  
:-

1160 030430 103 157 155 T6NBA: .ASCIZ 'Command Not Rejected'  
1161 030455 103 157 156 T6SSR: .ASCIZ 'Contents of TSSR Incorrect After Write Packet'  
1162 030533 125 156 145 T6INT: .ASCIZ 'Unexpected Interrupt Received On Write Packet'  
1163 030611 105 170 160 T6NINT: .ASCIZ 'Expected Interrupt Not Received On Write Packet'  
1164 030671 111 156 143 T6TSBA: .ASCIZ 'Incorrect TSBA Address After Packet Write'  
1165 030743 103 157 155 TST6ID: .ASCIZ 'Command Reject'

1166  
1167 030762 .EVEN  
030762 .EVEN  
030762 .EVEN  
ENDTST

L10052: TRAP CSETST





1223	031056	005037	002220	10\$:	CLR	FATFLG	:CLEAR FATAL ERROR FLAG	
1224	031062	005037	002222		CLR	INTRECV	:CLEAR INTERRUPT RECEIVED FLAG	
1225	031066	010465	000000		MOV	R4,TSDB(R5)	:SET THE PACKET ADDRESS	
1226	031072	004737	016326		JSR	PC,CHKTSSR	:WAIT FOR SSR TO SET	
1227	031076	103407			BCS	15\$	:BR IF CARRY SET (GOOD RETURN)	
1228	031100	010001			MOV	R0,R1	:SAVE CONTENTS OF TSSR	
1232	031102				ERRDF	ERRNO,T7SSR,PKTSSR	:DEVICE FATAL SSR FAILED TO SET	
	031102	104455					TRAP	C\$ERDF
	031104	001276					.WORD	702
	031106	033721					.WORD	T7SSR
	031110	012036					.WORD	PKTSSR
1233	031112	005237	002220	15\$:	INC	FATFLG	:SET FATAL ERROR FLAG	
1234	031116				CKLOOP		:LOOP ON ERROR, IF FLAG SET	
	031116	104406					TRAP	C\$CLP1
1235	031120				ESCAPE	SEG	:BY-PASS SUBTEST IF FATAL ERROR	
	031120	104410					TRAP	C\$ESCAPE
	031122	000152					.WORD	10000\$-
1236	031124	005737	002222		TST	INTRECV	:DID AN INTERRUPT OCCUR ?	
1237	031130	001404			BEQ	22\$	:BRANCH IF NOT	
1241	031132				ERRHRD	ERRNO,T7INT,PKTSSR		
	031132	104456					TRAP	C\$ERHRD
	031134	001277					.WORD	703
	031136	034101					.WORD	T7INT
	031140	012036					.WORD	PKTSSR
1242	031142	016501	000002	22\$:	MOV	TSSR(R5),R1	:GET THE CONTENTS OF TSSR	
1243	031146	012702	000200		MOV	#SSR,R2	:EXPECTED CONTENTS OF TSSR	
1244	031152	032701	000100		BIT	#OFL,R1	:IS OFF-LINE BIT SET ?	
1245	031156	001402			BEQ	25\$	:BRANCH IF NOT OFF-LINE	
1246	031160	052702	000100		BIS	#OFL,R2	:SET OFF-LINE IN EXPECTED DATA	
1247	031164	020201		25\$:	CMP	R2,R1	:DOES EXPECTED MATCH RECEIVED ?	
1248	031166	001404			BEQ	30\$	:OKAY IF MATCH	
1252	031170				ERRHRD	ERRNO,T7NBA,PKTSSR	:NBA NOT ZERO	
	031170	104456					TRAP	C\$ERHRD
	031172	001300					.WORD	704
	031174	033260					.WORD	T7NBA
	031176	012036					.WORD	PKTSSR
1253	031200			30\$:	CKLOOP		:LOOP ON ERROR ?	
	031200	104406					TRAP	C\$CLP1
1254	031202	004737	016326		JSR	PC,CHKTSSR	:WAIT FOR READY, NON-AMBIGUOUS	
1255	031206	016501	000000		MOV	TSBA(R5),R1	:GET TSBA REGISTER CONTENTS	
1256	031212	012702	033146		MOV	#T7BFR,R2	:START OF THE DATA BUFFER	
1257	031216	032762	000200	000012	BIT	#BIT7,XST2(R2)	:IS EXTENDED FEATURES BIT SET ?	
1258	031224	001404			BEQ	32\$	:BRANCH IF EXTENDED FEATURES OFF	
1259	031226	005237	002224		INC	EXTFEA	:SET EXTENDED FEATURES FOR SUBTEST 6	
1260	031232	062702	000002		ADD	#2,R2	:EXTRA WORD IF SPECIAL FEATURES	
1261	031236	062702	000016	32\$:	ADD	#14,R2	:EXPECTED CONTENTS OF TSBA	
1262	031242	020102			CMP	R1,R2	:COMPARE EXPECTED TO RECEIVED	
1263	031244	001404			BEQ	35\$	:ERROR IF NOT EQUAL	
1267	031246				ERRHRD	ERRNO,T7TSBA,EXPREC	:PRINT THE ERROR & EXPD/RCV	
	031246	104456					TRAP	C\$ERHRD
	031250	001301					.WORD	705
	031252	034170					.WORD	T7TSBA
	031254	015464					.WORD	EXPREC
1268								
1269								
1270	031256	004737	011104	35\$:	JSR	PC,CKRAM	:SEE IF DATA IN RAM IS CORRECT	
1271	031262	103404			BCS	40\$	:BRANCH IF PACKET IN RAM IS CORRECT	

















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1452 032046          BGNSUB          ;//////////////// BEGIN SUBTEST //////////////////
      032046          ;T7.4:
      032046 104402          TRAP      C$BSUB
1453
1454 032050          SETPRI  #PRI00          ;LOWER PRIORITY TO ALLOW INTERRUPTS
      032050 012700 000000          MOV      #PRI00,R0
      032054 104441          TRAP      C$SPRI
1455 032056 012703 033166          MOV      #T72DATA,R3          ;START OF TEST DATA FOR SUBTEST
1456 032062 012704 033120          MOV      #T7PACKET,R4        ;GET THE ADDRESS OF COMMAND PACKET
1457 032066 004737 034302          JSR      PC,T7REST          ;RESTORE PACKET TO STARTING VALUES
1458
1459
1460 032072 004737 015764          JSR      PC,SOFINIT          ;DO SOFT INIT OF CONTROLLER
1461 032076 103405          BCS     10$                ;BR IF SOFT INIT = OK
1465 032100 010001          MOV      R0,R1              ;SAVE CONTENTS OF TSSR
1466 032102          ERRDF  ERRNO,SFIERR,SFIMSG ;DEVICE FATAL ERROR DURING INIT
      032102 104455          TRAP      C$ERDF
      032104 001315          .WORD    717
      032106 003646          .WORD    SFIERR
      032110 012024          .WORD    SFIMSG
1467 032112 005037 002222          CLR      INTRECV            ;CLEAR INTERRUPT RECEIVED FLAG
1468 032116 052737 000001 033130 10$:  BIS     #1,T7DATA          ;MAKE ADDRESS ODD
1469 032124 010465 000000          MOV      R4,TSDB(R5)        ;SET THE PACKET ADDRESS
1470 032130 004737 016240          JSR      PC,WAITF           ;WAIT FOR SSR TO SET
1471 032134 103405          BCS     15$                ;BR IF CARRY SET (GOOD RETURN)
1472 032136 010001          MOV      R0,R1              ;SAVE CONTENTS OF TSSR
1476 032140          ERRDF  ERRNO,T7SSR,PKTSSR ;DEVICE FATAL SSR FAILED TO SET
      032140 104455          TRAP      C$ERDF
      032142 001316          .WORD    718
      032144 033721          .WORD    T7SSR
      032146 012036          .WORD    PKTSSR
1477 032150          15$:  CKLOOP          ;LOOP ON ERROR, IF FLAG SET
      032150 104406          TRAP      C$CLP1
1478 032152          ESCAPE  SUB          ;BY-PASS SUBTEST IF FATAL ERROR
      032152 104410          TRAP      C$ESCAPE
      032154 000116          .WORD    L10061-.
1479 032156 005737 002222          TST     INTRECV            ;DID AN INTERRUPT OCCUR ?
1480 032162 001404          BEQ     22$                ;BRANCH IF NOT
1484 032164          ERRHRD ERRNO,T7INT,PKTSSR
      032164 104456          TRAP      C$ERHRD
      032166 001317          .WORD    719
      032170 034101          .WORD    T7INT
      032172 012036          .WORD    PKTSSR
1485 032174 016501 000002          22$:  MOV      TSSR(R5),R1        ;GET THE CONTENTS OF TSSR
1486 032200 012702 102206          MOV      #SC!SSR!TSREJ!NBA,R2 ;EXPECTED CONTENTS OF TSSR
1487 032204 032701 000100          BIT     #OFL,R1            ;IS OFF-LINE BIT SET ?
1488 032210 001402          BEQ     25$                ;BRANCH IF NOT OFF-LINE
1489 032212 052702 000100          BIS     #OFL,R2            ;SET OFF-LINE IN EXPECTED DATA
    
```



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1490 032216 020201          25$:  CMP      R2,R1          ;DOES EXPECTED MATCH RECEIVED ?
1491 032220 001414          BEQ      30$          ;OKAY IF MATCH
1492 032222 010100          MOV      R1,R0          ;DATA FROM TSSR
1493 032224          XOR      R2,R0          ;FIND BITS IN ERROR
1494 032234 020027 002000      CMP      R0,#NBA       ;IS NBA ONLY BIT IN ERROR ?
1495 032240 001404          BEQ      30$          ;DON'T PRINT ERROR IF NBA ONLY BAD BIT
1499 032242          ERRHRD  ERRNO,T74REJ,PKTSSR ;COMMAND NOT REJECTED
      032242 104456          TRAP    C$ERHRD
      032244 001320          .WORD  720
      032246 033525          .WORD  T74REJ
      032250 012036          .WORD  PKTSSR
1500 032252          30$:  CKLOOP          ;LOOP ON ERROR ?
      032252 104406          TRAP    C$CLP1
1501 032254 032701 002000      BIT      #NBA,R1       ;IS NBA BIT SET ?
1502 032260 001004          BNE     35$          ;OKAY IF NBA SET
1506 032262          ERRHRD  ERRNO,T72NBA,PKTSSR ;NBA NOT SET
      032262 104456          TRAP    C$ERHRD
      032264 001321          .WORD  721
      032266 033202          .WORD  T72NBA
      032270 012036          .WORD  PKTSSR
1507
1508 032272          35$:  ENDSUB          ;////////////////// END SUBTEST ////////////////////
      032272          L10061:
1509 032272 104403          TRAP    C$ESUB
  
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 033076  
 033076 104403  
 1682  
 1683 033100 005737 002220  
 1684 033104 001402  
 1685 033106 004737 017172  
 1686 033112  
 1687 033112  
 033112 104432  
 033114 001234

55\$: ENDSUB  
  
 TST FATFLG  
 BEQ 60\$  
 JSR PC,CKDROP  
 60\$:  
 EXIT TST

:////////// END SUBTEST //////////  
 L10063: TRAP C\$ESUB  
  
 :ANY FATAL ERRORS ?  
 :BRANCH IF NOT  
 :TRY TO DROP THE UNIT  
  
 :ALL DONE THIS TEST  
 TRAP C\$EXIT  
 .WORD L10055-

1688  
 1689  
 1690  
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 1692  
 1694 033120  
 1696 033120  
 1697 033120 100004  
 1698 033122 033130  
 1699 033124 000000  
 1700 033126 000010  
 1701  
 1702 033130  
 1703 033130 033146  
 1704 033132 000000  
 1705 033134 000016  
 1706 033136 000000  
 1707 033140 000000  
 1708  
 1709 033142 000000 000000  
 1710 033146

:+  
 :LOCAL STORAGE FOR THIS TEST  
 :-

.=<.+10>&177770  
 T7PACKET:  
 .WORD 100004  
 .WORD T7DATA  
 .WORD 0  
 .WORD 8.

:COMMAND PACKET FOR TEST  
 :WRITE CHARACTERISTICS COMMAND, WITH ACK  
 :ADDRESS OF CHARACTERISTICS BLOCK  
  
 :STARTING VALUE OF BLOCK SIZE

T7DATA:  
 .WORD T7BFR  
 .WORD 0  
 .WORD 14.  
 .WORD 0  
 T7SP: .WORD 0

:CHARACTERISTICS DATA BLOCK  
 :ADDRESS OF MESSAGE BUFFER  
  
 :LENGTH OF MESSAGE BUFFER  
  
 :EXTFEA EXTRA WORD  
  
 :SPACE  
 :MESSAGE BUFFER

T7BFR: .BLKW 8.

:+  
 :TEST DATA FOR SUBTEST TWO

:DATA HAS FORMAT:

1ST WORD            OFFSET TO TEST WORD IN PACKET  
 2ND WORD            BITS TO SET FOR TEST

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 1712  
 1713  
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 1721  
 1722  
 1723 033166  
 1724 033166 000000 037140  
 1725 033172 000002 000001  
 1726 033176 000004 100100  
 1727 033202  
 1728  
 1729  
 1730  
 1731  
 1732  
 1733

T72DATA:  
 .WORD 0,BIT5!BIT6!BIT9!BIT10!BIT11!BIT12!BIT13  
 .WORD 2,BIT0  
 .WORD 4,BIT6!BIT15  
 T72DONE=.

:+  
 :LOCAL TEXT MESSAGES FOR TEST  
 :-

1734 033202 116 102 101 T72NBA: .ASCIZ 'NBA Not Set On Rejected WRITE CHARACTERISTICS'  
 1735 033260 127 122 111 T7NBA: .ASCIZ 'WRITE CHARACTERISTICS Command Not Accepted'



1736	033333	127	122	111	T72REJ: .ASCIZ	'WRITE CHARACTERISTICS Not Rejected With Non-Zero Unused Fields'
1737	033432	127	122	111	T73REJ: .ASCIZ	'WRITE CHARACTERISTICS Not Rejected With Invalid Data Count'
1738	033525	127	122	111	T74REJ: .ASCIZ	'WRITE CHARACTERISTICS Not Rejected With Invalid Block Address'
1739	033623	127	122	111	T75REJ: .ASCIZ	'WRITE CHARACTERISTICS Not Rejected With Invalid Buffer Length'
1740	033721	103	157	156	T7SSR: .ASCIZ	'Contents of TSSR Incorrect After WRITE CHARACTERISTICS'
1741	034010	105	170	160	T7NINT: .ASCIZ	'Expected Interrupt Not Received On WRITE CHARACTERISTICS'
1742	034101	125	156	145	T7INT: .ASCIZ	'Unexpected Interrupt Received On WRITE CHARACTERISTICS'
1743	034170	111	156	143	T7TSBA: .ASCIZ	'Incorrect TSBA Address After WRITE CHARACTERISTICS'
1744	034253	127	162	151	TST7ID: .ASCIZ	'Write Characteristics'
1745					.EVEN	
1746						

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1755 034302  
1756 034302  
1757 034306 012701 033120  
1758 034312 012721 100004  
1759 034316 012721 033130  
1760 034322 005021  
1761 034324 012721 000010  
1762 034330 012721 033146  
1763 034334 005021  
1764 034336 012721 000020  
1765 034342 005021  
1766 034344 005011  
1767 034346 000207  
1768 034350  
034350  
034350 104401  
1769

:+  
:ROUTINE TO RESTORE COMMAND PACKET TO START-UP (DEFAULT) VALUES  
:-

T7REST:

SAVREG ;SAVE THE REGISTERS  
MOV #T7PACKET,R1 ;START OF THE PACKET  
MOV #100004,(R1)+ ;WRITE CHARACTERISTICS WITH ACK  
MOV #T7DATA,(R1)+ ;ADDRESS OF CHAR DATA BLOCK  
CLR (R1)+ ;EXTENDED ADDRESS  
MOV #8,(R1)+ ;SIZE OF DATA BLOCK IN BYTES  
MOV #T7BFR,(R1)+ ;ADDRESS OF MESSAGE BUFFER  
CLR (R1)+ ;LENGTH OF MESSAGE BUFFER  
MOV #16,(R1)+  
CLR (R1)+  
CLR (R1)  
RTS PC ;RETURN  
ENDTST

L10055: TRAP CSETST



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.SBTTL TEST 8: VOLUME CHECK

THIS TEST VERIFIES THAT THE VOLUME CHECK (VCK) BIT, A FLAG HELD WITHIN THE M7196 AND APPEARING IN XST0, IS SET BY INITIALIZE AND CLEARED BY EXECUTING A WRITE CHARACTERISTICS COMMAND WITH THE CVC BIT SET. IT IS ALSO VERIFIED THAT A WRITE CHARACTERISTICS COMMAND WITH THE CVC BIT CLEAR DOES NOT AFFECT THE STATE OF THE VOLUME CHECK BIT. THE ACTUAL FUNCTION OF VOLUME CHECK, THAT OF PREVENTING OR ALLOWING A TAPE MOTION COMMAND DEPENDING UPON WHETHER VOLUME CHECK IS SET OR CLEAR, IS NOT CHECKED BY THIS TEST; THIS FUNCTIONALITY IS CHECKED IN THE INDIVIDUAL TESTS OF TAPE MOTION COMMANDS.

THE TEST PROCEEDS AS FOLLOWS:

1. THE CONTROLLER IS INITIALIZED BY WRITING INTO THE TSSR.
2. A WRITE CHARACTERISTICS COMMAND IS ISSUED (WITH CVC=0) AND XST0 IN THE RETURNED MESSAGE BUFFER IS EXAMINED; THE VCK BIT SHOULD BE CLEAR (0).
3. THE PREVIOUS STEP IS REPEATED TO VERIFY THAT VCK DOES NOT CHANGE (REMAINS AT 0).
4. A WRITE CHARACTERISTICS COMMAND IS ISSUED WITH CVC=1 AND THE VCK BIT IN XST0 IN THE MESSAGE BUFFER IS EXAMINED; THE VCK BIT SHOULD BE CLEAR (0).
5. A WRITE CHARACTERISTICS COMMAND IS ISSUED WITH CVC=0 AND THE VCK BIT IN XST0 IN THE MESSAGE BUFFER IS EXAMINED; THE VCK BIT SHOULD REMAIN CLEAR (0).

```

1805 034352          BGNTST
      034352
1810 034352 012700 035237      MOV      #TST8ID,R0      ;ASCII MESSAGE TO IDENTIFY TEST
1811 034356 004737 016500      JSR      PC,TSTSETUP    ;DO INITIAL TEST SETUP
1812 034362 012737 000024 002214  MOV      #20.,LOOPCNT   ;PERFORM 20 ITERATIONS
1813 034370          T8LOOP:
1814
1815 034370 012704 034760      MOV      #T8PACKET,R4   ;PACKET FOR WRITE CHARACTERISTICS
1816 034374 004737 015764      JSR      PC,SOFINIT     ;DO SOFT INIT OF CONTROLLER
1817 034400 103405          BCS      10$            ;BR IF SOFT INIT = OK
1821 034402 010001          MOV      R0,R1          ;SAVE CONTENTS OF TSSR
1822 034404          ERRDF  ERRNO,SFIERR,SFIMSG ;DEVICE FATAL ERROR DURING INIT
      034404 104455          TRAP      CSERDF
      034406 001441          .WORD    801
      034410 003646          .WORD    SFIERR
      034412 012024          .WORD    SFIMSG
1823 034414 042714 040000      10$:  BIC      #BIT14,(R4)   ;CLEAR THE CVC BIT
1824 034420 010465 000000      MOV      R4,TSDB(R5)   ;SET THE PACKET ADDRESS FOR WRITE CHAR
1825 034424 004737 016326      JSR      PC,CHKTSSR    ;WAIT FOR SSR TO SET
1826 034430 103405          BCS      15$            ;BR IF CARRY SET (GOOD RETURN)
1827 034432 010001          MOV      R0,R1          ;SAVE CONTENTS OF TSSR
1831 034434          ERRDF  ERRNO,T8SSR,PKTSSR ;DEVICE FATAL SSR FAILED TO SET
      034434 104455          TRAP      CSERDF
    
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1872 034620 000434 000020 000006 BIT #XSOVCK,XSTO(R2) ;IS VOLUME CHECK CLEAR IN XSTO ?
1873 034622 032762 BEQ 40$ ;OKAY IF VOLUME CHECK IS CLEARED
1877 034630 001406 MOV TSSR(R5),R1 ;CONTENTS OF TSSR FOR ERROR REPORT
1878 034632 016501 000002 ERRHRD ERRNO,T8VCK,PKTMES ;VOLUME CHECK NOT CLEARED
      034636 104456 TRAP CSERHRD
      034640 001447 .WORD 807
      034642 035022 .WORD T8VCK
      034644 012100 .WORD PKTMES
1879 034646 40$: CKLOOP ;LOOP ON ERROR ?
      034646 104406 TRAP CSCLP1
1880 034650 042714 040000 BIC #BIT14,(R4) ;CLEAR THE CVC BIT
1881 034654 010465 000000 MOV R4,TSDB(R5) ;SET THE PACKET ADDRESS FOR WRITE CHAR
1882 034660 004737 016326 JSR PC,CHKTSSR ;WAIT FOR SSR TO SET
1883 034664 103405 BCS 45$ ;BR IF CARRY SET (GOOD RETURN)
1884 034666 010001 MOV R0,R1 ;SAVE CONTENTS OF TSSR
1888 034670 ERRDF ERRNO,T8SSR,PKTSSR ;DEVICE FATAL SSR FAILED TO SET
      034670 104455 TRAP CSERDF
      034672 001450 .WORD 808
      034674 035150 .WORD T8SSR
      034676 012036 .WORD PKTSSR
1889 034700 45$: CKLOOP ;LOOP ON ERROR, IF FLAG SET
      034700 104406 TRAP CSCLP1
1890 034702 ESCAPE TST ;EXIT IF FATAL ERROR
      034702 104410 TRAP CSESCAPE
      034704 000350 .WORD L10064-.
1891 034706 032762 000020 000006 BIT #XSOVCK,XSTO(R2) ;IS VOLUME CHECK CLEAR IN XSTO ?
1892 034714 001406 BEQ 50$ ;OKAY IF VOLUME CHECK IS CLEARED
1896 034716 016501 000002 MOV TSSR(R5),R1 ;CONTENTS OF TSSR FOR ERROR REPORT
1897 034722 ERRHRD ERRNO,T8VCK,PKTMES ;VOLUME CHECK NOT CLEARED
      034722 104456 TRAP CSERHRD
      034724 001451 .WORD 809
      034726 035022 .WORD T8VCK
      034730 012100 .WORD PKTMES
1898 034732 50$: CKLOOP ;LOOP ON ERROR ?
      034732 104406 TRAP CSCLP1
1899 034734 004737 016446 60$: JSR PC,TSTLOOP ;SHOULD WE DO ITERATIONS ?
1900 034740 103002 BCC 62$ ;BRANCH IF NOT
1901 034742 000137 034370 JMP T8LOOP ;LOOP UNTIL COUNT EXPIRED
1902 034746 62$: EXIT TST ;ALL DONE THIS TEST
      034746 104432 TRAP CSEXIT
      034750 000304 .WORD L10064-.

1903
1904 ;+
1905 ;LOCAL STORAGE FOR THIS TEST
1906 ;-
1907
1909 034760
1911 034760 T8PACKET: .=<. +10>&177770
1912 034760 100004 .WORD 100004 ;COMMAND PACKET FOR TEST
1913 034762 034770 .WORD T8DATA ;WRITE CHARACTERISTICS COMMAND
1914 034764 000000 .WORD 0 ;ADDRESS OF CHARACTERISTICS BLOCK
1915 034766 000010 .WORD 10 ;STARTING VALUE OF COUNTER
1916
1917 034770 T8DATA: ;CHARACTERISTICS DATA BLOCK
1918 034770 035002 .WORD T8BFR ;ADDRESS OF MESSAGE BUFFER
1919 034772 000000 .WORD 0

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1920 034774 000020 .WORD 16. ;LENGTH OF MESSAGE BUFFER  
1921 034776 000000 000000 .WORD 0,0  
1922  
1923 035002 T8BFR: .BLKW 8. ;MESSAGE BUFFER  
1924  
1925

1926  
1927 ;+  
1928 ;LOCAL TEXT MESSAGES FOR TEST  
1929 ;-

1930 035022 126 157 154 T8VCK: .ASCIZ 'Volume Check Bit Not Cleared'  
1931 035057 126 157 154 T8NVCK: .ASCIZ 'Volume Check Bit (VCK) Not Clear After Initialize (XST0)'  
1932 035150 103 157 156 T8SSR: .ASCIZ 'Contents of TSSR Incorrect After Write Characteristics'  
1933 035237 126 157 154 T8T8ID: .ASCIZ 'Volume Check'

1934  
1935 035254 .EVEN  
035254 .ENDTST

L10064: TRAP CSETST

104401





















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2170 036164          BGNSUB          :////////// BEGIN SUBTEST //////////
      036164          :                      T9.4:
      036164 104402          TRAP          C$BSUB
2171
2172 036166          SETPRI #PRI00          :LOWER PRIORITY TO ALLOW INTERRUPTS
      036166 012700 000000          MOV          #PRI00,R0
      036172 104441          TRAP          C$SPRI
2173 036174 012703 037262          MOV          #T92DATA,R3          :START OF TEST DATA FOR SUBTEST
2174 036200 012704 037220          MOV          #T9PACKET,R4          :GET THE ADDRESS OF COMMAND PACKET
2175 036204 004737 040316          JSR          PC,T9REST          :RESTORE PACKET TO STARTING VALUES
2176
2177
2178 036210 004737 015764          JSR          PC,SOFINIT          :DO SOFT INIT OF CONTROLLER
2179 036214 103405          BCS          10$          :BR IF SOFT INIT = OK
2183 036216 010001          MOV          R0,R1          :SAVE CONTENTS OF TSSR
2184 036220          ERRDF          ERRNO,SFIERR,SFIMSG :DEVICE FATAL ERROR DURING INIT
      036220 104455          TRAP          C$ERDF
      036222 001621          .WORD          913
      036224 003646          .WORD          SFIERR
      036226 012024          .WORD          SFIMSG
2185 036230 005037 002222          CLR          INTRECV          :CLEAR INTERRUPT RECEIVED FLAG
2186 036234 052737 000001 037230 10$: BIS          #1,T9DATA          :MAKE ADDRESS ODD
2187 036242 010465 000000          MOV          R4,TSDB(R5)          :SET THE PACKET ADDRESS
2188 036246 004737 016240          JSR          PC,WAITF          :WAIT FOR SSR TO SET
2189 036252 103405          BCS          15$          :BR IF CARRY SET (GOOD RETURN)
2190 036254 010001          MOV          R0,R1          :SAVE CONTENTS OF TSSR
2194 036256          ERRDF          ERRNO,T9SSR,PKTSSR :DEVICE FATAL SSR FAILED TO SET
      036256 104455          TRAP          C$ERDF
      036260 001622          .WORD          914
      036262 037737          .WORD          T9SSR
      036264 012036          .WORD          PKTSSR
2195 036266          CKLOOP          :LOOP ON ERROR, IF FLAG SET
      036266 104406          15$:          TRAP          C$CLP1
2196 036270          ESCAPE          SUB          :BY-PASS SUBTEST IF FATAL ERROR
      036270 104410          TRAP          C$ESCAPE
      036272 000056          .WORD          L10071-.
2197 036274 005737 002222          TST          INTRECV          :DID AN INTERRUPT OCCUR ?
2198 036300 001004          BNE          22$          :BRANCH IF YES
2202 036302          ERRHRD          ERRNO,T9NINT,PKTSSR
      036302 104456          TRAP          C$ERHRD
      036304 001623          .WORD          915
      036306 040026          .WORD          T9NINT
      036310 012036          .WORD          PKTSSR
2203 036312 016501 000002          22$: MOV          TSSR(R5),R1          :GET THE CONTENTS OF TSSR
2204 036316 012702 102206          MOV          #SC!SSR!TSREJ!NBA,R2 :EXPECTED CONTENTS OF TSSR
2205 036322 032701 000100          BIT          #OFL,R1          :IS OFF-LINE BIT SET ?
2206 036326 001402          BEQ          25$          :BRANCH IF NOT OFF-LINE
2207 036330 052702 000100          BIS          #OFL,R2          :SET OFF-LINE IN EXPECTED DATA

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2208 036334 020201          25$:  CMP      R2,R1          ;DOES EXPECTED MATCH RECEIVED ?
2209 036336 001404          BEQ      30$          ;OKAY IF MATCH
2213 036340          ERRHRD  ERRNO,T94REJ,PKTSSR ;COMMAND NOT REJECTED
      036340 104456          TRAP    CSERHRD
      036342 001624          .WORD  916
      036344 037543          .WORD  T94REJ
      036346 012036          .WORD  PKTSSR
2214 036350          30$:
2215
2216 036350          ENDSUB          ;////////////////// END SUBTEST ////////////////////
      036350          L10071:
2217 036350 104403          TRAP    C$ESUB
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2364 037010          BGNSUB          ;////////// BEGIN SUBTEST ////////////
      037010          ;              T9.7:          TRAP      CSBSUB
      037010 104402
2365
2366 037012          SETPRI  #PRI00      ;LOWER PRIORITY TO ALLOW INTERRUPTS
      037012 012700 000000          MOV      #PRI00,R0
      037016 104441          TRAP      CSSPRI
2367 037020 012704 037220          MOV      #T9PACKET,R4      ;GET THE ADDRESS OF COMMAND PACKET
2368 037024 004737 040316          JSR      PC,T9REST      ;SET UP A VALID PACKET
2369 037030 004737 015764          JSP      PC,SOFINIT      ;DO SOFT INIT OF CONTROLLER
2370 037034 103405          BCS      10$      ;BR IF SOFT INIT = OK
2374 037036 010001          MOV      R0,R1      ;SAVE CONTENTS OF TSSR
2375 037040          ERRDF  ERRNO,SFIERR,SFIMSG ;DEVICE FATAL ERROR DURING INIT
      037040 104455          TRAP      CSERDF
      037042 001635          .WORD   925
      037044 003646          .WORD   SFIERR
      037046 012024          .WORD   SFIMSG
2376 037050 005037 002222          10$:  CLR      INTRECV      ;CLEAR INTERRUPT RECEIVED FLAG
2377 037054 052714 000200          BIS      #BIT7,(R4)      ;ENABLE INTERRUPTS
2378 037060 010465 000000          MOV      R4,TSDB(R5)      ;SET THE PACKET ADDRESS
2379 037064 004737 016326          JSR      PC,CHKTSSR      ;WAIT FOR SSR TO SET
2380 037070 103405          BCS      15$      ;BR IF CARRY SET (GOOD RETURN)
2381 037072 010001          MOV      R0,R1      ;SAVE CONTENTS OF TSSR
2385 037074          ERRDF  ERRNO,T9SSR,PKTSSR ;DEVICE FATAL SSR FAILED TO SET
      037074 104455          TRAP      CSERDF
      037076 001636          .WORD   926
      037100 037737          .WORD   T9SSR
      037102 012036          .WORD   PKTSSR
2386 037104          15$:  CKLOOP      ;LOOP ON ERROR, IF FLAG SET
      037104 104406          TRAP      CSCLP1
2387 037106          ESCAPE  SUB      ;BY-PASS SUBTEST IF FATAL ERROR
      037106 104410          TRAP      CSESCAPE
      037110 000102          .WORD   L10074-
2388 037112 005737 002222          TST      INTRECV      ;DID AN INTERRUPT OCCUR ?
2389 037116 001004          BNE      22$      ;BRANCH IF YES
2393 037120          ERRHRD  ERRNO,T9NINT,PKTSSR
      037120 104456          TRAP      CSERHRD
      037122 001637          .WORD   927
      037124 040026          .WORD   T9NINT
      037126 012036          .WORD   PKTSSR
2394 037130          22$:  CKLOOP      ;LOOP ON ERROR ?
      037130 104406          TRAP      CSCLP1
2395
2396 037132 005037 002222          CLR      INTRECV      ;CLEAR INTERRUPT RECEIVED FLAG
2397 037136 042714 000200          BIC      #BIT7,(R4)      ;DISABLE INTERRUPTS
2398 037142 010465 000000          MOV      R4,TSDB(R5)      ;SET THE PACKET ADDRESS
2399 037146 004737 016326          JSR      PC,CHKTSSR      ;WAIT FOR SSR TO SET
2400 037152 103405          BCS      25$      ;BR IF CARRY SET (GOOD RETURN)
    
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2401 037154 010001          MOV    R0,R1          ;SAVE CONTENTS OF TSSR
2405 037156          ERRDF  ERRNO,T9SSR,PKTSSR ;DEVICE FATAL SSR FAILED TO SET
      037156 104455          TRAP  C$ERDF
      037160 001640          .WORD 928
      037162 037737          .WORD T9SSR
      037164 012036          .WORD PKTSSR
2406 037166          25$: CKLOOP          ;LOOP ON ERROR, IF FLAG SET
      037166 104406          TRAP  C$CLP1
2407 037170          ESCAPE SUB          ;BY-PASS SUBTEST IF FATAL ERROR
      037170 104410          TRAP  C$ESCAPE
      037172 000020          .WORD L10074-.
2408 037174 005737 002222  TST    INTRECV        ;DID AN INTERRUPT OCCUR ?
2409 037200 001404          BEQ   30$            ;BRANCH IF NOT
2413 037202          ERRHRD ERRNO,T9INT,PKTSSR
      037202 104456          TRAP  C$ERHRD
      037204 001641          .WORD 929
      037206 040117          .WORD T9INT
      037210 012036          .WORD PKTSSR
2414 037212          30$:
2415 037212          ENDSUB          ;////////////////// END SUBTEST ////////////////////
      037212 104403          L10074:
2416 037214          TRAP  C$ESUB
2417 037214          EXIT  TST          ;ALL DONE THIS TEST
      037216 001152          TRAP  C$EXIT
      037216 001152          .WORD L10065-.
2418
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2426 037220          T9PACKET:
2427 037220 100204          .WORD 100204
2428 037222 037230          .WORD T9DATA
2429 037224 000000          .WORD 0
2430 037226 000010          .WORD 8.
      ;COMMAND PACKET FOR TEST
      ;WRITE CHAR COMMAND, WITH IE, ACK
      ;ADDRESS OF CHARACTERISTICS BLOCK
      ;STARTING VALUE OF BLOCK SIZE
2432 037230          T9DATA:
2433 037230 037242          .WORD T9BFR
2434 037232 000000          .WORD 0
2435 037234 000016          .WORD 14.
2436 037236 000000 000000 .WORD 0.0
      ;CHARACTERISTICS DATA BLOCK
      ;ADDRESS OF MESSAGE BUFFER
      ;LENGTH OF MESSAGE BUFFER
2438 037242          T9BFR: .BLKW 8.          ;MESSAGE BUFFER
2439
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2448
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2450
2451 037262          T92DATA:
      ;+
      ;LOCAL STORAGE FOR THIS TEST
      ;-
      ;TEST DATA FOR SUBTEST TWO
      ;DATA HAS FORMAT:
      ;
      ;      1ST WORD      OFFSET TO TEST WORD IN PACKET
      ;      2ND WORD      BITS TO SET FOR TEST
      ;-
  
```



2452 037262 000000 037140  
 2453 037266 000002 000001  
 2454 037272 000004 100100  
 2455 037276

.WORD 0,BIT5!BIT6!BIT9!BIT10!BIT11!BIT12!BIT13  
 .WORD 2,BIT0  
 .WORD 4,BIT6!BIT15

T92DONE=.

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 2458  
 2459  
 2460

:+  
 :LOCAL TEXT MESSAGES FOR TEST  
 :-

2461						
2462	037276	127	122	111	T9NBA: .ASCIZ	'WRITE CHARACTERISTICS Command Not Accepted'
2463	037351	127	122	111	T92REJ: .ASCIZ	'WRITE CHARACTERISTICS Not Rejected With Non-Zero Unused Fields'
2464	037450	127	122	111	T93REJ: .ASCIZ	'WRITE CHARACTERISTICS Not Rejected With Invalid Data Count'
2465	037543	127	122	111	T94REJ: .ASCIZ	'WRITE CHARACTERISTICS Not Rejected With Invalid Block Address'
2466	037641	127	122	111	T95REJ: .ASCIZ	'WRITE CHARACTERISTICS Not Rejected With Invalid Buffer Length'
2467	037737	103	157	156	T9SSR: .ASCIZ	'Contents of TSSR Incorrect After WRITE CHARACTERISTICS'
2468	040026	105	170	160	T9NINT: .ASCIZ	'Expected Interrupt Not Received On WRITE CHARACTERISTICS'
2469	040117	125	156	145	T9INT: .ASCIZ	'Unexpected Interrupt Received On WRITE CHARACTERISTICS'
2470	040206	111	156	143	T9TSBA: .ASCIZ	'Incorrect TSBA Address After WRITE CHARACTERISTICS'
2471	040271	103	157	155	T9T9ID: .ASCIZ	'Completion Interrupt'
2472					.EVEN	
2473						

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2481  
2482 040316  
2483 040316  
2484 040322 012701 037220  
2485 040326 012721 100204  
2486 040332 012721 037230  
2487 040336 005021  
2488 040340 012721 000010  
2489 040344 012721 037242  
2490 040350 005021  
2491 040352 012721 000016  
2492 040356 005021  
2493 040360 005011  
2494 040362 005037 037242  
2495 040366 000207  
2496 040370  
040370  
040370 104401

:+  
:ROUTINE TO RESTORE COMMAND PACKET TO START-UP (DEFAULT) VALUES  
:-

T9REST: SAVREG ;SAVE THE REGISTERS  
MOV #T9PACKET,R1 ;START OF THE PACKET  
MOV #100204,(R1)+ ;WRITE CHARACTERISTICS WITH ACK, IE  
MOV #T9DATA,(R1)+ ;ADDRESS OF CHAR DATA BLOCK  
CLR (R1)+ ;EXTENDED ADDRESS  
MOV #8,(R1)+ ;SIZE OF DATA BLOCK IN BYTES  
MOV #T9BFR,(R1)+ ;ADDRESS OF MESSAGE BUFFER  
CLR (R1)+ ;LENGTH OF MESSAGE BUFFER  
MOV #14,(R1)+  
CLR (R1)+  
CLR (R1)  
CLR T9BFR ;CLEAR 1ST LOC IN MESSAGE BUFFER  
RTS PC ;RETURN  
ENDTST

L10065: TRAP CSETST









































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2980 042510
2981 042510 100204
2982 042512 042520
2983 042514 000000
2984 042516 000010
2985
2986 042520
2987 042520 042532
2988 042522 000000
2989 042524 000016
2990 042526 000000 000000
2991
2992 042532
2993
2994
2995
2996
2997
2998 042552
2999 042552 100204
3000 042554 042562
3001 042556 000000
3002 042560 000010
3003
3004 042562
3005 042562 042574
3006 042564 000000
3007 042566 000016
3008 042570 000000 000000
3009
3010 042574
3011
3012
3013
3014
3015
3016
3017 042614 115 145 163
3018 042711 116 102 101
3019 042773 116 102 101
3020 043050 103 157 156
3021 043137 105 170 160
3022 043230 125 156 145
3023 043317 102 141 163
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```

```

;+
;LOCAL STORAGE FOR THIS TEST
;-

T10PACKET:
    .WORD 100204
    .WORD T10DATA
    .WORD 0
    .WORD 8.
;COMMAND PACKET FOR TEST
;WRITE CHAR COMMAND, WITH IE, ACK
;ADDRESS OF CHARACTERISTICS BLOCK
;STARTING VALUE OF BLOCK SIZE

T10DATA:
    .WORD T10BFR
    .WORD 0
    .WORD 14.
    .WORD 0.0
;CHARACTERISTICS DATA BLOCK
;ADDRESS OF MESSAGE BUFFER
;LENGTH OF MESSAGE BUFFER

T10BFR: .BLKW 8.
;MESSAGE BUFFER

;+
;TEST DATA FOR SUBTEST FOUR
;-

T10PKT:
    .WORD 100204
    .WORD T10DTA
    .WORD 0
    .WORD 8.
;COMMAND PACKET FOR TEST
;WRITE CHAR COMMAND, WITH IE, ACK
;ADDRESS OF CHARACTERISTICS BLOCK
;STARTING VALUE OF BLOCK SIZE

T10DTA:
    .WORD T10BUFR
    .WORD 0
    .WORD 14.
    .WORD 0.0
;CHARACTERISTICS DATA BLOCK
;ADDRESS OF MESSAGE BUFFER
;LENGTH OF MESSAGE BUFFER

T10BUFR: .BLKW 8.
;MESSAGE BUFFER

;+
;LOCAL TEXT MESSAGES FOR TEST
;-

T10MBF: .ASCIZ 'Message Buffer Modified after MESSAGE BUFFER RELEASE Command'
T10NBA: .ASCIZ 'NBA Not Clear After WRITE CHARACTERISTICS Command'
T10NNBA: .ASCIZ 'NBA Set After MESSAGE BUFFER RELEASE Command'
T10SSR: .ASCIZ 'Contents of TSSR Incorrect After WRITE CHARACTERISTICS'
T10NINT: .ASCIZ 'Expected Interrupt Not Received On WRITE CHARACTERISTICS'
T10INT: .ASCIZ 'Unexpected Interrupt Received On WRITE CHARACTERISTICS'
TST10ID: .ASCIZ 'Basic Packet Protocol'

.EVEN

```

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 3034 043346  
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 3036 043352 012701 042510  
 3037 043356 012721 100204  
 3038 043362 012721 042520  
 3039 043366 005021  
 3040 043370 012721 000010  
 3041 043374 012721 042532  
 3042 043400 005021  
 3043 043402 012721 000016  
 3044 043406 005021  
 3045 043410 005011  
 3046 043412 005037 042532  
 3047 043416 000207  
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 3054 043420  
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 3056 043424 012701 042552  
 3057 043430 012721 100204  
 3058 043434 012721 042562  
 3059 043440 005021  
 3060 043442 012721 000010  
 3061 043446 012721 042574  
 3062 043452 005021  
 3063 043454 012721 000016  
 3064 043460 005021  
 3065 043462 005011  
 3066 043464 005037 042574  
 3067 043470 000207  
 3068 043472  
 043472 104401

```

: +
: ROUTINE TO RESTORE COMMAND PACKET TO START-UP (DEFAULT) VALUES
: -
    
```

```

T10RST:
    SAVREG                ;SAVE THE REGISTERS
    MOV #T10PACKET,R1     ;START OF THE PACKET
    MOV #100204,(R1)+     ;WRITE CHARACTERISTICS WITH ACK, IE
    MOV #T10DATA,(R1)+    ;ADDRESS OF CHAR DATA BLOCK
    CLR (R1)+             ;EXTENDED ADDRESS
    MOV #8,(R1)+          ;SIZE OF DATA BLOCK IN BYTES
    MOV #T10BFR,(R1)+     ;ADDRESS OF MESSAGE BUFFER
    CLR (R1)+
    MOV #14,(R1)+        ;LENGTH OF MESSAGE BUFFER
    CLR (R1)+
    CLR (R1)
    CLR T10BFR           ;CLEAR 1ST LOC IN MESSAGE BUFFER
    RTS PC               ;RETURN
    
```

```

: +
: ROUTINE TO RESTORE COMMAND PACKET #2 TO START-UP (DEFAULT) VALUES
: -
    
```

```

T10RT2:
    SAVREG                ;SAVE THE REGISTERS
    MOV #T10PKT,R1        ;START OF THE PACKET
    MOV #100204,(R1)+     ;WRITE CHARACTERISTICS WITH ACK, IE
    MOV #T10DTA,(R1)+    ;ADDRESS OF CHAR DATA BLOCK
    CLR (R1)+             ;EXTENDED ADDRESS
    MOV #8,(R1)+          ;SIZE OF DATA BLOCK IN BYTES
    MOV #T10BUFR,(R1)+   ;ADDRESS OF MESSAGE BUFFER
    CLR (R1)+
    MOV #14,(R1)+        ;LENGTH OF MESSAGE BUFFER
    CLR (R1)+
    CLR (R1)
    CLR T10BUFR         ;CLEAR 1ST LOC IN MESSAGE BUFFER
    RTS PC               ;RETURN
    ENDTST
    
```

L10075: TRAP CSETST



























```

3382 044644          EXIT  TST          ;ALL DONE THIS TEST
      044644 104432
      044646 000762          TRAP      C$EXIT
                                   .WORD  L10102-.
3383
3384
3385      ;+
3386      ;LOCAL STORAGE FOR THIS TEST
3387      ;-
3391 044650          T11PACKET:      ;COMMAND PACKET FOR TEST
3392 044650 100204      .WORD      100204      ;WRITE CHAR COMMAND, WITH IE, ACK
3393 044652 044660      .WORD      T11DATA      ;ADDRESS OF CHARACTERISTICS BLOCK
3394 044654 000000      .WORD      0
3395 044656 000010      .WORD      8.          ;STARTING VALUE OF BLOCK SIZE
3396
3397 044660          T11DATA:          ;CHARACTERISTICS DATA BLOCK
3398 044660 044672      .WORD      T11BFR      ;ADDRESS OF MESSAGE BUFFER
3399 044662 000000      .WORD      0
3400 044664 000016      .WORD      14.         ;LENGTH OF MESSAGE BUFFER
3401 044666 000000 000000 .WORD      0,0
3402
3403 044672          T11BFR: .BLKW 8.          ;MESSAGE BUFFER
3404
3405
3407          .=<.+10>&177770
3409 044720          T11PK2:          ;COMMAND PACKET FOR TEST
3410 044720 100204      .WORD      100204      ;WRITE CHAR COMMAND, WITH IE, ACK
3411 044722 044730      .WORD      T11DTA      ;ADDRESS OF CHARACTERISTICS BLOCK
3412 044724 000000      .WORD      0
3413 044726 000010      .WORD      8.          ;STARTING VALUE OF BLOCK SIZE
3414
3415 044730          T11DTA:          ;CHARACTERISTICS DATA BLOCK
3416 044730 044742      .WORD      T11BF2      ;ADDRESS OF MESSAGE BUFFER
3417 044732 000000      .WORD      0
3418 044734 000016      .WORD      14.         ;LENGTH OF MESSAGE BUFFER
3419 044736 000000 000000 .WORD      0,0
3420
3421 044742          T11BF2: .BLKW 8.          ;MESSAGE BUFFER
3422
3423
3424
3425      ;+
3426      ;LOCAL TEXT MESSAGES FOR TEST
3427      ;-
3428
3429 044762          111      116      111  T11NBA: .ASCIZ  'INITIALIZE Command Not Accepted'
3430 045022          111      116      111  T112REJ: .ASCIZ  'INITIALIZE Not Rejected With Non-Zero Mode Field'
3431 045103          107      105      124  T113REJ: .ASCIZ  'GET STATUS Not Accepted'
3432 045133          107      105      124  T114REJ: .ASCIZ  'GET STATUS Not Rejected With Non-Zero Mode Field'
3433 045214          103      157      156  T11SSR: .ASCIZ  'Contents of TSSR Incorrect After INITIALIZE'
3434 045270          103      157      156  T11SR2: .ASCIZ  'Contents of TSSR Incorrect After GET STATUS'
3435 045344          105      170      160  T11NINT: .ASCIZ  'Expected Interrupt Not Received On INITIALIZE'
3436 045422          111      156      143  T11TSBA: .ASCIZ  'Incorrect TSBA Address After INITIALIZE'
3437 045472          116      157      156  TST11ID: .ASCIZ  'Non-Tape Motion Commands'
3438          .EVEN
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045552 005021  
045554 005021  
045556 005011  
045560 005037 044672  
045564 000207  
  
045566  
045566  
045572 012701 044650  
045576 012721 100217  
045602 005021  
045604 005021  
045606 005021  
045610 005021  
045612 005021  
045614 005021  
045616 005021  
045620 005011  
045622 005037 044672  
045626 000207  
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045630 104401  
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```

:+
:ROUTINE TO RESTORE COMMAND PACKET TO START-UP (DEFAULT) VALUES
:INITIALIZE COMMAND
:-

T11REST:
  SAVREG          :SAVE THE REGISTERS
  MOV #T11PACKET,R1 :START OF THE PACKET
  MOV #100213,(R1)+ :INITIALIZE WITH ACK, IE
  CLR (R1)+       :ADDRESS OF CHAR DATA BLOCK
  CLR (R1)+       :EXTENDED ADDRESS
  CLR (R1)+       :SIZE OF DATA BLOCK IN BYTES
  CLR (R1)+       :ADDRESS OF MESSAGE BUFFER
  CLR (R1)+       :LENGTH OF MESSAGE BUFFER
  CLR (R1)
  CLR T11BFR      :CLEAR 1ST LOC IN MESSAGE BUFFER
  RTS PC          :RETURN

:+
:ROUTINE TO RESTORE COMMAND PACKET TO START-UP (DEFAULT) VALUES
:GET STATUS COMMAND
:-

T11RT2:
  SAVREG          :SAVE THE REGISTERS
  MOV #T11PACKET,R1 :START OF THE PACKET
  MOV #100217,(R1)+ :GET STATUS WITH ACK, IE
  CLR (R1)+       :ADDRESS OF CHAR DATA BLOCK
  CLR (R1)+       :EXTENDED ADDRESS
  CLR (R1)+       :SIZE OF DATA BLOCK IN BYTES
  CLR (R1)+       :ADDRESS OF MESSAGE BUFFER
  CLR (R1)+       :LENGTH OF MESSAGE BUFFER
  CLR (R1)
  CLR T11BFR      :CLEAR 1ST LOC IN MESSAGE BUFFER
  RTS PC          :RETURN
  ENDTST

L10102: TRAP CSETST

ENDMOD

```



```

1          .TITLE  TSV6 - PARAMETER CODING
7
12
18
19 045632   TSV6::  BGNMOD  TSV6
   045632
20
21
22          .SBTTL  HARDWARE PARAMETER CODING SECTION
23
24          :++
25          : THE HARDWARE PARAMETER CODING SECTION CONTAINS MACROS
26          : THAT ARE USED BY THE SUPERVISOR TO BUILD P-TABLES.  THE
27          : MACROS ARE NOT EXECUTED AS MACHINE INSTRUCTIONS BUT ARE
28          : INTERPRETED BY THE SUPERVISOR AS DATA STRUCTURES.  THE
29          : MACROS ALLOW THE SUPERVISOR TO ESTABLISH COMMUNICATIONS
30          : WITH THE OPERATOR.
31          :--
32 045632   BGNHRD
   045632   .WORD  L10107-L$HARD/2
   045634   L$HARD::
33
34 045634   GPRMA  HPM1,0,0,160010,177776,YES      ;GET TSBA/TSDB REGISTER ADDRESS.
   045634   .WORD  T$CODE
   045636   .WORD  HPM1
   045640   .WORD  T$LOLIM
   045642   .WORD  T$HILIM
35 045644   GPRMA  HPM2,2,0,0,776,YES              ;GET VECTOR ADDRESS.
   045644   .WORD  T$CODE
   045646   .WORD  HPM2
   045650   .WORD  T$LOLIM
   045652   .WORD  T$HILIM
36          :GPRMD  HPM3,4,0,340,0,7,YES          ;GET INTERRUPT PRIORITY.
37 045654   ENDHRD
   .EVEN
   045654   L10107:
38 045654   104    105    126  HPM1:  .ASCIZ  'DEVICE ADDRESS (TSBA/TSDB) '
39 045710   111    116    124  HPM2:  .ASCIZ  'INTERRUPT VECTOR '
40 045734   111    116    124  HPM3:  .ASCIZ  'INTERRUPT PRIORITY '
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045764  
045764 000003  
045766  
045766 001130  
045770 046024  
045772 177777  
045774  
045774  
045774  
105 116 101  
111 116 110  
120 105 122  
120 105 122  
046134  
046134  
046400  
046400 000000  
046402 000000  
046404  
046404 000001

```

.SBTTL SOFTWARE PARAMETER CODING SECTION

:++
: THE SOFTWARE PARAMETER CODING SECTION CONTAINS MACROS
: THAT ARE USED BY THE SUPERVISOR TO BUILD P-TABLES. THE
: MACROS ARE NOT EXECUTED AS MACHINE INSTRUCTIONS BUT ARE
: INTERPRETED BY THE SUPERVISOR AS DATA STRUCTURES. THE
: MACROS ALLOW THE SUPERVISOR TO ESTABLISH COMMUNICATIONS
: WITH THE OPERATOR.
:--

      BGNSFT
      .WORD L10110-L$SOFT/2
L$SOFT::
:      GPRML   SPM1,0,-1,YES           ; GET TRANSPORT TEST FLAG.
:      GPRML   SPM4,2,-1,YES           ; GET ITERATION CONTROL.
      .WORD    T$CODE
      .WORD    SPM4
      .WORD    -1
:      GPRMD   SPM6,4,D,7777,0,7777,YES ; GET LOCAL ERROR LIMIT
:      GPRMD   SPM7,6,D,7777,0,7777,YES ; GET GLOBAL ERROR LIMIT
      ENDSFT
      .EVEN
L10110:
      SPM1:   .ASCIZ  'ENABLE TRANSPORT TESTS '
      SPM4:   .ASCIZ  'INHIBIT ITERATIONS '
      SPM6:   .ASCIZ  'PER TEST ERROR LIMIT '
      SPM7:   .ASCIZ  'PER UNIT ERROR LIMIT '
      .SBTTL  PATCH AREA

:
: FINALLY A GENEROUS PATCH AREA.
:
: AND AN ADJUSTMENT TO ACCOUNT FOR THE "LASTAD BIT7" HACK
: DESCRIBED IN "SUPPRG.MEM" (FOR REV C).
:
PATCH::
      .BLKW   32.
      .=.!377+1
      LASTAD           ;SET LAST USED ADDRESS.
      .EVEN
      .WORD    0
      .WORD    0
L$LAST::
      ENDMOD
      .END
    
```



ADSSR 012116 G	C\$AU = 000052	DEVDR0 023366	FRESIZ 003122 G	INTFLA 016135
ADR = 000020 G	C\$AUTO= 000061	DEVNRD 023305	FUSI 004113	INTMAS 016134
AMBTSS 006631	C\$BRK = 000022	DEVNXR 023223	F\$AU = 000015	INTR 016206 G
ASSEMB= 000010	C\$BSEG= 000004	DEVONL 023153	F\$AUTO= 000020	INTREC 002222 G
A1716 = 000003	C\$BSUB= 000002	DEVSUM 023116	F\$BGN = 000040	INTVEC 016136
BADDAT 003152 G	C\$CEFG= 000045	DFPTBL 002154 G	F\$CLEA= 000007	INTX 004274
BADSSR 015670 G	C\$CLCK= 000062	DIAGMC= 000000	F\$DU = 000016	INVERT 021176 G
BDVPCR= 177520 G	C\$CLEA= 000012	DICEA = 000001	F\$END = 000041	IOKCKI= 000200
BENBSW 002226 G	C\$CLOS= 000035	DSBINT 016174	F\$HARD= 000004	IOKSTP= 000001
BIE = 040000	C\$CLP1= 000006	DUAD12 004637	F\$HW = 000013	IPRI 002210 G
BIT0 = 000001 G	C\$CVEC= 000036	DUFLG 003106 G	F\$INIT= 000006	ISR = 000100 G
BIT00 = 000001 G	C\$DCLN= 000044	DUMMY 003056	F\$JMP = 000050	IVEC 002206 G
BIT01 = 000002 G	C\$DODU= 000051	EF.CON= 000036 G	F\$MOD = 000000	IXE = 004000 G
BIT02 = 000004 G	C\$DRPT= 000024	EF.NEW= 000035 G	F\$MSG = 000011	ISAU = 000041
BIT03 = 000010 G	C\$DU = 000053	EF.PWR= 000034 G	F\$PROT= 000021	ISAUTO= 000041
BIT04 = 000020 G	C\$EDIT= 000003	EF.RES= 000037 G	F\$PWR = 000017	ISCLN = 000041
BIT05 = 000040 G	C\$ERDF= 000055	EF.STA= 000040 G	F\$RPT = 000012	ISDU = 000041
BIT06 = 000100 G	C\$ERHR= 000056	EMAXDU 016767	F\$SEG = 000003	ISHRD = 000041
BIT07 = 000200 G	C\$ERRO= 000060	EN = 000000	F\$SOFT= 000005	ISINIT= 000041
BIT08 = 000400 G	C\$ERSF= 000054	ENAINI 016142	F\$SRV = 000010	ISMOD = 000041
BIT09 = 001000 G	C\$ERSO= 000057	ENVIRN 020620	F\$SUB = 000002	ISMSG = 000041
BIT1 = 000002 G	C\$ESCA= 000010	EPRTSW 002176 G	F\$SW = 000014	ISPROT= 000040
BIT10 = 002000 G	C\$ESEG= 000005	EPRT1 006354	F\$TEST= 000001	ISPTAB= 000041
BIT11 = 004000 G	C\$ESUB= 000003	EPRT2 006354	GDDAT 003154 G	ISPWR = 000041
BIT12 = 010000 G	C\$ETST= 000001	ERCM 011723	GERRMA 002172 G	ISRPT = 000041
BIT13 = 020000 G	C\$EXIT= 000032	ERRHI 002234 G	GETPAT 020164 G	ISSEG = 000041
BIT14 = 040000 G	C\$GETB= 000026	ERRK 016746	GETSEL 020246 G	ISSETU= 000041
BIT15 = 100000 G	C\$GETW= 000027	ERRLO 002236 G	G\$CNTO= 000200	ISSFT = 000041
BIT2 = 000004 G	C\$GMAN= 000043	ERRNO = 002144	G\$DELM= 000372	ISSRV = 000041
BIT3 = 000010 G	C\$GPHR= 000042	ERRVEC= 000004 G	G\$DISP= 000003	ISSUB = 000041
BIT4 = 000020 G	C\$GPLO= 000030	ERTABE 003372	G\$EXCP= 000400	ISTST = 000041
BIT5 = 000040 G	C\$GPRI= 000040	ERTABL 003172	G\$HILI= 000002	JSJMP = 000167
BIT6 = 000100 G	C\$INIT= 000011	ESUM 016750	G\$LOLI= 000001	KIPAR0= 172340
BIT7 = 000200 G	C\$INLP= 000020	EVL = 000004 G	G\$NO = 000000	KIPAR1= 172342
BIT8 = 000400 G	C\$MANI= 000050	EXBCNT= 000010	G\$OFFS= 000400	KIPAR2= 172344
BIT9 = 001000 G	C\$MEM = 000031	EXPBRE 015472 G	G\$OFSI= 000376	KIPAR3= 172346
BOE = 000400 G	C\$MSG = 000023	EXPD 002230 G	G\$PRMA= 000001	KIPAR4= 172350
BRINIT 004453	C\$OPEN= 000034	EXPBOT 004527	G\$PRMD= 000002	KIPAR5= 172352
BSELO = 000000	C\$PNTB= 000014	EXPOT2 004563	G\$PRML= 000000	KIPAR6= 172354
BSEL1 = 000001	C\$PNTF= 000017	EXPMSG 002320 G	G\$RADA= 000140	KIPAR7= 172356
CHKAMB 016034	C\$PNTS= 000016	EXPREC 015464 G	G\$RADB= 000000	KIPDR0= 172300
CHKMAN 020470 G	C\$PNTX= 000015	EXTA 005766	G\$RADD= 000040	KIPDR1= 172302
CHKTSS 016326	C\$QIO = 000377	EXTEND 005764	G\$RADL= 000120	KIPDR2= 172304
CKDROP 017172	C\$RDBU= 000007	EXTFEA 002224 G	G\$RADO= 000020	KIPDR3= 172306
CKEMAX 017072	C\$REFG= 000047	E\$END = 002100	G\$XFER= 000004	KIPDR4= 172310
CKMSG 011350 G	C\$RESE= 000033	E\$LOAD= 000035	G\$YES = 000010	KIPDR5= 172312
CKMSG2 011470 G	C\$REVI= 000003	FATERR= 000060	HIADDR= 001400	KIPDR6= 172314
CKRAM 011104 G	C\$RFLA= 000021	FATFLG 002220 G	HOE = 100000 G	KIPDR7= 172316
CKRAM2 011214 G	C\$RPT = 000025	FERCM 011712	HPM1 045654	KTENAB 003130 G
CMDPKT 021250 G	C\$SEFG= 000046	FIFEXP 012160 G	HPM2 045710	KTF LG 003126 G
CMPMEM 017650	C\$SPRI= 000041	FIF1MS 012232	HPM3 045734	KTINIT 021044
CONF IG 017240	C\$SVEC= 000037	FIF2MS 012301	IBE = 010000 G	KTOFF 017264
COUNT 002306 G	C\$TPRI= 000013	FILLME 017412	IDU = 000040 G	KTON 017246
CSRADD 002204 G	DATA 002310 G	FNOINT 004211	IER = 020000 G	LERRMA 002170 G
CTAB 003160 G	DATASC 020222	FORCER 002174 G	IFAU LT 004252	LISTAL= 000001
CTABE 003172 G	DEBUGM 011622	FREE 003120 G	INCERK 017034	LOE = 040000 G
CTABM 003160 G	DEV CNT 002216 G	FREEHI 003124	INTCPC 016140	LOOPCN 002214 G



SYMBOL		TABLE	
LOOPCO	013116	L10001	002174
LOOPFL	003156 G	L10002	005762
LOT	= 000010 G	L10003	012034
LSACP	002110 G	L10004	012052
LSAPT	002036 G	L10005	012070
LSAU	022342 G	L10006	012076
LSAUT	002070 G	L10007	012114
LSAUTO	022546 G	L10010	012132
LSCCP	002106 G	L10011	012156
LSCLEA	022626 G	L10012	012230
LSCO	002032 G	L10013	012400
LSDEPO	002011 G	L10014	013114
LSDESC	003404 G	L10015	013742
LSDESP	002076 G	L10016	013764
LSDEVP	002060 G	L10017	015470
LSDISP	002124 G	L10020	015476
LSDLY	002116 G	L10021	015504
LSDTP	002040 G	L10022	015516
LSDTYP	002034 G	L10023	015540
LSDU	022440 G	L10024	015566
LSDUT	002072 G	L10025	015726
LSDVTY	003376 G	L10026	016236
LSEF	002052 G	L10030	022272
LSENVI	002044 G	L10031	022436
LSETP	002102 G	L10032	022544
LSEXP1	002046 G	L10033	022624
LSEXP4	002064 G	L10034	022652
LSEXP5	002066 G	L10035	023114
LSHARD	045634 G	L10036	023654
LSHIME	002120 G	L10037	023522
LSHPCP	002016 G	L10040	023604
LSHPTP	002022 G	L10041	024352
LSHW	002154 G	L10042	025044
LSICP	002104 G	L10043	026400
LSINIT	021546 G	L10044	025306
LSLADP	002026 G	L10045	025606
LSLAST	046404 G	L10046	026104
LSLOAD	002100 G	L10047	027504
LSLUN	002074 G	L10050	026752
LSMREV	002050 G	L10051	027322
LSNAME	002000 G	L10052	030762
LSPRIO	002042 G	L10053	030034
LSPROT	021536 G	L10054	030360
LSPRT	002112 G	L10055	034350
LSREPP	002062 G	L10056	031314
LSREV	002010 G	L10057	031600
LSRPT	022654 G	L10060	032044
LSOFT	045766 G	L10061	032272
LSSPC	002056 G	L10062	032536
LSSPCP	002020 G	L10063	033076
LSSPTP	002024 G	L10064	035254
LSSTA	002030 G	L10065	040370
LSSW	002164 G	L10066	035516
LSTEST	002114 G	L10067	035756
LSTIML	002014 G	L10070	036162
LSUNIT	002012 G	L10071	036350
L10000	002162	L10072	036554
		L10073	037006
		L10074	037212
		L10075	043472
		L10076	040772
		L10077	041364
		L10100	042254
		L10101	042502
		L10102	045630
		L10103	043740
		L10104	044176
		L10105	044416
		L10106	044642
		L10107	045654
		L10110	045774
		MEMADD	013744 G
		MEMCK	021266 G
		MENASC	020437
		MENERR	020364
		MENRES	020466
		MMVEC	= 000250
		MSA.FR	= 000006
		MSA.NO	= 000000
		MSA.NR	= 000004
		MSA.VO	= 000002
		MSGEXP	012134 G
		MSGLOO	013054 G
		MSGSTA	012340 G
		MSGSUB	013732 G
		MS.ATT	= 000006
		MS.EXT	= 000200
		MS.RSD	= 000001
		MS.RSF	= 000020
		MS.RST	= 000010
		M2901	026124
		M8186	005550
		M8189	005641
		NBA	= 002000
		NEWPAS	022000
		NODEV	003110 G
		NOINIT	004331
		NOINTR	004215
		NOITS	002166 G
		NOMAN	020524
		NOMEM	005454
		NP.IR	= 000200
		NP.LOO	= 000040
		NP.OUT	= 000100
		NP.WRP	= 000020
		NSI	004146
		NSINIT	004403
		NUL	004523
		NULCR	004524
		NXM	= 004000
		NXMFLG	003132 G
		NXMHI	003136 G
		NXMLO	003134 G
		NXMTST	021442
		NXR	003734
		NXRERR	005732 G
		NXRX	003773
		NXTU	022012
		OFL	= 000100
		ONEFIL	= 000000
		OSAPTS	= 000000
		OSAU	= 000001
		OSBGNR	= 000001
		OSBGNS	= 000001
		OSDU	= 000001
		OSERRT	= 000000
		OSGNSW	= 000001
		OSPOIN	= 000001
		OSSETU	= 000000
		PASRPT	022044
		PATCH	046134 G
		PATDAT	020220
		PC.ERA	= 002400
		PC.IER	= 002000
		PC.NOO	= 001000
		PC.REL	= 000000
		PC.REW	= 000400
		PKBCNT	= 000006
		PKHI	= 000004
		PKLOW	= 000002
		PKTADD	007550
		PKTFRM	007512
		PKTGET	012054 G
		PKTMES	012100 G
		PKTRAM	004741 G
		PKTSSR	012036 G
		PNT	= 001000 G
		PRAMPK	013766
		PRASC	014513
		PRBEXP	015460
		PRBMSG	015326
		PRBREC	015462
		PRBTOT	015413
		PRBYTE	015112 G
		PRI	= 002000 G
		PRIADD	010154
		PRIAO	010224
		PRIBXO	007606 G
		PRIEQU	010054
		PRIPKT	007364 G
		PRIRAM	010062
		PRITAD	010270
		PRITSS	006020
		PRITO	010352
		PRITI	010415
		PRIXOR	007736 G
		PRI00	= 000000 G
		PRI01	= 000040 G
		PRI02	= 000100 G
		PRI03	= 000140 G
		PRI04	= 000200 G
		PRI05	= 000240 G
		PRI06	= 000300 G
		PRI07	= 000340 G
		PRMESS	014232
		PRMNO	002316 G
		PRMSGE	014542 G
		PRMSG0	014722
		PRMSG1	014767
		PRMSG2	015025
		PROASC	014410
		PR1ASC	014455
		PST32W	003146 G
		PUNIT	022274
		PW.D11	= 000021
		PW.D13	= 000022
		PW.D22	= 000020
		PW.NOP	= 000000
		PW.NO1	= 000023
		PW.RDE	= 000024
		PW.RDR	= 000001
		PW.RDS	= 000005
		PW.RFI	= 000003
		PW.WCT	= 000006
		PW.WFI	= 000004
		PW.WFM	= 000007
		PW.WMI	= 000010
		PW.WNP	= 000011
		PW.WTR	= 000002
		P.ACK	= 100000
		P.CMD	= 000037
		P.CONT	= 000012
		P.CVC	= 040000
		P.FMT	= 000140
		P.FORM	= 000011
		P.GETS	= 000017
		P.IE	= 000200
		P.INIT	= 000013
		P.MODE	= 007400
		P.OPP	= 020000
		P.POSI	= 000010
		P.READ	= 000001
		P.SWB	= 010000
		P.WRIT	= 000005
		P.WRTC	= 000004
		P.WRTS	= 000006
		QVP	002202 G
		RAMASC	014146
		RAMDAT	002240 G
		RAMERR	015500 G
		RAMEXP	015520 G
		RAMFOR	010112
		RAMSIZ	002300 G
		RAMTAD	015506 G
		RCVHIA	002302 G
		RCVLOA	002304 G
		RDERR	005202
		RECMG	002464 G



SYMBOL TABLE	
RECV	002232 G
REGSAV	020130
RETERR	005366
REWIND	011004 G
RMCHBE=	000167
RMCHEN=	000200
RMM5GB=	000215
RMM5GE=	000234
RMPKTB=	000201
RMPKTE=	000210
RMR	= 010000
RWPACK	011100
SC	= 100000
SCE	= 020000
SCHERR	005274
SCME	005007
SDELAY	010650
SELASC	020432
SELDAT=	000004
SEL2	= 000002
SETMAP	017306
SETU	022076
SFFMSG	012072 G
SFHERR	003701
SFIERR	003646
SFIMSG	012024 G
SFPTBL	002164 G
SIFLAG	003150 G
SIMSG	011756
SKIPT	003374
SOFINI	015764 G
SPACE	010462 G
SPM1	045774
SPM4	046024
SPM6	046054
SPM7	046104
SRO	= 177572
SR1	= 177574
SR2	= 177576
SR3	= 172516
SSR	= 000200
STATCO	012402
SVCGBL=	000000
SVCINS=	000000
SVC5UB=	000001
SVCTAG=	000000
SVCTST=	000001
S\$LSYM=	010000
SO.IDB=	000010
SO.IFB=	000002
SO.IFP=	000001
SO.ILD=	000020
SO.ION=	000040
SO.IRD=	000100
SO.IRW=	000004
SO.ISP=	000200
S1.ICE=	002000
S1.IEO=	010000
S1.IFM=	001000
S1.IHE=	000400
S1.IID=	004000
S1.IIR=	020000
S1.IZR=	040000
S1.PAR=	100000
S2.ATI=	000010
S2.BTI=	000004
S2.DIM=	000200
S2.ILW=	000100
S2.INR=	000020
S2.OUT=	000040
S2.UND=	000003
TBLEND=	003056 G
TCOASC	006472
TCOCOD	006672
TEMP1	003112 G
TEMP2	003114 G
TERCLS=	000016
TESTNO=	000013
TEXASC	006431
TFCASC	006533
TIMEXP	015542 G
TIMSGO	015570
TINERR	012011
TMPBFR	002630 G
TNAM	016674
TRANST	002164 G
TSBA	= 000000 G
TSBAH	= 000001 G
TSBAM2	026214
TSBAM3	026276
TSDB	= 000000 G
TSDBH	= 000001 G
TSFCOD	007232
TSREJ	= 000006
TSSDEF	006602
TSSR	= 000002 G
TSSRBI	003476 G
TSSRFO	006411
TSSRH	= 000003 G
TSSX	004014
TSTBLK	002750 G
TSTCNT	002212 G
TSTEND	016710
TSTFLA	002312 G
TSTLOO	016446 G
TSTPTR	002314 G
TSTSET	016500 G
TST1ID	023634
TST10I	043317
TST11I	045472
TST2ID	024324
TST3ID	025017
TST4ID	026356
TST5ID	027354
TST6ID	030743
TST7ID	034253
TST8ID	035237
TST9ID	040271
TSV2	002000 G
TSV3	002174 G
TSV4	021536 G
TSV5	023436 G
TSV6	045632 G
TTIBFR=	177562 G
TTICSR=	177560 G
TTIVEC=	000060 G
TSARGC=	000003
TSCODE=	001130
TSERRN=	002144
TSEXCP=	000000
TSFLAG=	000040
TSGMAN=	000000
TSHILI=	000776
TSLAST=	000001
T\$LOLI=	000000
T\$LSYM=	010000
T\$LTNO=	000013
T\$NEST=	177777
T\$NSO	= 000000
T\$NS1	= 000005
T\$NS2	= 000002
T\$NS3	= 000003
T\$PTNU=	000000
T\$SAVL=	177777
T\$SEGL=	177777
T\$SEK0=	010000
T\$SUBN=	000004
T\$TAGL=	177777
T\$TAGN=	010111
T\$TEMP=	000000
T\$TEST=	000013
T\$STM=	177777
T\$ST\$S=	000001
T\$SAU	= 010031
T\$SAUT=	010033
T\$SCLE=	010034
T\$SDU	= 010032
T\$SHAR=	010107
T\$SHW	= 010000
T\$SINI=	010030
T\$SM5G=	010025
T\$SPRO=	010027
T\$SRPT=	010035
T\$SSEG=	010000
T\$S\$OF=	010110
T\$SSRV=	010026
T\$SSUB=	010106
T\$SSW	= 010001
T\$STES=	010102
T1	023436 G
T1LOOP	023454
T1.1	023456
T1.2	023536
T10	040372 G
T10BFR	042532 G
T10BUF	042574
T10DAT	042520
T10DTA	042562
T10INT	043230
T10LOO	040410
T10MBF	042614
T10NBA	042711
T10NIN	043137
T10NNB	042773
T10PAC	042510
T10PKT	042552
T10RST	043346
T10RT2	043420
T10SSR	043050
T10.1	040410
T10.2	040774
T10.3	041366
T10.4	042256
T11	043474 G
T11BFR	044672 G
T11BF2	044742
T11DAT	044660
T11DTA	044730
T11LOO	043512
T11NBA	044762
T11NIN	045344
T11PAC	044650
T11PK2	044720
T11RES	045524
T11RT2	045566
T11SR2	045270
T11SSR	045214
T11TSB	045422
T11.1	043512
T11.2	043754
T11.3	044200
T11.4	044420
T112RE	045022
T113RE	045103
T114RE	045133
T2	023656 G
T2LOOP	023674
T2SSR	024252
T2TSBA	024140
T2TSSR	024205
T23A	003140 G
T23B	003142 G
T3	024354 G
T3BFLG	003144 G
T3LOOP	024372
T3SSR	024746
T3TSBA	024636
T3TSSR	024702
T4	025046 G
T4LOOP	025066
T4.1	025046
T4.2	025310
T4.3	025610
T5	026402 G
T5ADDR	027442
T5LOOP	026420
T5MEM	027404
T5.1	026424
T5.2	026772
T6	027506 G
T6INT	030533
T6LOOP	027524
T6NBA	030430
T6NINT	030611
T6PACK	030420
T6SSR	030455
T6TSBA	030671
T6.1	027524
T6.2	030050
T7	030764 G
T7BFR	033146
T7DATA	033130
T7INT	034101
T7LOOP	031002
T7NBA	033260
T7NINT	034010
T7PACK	033120
T7REST	034302
T7SP	033140
T7SSR	033721
T7TSBA	034170
T7.1	031002
T7.2	031330
T7.3	031602
T7.4	032046
T7.5	032274
T7.6	032540
T72DAT	033166
T72DON=	033202
T72NBA	033202
T72REJ	033333
T73REJ	033432
T74REJ	033525
T75REJ	033623
T8	034352 G
T8BFR	035002
T8DATA	034770
T8LOOP	034370
T8NVCK	035057
T8PACK	034760
T8SSR	035150
T8VCK	035022
T9	035256 G
T9BFR	037242
T9DATA	037230



T9INT	040117	UAM	= 000200 G	WRTCHR	010652 G	XS00NL	= 000100	X2.EXT	= 000200
T9LOOP	035300	UNITN	002200 G	WRTERR	005107	XSOPED	= 000010	X2.OPM	= 100000
T9NBA	037276	UNREC	= 000006	WRTMSG	005052	XSORLL	= 010000	X2.RCE	= 040000
T9NINT	040026	USI	004117	WSMBK	021260 G	XSORLS	= 040000	X2.REV	= 000077
T9PACK	037220	WAITF	016240 G	XFERAS	015730	XSOTMK	= 100000	X2.SPA	= 035400
T9REST	040316	WC.IFA	= 000200	XNXM	016366	XSOVCK	= 000020	X2.UNI	= 000007
T9SSR	037737	WC.IFE	= 000002	XORBFO	007670	XSOWLE	= 004000	X2.WCF	= 002000
T9TSBA	040206	WC.IGO	= 000001	XORFOR	010006	XSOWLK	= 000004	X3.DCK	= 000010
T9.1	035300	WC.IRE	= 000010	XST0	= 000006 G	XXCOMM	003116 G	X3.MBZ	= 000006
T9.2	035546	WC.IRW	= 000004	XST1	= 000010 G	XSALWA	= 000000	X3.MDE	= 177400
T9.3	035760	WC.IOT	= 000100	XST2	= 000012 G	XSFALS	= 000040	X3.OPI	= 000100
T9.4	036164	WC.IIT	= 000040	XST3	= 000014 G	XSOFFS	= 000400	X3.REV	= 000040
T9.5	036352	WC.ISR	= 000020	XST4	= 000016 G	XSTRUE	= 000020	X3.RIB	= 000001
T9.6	036556	WF.IED	= 000010	XS0BOT	= 000002	X1.COR	= 020000	X3.SPA	= 000200
T9.7	037010	WF.IER	= 000004	XS0EOT	= 000001	X1.DLT	= 100000	X3.TRF	= 000020
T92DAT	037262	WF.IHI	= 000200	XS0IE	= 000040	X1.MBZ	= 017375	X4.HSP	= 100000
T92DON	037276	WF.IRE	= 000040	XS0ILA	= 000400	X1.RBP	= 000400	X4.MBZ	= 017400
T92REJ	037351	WF.IWF	= 000020	XS0ILC	= 001000	X1.SPA	= 040000	X4.RCE	= 040000
T93REJ	037450	WF.IWR	= 000100	XS0LET	= 020000	X1.UNC	= 000002	X4.TSM	= 020000
T94REJ	037543	WF.I3R	= 000002	XS0MOT	= 000200	X2.BUF	= 000100	X4.WRC	= 000377
T95REJ	037641	WF.I4R	= 000001	XS0NEF	= 002000				

. ABS. 046404 000  
000000 001

ERRORS DETECTED: 0

VIRTUAL MEMORY USED: 28224 WORDS ( 111 PAGES)

DYNAMIC MEMORY: 20346 WORDS ( 78 PAGES)

ELAPSED TIME: 00:34:19

CVTSA0, CVTSA0/-SP=SVC/ML, TSV1A, TSV22A, TSV3A, TSV4, TSV5A, TSV6