

# RP04/5/6

RP04/5/6 FMTR  
CZRJBDO

AH-9187D-MC

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FICHE 1 OF 1

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I D E N T I F I C A T I O N

PRODUCT CODE: AC-9185D-MC  
PRODUCT NAME: CZRJBD0 RP04/5/6 FORMATTER PROGRAM  
PRODUCT DATE: MAY, 1979  
MAINTAINER: DIAGNOSTIC ENGINEERING  
AUTHOR: C. HESS

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CONTENTS

44  
45  
46  
47  
48  
49  
50  
51  
52  
53  
54  
55  
56  
57  
58  
59  
60  
61  
62  
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64  
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66  
67  
68  
69  
70  
71  
72  
73  
74  
75  
76  
77  
78  
79  
80  
81

- 1. ABSTRACT
- 2. REQUIREMENTS
  - 2.1 EQUIPMENT
  - 2.2 PRELIMINARY PROGRAMS
  - 2.3 PROGRAMMABLE DRIVES
- 3. LOADING PROCEDURES
- 4. STARTING PROCEDURES
  - 4.1 STARTING ADDRESSES
  - 4.2 OPERATOR ACTION
  - 4.3 RH11 - RH70 UNIBUS ADDRESS
  - 4.4 OTHER UNIBUS ADDRESSES
- 5. SWITCH REGISTER SETTINGS
- 6. ERROR MESSAGES
- 7. MISCELLANEOUS
  - 7.1 FORMAT TIMES
  - 7.2 HALTING THE PROGRAM
  - 7.3 SURFACE VERIFICATION
- 8. PROGRAM DESCRIPTION
  - 8.1 FORMAT OPERATION
  - 8.2 CHECK OPERATION
  - 8.3 POSITIONER VERIFICATION
- 9. PROGRAM LISTING

82  
83  
84  
85  
86  
87  
88  
89  
90  
91  
92  
93  
94  
95  
96  
97  
98  
99  
100  
101  
102  
103  
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1. ABSTRACT  
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THE RP04/5/6 FORMATTER PROGRAM FORMATS THE DISK PACK AND PERFORMS A CURSORY CHECK OF THE PACK'S SURFACE. THE PROGRAM ALLOWS THE OPERATOR TO SPECIFY ADDRESS LIMITS, PATTERNS, AND EITHER 16 BIT OR 18 BIT FORMAT MODE. THE PROGRAM VERIFIES EACH TRACK WRITTEN AS WELL AS VERIFYING THE FORMAT OPERATION.

2. REQUIREMENTS  
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2.1 EQUIPMENT

PDP-11 PROCESSOR  
8K MEMORY  
TELETYPE  
PROGRAM LOAD DEVICE  
KW11-L OR KW11-P CLOCK  
RH11 OR RH70 WITH 1 - 8 RP04, RP05, RP06 DISK DRIVES

2.2 PRELIMINARY PROGRAMS

RP04/5/6 DISKLESS CONTROLLER TEST  
PART 1 (MAINDEC-11-DZRJG)  
PART 2 (MAINDEC-11-DZRJH)

RP04/5/6 FUNCTIONAL CONTROLLER TEST  
PART 1 (MAINDEC-11-DZRJI)  
PART 2 (MAINDEC-11-DZRJJ)

2.3 PROGRAMMABLE DRIVES (DUAL PORT ENABLED)

THIS REV INCORPORATES A SAFEGUARD TO PREVENT INADVERTENT CORRUPTION OF DISK PACKS IN PROGRAMMABLE DRIVES. THIS IS A POTENTIAL HAZARD IN RUNNING THIS PROGRAM IN A MULTIPROCESSOR SYSTEM. FOR THE STANDARD STARTING ADDRESS OF 200 THE PROGRAM HAS BEEN MODIFIED TO PREVENT INITIALIZING DRIVES FOUND TO BE PROGRAMMABLE. THIS MODIFICATION APPLIES ONLY TO THE FIELD ENVIRONMENT (XXDP CHAIN, STANDALONE) WHERE LOCATION 42 DOES NOT EQUAL LOCATION 46. FOR THE MANUFACTURING ENVIRONMENT (WHERE LOCATION 42 EQUALS LOCATION 46) PROGRAMMABLE DRIVES WILL NOT BE INHIBITED. IF THE OPERATOR DESIRES TO RUN THIS PROGRAM USING PROGRAMMABLE DRIVES IN A FIELD ENVIRONMENT USE STARTING ADDRESS 220, WHERE 220 IS THE SAME AS 200 WITHOUT INHIBITING PROGRAMMABLE DRIVES. SEE SECTION 4.1 FOR A SUMMARY OF ALL STARTING ADDRESSES.

3. LOADING PROCEDURES  
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THE PROGRAM MAY BE LOADED FROM PAPER TAPE USING THE ABSOLUTE LOADER OR IT MAY BE LOADED FROM THE APPROPRIATE 'XXDP' MEDIA USING THE ASSOCIATED LOADER. THE PROGRAM MAY NOT BE INCLUDED IN AN 'XXDP' CHAIN.

4. STARTING PROCEDURES

4.1 STARTING ADDRESSES

THE PROGRAM IS STARTED FROM LOCATION 200(8) IF THE ADDRESS OF THE RH11 OR RH70 WILL NOT BE CHANGED PROGRAMMABLE DRIVES ARE INHIBITED, STANDALONE AND XXDP CHAIN-SEE SECTION 2.3

THE PROGRAM IS STARTED FROM LOCATION 204(8) IF THE ADDRESS OF THE RH11 OR RH70 IS TO BE CHANGED FROM THE PRELOADED VALUE. (SEE SECTION 4.3) PROGRAMMABLE DRIVES ARE NOT INHIBITED.

STARTING ADDRESS 220 IS THE SAME AS 200 BUT WITH NO INHIBITIONS.

4.2 OPERATION ACTION

1. LOAD THE PROGRAM INTO MEMORY (SEE SECTION 3).
2. LOAD THE STARTING ADDRESS - 200(8) OR 204(8) OR 220(8).
3. SET THE SWITCHES AS REQUIRED AND PRESS 'START'.

IF THIS IS THE PROGRAM'S FIRST START, THE STATUS OF THE DRIVES ON THE SELECTED MASSBUS SUBSYSTEM WILL BE TYPED OUT. THIS TYPEOUT MAY BE INHIBITED ON SUBSEQUENT STARTS BY SETTING SW<02>.

4. THE PROGRAM WILL THEN TYPE THE FOLLOWING MESSAGE:

'PROGRAM MODE (C OR F):'

ENTER THE APPROPRIATE CODE: 'C' FOR 'CHECK' OPERATION OR 'F' FOR 'FORMAT & VERIFY'. IF A 'CARRIAGE RETURN' IS ENTERED IN RESPONSE TO THE REQUEST, THE PROGRAM WILL ASSUME 'FORMAT & VERIFY'.

5. THE PROGRAM WILL THEN ASK FOR THE FORMATTING MODE:

'OPERATE IN 22 SECTOR (16 BIT) MODE (Y OR N)'

ENTER THE APPROPRIATE CHARACTER: 'Y' FOR 16 BIT MODE OR 'N' FOR 18 BIT MODE. IF A 'CARRIAGE RETURN' IS ENTERED IN RESPONSE TO THIS REQUEST, THE PROGRAM WILL ASSUME 16 BIT MODE.

6. THE PROGRAM WILL THEN ASK FOR A DRIVE:

'DRIVE: '

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ENTER THE ADDRESS OF THE DRIVE TO BE FORMATTED. A 'PERIOD' OR 'CARRIAGE RETURN' ENTRY WILL SELECT DRIVE 0. IF THE DRIVE SELECTED IS NOT AVAILABLE, THE PROGRAM WILL TYPE AN ERROR MESSAGE AND RETURN TO THE DRIVE ADDRESS REQUEST.

7. AFTER THE OPERATOR HAS SELECTED A DRIVE, THE PROGRAM WILL ASK FOR ADDRESS LIMITS FOR THE SELECTED DRIVE:

'ENTER ADDRESS LIMITS: '

THE PREVIOUSLY SELECTED OR DEFAULT VALUES FOR BEGINNING CYLINDER AND TRACK AND FOR ENDING CYLINDER AND TRACK WILL BE TYPED OUT. IF A 'CARRIAGE RETURN' IS TYPED AS A RESPONSE, THE PRESENT VALUE WILL BE USED; IF A 'PERIOD' IS TYPED, THE PROGRAM WILL BYPASS THE REMAINING ENTRIES AND WILL USE THEIR PRESENT VALUES. NOTE THAT THE CYLINDER AND TRACK VALUES ARE D E C I M A L NUMBERS. THE ADDRESS SPECIFIED BY THE BEGINNING CYLINDER AND TRACK MUST BE LESS THAN THE ADDRESS SPECIFIED BY THE ENDING CYLINDER AND TRACK ADDRESS.

7. THE PROGRAM WILL THEN ASK FOR THE DATA PATTERN:

'SELECT DATA PATTERN  
(0) ZERO'S  
(1) ONES  
(2) WORST CASE:'

ENTER THE CODE FOR THE REQUIRED PATTERN. 'CARRIAGE RETURN' OR 'PERIOD' ENTRIES WILL CAUSE THE PROGRAM TO USE THE 'WORST CASE' PATTERN.

THE 'WORST CASE' PATTERN IS THE FOLLOWING OCTAL SEQUENCE REPEATED THROUGH THE DATA AREA OF THE SECTOR:

165555  
133333

8. THE PROGRAM WILL THEN TYPE:

'STARTING FORMAT ON DRIVE N'

OR

'STARTING CHECK ON DRIVE N'

9. THE OPERATOR CAN DETERMINE WHERE THE DRIVE IS DURING THE FORMAT OR CHECK OPERATION BY TYPING A 'CONTROL C'. THE PROGRAM WILL TYPE THE FOLLOWING MESSAGE:

'PRESENT ADDRESS IS: CXXX TXX'

IF A 'CONTROL C' IS TYPED WHILE THE PROGRAM IS TYPING THE CURRENT ADDRESS, THE PROGRAM WILL ABORT THE FORMAT (OR CHECK) OPERATION AND RETURN TO THE 'DRIVE' REQUEST.

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10. IF THE OPERATOR DOES NOT SELECT A DIFFERENT DRIVE WHEN THE FORMAT OR CHECK OPERATION HAS COMPLETED, THE PROGRAM WILL NOT ALTER THE ADDRESS LIMITS SPECIFIED AT THE START OF THE PREVIOUS OPERATION. IF THE FORMAT OR CHECK OPERATION IS TERMINATED BY A 'CONTROL C' OR IF A DIFFERENT DRIVE IS SELECTED, THE PROGRAM WILL RESET THE ADDRESS LIMITS TO THE VALUES APPROPRIATE FOR THE DRIVE TYPE.
11. TO CHANGE EITHER THE ADDRESSING MODE OR THE OPERATION MODE, THE PROGRAM MUST BE STARTED FROM LOCATION 200(8) OR 204(8) AGAIN.

#### 4.3 RH11 - RH70 UNIBUS ADDRESS

THE PROGRAM ASSUMES THAT THE RH11 OR RH70 ADDRESSES START AT 176700 AND THAT THE VECTOR ADDRESS IS 254. THESE ADDRESSES MAY BE CHANGED WHEN THE PROGRAM IS STARTED FROM LOCATION 204(8). IF THE RH11 - RH70 IS NOT AT THE DEFAULT ADDRESS, THE PROGRAM MUST BE STARTED FROM 204(8) INITIALLY AS THE PROGRAM GOES THROUGH THE ADDRESS CHANGE ROUTINE AT INITIAL START ONLY. ENTER THE RH11 RH70 ADDRESS IN RESPONSE TO THE REQUEST FROM THE PROGRAM.

#### 4.4 OTHER UNIBUS ADDRESSES

LOC	TAG	CONTENTS	FUNCTION
---	---	-----	-----
1144	\$TKS	177560	TTY KEYBOARD STATUS REGISTER
1146	\$TKB	177562	TTY KEYBOARD BUFFER REGISTER
1150	\$TPS	177564	TTY PRINTER STATUS REGISTER
1152	\$TPB	177566	TTY PRINTER BUFFER REGISTER
1176	\$LKCSR	172540	KW11-P CONTROL REGISTER
1200	\$LKCSB	172542	KW11-P COUNTER REGISTER
1202	\$LPVEC	104	KW11-P VECTOR ADDRESS
1206	\$LKS	177546	KW11-L CONTROL REGISTER
1210	\$LKV	100	;ADDRESS OF KW11-L VECTOR

#### 5. SWITCH REGISTER SETTINGS

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SW<15>=1...HALT ON ERROR  
SW<13>=1...INHIBIT ERROR TYPEOUTS  
SW<10>=1...BELL ON ERROR  
SW<09>=1...LOOP ON ERROR  
SW<07>=1...PRINT SOFT ERROR REPORTS AS THEY OCCUR  
SW<02>=1...DON'T DISPLAY SYSTEM STATUS AFTER INITIAL START  
SW<01>=1...LOOP ON THE CURRENT TRACK  
SW<00>=1...LOOP THE PROGRAM ON THE SELECTED DRIVE

IF THE PROGRAM IS BEING RUN ON A SWITCHLESS PROCESSOR (I.E. AN 11/34) THE PROGRAM WILL DETERMINE THAT THE HARDWARE SWITCH REGISTER IS NOT PRESENT AND WILL USE A 'SOFTWARE' SWITCH REGISTER. THE 'SOFTWARE' SWITCH REGISTER IS LOCATED AT LOCATION 176 (8). THE SETTINGS OF THE 'SOFTWARE' SWITCHES ARE CONTROLLED THROUGH A KEYBOARD ROUTINE WHICH IS CALLED BY TYPING A 'CONTROL G'. THE PROGRAM WILL

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RECOGNIZE THE 'CONTROL G' AT ANY TIME EXCEPT WHEN THE PROGRAM IS AT A HIGHER PRIORITY PROCESSING AN RP04/5/6 INTERRUPT. THE 'SOFTWARE' SWITCH VALUES ARE ENTERED AS AN OCTAL NUMBER IN RESPONSE TO THE PROMPT FROM THE SWITCH ENTRY ROUTINE:

'SWR = NNNNNN NEW ='

EACH TIME SWITCH SETTING ARE ENTERED, THE ENTIRE SWITCH REGISTER IMAGE MUST BE ENTERED. LEADING ZEROS ARE NOT REQUIRED., 'RUBOUT' AND 'CONTROL U' FUNCTIONS MAY BE USED TO CORRECT TYPING ERRORS DURING SWITCH ENTRY.

ON PROCESSORS WITH HARDWARE SWITCH REGISTERS, THE 'SOFTWARE' SWITCH REGISTER MAY BE USED. IF THE PROGRAM FINDS ALL 16 SWITCHES IN THE 'UP' POSITION, ALL SWITCH REGISTER REFERENCES WILL BE TO THE 'SOFTWARE' REGISTER AND THE PROCEDURES DESCRIBED ABOVE MUST BE FOLLOWED.

6. ERROR MESSAGES  
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1. 'RH11 INTERRUPT OCCURRED (RPAS=0) - AN INTERRUPT OCCURRED, BUT NOTHING ON THE MASSBUS IS INDICATING AN ATTENTION.
2. 'UNEXPECTED ATTENTION OCCURRED' - THE INDICATED DRIVE INTERRUPTED, BUT NO INTERRUPT WAS EXPECTED FROM THE INDICATED DRIVE.
3. 'MASSBUS PARITY ERROR (MCPE=1)' - A CONTROL BUS PARITY ERROR WAS DETECTED BY THE RH11 WHEN THE INDICATED REGISTER WAS READ.
4. 'MASSBUS PARITY ERROR (PAR=1)' - A CONTROL BUS PARITY ERROR OCCURRED WHEN THE INDICATED REGISTER WAS WRITTEN.
5. 'ADDRESS PLUG CHANGE BIT SET' - THE PROGRAM FOUND THE 'OPE' BIT SET FOR THE INDICATED DRIVE.
6. 'RH11 DIDN'T RESPOND TO ADDRESSING' - THE PROGRAM ACCESSED THE RH11 AT THE INDICATED ADDRESS AND RECEIVED NO RESPONSE.
7. 'DRIVE OFFLINE' - THE INDICATED DRIVE HAS GONE OFFLINE
8. 'PERSISTENT DRIVE UNSAFE ERROR' - THE INDICATED DRIVE HAS BECOME UNSAFE AND THE CONDITION CANNOT BE CLEARED BY ISSUING 'DRIVE CLEAR' INSTRUCTIONS.
9. 'UNCORRECTABLE MASSBUS PARITY ERROR' - THE PROGRAM ATTEMPTED TO PERFORM AN OPERATION AND DETECTED 3 SUCESSIVE MASSBUS PARITY ERRORS OR THE PROGRAM ATTEMPTED TO CLEAR A 'PAR' ERROR IN THE DRIVE AND A PARITY ERROR OCCURRED.
10. 'SOFTWARE TIMEOUT' - THE OPERATION FAILED TO COMPLETE WITHIN 1 SECOND.

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11. 'DRIVE UNSAFE ERROR' - A NON-PERSISTENT UNSAFE ERROR OCCURRED DURING THE OPERATION.
12. 'CONTROLLER/DRIVE ERROR DURING WRITE' - THE INDICATED NON-DATA ERROR WAS DETECTED DURING A FORMAT OPERATION.
13. 'CONTROLLER/DRIVE ERROR DURING WRITE CHECK' - A NON-DATA ERROR OCCURRED DURING THE WRITE CHECK.
14. 'DATA ERROR DURING WRITE CHECK' - A DATA RELATED ERROR OCCURRED DURING A WRITE CHECK OPERATION. A DATA ERROR IS CONSIDERED TO BE A 'DCK' ERROR.
15. 'RETRIES NOT SUCESSFUL - SECTOR NOT ACCEPTABLE' - THE INDICATED SECTOR FAILED DURING RETRY FOLLOWING A DATA ERROR.
16. 'CONTROLLER/DRIVE ERROR VERIFYING HEADERS' - AN ERROR OCCURRED DURING THE VERIFICATION PASS AFTER FORMATTING.
17. ''HCE' ERROR VERIFYING HEADERS' - A HEADER ERROR OCCURRED DURING THE VERIFICATION PASS AFTER FORMATTING.
18. 'CYLINDER FIELD IN HEADER IS NOT CORRECT' - THE CYLINDER FIELD FROM A HEADER READ DURING THE VERIFICATION PASS IS NOT CORRECT.
19. 'WRITE CHECK ERROR' - THE RH11 REPORTED A WRITE CHECK ERROR AND NO DRIVE ERRORS WERE SET.
20. 'HARDWARE ERROR DURING WRITE CHECK' - AN ERROR THAT WAS NEITHER A DATA ERROR, WRITE CHECK ERROR, NOR CONTROLLER ERROR OCCURRED DURING A WRITE CHECK. THESE ERRORS COULD BE ONE OF THE FOLLOWING: 'OPI', 'DTE', 'HCRC', 'HCE', OR 'FER'.

7. MISCELLANEOUS  
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7.1 FORMAT TIME

IT TAKES APPROXIMATELY 8 MINUTES TO FORMAT AN ENTIRE RP04/5 PACK AND APPROXIMATELY 16 MINUTES TO FORMAT AN RP06 PACK. THE 'CHECK' MODE TIME FOR AN ENTIRE PACK IS 4 MINUTES FOR RP04/5'S AND 8 MINUTES FOR RP06'S.

7.2 HALTING THE PROGRAM

THE OPERATOR SHOULD NOT HALT THE PROGRAM DURING A FORMAT OPERATION. HALTING THE PROGRAM MAY LEAVE A SECTOR INCORRECTLY FORMATTED. TO TERMINATE THE FORMAT, TYPE A 'CONTROL C' AND WHILE THE PROGRAM IS TYPING THE ADDRESS, TYPE ANOTHER 'CONTROL C'; THIS SEQUENCE RETURNS THE PROGRAM TO THE DRIVE ADDRESS ENTRY ROUTINE.

7.3 SURFACE VERIFICATION

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THE FORMATTER PROGRAM IS NOT INTENDED TO BE USED TO PERFORM DISK  
PACK VERIFICATION. IF THE PROGRAM REPORTS A SECTOR AS BEING 'NOT  
ACCEPTABLE', THIS MAY IN FACT INDICATE A BAD SURFACE; HOWEVER,  
SECTORS WHICH 'PASSED' MAY OR MAY NOT BE GOOD.

8. PROGRAM DESCRIPTION  
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8.1 FORMAT OPERATION

THE PROGRAM FORMATS THE PACK ONE TRACK AT A TIME BETWEEN THE LIMITS  
SPECIFIED BY THE DRIVE.

THE FORMAT OPERATION CONSISTS OF A WRITE HEADER AND DATA COMMAND FOR  
THE ENTIRE TRACK FOLLOWED BY A WRITE CHECK HEADER AND DATA COMMAND.  
IF AN ERROR OCCURS DURING THE WRITE, THE PROGRAM WILL RETRY THE  
WRITE BEFORE CONTINUING. IF A DATA RELATED ERROR IS DETECTED  
DURING THE WRITE CHECK (A DATA ERROR IS DEFINED AS ONE OF  
THE FOLLOWING ERRORS: 'DCK', 'OPI', 'DTE', 'HCRC', 'HCE',  
OR 'FER'), THE SECTOR IN ERROR WILL BE REWRITTEN. THE PROGRAM WILL  
CHECK THE SECTOR TWICE; IF A DATA ERROR IS DETECTED IN THE SECTOR  
DURING EITHER OF THE WRITE CHECKS, THE PROGRAM WILL DECLARE  
THE SECTOR AS BEING 'UNACCEPTABLE'. FOLLOWING THIS SEQUENCE,  
THE REMAINDER OF THE TRACK IS CHECKED. IF DATA ERRORS ARE ENCOUNTERED  
IN ANY OF THE REMAINING SECTORS, EACH SECTOR WILL BE HANDLED AS  
DESCRIBED ABOVE.

IF A NON-DATA RELATED ERROR OCCURS DURING THE WRITE CHECK, THE  
PROGRAM WILL RETRY THE COMMAND FOR THE ENTIRE TRACK BEFORE PROCEEDING  
TO THE NEXT TRACK.

IT SHOULD BE NOTED THAT THE FORMATTER PROGRAM FORMATS THE PACK ONLY  
AND IS NOT DIRECTLY VERIFYING THE SURFACE OF THE PACK. ERRORS  
THAT ARE ENCOUNTERED MAY BE THE RESULT OF BAD AREAS ON THE PACK  
BUT, AS THE PROGRAM IS NOT DESIGNED TO PERFORM AN EXHAUSTIVE CHECK  
OF THE DISK PACK SURFACE, THE PROGRAM CANNOT MAKE THIS CONCLUSION.  
IN GENERAL, HOWEVER, THE OPERATOR CAN ASSUME THAT SECTORS WHICH  
THE PROGRAM CALLS 'UNACCEPTABLE' INDICATE BAD AREAS OF THE PACK;  
UNFORTUNATELY, SECTORS WHICH 'PASS' CANNOT BE ASSUMED TO BE GOOD.

DURING THE FORMAT OPERATION, THE PROGRAM FILLS THE DATA FIELD  
WITH THE PATTERN SELECTED BY THE OPERATOR. THE KEYWORDS IN THE  
HEADER ARE ALWAYS SET TO ZERO.

8.2 CHECK OPERATION

THE CHECK OPERATION IS IDENTICAL TO THE WRITE CHECK PORTION OF  
THE FORMAT OPERATION DESCRIBED IN SECTION 8.1 EXCEPT  
THAT THE PROGRAM WILL NOT RE-WRITE ERROR SECTORS.

8.3 POSITIONER VERIFICATION

AFTER THE PROGRAM COMPLETES THE FORMAT OPERATION, THE

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POSITIONER IS RETURNED TO THE STARTING CYLINDER AND THE HEADER FROM SECTOR 0 ON THE STARTING TRACK IS READ. THE CYLINDER ADDRESS FIELD FROM THE HEADER IS COMPARED TO THE REQUESTED CYLINDER; IF THE CYLINDER ADDRESSES DO NOT COMPARE, AN ERROR MESSAGE IS TYPED. THE PROGRAM CHECKS THE CYLINDER ADDRESS FIELD FROM THE HEADER OF SECTOR 0 ON THE BEGINNING TRACK OF EACH CYLINDER FORMATTED. THIS CHECK IS PERFORMED TO CONFIRM THAT THE DISK'S POSITIONER ADVANCED PROPERLY DURING THE FORMAT OPERATION.

9. PROGRAM LISTING

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.TITLE CZRJBDO, RP04/5/6 FMTR
;*COPYRIGHT (C) 1976,1978
;*DIGITAL EQUIPMENT CORP.
;*MAYNARD, MASS. 01754
;*
;*PROGRAM BY C. HESS
;*
;*THIS PROGRAM WAS ASSEMBLED USING THE PDP-11 MAINDEC SYSMAC
;*PACKAGE (MAINDEC-11-DZQAC-C3), JAN 19, 1977.
;*

.SBTTL OPERATIONAL SWITCH SETTINGS
;*
;*      SWITCH          USE
;*      -----          -
;*      15             HALT ON ERROR
;*      13             INHIBIT ERROR TYPEOUTS
;*      10             BELL ON ERROR
;*      9              LOOP ON ERROR
;*      2              DON'T DISPLAY SYSTEM STATUS AFTER INITIAL START
;*      1              LOOP ON THE CURRENT TRACK
;*      0              LOOP THE PROGRAM ON THE SELECTED DRIVE

.SBTTL TRAP CATCHER
          .=0
;*ALL UNUSED LOCATIONS FROM 4 - 776 CONTAIN A ".+2,HALT"
;*SEQUENCE TO CATCH ILLEGAL TRAPS AND INTERRUPTS
;*LOCATION 0 CONTAINS 0 TO CATCH IMPROPERLY LOADED VECTORS
          .=174
DISPREG: .WORD 0          ;;SOFTWARE DISPLAY REGISTER
SWREG:   .WORD 0          ;;SOFTWARE SWITCH REGISTER

.SBTTL ACT11 HOOKS
;*****
;HOOKS REQUIRED BY ACT11
          $SVPC=.          ;SAVE PC
          .=46
          $ENDAD          ;;1)SET LOC.46 TO ADDRESS OF $ENDAD IN .$EOP
          .=52
  
```

000000  
000174 000000  
000176 000000  
000200  
000046  
00046 005422  
000052

```

530 000052 020000          .WORD 20000          ;;2)SFT LOC.52 TO 20000
531          000200          .=$SVPC            ;; RESTORE PC
532
533          .SBTTL STARTING ADDRESS = 200
534          .=200
535 000200 000137 002132   JMP BEGIN1          ;NORMAL STARTING ADDRESS (INHIBIT PROGRAMMABLE DRIVES
536                                     ;SEE SEC 2.3)
537
538          .SBTTL STARTING ADDRESS TO CHANGE THE RH11 ADDRESS = 204
539 000204 000137 002114   JMP BEGIN          ;CHANGE THE RH11 ADDRESS (NO INHIBITIONS)
540
541          .=220
542 000220 000137 002100   JMP BEGIN3         ;SAME AS 200 WITH NO INHIBITIONS
543
544          .SBTTL BASIC DEFINITIONS
545
546          ;*INITIAL ADDRESS OF THE STACK POINTER *** 1100 ***
547          001100   STACK= 1100
548          .EQUIV EMT,ERROR      ;;BASIC DEFINITION OF ERROR CALL
549          .EQUIV IOT,SCOPE      ;;BASIC DEFINITION OF SCOPE CALL
550
551          ;*MISCELLANEOUS DEFINITIONS
552          000011   HT= 11          ;;CODE FOR HORIZONTAL TAB
553          000012   LF= 12          ;;CODE FOR LINE FEED
554          000015   CR= 15          ;;CODE FOR CARRIAGE RETURN
555          000200   CRLF= 200       ;;CODE FOR CARRIAGE RETURN-LINE FEED
556          177776   PS= 177776     ;;PROCESSOR STATUS WORD
557          .EQUIV PS,PSW
558          177774   STKLMT= 177774  ;;STACK LIMIT REGISTER
559          177772   PIRQ= 177772    ;;PROGRAM INTERRUPT REQUEST REGISTER
560          177570   DSWR= 177570   ;;HARDWARE SWITCH REGISTER
561          177570   DDISP= 177570  ;HARDWARE DISPLAY REGISTER
562
563          ;*GENERAL PURPOSE REGISTER DEFINITIONS
564          000000   R0= %0          ;;GENERAL REGISTER
565          000001   R1= %1          ;;GENERAL REGISTER
566          000002   R2= %2          ;;GENERAL REGISTER
567          000003   R3= %3          ;;GENERAL REGISTER
568          000004   R4= %4          ;;GENERAL REGISTER
569          000005   R5= %5          ;;GENERAL REGISTER
570          000006   R6= %6          ;;GENERAL REGISTER
571          000007   R7= %7          ;;GENERAL REGISTER
572          000006   SP= %6         ;;STACK POINTER
573          000007   PC= %7         ;;PROGRAM COUNTER
574
575          ;*PRIORITY LEVEL DEFINITIONS
576          000000   PR0= 0          ;;PRIORITY LEVEL 0
577          000040   PR1= 40         ;;PRIORITY LEVEL 1
578          000100   PR2= 100       ;;PRIORITY LEVEL 2
579          000140   PR3= 140       ;;PRIORITY LEVEL 3
580          000200   PR4= 200       ;;PRIORITY LEVEL 4
581          000240   PR5= 240       ;;PRIORITY LEVEL 5
582          000300   PR6= 300       ;;PRIORITY LEVEL 6
583          000340   PR7= 340       ;;PRIORITY LEVEL 7
584
585          ;*'SWITCH REGISTER' SWITCH DEFINITIONS
    
```

586	100000	SW15=	100000
587	040000	SW14=	40000
588	020000	SW13=	20000
589	010000	SW12=	10000
590	004000	SW11=	4000
591	002000	SW10=	2000
592	001000	SW09=	1000
593	000400	SW08=	400
594	000200	SW07=	200
595	000100	SW06=	100
596	000040	SW05=	40
597	000020	SW04=	20
598	000010	SW03=	10
599	000004	SW02=	4
600	000002	SW01=	2
601	000001	SW00=	1
602		.EQUIV	SW09,SW9
603		.EQUIV	SW08,SW8
604		.EQUIV	SW07,SW7
605		.EQUIV	SW06,SW6
606		.EQUIV	SW05,SW5
607		.EQUIV	SW04,SW4
608		.EQUIV	SW03,SW3
609		.EQUIV	SW02,SW2
610		.EQUIV	SW01,SW1
611		.EQUIV	SW00,SW0

; \*DATA BIT DEFINITIONS (BIT00 TO BIT15)

614	100000	BIT15=	100000
615	040000	BIT14=	40000
616	020000	BIT13=	20000
617	010000	BIT12=	10000
618	004000	BIT11=	4000
619	002000	BIT10=	2000
620	001000	BIT09=	1000
621	000400	BIT08=	400
622	000200	BIT07=	200
623	000100	BIT06=	100
624	000040	BIT05=	40
625	000020	BIT04=	20
626	000010	BIT03=	10
627	000004	BIT02=	4
628	000002	BIT01=	2
629	000001	BIT00=	1
630		.EQUIV	BIT09,BIT9
631		.EQUIV	BIT08,BIT8
632		.EQUIV	BIT07,BIT7
633		.EQUIV	BIT06,BIT6
634		.EQUIV	BIT05,BIT5
635		.EQUIV	BIT04,BIT4
636		.EQUIV	BIT03,BIT3
637		.EQUIV	BIT02,BIT2
638		.EQUIV	BIT01,BIT1
639		.EQUIV	BIT00,BIT0

; \*BASIC 'CPU' TRAP VECTOR ADDRESSES

640  
641

```

642      000004      ERRVEC= 4      ;;TIME OUT AND OTHER ERRORS
643      000010      RESVEC= 10     ;;RESERVED AND ILLEGAL INSTRUCTIONS
644      000014      TBITVEC=14     ;;'T' BIT
645      000014      TRTVEC= 14     ;;TRACE TRAP
646      000014      BPTVEC= 14     ;;BREAKPOINT TRAP (BPT)
647      000020      IOTVEC= 20     ;;INPUT/OUTPUT TRAP (IOT) **SCOPE**
648      000024      PWRVEC= 24     ;;POWER FAIL
649      000030      EMTVEC= 30     ;;EMULATOR TRAP (EMT) **ERROR**
650      000034      TRAPVEC=34     ;;'TRAP' TRAP
651      000060      TKVEC= 60      ;;TTY KEYBOARD VECTOR
652      000064      TPVEC= 64      ;;TTY PRINTER VECTOR
653      000240      PIRQVEC=240    ;;PROGRAM INTERRUPT REQUEST VECTOR
  
```

\*\*\*\*\*

.SBTTL RH11 REGISTERS

\*\*\*\*\*

;CONTROL AND STATUS REGISTER 1 (RPCS1)

```

663      000100      IE=      100      ;INTERRUPT ENABLE (BIT #6)
664      000200      RDY=      200      ;READY (BIT #7)
665      000400      A16=      400      ;HIGH ORDER BUS ADDRESS BIT (BIT #8)
666      001000      A17=     1000      ;HIGH ORDER BUS ADDRESS BIT (BIT #9)
667      002000      PSEL=     2000      ;PORT SELECT (BIT #10)
668      020000      MCPE=    20000      ;MASSBUS PARITY ERROR (BIT #13)
669      040000      TRE=     40000      ;TRANSFER ERROR (BIT #14)
670      ;SC=     100000      ;SPECIAL CONDITION (BIT #15)
  
```

;WORD COUNT REGISTER (RPWC)  
 ;(EACH BIT IS CALLED BY BIT NUMBER)

;BUS ADDRESS REGISTER (RPBA)  
 ;(EACH BIT IS CALLED BY BIT NUMBER)

;CONTROL AND STATUS REGISTER 2 (RPCS2)

```

680      000001      US1=      1      ;UNIT SELECT (BIT #0)
681      000002      US2=      2      ;UNIT SELECT (BIT #1)
682      000004      US4=      4      ;UNIT SELECT (BIT #2)
683      000010      BAI=     10      ;BUS ADDRESS INCREMENT INHIBIT (BIT #3)
684      000020      PAT=     20      ;MASSBUS PARITY TEST (BIT #4)
685      000040      CLR=     40      ;CLEAR (BIT #5)
686      000100      IR=     100      ;INPUT READY (BIT #6)
687      000200      OR=     200      ;OUTPUT READY (BIT #7)
688      000400      MPE=     400      ;MASS BUS PARITY ERROR (BIT #8)
689      001000      MXF=    1000      ;MISSED TRANSFER ERROR (BIT #9)
690      002000      PGE=    2000      ;PROGRAM ERROR (BIT #10)
691      004000      NEM=    4000      ;NON EXISTENT MEMORY (BIT #11)
692      010000      NED=   10000      ;NON EXISTENT DRIVE (BIT #12)
693      020000      UPE=   20000      ;UNIBUS PARITY ERROR (BIT #13)
694      040000      WCE=   40000      ;WRITE CHECK ERROR (BIT #14)
695      100000      DLT=  100000      ;DATA LATE (BIT #15)
  
```

;DATA BUFFER REGISTER (RPDB)

696  
 697

```

698      ;(EACH BIT IS CALLED BY BIT NUMBER)
699
700
701      ;:*****
702
703      .SBTTL  RP04/5/6 REGISTERS
704
705      ;:*****
706
707      ;CONTROL AND STATUS 1 REGISTER. (#00)
708
709      000001      GO=      1      ;GO BIT (BIT #0)
710      000002      F1=      2      ;FUNCTION CODE BIT #1
711      000004      F2=      4      ;FUNCTION CODE BIT #2
712      000010      F3=     10      ;FUNCTION CODE BIT #3
713      000020      F4=     20      ;FUNCTION CODE BIT #4
714      000040      F5=     40      ;FUNCTION CODE BIT #5
715      004000      DVA=    4000    ;DEVICE AVAILABLE (BIT #11)
716
717      ;DRIVE STATUS REGISTER (RPDS1) (#01)
718
719      ;DF5=      1      DRIVE FORWARD 5'/SEC. (BIT #0)
720      000002      DFF20=  2      ;DRIVE FORWARD 20'/SEC. (BIT #1)
721      000004      DIGB=   4      ;DRIVE TO INNER GUARD BAND (BIT #2)
722      000010      GRV=   10      ;GO REVERSE (BIT #3)
723      000020      DL64=  20      ;DIFFERENCE LESS THAN 64 (BIT #4)
724      000040      DE1=   40      ;DIFFERENCE EQUALS 1 (BIT #5)
725      000100      VV=   100     ;VOLUME VALID (BIT #6)
726      000200      DRY=   200     ;DRIVE READY (BIT #7)
727      000400      DPR=   400     ;DRIVE PRESENT (BIT #8)
728      001000      PGM=  1000    ;PROGRAMABLE (BIT #9)
729      002000      LST=  2000    ;LAST SECTOR TRANSFERRED (BIT #10)
730      004000      WRL=  4000    ;WRITE LOCK (BIT #11)
731      010000      MOL= 10000    ;MEDIUM ON-LINE (BIT #12)
732      020000      PIP= 20000    ;POSITIONING OPERATION IN PROGRESS (BIT #13)
733      040000      ERR= 40000    ;COMPOSITE ERROR (BIT #14)
734      100000      ATA=100000    ;ATTENTION ACTIVE (BIT #15)
735
736      ;ERROR REGISTER #01 (RPER1) (#02)
737
738      000001      ILF=      1      ;ILLEGAL FUNCTION (BIT #0)
739      000002      ILR=      2      ;ILLEGAL REGISTER (BIT #1)
740      000004      RMR=      4      ;REGISTER MODIFICATION REFUSED (BIT #2)
741      000010      PAR=     10      ;PARITY ERROR (BIT #3)
742      000020      FER=     20      ;FORMAT ERROR (BIT #4)
743      000040      WCF=     40      ;WRITE CLOCK FAIL (BIT #5)
744      000100      ECH=    100     ;ECC HARD ERROR (BIT #6)
745      000200      HCE=    200     ;HEADER COMPARE ERROR (BIT #7)
746      000400      HCRC=   400     ;HEADER CRC ERROR (BIT #8)
747      001000      AOE=  1000    ;ADDRESS OVERFLOW ERROR (BIT #9)
748      002000      IAE=  2000    ;INVALID ADDRESS ERROR (BIT #10)
749      004000      WLE=  4000    ;WRITE LOCK ERROR (BIT #11)
750      010000      DTE= 10000    ;DRIVE TIMING ERROR (BIT #12)
751      020000      OPI= 20000    ;OPERATION INCOMPLETE (BIT #13)
752      040000      UNS= 40000    ;DRIVE UNSAFE (BIT #14)
753      100000      DCK=100000    ;DATA CHECK ERROR (BIT 15)
  
```

```

754
755      ;MAINTAINABILITY REGISTER (RPMR) (#03)
756
757      000001      DMD= 1      ;DIAGINOSTIC MODE (BIT #0)
758      000002      MCLK= 2     ;MAINTAINABILITY CLOCK (BIT #1)
759      000004      MINX= 4     ;MAINTAINABILITY INDEX (BIT #2)
760      000010      MSTCK= 10   ;MAINTAINABILITY SECTOR CLOCK (BIT #3)
761      000020      MRD= 20     ;MAINTAINABILITY READ (BIT #4)
762      000040      MWR= 40     ;MAINTAINABILITY WRITE (BIT #5)
763      000200      DTSY= 200   ;MAINTAINABILITY SYNC DETECTED (BIT #7)
764
765      ;ATTENTION SUMMARY PSEUDO-REGISTER (RPAS) (#04)
766
767      000001      AT0= 1      ;DEVICE 0 (BIT #0)
768      000002      AT1= 2     ;DEVICE 1 (BIT #1)
769      000004      AT2= 4     ;DEVICE 2 (BIT #2)
770      000010      AT3= 10    ;DEVICE 3 (BIT #3)
771      000020      AT4= 20    ;DEVICE 4 (BIT #4)
772      000040      AT5= 40    ;DEVICE 5 (BIT #5)
773      000100      AT6= 100   ;DEVICE 6 (BIT #6)
774      000200      AT7= 200   ;DEVICE 7 (BIT #7)
775
776      ;DESIRED SECTOR/TRACK ADDRESS REGISTER (RPDA) (#05)
777      ;(EACH BIT IS CALLED BY BIT NUMBER)
778
779      ;DRIVE TYPE REGISTER (RPDT) (#06)
780
781      000001      DT00= 1     ;DRIVE TYPE NUMBER BIT 1
782      000002      DT01= 2     ;DRIVE TYPE NUMBER BIT 2
783      000004      DT02= 4     ;DRIVE TYPE NUMBER BIT 3
784      000010      DT03= 10    ;DRIVE TYPE NUMBER BIT 4
785      000020      DT04= 20    ;DRIVE TYPE NUMBER BIT 5
786      000040      DT05= 40    ;DRIVE TYPE NUMBER BIT 6
787      000100      DT06= 100   ;DRIVE TYPE NUMBER BIT 7
788      000200      DT07= 200   ;DRIVE TYPE NUMBER BIT 8
789      000400      DT08= 400   ;DRIVE TYPE NUMBER BIT 9
790      004000      DRQ= 4000   ;DRIVE REQUEST REQUIRED (BIT #11)
791      020000      MOH= 20000  ;MOVING HEAD (BIT #13)
792      040000      TAP= 40000  ;TAPE DRIVE (BIT #14)
793      100000      NBA= 100000 ;NOT BLOCK ADDRESSED (BIT #15)
794
795      ;LOOK-AHEAD REGISTER (RPLA) (#07)
796
797      000001      EXT1= 1     ;EXTENSION 1 (BIT #0)
798      000002      EXT2= 2     ;EXTENSION 2 (BIT #1)
799      000004      EXT4= 4     ;EXTENSION 3 (BIT #2)
800      000010      EXT10= 10   ;EXTENSION 4 (BIT #3)
801      000020      EXT20= 20   ;EXTENSION 5 (BIT #4)
802      000040      EXT40= 40   ;EXTENSION 6 (BIT #5)
803      000100      SC1= 100    ;SECTOR COUNT FIELD 0 (BIT #6)
804      000200      SC2= 200    ;SECTOR COUNT FIELD 1 (BIT #7)
805      ;SC4= 400              ;SECTOR COUNT FIELD 2 (BIT #8)
806      001000      SC10= 1000  ;SECTOR COUNT FIELD 3 (BIT #9)
807      002000      SC20= 2000  ;SECTOR COUNT FIELD 4 (BIT #10)
808      004000      TRK1= 4000  ;TRACK FIELD 1 (BIT #11)
809      010000      TRK2= 10000 ;TRACK FIELD 2 (BIT #12)
    
```

810	020000	TRK4= 20000	;TRACK FIELD 3 (BIT #13)
811	040000	TRK10= 40000	;TRACK FIELD 4 (BIT #14)
812	100000	TRK20= 100000	;TRACK FIELD 5 (BIT #15)
813			
814		;RP04 ERROR REGISTER #2 (RPER2) (#10)	
815			
816	000001	WCU= 1	;WRITE CURRENT UNSAFE (BIT #0)
817	000002	CSF= 2	;CURRENT SINK FAILURE (BIT #1)
818	000004	WSU= 4	;WRITE SELECT UNSAFE (BIT #2)
819	000010	CSU= 10	;CURRENT SWITCH UNSAFE (BIT #3)
820	000020	MSE= 20	;MOTOR SEQUENCE ERROR (BIT #4)
821	000040	TDF= 40	;TRANSITIONS DETECTOR FAILURE (BIT #5)
822	000100	TUF= 100	;TRANSITIONS UNSAFE (BIT #6)
823	000200	FEN= 200	;FAILSAFE ENABLED (BIT #7)
824	000400	WRU= 400	;WRITE READY UNSAFE (BIT #8)
825	001000	MHS= 1000	;MULTIPLE HEAD SELECT (BIT #9)
826	002000	NHS= 2000	;NO HEAD SELECTION (BIT #10)
827	004000	IXE= 4000	;INDEX ERROR (BIT #11)
828	010000	VU30= 10000	;30VOLT UNSAFE (BIT #12)
829	020000	PLU= 20000	;PLO UNSAFE (BIT #13)
830	100000	ACU= 100000	;AC UNSAFE (BIT #15)
831			
832		;RP05/6 ERROR REGISTER #02 (RPER2) (#10)	
833			
834	000001	WCU= 1	;WRITE CURRENT UNSAFE (BIT #0)
835	000002	CSF= 2	;CURRENT SINK FAILURE (BIT #1)
836	000004	WSU= 4	;WRITE SELECT UNSAFE (BIT #2)
837	000010	CSU= 10	;CURRENT SWITCH UNSAFE (BIT #3)
838	000020	RAW= 20	;READ AND WRITE (BIT #4)
839	000040	TDF= 40	;TRANSITIONS DETECTOR FAILURE (BIT #5)
840	000100	TUF= 100	;TRANSITIONS UNSAFE (BIT #6)
841	000200	ABS= 200	;ABNORMAL STOP (BIT #7)
842	000400	WRU= 400	;WRITE READY UNSAFE (BIT #8)
843	001000	MHS= 1000	;MULTIPLE HEAD SELECT (BIT #9)
844	002000	NHS= 2000	;NO HEAD SELECTION (BIT #10)
845	004000	IXE= 4000	;INDEX ERROR (BIT #11)
846	020000	PLU= 20000	;PLO UNSAFE (BIT #12)
847			
848		;OFFSET REGISTER (RPOF) (#11)	
849			
850	000001	OF25= 1	;OFFSET 25 MICRO INCHES (BIT #0)
851	000002	OF50= 2	;OFFSET 50 MICRO INCHES (BIT #1)
852	000004	OF100= 4	;OFFSET 100 MICRO INCHES (BIT #2)
853	000010	OF200= 10	;OFFSET 200 MICRO INCHES (BIT #3)
854	000020	OF400= 20	;OFFSET 400 MICRO INCHES (BIT #4)
855	000040	OF800= 40	;OFFSET 800 MICRO INCHES (BIT #5)
856	000200	OFREV= 200	;OFFSET NEGATIVE (REVERSE) (BIT #5)
857	002000	HCI= 2000	;HEADER COMPARE INHIBIT (BIT #10)
858	004000	ECI= 4000	;ERROR CORRECTION CODE INHIBIT (BIT #11)
859	010000	FMT22= 10000	;FORMAT BIT (BIT #12)
860			
861		;DESIRED CYLINDER ADDRESS (RPCA) (#12)	
862		;(EACH BIT IS CALLED BY BIT NUMBER)	
863			
864		;CURRENT CYLINDER ADDRESS (RPCC) (#13)	
865		;(EACH BIT IS CALLED BY BIT NUMBER)	

```

866
867      ;SERIAL NUMBER REGISTER (RPSN) (#14)
868      ;(EACH IS CALLED BY BIT NUMBER)
869
870      ;RP04 ERROR REGISTER #03 (RPER3) (#15)
871
872      000001      PSU=      1      ;PACK SPEED UNSAFE (BIT #0)
873      000002      VUF=      2      ;VELOCITY UNSAFE (BIT #1)
874      000010      UWR=     10      ;ANY UNSAFE EXCEPT READ/WRITE (BIT #3)
875      000040      ACL=     40      ;AC LOW (BIT #5)
876      000100      DCL=    100      ;DC LOW (BIT #6)
877      040000      SKI=   40000     ;SEEK INCOMPLETE (BIT #14)
878      100000      OCYL=  100000    ;OFF CYLINDER (BIT #15)
879
880      ;RP05/6 ERROR REGISTER #03 (RPER3) (#15)
881
882      000001      DCU=      1      ;DC UNSAFE (BIT #0)
883      000002      WAO=      2      ;WRITE AND OFFSET (BIT #1)
884      000040      ACL=     40      ;AC LOW (BIT #5)
885      000100      DCL=    100      ;DC LOW (BIT #6)
886      020000      OPE=   20000     ;OPERATOR PLUG ERROR (BIT #13)
887      040000      SKI=   40000     ;SEEK INCOMPLETE (BIT #14)
888      100000      OCYL=  100000    ;OFF CYLINDER ERROR (BIT #15)
889
890      ;ECC POSITION REGISTER (RPEC1) (#16)
891      ;(EACH BIT IS CALLED BY BIT NUMBER)
892
893      ;ECC PATTERN REGISTER (RPEC2) (#17)
894      ;(EACH BIT IS CALLED BY BIT NUMBER)
895
896      ;:*****
897
898      .SBTTL  RP04/5/6 DRIVER COMMANDS
899
900      ;:*****
901
902      000101      RNOP   =      101      ;NO OPERATION
903      000103      UNLOAD =      103      ;UNLOAD
904      000105      SEEK   =      105      ;SEEK
905      000107      RECAL  =      107      ;RECALIBRATE
906      000111      DRVCLR =      111      ;DRIVE CLEAR
907      000113      RELSE  =      113      ;RELEASE
908      000115      OFFSET =      115      ;OFFSET
909      000117      RTC    =      117      ;RETURN TO CENTER LINE
910      000121      READIN =      121      ;READ IN PRESET
911      000123      ACK    =      123      ;PACK ACKNOWLEDGE
912      000131      SEARCH =      131      ;SEARCH
913      000141      GETREG =      141      ;GET REGISTERS
914      000143      SETFMT =      143      ;SET FORMAT (& ECI OR HCI)
915      000145      SELDRV =      145      ;SELECT DRIVE
916      000151      WCKD   =      151      ;WRITE CHECK DATA
917      000153      WCKHD  =      153      ;WRITE CHECK HEADER & DATA
918      000161      WRDAT  =      161      ;WRITE DATA
919      000163      WRTHD  =      163      ;WRITE HEADER & DATA
920      000171      RDDAT  =      171      ;READ DATA
921      000173      RDHD   =      173      ;READ HEADER & DATA
  
```

CZRJBD, RP04/5/6 FMTR MACY11 30A(1052) 24-MAY-79 14:10 PAGE 20<sup>F</sup> 2  
CZRJBD.P11 28-MAR-79 11:23 RP04/5/6 DRIVER COMMANDS

SEQ 0018

922  
923

```

924          .SBTTL COMMON TAGS
925
926          ;:*****
927          ;*THIS TABLE CONTAINS VARIOUS COMMON STORAGE LOCATIONS
928          ;*USED IN THE PROGRAM.
929
930          001100          . =1100
931 001100  $CMTAG:          ;; START OF COMMON TAGS
932 001100  $PASS: .WORD 0          ;; CONTAINS PASS COUNT
933 001102    000          $TSTNM: .BYTE 0          ;; CONTAINS THE TEST NUMBER
934 001103    000          $ERFLG: .BYTE 0          ;; CONTAINS ERROR FLAG
935 001104 000000          $ICNT: .WORD 0          ;; CONTAINS SUBTEST ITERATION COUNT
936 001106 000000          $LPADR: .WORD 0          ;; CONTAINS SCOPE LOOP ADDRESS
937 001110 000000          $LPERR: .WORD 0          ;; CONTAINS SCOPE RETURN FOR ERRORS
938 001112 000000          $ERTTL: .WORD 0          ;; CONTAINS TOTAL ERRORS DETECTED
939 001114    000          $ITEMB: .BYTE 0          ;; CONTAINS ITEM CONTROL BYTE
940 001115    001          $ERMAX: .BYTE 1          ;; CONTAINS MAX. ERRORS PER TEST
941 001116 000000          $ERRPC: .WORD 0          ;; CONTAINS PC OF LAST ERROR INSTRUCTION
942 001120 000000          $GDADR: .WORD 0          ;; CONTAINS ADDRESS OF 'GOOD' DATA
943 001122 000000          $BDADR: .WORD 0          ;; CONTAINS ADDRESS OF 'BAD' DATA
944 001124 000000          $GDDAT: .WORD 0          ;; CONTAINS 'GOOD' DATA
945 001126 000000          $BDDAT: .WORD 0          ;; CONTAINS 'BAD' DATA
946 001130 000000          .WORD 0          ;; RESERVED--NOT TO BE USED
947 001132 000000          .WORD 0
948 001134    000          $AUTOB: .BYTE 0          ;; AUTOMATIC MODE INDICATOR
949 001135    000          $INTAG: .BYTE 0          ;; INTERRUPT MODE INDICATOR
950 001136 000000          .WORD 0
951 001140 177570          SWR: .WORD DSWR          ;; ADDRESS OF SWITCH REGISTER
952 001142 177570          DISPLAY: .WORD DDISP          ;; ADDRESS OF DISPLAY REGISTER
953 001144 177560          $TKS: 177560          ;; TTY KBD STATUS
954 001146 177562          $TKB: 177562          ;; TTY KBD BUFFER
955 001150 177564          $TPS: 177564          ;; TTY PRINTER STATUS REG. ADDRESS
956 001152 177566          $TPB: 177566          ;; TTY PRINTER BUFFER REG. ADDRESS
957 001154    000          $NULL: .BYTE 0          ;; CONTAINS NULL CHARACTER FOR FILLS
958 001155    002          $FILLS: .BYTE 2          ;; CONTAINS # OF FILLER CHARACTERS REQUIRED
959 001156    012          $FILLC: .BYTE 12          ;; INSERT FILL CHARS. AFTER A 'LINE FEED'
960 001157    000          $TPFLG: .BYTE 0          ;; 'TERMINAL AVAILABLE' FLAG (BIT<07>=0=YES)
961 001160 000000          $ESCAPE: 0          ;; ESCAPE ON ERROR ADDRESS
962 001162 177607 000377          $BELL: .ASCIZ <207><377><377>          ;; CODE FOR BELL
963 001166    077          $QUES: .ASCII /?/          ;; QUESTION MARK
964 001167    015          $CRLF: .ASCII <15>          ;; CARRIAGE RETURN
965 001170 000012          $LF: .ASCIZ <12>          ;; LINE FEED
966          ;:*****
967          000015          CR = 15
968          000012          LF = 12
969 001172 176700          $RPADR: .WORD 176700          ;; RH11/RP04/5/6 UNIBUS ADDRESS
970 001174 000254          $RPVEC: .WORD 254          ;; RH11 INTERRUPT VECTOR
971 001176 172540          $LKCSR: .WORD 172540          ;; ADDRESS OF KW11-P CSR
972 001200 172542          $LKCSB: .WORD 172542          ;; ADDRESS OF KW11-P COUNTER BUFFER
973 001202 000104 000106          $LPVEC: .WORD 104,106          ;; ADDRESS OF KW11-P VECTOR
974 001206 177546          $LKS: .WORD 177546          ;; ADDRESS OF KW11-L CONTROL REGISTER
975 001210 000100 000102          $LLVEC: .WORD 100,102          ;; ADDRESS OF KW11-L VECTOR
976 001214 000000          DRIVE: .WORD 0          ;; CONTAINS DRIVE NUMBER SELECTED
977 001216 000000          SOFSW: .WORD 0          ;; CONTENTS ARE FOR SOFTWARE DECISIONS
978 001220 000000          MODE: .WORD 0          ;; 'FORMAT & VERIFY' OR 'CHECK' MODE INDICATOR
979 001222 000632          ENDCYL: .WORD 410.          ;; ENDING CYLINDER
    
```



```

1036 001356 000          FMTDPB: .BYTE 0          ;DRIVE NUMBER
1037 001357 000          .BYTE 0          ;OFFSET VALUE OR FMT22,ECI, AND HCI
1038 001360 000          .BYTE 0          ;COMMAND
1039 001361 000          .BYTE 0          ;PSEL AND A17 AND A16
1040 001362 000000      .WORD 0          ;WORD COUNT (NEG)
1041 001364 000000      .WORD 0          ;BUFFER ADDRESS
1042 001366 000          .BYTE 0          ;SECTOR ADDRESS
1043 001367 000          .BYTE 0          ;TRACK ADDRESS
1044 001370 000000      .WORD 0          ;CYLINDER ADDRESS
1045 001372 001306      .WORD RP.REG     ;ERROR TABLE POINTER
1046 001374 000000      .WORD 0          ;STATUS-ERROR INDICATOR
1047                                     ;BIT 15 = 1: ERROR OCCURRED
1048                                     ;BIT 07 = 1: DONE
1049                                     ;BIT 14-10 AND BIT 06-03
1050                                     ;INDICATE TYPE OF ERROR
1051
1052
1053
1054

```

;PARAMETER POINTER TABLE

```

1055 001376 001430 000000 001224 TABLE: PAR1,0,BEGCYL
1056 001404 001443 000022 001230      PAR2,18.,BEGTRK
1057 001412 001456 000000 001222      PAR3,0,ENDCYL
1058 001420 001467 000022 001226      PAR4,18.,ENDTRK,0
1059 001426 000000

```

;ASCII MESSAGES FOR ADDRESS PARAMETERS

```

1060
1061
1062
1063 001430 052123 051101 020124 PAR1: .ASCIZ @START CYL @
1064 001436 054503 020114 000      PAR2: .ASCIZ @START TRK @
1065 001443 123 040524 052122      PAR3: .ASCIZ @END CYL @
1066 001450 052040 045522 000040      PAR4: .ASCIZ @END TRK @
1067 001456 047105 020104 054503
1068 001464 020114 000
1069 001467 105 042116 052040
1070 001474 045522 000040
1071

```

;SECTOR BUFFER ADDRESS TABLE

```

1072
1073
1074 001500 025574      ADRTBL: .WORD BUFP          ;ADDRESS OF SECTOR 0
1075 001502 026604      .WORD BUFP+<520.*1.> ;ADDRESS OF SECTOR 1
1076 001504 027614      .WORD BUFP+<520.*2.> ;ADDRESS OF SECTOR 2
1077 001506 030624      .WORD BUFP+<520.*3.> ;ADDRESS OF SECTOR 3
1078 001510 031634      .WORD BUFP+<520.*4.> ;ADDRESS OF SECTOR 4
1079 001512 032644      .WORD BUFP+<520.*5.> ;ADDRESS OF SECTOR 5
1080 001514 033654      .WORD BUFP+<520.*6.> ;ADDRESS OF SECTOR 6
1081 001516 034664      .WORD BUFP+<520.*7.> ;ADDRESS OF SECTOR 7
1082 001520 035674      .WORD BUFP+<520.*8.> ;ADDRESS OF SECTOR 8
1083 001522 036704      .WORD BUFP+<520.*9.> ;ADDRESS OF SECTOR 9
1084 001524 037714      .WORD BUFP+<520.*10.> ;ADDRESS OF SECTOR 10
1085 001526 040724      .WORD BUFP+<520.*11.> ;ADDRESS OF SECTOR 11
1086 001530 041734      .WORD BUFP+<520.*12.> ;ADDRESS OF SECTOR 12
1087 001532 042744      .WORD BUFP+<520.*13.> ;ADDRESS OF SECTOR 13
1088 001534 043754      .WORD BUFP+<520.*14.> ;ADDRESS OF SECTOR 14
1089 001536 044764      .WORD BUFP+<520.*15.> ;ADDRESS OF SECTOR 15
1090 001540 045774      .WORD BUFP+<520.*16.> ;ADDRESS OF SECTOR 16
1091 001542 047004      .WORD BUFP+<520.*17.> ;ADDRESS OF SECTOR 17

```

1092 001544 050014 .WORD BUFP+<520.\*18.> ;ADDRESS OF SECTOR 18  
1093 001546 051024 .WORD BUFP+<520.\*19.> ;ADDRESS OF SECTOR 19  
1094 001550 052034 .WORD BUFP+<520.\*20.> ;ADDRESS OF SECTOR 20  
1095 001552 053044 .WORD BUFP+<520.\*21.> ;ADDRESS OF SECTOR 21

1096  
1097 ;REMAINING WORD COUNT TABLE  
1098

1099 001554 000404 WCTBL: .WORD 260. ;REMAINING WORD COUNT AFTER SECTOR 0  
1100 001556 001010 .WORD 260.+<260.\*1.> ;REMAINING WORD COUNT AFTER SECTOR 1  
1101 001560 001414 .WORD 260.+<260.\*2.> ;REMAINING WORD COUNT AFTER SECTOR 2  
1102 001562 002020 .WORD 260.+<260.\*3.> ;REMAINING WORD COUNT AFTER SECTOR 3  
1103 001564 002424 .WORD 260.+<260.\*4.> ;REMAINING WORD COUNT AFTER SECTOR 4  
1104 001566 003030 .WORD 260.+<260.\*5.> ;REMAINING WORD COUNT AFTER SECTOR 5  
1105 001570 003434 .WORD 260.+<260.\*6.> ;REMAINING WORD COUNT AFTER SECTOR 6  
1106 001572 004040 .WORD 260.+<260.\*7.> ;REMAINING WORD COUNT AFTER SECTOR 7  
1107 001574 004444 .WORD 260.+<260.\*8.> ;REMAINING WORD COUNT AFTER SECTOR 8  
1108 001576 005050 .WORD 260.+<260.\*9.> ;REMAINING WORD COUNT AFTER SECTOR 9  
1109 001600 005454 .WORD 260.+<260.\*10.> ;REMAINING WORD COUNT AFTER SECTOR 10  
1110 001602 006060 .WORD 260.+<260.\*11.> ;REMAINING WORD COUNT AFTER SECTOR 11  
1111 001604 006464 .WORD 260.+<260.\*12.> ;REMAINING WORD COUNT AFTER SECTOR 12  
1112 001606 007070 .WORD 260.+<260.\*13.> ;REMAINING WORD COUNT AFTER SECTOR 13  
1113 001610 007474 .WORD 260.+<260.\*14.> ;REMAINING WORD COUNT AFTER SECTOR 14  
1114 001612 010100 .WORD 260.+<260.\*15.> ;REMAINING WORD COUNT AFTER SECTOR 15  
1115 001614 010504 .WORD 260.+<260.\*16.> ;REMAINING WORD COUNT AFTER SECTOR 16  
1116 001616 011110 .WORD 260.+<260.\*17.> ;REMAINING WORD COUNT AFTER SECTOR 17  
1117 001620 011514 .WORD 260.+<260.\*18.> ;REMAINING WORD COUNT AFTER SECTOR 18  
1118 001622 012120 .WORD 260.+<260.\*19.> ;REMAINING WORD COUNT AFTER SECTOR 19  
1119 001624 012524 .WORD 260.+<260.\*20.> ;REMAINING WORD COUNT AFTER SECTOR 20  
1120 001626 013130 .WORD 260.+<260.\*21.> ;REMAINING WORD COUNT AFTER SECTOR 21

1121  
1122 .EVEN  
1123

ERROR POINTER TABLE

```

1124 .SBTTL ERROR POINTER TABLE
1125
1126 :*THIS TABLE CONTAINS THE INFORMATION FOR EACH ERROR THAT CAN OCCUR.
1127 :*THE INFORMATION IS OBTAINED BY USING THE INDEX NUMBER FOUND IN
1128 :*LOCATION $ITEMB. THIS NUMBER INDICATES WHICH ITEM IN THE TABLE IS PERTINENT.
1129 :*NOTE1: IF $ITEMB IS 0 THE ONLY PERTINENT DATA IS ($ERRPC).
1130 :*NOTE2: EACH ITEM IN THE TABLE CONTAINS 4 POINTERS EXPLAINED AS FOLLOWS:
1131
1132 :* EM ::POINTS TO THE ERROR MESSAGE
1133 :* DH ::POINTS TO THE DATA HEADER
1134 :* DT ::POINTS TO THE DATA
1135 :* DF ::POINTS TO THE DATA FORMAT
1136
1137
1138 001630 $ERRTB:
1139 :ERROR 1
1140
1141 001630 022512 EM1 ;RH11 INTERRUPT OCCURRED (RPAS=0)
1142 001632 023674 DH1
1143 001634 025146 DT1
1144 001636 025460 DF1
1145
1146 :ERROR 2
1147
1148 001640 022553 EM2 ;UNEXPECTED ATTENTION OCCURRED
1149 001642 023701 DH2
1150 001644 025150 DT2
1151 001646 025464 DF2
1152
1153 :ERROR 3
1154
1155 001650 022611 EM3 ;MASSBUS PARITY ERROR (MCPE=1)
1156 001652 023756 DH3
1157 001654 025164 DT3
1158 001656 025470 DF3
1159
1160 :ERROR 4
1161
1162 001660 022647 EM4 ;MASSBUS PARITY ERROR (PAR=1)
1163 001662 024004 DH4
1164 001664 025172 DT4
1165 001666 025474 DF4
1166
1167 :ERROR 5
1168
1169 001670 022704 EM5 ;ADDRESS PLUG CHANGE BIT SET
1170 001672 023701 DH2
1171 001674 025150 DT2
1172 001676 025464 DF2
1173
1174 :ERROR 6
1175
1176 001700 022740 EM6 ;RH11 DIDN'T RESPOND TO ADDRESSING
1177 001702 024043 DH6
1178 001704 025202 DT6
1179 001706 025460 DF1
    
```



1236				
1237				
1238				
1239	002010	023303	EM17	
1240	002012	024326	DH17	
1241	002014	025260	DT17	
1242	002016	025514	DF17	
1243				
1244				
1245				
1246	002020	023361	EM20	
1247	002022	024375	DH20	
1248	002024	025272	DT20	
1249	002026	025520	DF20	
1250				
1251				
1252				
1253	002030	023417	EM21	
1254	002032	024056	DH10	
1255	002034	025204	DT10	
1256	002036	025500	DF10	
1257				
1258				
1259				
1260	002040	023470	EM22	
1261	002042	024056	DH10	
1262	002044	025204	DT10	
1263	002046	025500	DF10	
1264				

1237 ;ERROR 17

1239 ;RETRIES NOT SUCCESSFUL - SECTOR NOT ACCEPTABLE

1244 ;ERROR 20

1246 ;DATA ERROR DURING WRITE CHECK

1251 ;ERROR 21

1253 ;CONTROLLER/DRIVE ERROR VERIFYING HEADERS

1258 ;ERROR 22

1260 ;'HCE' ERROR VERIFYING HEADERS

```
1265 ;ERROR 23
1266
1267 002050 023537 EM23 ;CYLINDER FIELD IN HEADER IS NOT CORRECT
1268 002052 024674 DH23
1269 002054 025354 DT23
1270 002056 025540 DF23
1271
1272 ;ERROR 24
1273
1274 002060 023607 EM24 ;WRITE CHECK ERROR
1275 002062 024772 DH24
1276 002064 025372 DT24
1277 002066 025544 DF24
1278
1279 ;ERROR 25
1280
1281 002070 023631 EM25 ;HARDWARE ERROR DURING WRITE CHECK
1282 002072 024375 DH20
1283 002074 025272 DT20
1284 002076 025520 DF20
1285
1286 ;:*****
1287
1288 .SBTTL MAIN PROGRAM
```

1289  
1290  
1291  
1292  
1293  
1294  
1295  
1296

002100 005037 001302  
002104 012737 000001 013310  
002112 000413  
002114 012737 177777 001302  
002122 012737 000001 013310

;;\*\*\*\*\*

BEGIN3: CLR CHGADR ;CLEAR THE INDICATOR  
MOV #1, TSTPGM ;ENABLE PROGRAMMABLE DRIVES  
BR BEGIN2  
BEGIN: MOV #-1,CHGADR ;SET CHANGE 'RH11 BUS ADDRESS' INDICATOR  
MOV #1, TSTPGM ;ENABLE PROGRAMMABLE DRIVES









































































































































```
5085 026434 026464                8$           ;DIGIT 8-9
5086 026436 105711                3$:  TSTB      (R1)         ;DOES A 'CR' FOLLOW THE 'PERIOD'
5087 026440 001011                BNE      8$             ;BR IF NOT
5088 026442 005724                TST      (R4)+         ;INCREMENT THE RETURN
5089 026444 005724                4$:  TST      (R4)+         ;INCREMENT THE RETURN INDEX
5090 026446 005724                5$:  TST      (R4)+         ;INCRFMENT THE RETURN INDEX
5091 026450 023703 026074         CMP      LIMIT,R3       ;INPUT VALUE TOO LARGE ?
5092 026454 101003                BHI      8$             ;BR IF IT IS
5093 026456 000401                BR       7$             ;BR IF NOT
5094 026460 005725                6$:  TST      (R5)+         ;INCREMENT THE RETURN ADDRESS
5095 026462 005725                7$:  TST      (R5)+         ;INCREMENT THE RETURN ADDRESS
5096 026464 060405                8$:  ADD      R4,R5       ;SETUP FOR PROPER RETURN
5097 026466 010302                MOV      R3,R2         ;LOAD ENTERED VALUE
5098 026470 005726                TST      (SP)+         ;CLEAN OFF THE STACK
5099 026472 012603                MOV      (SP)+,R3       ;RESTORE R3
5100 026474 012604                MOV      (SP)+,R4       ;RESTORE R4
5101 026476 011505                MOV      (R5),R5        ;GET RETURN ADDRESS
5102 026500 000205                RTS      R5            ;RETURN
5103
5104
5105
5106                000001                .END
```





























