

# RL11,RLV11

DRIVE TEST PART 2  
CZRLDB0

AH-E049B-MC  
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IDENTIFICATION

B 1

SEQ 0001

PRODUCT CODE: AC-E048B-MC  
PRODUCT NAME: CZRLDBO RL01 DRIVE TEST PART 2  
DATE CREATED: 11-OCT-78  
MAINTAINER: DIAGNOSTIC ENGINEERING  
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1.0 GENERAL INFORMATION

1.1 PROGRAM ABSTRACT

1.1.1 STRUCTURE OF PROGRAM

THIS DIAGNOSTIC OCCUPIES 14.5K WORDS OF MEMORY AND IS COMPATIBLE WITH BOTH XXDP AND ACT. IT CAN BE RUN STANDALONE UNDER XXDP, AND CAN BE CHAINED UNDER XXDP, ACT AND APT IN ACT MODE (SEE "CREATE CORE IMAGE" COMMAND BELOW FOR DETAILS OF CHAINING PROCEDURE). IT IS A SINGLE PROGRAM FROM THE STANDPOINT OF THE DIAGNOSTIC USER, BUT WE HAVE INCORPORATED INTO IT A CONTROL MODULE WHICH WILL LATER BE RELEASED INDEPENDENTLY AS A DIAGNOSTIC SUPERVISOR.

WHEN THIS DIAGNOSTIC IS STARTED AT ADDRESS 200, CONTROL GOES FIRST TO THE SUPERVISOR PORTION, WHICH WILL ASK CERTAIN "HARD CORE" QUESTIONS ABOUT THE ENVIRONMENT. THEN IT WILL ENTER COMMAND MODE, INDICATED BY A PROMPT CHARACTER (DS B>). AT COMMAND MODE THE OPERATOR MAY ENTER ANY OF SEVERAL COMMANDS AS DESCRIBED BELOW.

THE SUPERVISOR CODING FOLLOWS IMMEDIATELY THE DIAGNOSTIC TEST CODING, BUT THE SUPERVISOR LISTING HAS BEEN SUPPRESSED FOR GENERAL DISTRIBUTION. A LIMITED DISTRIBUTION HAS BEEN MADE TO FIELD SERVICE OF THE SUPERVISOR ASSEMBLY LISTING, AND IT MAY BE CONSULTED IN EVENT OF A SOFTWARE PROBLEM.

1.1.2 DIAGNOSTIC INFORMATION

THIS PROGRAM TESTS AND EXERCISES RL01 DISK DRIVES RL11/RLV11 CONTROLLERS (4 DRIVES PER CONTROLLER). THE ENTIRE PROGRAM IS RUN ON THE FIRST DRIVE BEFORE STARTING ON THE SECOND. THE PROGRAM STARTS BY TESTING THE SIMPLEST FUNCTIONS FIRST USING THE LOGIC TESTED IN EARLIER TESTS TO TEST MORE COMPLEX FUNCTIONS.

THIS PROGRAM FIRST TESTS THE RL01 INTERFACE AND BASIC DRIVE LOGIC. IT THEN BEGINS TESTING THE SEEK OPERATIONS USING SINGLE DIFFERENCES, PROCEEDING INTO SEEKS OF GREATER DIFFERENCES. SEEK TIMING IS DONE AFTER THE SEEK LOGIC HAS BEEN TESTED.

DATA TRANSFERS ARE DONE AFTER ALL THE SEEK TESTS. THE FIRST DATA TRANSFER IS READING OF THE BAD SECTOR FILES WHICH ARE STORED AND USED LATER TO PREVENT TESTING ON BAD SECTORS. FOLLOWING DATA READ AND WRITE TESTING, THE PROGRAM TESTS FOR OVERWRITE PROBLEMS AND ADJACENT CYLINDER INTERFERENCE.

SEEK TIMING, ROTATIONAL TIMING, AND WRITE LOCK DATA PROTECTION ARE DONE IF MANUAL INTERVENTION IS REQUESTED.

1.2 SYSTEM REQUIREMENTS

1.2.1 HARDWARE REQUIREMENTS

PDP-11/LSI-11 PROCESSOR WITH 16K OR MORE OF MEMORY  
CONSOLE DEVICE (LA30,LA36,VT50,ETC.)  
RL11/RLV11 CONTROLLER(S)  
1 - 8 RLO1 DRIVES  
1 - 8 RLO1K CARTRIDGES WITH BAD SECTOR FILE  
KW11P, KW11L (OPTIONAL)  
LINEPRINTER(OPTIONAL)

### 1.2.2 SOFTWARE REQUIREMENTS

-----

CXRLDBO RLO1 DRIVE TEST PART 2  
(FORMERLY MD-11-DZRLD-A)

### 1.3 RELATED DOCUMENTS AND STANDARDS

RLO1 USERS MANUAL (EK-RLO1-UG-PRE)  
XXDP USERS MANUAL

### 1.4 DIAGNOSTIC HIERARCY PREREQUISITES

THE RLO1 SUBSYSTEM SHOULD HAVE SUCCESSFULLY RUN THE FOLLOWING PROGRAMS:

CZRLABO	RL11/RLV11 RLO1 CONTROLLER TEST (PART 1)
CZRLBBO	RL11/RLV11 RLO1 CONTROLLER TEST (PART 2)
CVRLAAO	RLV11 RLO1 DISKLESS TEST (RLV11 ONLY)
CZRLCBO	RLO1 DRIVE TEST (PART 1)

### 1.5 ASSUMPTIONS

THE HARDWARE OTHER THAN THE RLO1 SUBSYSTEM IS ASSUMED TO WORK PROPERLY. FALSE ERRORS MAY BE REPORTED IF THE PROCESSOR, ETC., DO NOT FUNCTION PROPERLY.

### 2.0 OPERATING INSTRUCTIONS

#### 2.1 HOW TO RUN THIS DIAGNOSTIC

##### 2.1.1 THE SIX STEPS OF EXECUTION

THIS DIAGNOSTIC SHOULD BE LOADED AND STARTED USING NORMAL XXDP PROCEDURES. THE START COMMAND SHOULD NOT SPECIFY AN ADDRESS, BECAUSE THE DIAGNOSTIC HAS THE PROPER TRANSFER ADDRESS CODED INTO IT.

WHEN THIS DIAGNOSTIC IS STARTED, THE FOLLOWING STEPS WILL OCCUR:

\*\*\*\*\*  
\* STEP 1 \*  
\*\*\*\*\*

PROVIDE COPY TO USER AVAILABLE NOW FOR USE

A SHORT SERIES OF 'HARDCORE QUESTIONS' WILL BE ASKED:

QUESTION	MEANING
L-CLK (L) N ?	IS THERE AN L-CLOCK?
P-CLK (L) N ?	IS THERE AN P-CLOCK?
50HZ (L) N ?	IS THE POWER 50 CYCLES (AS IN EUROPE)?
LSI (L) N ?	IS MACHINE AN LSI?
LPT (L) N ?	IS THERE A LINE PRINTER?
MEM (K) (D) 16 ?	HOW MANY K OF MEMORY ARE THERE?

THE DEFAULTS (SHOWN AFTER EACH QUESTION) CAN BE SELECTED BY HITTING CARRIAGE RETURN. IT IS POSSIBLE THAT NOT ALL OF THE QUESTIONS WILL BE ASKED: FOR EXAMPLE, IF YOU SAY 'YES' TO THE L-CLOCK QUESTION, THE P-CLOCK QUESTION WILL NOT BE ASKED.

IF NEITHER P OR L CLOCK ARE ANSWERED YES THE OPERATOR WILL BE ASKED TO TYPE TWO CHARACTERS 4 SECONDS APART.

\*\*\*\*\*  
\* STEP 2 \*  
\*\*\*\*\*

WHEN YOU HAVE ANSWERED ALL THE HARDCORE QUESTIONS, THE DIAGNOSTIC WILL ISSUE THE PROMPT 'DS-B>'. FROM THIS POINT UNTIL THE TIME WHEN YOU RESTART XXDP, YOU WILL BE TALKING TO THE DIAGNOSTIC, NOT XXDP. WE WILL REFER TO THE PRESENCE OF THIS PROMPT AS BEING IN DIAGNOSTIC COMMAND MODE, AS OPPOSED TO XXDP COMMAND MODE.

AT THIS POINT YOU WILL ENTER A 'START' COMMAND. THIS IS NOT THE SAME AS THE XXDP 'START' COMMAND, WHICH YOU ALREADY ISSUED IN RESPONSE TO THE XXDP DOT PROMPT. THIS 'START' COMMAND CAN TAKE A NUMBER OF SWITCHES AND FLAGS (ALL OPTIONAL) AND THE DETAILS OF THESE ARE SET FORTH IN '2.3 DETAILS OF COMMANDS AND SYNTAX'. HOWEVER, IN ORDER TO USE THE PROGRAM, ALL YOU NEED TO SAY IS SOMETHING LIKE THIS:

STA/PASS:1/FLAGS:HOE

THINGS TO NOTE HERE:

1. ONLY THE FIRST THREE CHARACTERS OF THIS OR ANY COMMAND AT THE 'DS-B>' LEVEL NEED TO BE TYPED.
2. THE 'PASS' SWITCH SPECIFIES HOW MANY PASSES YOU DESIRE. A PASS CONSISTS OF RUNNING THE FULL DIAGNOSTIC AGAINST ALL UNITS BEING TESTED (THIS WILL BE EXPLAINED SHORTLY). ONE PASS IS SPECIFIED IN THE ABOVE EXAMPLE.
3. THE 'FLAGS' SWITCH MAY SPECIFY ANY OF A NUMBER OF FLAGS, BUT THE MAIN USEFUL ONES ARE:

LOE	LOOP ONE ERROR
HOE	HALT ON ERROR
IER	INHIBIT ERROR PRINTOUT

T  
T  
A  
C  
T  
B  
T  
Q  
I  
T  
4  
6  
T  
E  
Q  
2  
T  
L  
W  
R  
A  
R  
B  
A  
V  
A  
B  
A  
D  
A  
2  
T  
C  
S  
T  
A

THE HOE FLAG IS SPECIFIED IN THE ABOVE EXAMPLE (WE'LL SEE WHY SHORTLY).

\*\*\*\*\*  
\* STEP 3 \*  
\*\*\*\*\*

WHEN YOU HAVE TYPED IN A "START" COMMAND, THE DIAGNOSTIC WILL COME BACK WITH THE QUESTION "# UNITS?" TO WHICH YOU SHOULD RESPOND BY TYPING IN THE NUMBER OF DEVICES YOU WISH TO TEST.

A WORD OF WARNING HERE: THE NUMBER OF UNITS DEPENDS ON THE TARGET DEVICE OF THE DIAGNOSTIC. FOR EXAMPLE, IF THE DIAGNOSTIC IS DIRECTED AT A DISK DRIVE, THEN THE NUMBER OF UNITS WOULD BE THE NUMBER OF DRIVES TO BE TESTED. WHEREAS IF THE DIAGNOSTIC WAS DIRECTED AT THE DISK CONTROLLER, THEN THE NUMBER OF UNITS WOULD BE THE NUMBER OF CONTROLLERS. THE TARGET DEVICE OF A DIAGNOSTIC CAN ALWAYS BE DETERMINED BY INSPECTING THE "HEADER" STATEMENT NEAR THE BEGINNING OF THE SOURCE CODE. ONE OF THE OPERANDS OF THIS "HEADER" STATEMENT SHOULD BE THE DEVICE TYPE OF THE DIAGNOSTIC.

\*\*\*\*\*  
\* STEP 4 \*  
\*\*\*\*\*

WHEN YOU HAVE TYPED IN THE NUMBER OF UNITS TO BE TESTED, THE DIAGNOSTIC WILL ASK YOU THE 'HARDWARE QUESTIONS'. THE ANSWERS TO THESE QUESTIONS ARE USED TO BUILD TABLES IN CORE, CALLED 'HARDWARE P-TABLES'. ONE HARDWARE P-TABLE WILL BE BUILT FOR EACH UNIT TO BE TESTED.

THERE ARE SEVERAL HARDWARE QUESTIONS AND THE ENTIRE SERIES WILL BE POSED N TIMES, WHERE N IS THE NUMBER OF UNITS.

THIS REPRESENTS A NEW PHILOSOPHY IN DIAGNOSTIC ENGINEERING. DIAGNOSTICS IN THE FUTURE WILL NOT BE WRITTEN TO AUTOSIZE OR ASSUME STANDARD ADDRESSES: INSTEAD, THEY WILL ASK THE OPERATOR FOR ALL THE INFORMATION THEY NEED TO TEST THE DEVICE.

\*\*\*\*\*  
\* STEP 5 \*  
\*\*\*\*\*

AFTER YOU HAVE ANSWERED ALL THE HARDWARE QUESTIONS (SEC 2.5) FOR ALL THE UNITS, YOU WILL BE ASKED 'CHANGE SW?' IF YOU WANT TO BE ASKED THE SOFTWARE QUESTIONS THAT DETERMINE THE BEHAVIOR OF THIS PROGRAM, TYPE 'Y'. IF YOU WANT TO TAKE ALL THE DEFAULTS TO THESE QUESTIONS, TYPE 'N'. IF YOU TYPE 'Y' YOU WILL BE ASKED THE SOFTWARE QUESTIONS (SEC 2.6), AND THE ANSWERS WILL BE PUT INTO THE SOFTWARE P-TABLE IN THE PROGRAM. THE SERIES OF QUESTIONS WILL BE ASKED JUST ONCE, REGARDLESS OF THE NUMBER OF UNITS TO BE TESTED.

\*\*\*\*\*  
\* STEP 6 \*  
\*\*\*\*\*

AFTER YOU HAVE ANSWERED THE SOFTWARE QUESTIONS, THE DIAGNOSTIC WILL BEGIN TO EXECUTE THE HARDWARE TEST CODE. THERE ARE SEVERAL THINGS THAT CAN HAPPEN NEXT, DEPENDING ON WHETHER A HARDWARE ERROR IS ENCOUNTERED AND ALSO ON WHAT SWITCH VALUES YOU SELECTED ON THE START COMMAND. CONSIDER THE POSSIBILITIES:

1. IF NO ERROR IS ENCOUNTERED, THEN THE DIAGNOSTIC WILL SIMPLY EXECUTE THE DESIRED NUMBER OF PASSES AND RETURN TO COMMAND MODE (PROMPT DS-B>).

2. IF AN ERROR IS ENCOUNTERED, THEN ONE OF THREE THINGS HAPPENS, DEPENDING ON THE SETTINGS OF THE HOE AND LOE FLAGS.

HOE SET: THE ERROR WILL BE REPORTED ON THE CONSOLE AND THE DIAGNOSTIC WILL RETURN TO COMMAND MODE.

LOE SET: THE DIAGNOSTIC WILL LOOP ENLESSLY ON THE BLOCK OF CODE THAT DETECTED THE ERROR.

NEITHER HOE NOR LOE SET: THE ERROR WILL BE REPORTED ON THE CONSOLE AND NORMAL EXECUTION WILL RESUME AS IF NO ERROR HAD OCCURED.

#### 2.1.2 SAMPLE RUN-THROUGH

LET'S SEE HOW ALL THIS WORKS IN A REAL SITUATION. RECALL THAT WE ENTERED THE COMMAND 'STA/PASS:1/FLAGS:HOE'. THIS WOULD BE A VERY TYPICAL WAY TO RUN THE DIAGNOSTIC. IF NO ERRORS ARE ENCOUNTERED, THE SINGLE REQUESTED PASS WILL BE EXECUTED AND THE PROMPT WILL BE REISSUED.

IF AN ERROR IS ENCOUNTERED, THE ERROR WILL BE REPORTED AND THE PROMPT WILL BE REISSUED (BECAUSE THE HOE FLAG IS SET). AT THIS POINT THERE ARE FOUR DIFFERENT WAYS YOU CAN GET THE PROGRAM GOING AGAIN:

1. ISSUE ANOTHER 'START' COMMAND (THUS GOING THRU ALL OF STEPS 2, 3, 4, 5, AND 6 AGAIN)
2. ISSUE A 'RESTART' COMMAND (SAME AS START COMMAND EXCEPT THAT THE HARDWARE QUESTIONS ARE NOT ASKED)
3. ISSUE A 'CONTINUE' COMMAND (EXECUTION WILL RESUME AT THE BEGINNING OF THE PARTICULAR HARDWARE TEST (MOST DIAGNOSTICS CONSIST OF A NUMBER OF THESE) THAT IT WAS IN WHEN THE ERROR HALT OCCURED. NO QUESTIONS ASKED.
4. ISSUE A 'PROCEED' COMMAND: EXECUTION WILL RESUME AT THE INSTRUCTION FOLLOWING THE ERROR REPORT (THIS IS A SPECIAL COMMAND AND CAN BE ISSUED ONLY AT A HALT ON ERROR).

THE MOST TYPICAL THING TO DO HERE IS TO ISSUE THE PROCEED, BUT WITH DIFFERENT FLAG SETTINGS. PROBABLY YOU WOULD WANT TO SAY

PRO/FLAGS:IER:LOE:HOE=0

A  
T  
I  
R  
I  
W  
A  
R  
I  
D  
I  
W  
H  
I  
W  
B  
H  
F  
L  
R  
A  
T  
I  
2  
  
T  
I  
Q  
M  
B  
I  
T  
T  
I  
T  
P  
T  
A  
T  
A  
I  
T  
W

THIS WILL DO THE FOLLOWING:

1. TURN ON THE IER (INHIBIT ERROR PRINTOUT) FLAG
2. TURN ON THE LOE FLAG
3. TURN OFF THE HOE FLAG
4. RESUME EXECUTION AT INSTRUCTION AFTER ERROR REPORT

THE DIAGNOSTIC WILL NOW LOOP ON THE BLOCK OF CODE THAT DETECTED AND REPORTED THE ERROR, BUT NO ERROR PRINTOUT WILL OCCUR. THUS YOU CAN STUDY THE ERROR OR SCOPE IT OR WHATEVER.

WHEN YOU'VE SEEN ENOUGH, YOU MAY HIT CONTROL/C. THIS WILL TAKE YOU OUT OF THE LOOP AND PUT YOU BACK INTO COMMAND MODE. YOU NOW HAVE THREE CHOICES:

1. START
2. RESTART
3. CONTINUE

LET'S SAY YOU'VE REPAIRED THE DEFECT FOUND ABOVE AND WANT TO FINISH RUNNING THE DIAGNOSTIC. YOU WOULD TYPE

CON/FLAGS:HOE:IER=0:LOE=0

THIS WILL RESTORE THE FLAGS TO THEIR ORIGINAL VALUES AND RESUME EXECUTION AT THE BEGINNING OF THE HARDWARE TEST YOU WERE IN. IF THE ERROR DOES NOT RECUR, THE EXECUTION WILL FLOW RIGHT ON THRU TO THE NEXT ERROR OR TO END OF PASS.

IF AT END OF PASS YOU WANT TO RUN THE DIAGNOSTIC AGAIN, YOU HAVE TWO CHOICES:

1. START
2. RESTART

YOU WOULD CHOOSE ONE, DEPENDING ON WHETHER YOU WANTED TO ANSWER THE HARDWARE QUESTIONS AGAIN.

T  
O  
O  
R  
T  
I  
C  
O  
3  
T  
I  
E  
M  
D  
T

THE FULL PRINT-OUT FROM THE ABOVE DIALOGUE MIGHT LOOK LIKE THIS:

	BY WHOM ENTERED:
.R DZRKXX	O
DZRKXX	D
L-CLK (L) N ? Y	D,O
50HZ (L) N ?	D
LSI (L) N ?	D
LPT (L) N ?	D
MEM (K) (D) 16 ?	D
DS-B>STA/PASS:1/FLAGS:HOE	D,O
# UNITS (D) ? 2	D,O
UNIT 1	D
CSR (O) ?	D,O
VECTOR (O) ?	D,O
BR LEVEL (O) ?	D,O
DRIVE (O) ? 0	D,O
UNIT 2	D
CSR (O) ?	D,O
VECTOR (O) ?	D,O
BR LEVEL (O) ?	D,O
DRIVE (O) ? 1	D,O
CHANGE SW (L) ? N	D,O
DZRKXX HARD ERR 00004 TST 003 SUB 002 PC:004130	D
ERR HLT	D
DS-B>PRO/FLAGS:IER:LOE:HOE=0	D,O

\*\*\*\*\*  
AT THIS POINT THE DIAGNOSTIC IS LOOPING ON THE  
ERROR WITHOUT PRINTING ANYTHING. YOU CAN SCOPE  
THE ERROR UNTIL YOU HAVE LOCATED IT, THEN ^C OUT  
\*\*\*\*\*

^C	O
DS-B>CON/FLAGS:HOE:IER:LOE=0	D,O
CHANGE SW (L) ? N	D,O
DZRKXX EOP 1	D
DS-B>RESTART/PASS:1	D,O
CHANGE SW (L) ? N	D,O

-----  
-----  
-----  
-----

## 2.2 HOW TO CREATE A CHAINABLE FILE

THE DIAGNOSTIC AS RECEIVED FROM RELEASE ENGINEERING CANNOT BE RUN IN CHAIN MODE. THAT IS WHY IT BEARS THE EXTENSION 'BIN' INSTEAD OF 'BIC'. THERE IS A WAY, HOWEVER, TO CREATE A CHAINABLE PROGRAM FROM WHAT YOU'VE GOT.

IT CONSISTS OF RUNNING THE PROGRAM WITH THE SPECIAL COMMAND 'CCI' ISSUED WHERE YOU WOULD NORMALLY ISSUE A START COMMAND (TO THE PROMPT DS-B>). THIS COMMAND CAUSES THE DIAGNOSTIC TO GO THRU ALL THE QUESTIONS AND ANSWERS AND THEN TO HALT, JUST WHERE IT WOULD ORDINARILY BEGIN EXECUTION OF THE HARDWARE TEST CODE. AT THIS POINT YOU CAN DUMP THE PROGRAM AS IT SITS IN CORE TO THE LOAD MEDIUM, WITH THE NEW EXTENSION 'BIC'.

HERE IS A SAMPLE DIALOGUE TO ACCOMPLISH THIS:

```
.R UPD2
RESTART: XXXXXX
*CLR
*LOAD DIAG.BIN
XFER:200 CORE:0,60602
*START 200
L-CLK (L) N ?
-----
-----
```

```
DS-B>CCI
# UNITS (D) ? 4
-----
-----
```

```
CHANGE SW (L) ? N
PTAB END: 60632
```

```
*****
*AT THIS POINT THE MACHINE HALTS AND*
*YOU MUST RESTART AT ADDRESS XXXXXX*
*****
```

```
*HICORE 60632
CORE: 0,60632
*DUMP DK0: DIAG.BIC
```

THE RESULT OF DOING THIS IS THAT YOU CAN NOW BUILD AN XXDP CHAIN FILE CONTAINING THE XXDP COMMAND

```
.R DIAG.BIC
```

AND THE DIAGNOSTIC WILL EXECUTE WITHOUT MANUAL INTERVENTION, USING THE ANSWERS THAT YOU GAVE IT WHEN YOU DID THE CCI COMMAND.

## 2.3 DETAILS OF COMMANDS AND SYNTAX

### 2.3.1 TABLE OF COMMAND VALIDITY

THERE ARE FOUR WAYS OF ENTERING DIAGNOSTIC COMMAND MODE, AND DIFFERENT SUBSETS OF THE DIAG COMMAND SET ARE AVAILABLE WITH EACH:

HOW ENTERED	LEGAL COMMANDS
1. OPERATOR ENTERED 'RUN DIAG'	START PRINT DISPLAY FLAGS ZFLAGS
2. DIAGNOSTIC HAS FINISHED ALL ITS REQUESTED PASSED	START RESTART PRINT DISPLAY FLAGS ZFLAGS
3. OPERATOR INTERRUPTED THE DIAGNOSTIC WITH CTRL/C	START RESTART CONTINUE PRINT DISPLAY FLAGS ZFLAGS
4. AN ERROR WAS ENCOUNTERED WITH THE HOE FLAG SET SET	START RESTART CONTINUE PROCEED PRINT DISPLAY FLAGS ZFLAGS

### 2.3.2 COMMAND SYNTAX

```
*****  
STA(RT)/TESTS:TEST-LIST/PASS:PASS-CNT/FLAGS:FLAG-LIST/EOP:EOP-INCR  
*****
```

THE DIAGNOSTIC IN CORE IS EXECUTED IN ACCORDANCE WITH THE SWITCHES SPECIFIED. THE MESSAGE '# UNITS?' IS PRINTED. THE START COMMAND MAY BE ISSUED WHEN DIAGNOSTIC COMMAND MODE HAS BEEN ENTERED VIA ONE OF THE FOLLOWING: A) OPERATOR TYPED 'RUN DIAGNOSTIC' B) DIAGNOSTIC FINISHED EXECUTING C) ERROR WAS ENCOUNTERED WITH HOE FLAG SET D) OPERATOR ENTERED CONTROL/C.

AFTER THE OPERATOR RESPONDS TO '# UNITS?', THE HARDWARE DIALOGUE IS INITIATED. WHEN IT IS COMPLETED, THE QUESTIONS 'CHANGE SW?' IS ISSUED, AND THE ANSWERS, IF GIVEN, BECOME THE NEW DEFAULTS. THEREFORE IT IS NECESSARY TO RELOAD THE PROGRAM IN ORDER TO RETURN TO THE LOAD DEFAULTS.

THE SWITCH ARGUMENTS ARE AS FOLLOWS:

'TEST-LIST' IS A SEQUENCE OF DECIMAL NUMBERS (1:2 ETC.) OR RANGES OF DECIMAL NUMBERS (1-5:8-10 ETC.) THAT SPECIFY THE TESTS TO BE EXECUTED. THE NUMBERS ARE SEPARATED BY COLONS. THE NUMBERS RANGE FROM 1 TO THE LARGEST TEST NUMBER IN THE DIAGNOSTIC. THEY MAY BE SPECIFIED IN ANY ORDER. TESTS WILL BE EXECUTED IN NUMERICAL ORDER REGARDLESS OF THE ORDER OF SPECIFICATION. THE DEFAULT IS TO EXECUTE ALL TESTS.

'PASS-CNT' IS A DECIMAL NUMBER INDICATING THE DESIRED NUMBER OF PASSES. A PASS IS DEFINED AS THE EXECUTION OF THE FULL DIAGNOSTIC (ALL SELECTED TESTS) AGAINST ALL UNITS SUBMITTED. THE DEFAULT IS NON-ENDING EXECUTION. 'FLAG-LIST' IS A SEQUENCE OF ELEMENTS OF THE FORM <FLAG>, <FLAG=1>, OR <FLAG=0>, SEPARATED BY COLONS, WHERE <FLAG> HAS ONE OF THE FOLLOWING VALUES:

HOE	HALT ON ERROR, CAUSING COMMAND MODE TO BE ENTERED WHEN AN ERROR IS ENCOUNTERED
LOE	LOOP ON ERROR, CAUSING THE DIAGNOSTIC TO LOOP CONTINUOUSLY WITHIN THE SMALLEST DEFINED BLOCK OF CODING (SEGMENT, SUBTEST, OR TEST) CONTAINING THE ERROR
IER	INHIBIT ERROR REPORTING
IBE	INHIBIT BASIC ERROR REPORTS
IXE	INHIBIT EXTENDED ERROR REPORTS
PRI	DIRECT ALL MESSAGES TO A LINE PRINTER
PNT	PRINT NUMBER OF TES BEING EXECUTED
BOE	BELL ON ERROR
UAM	RUN IN UNATTENDED MODE, BYPASSING MANUAL INTERVENTION TESTS
ISR	INHIBIT STATISTICAL REPORTS
IDU	INHIBIT DROPPING OF UNITS BY DIAGNOSTIC

THE FLAGS NAMED OR EQUATED TO 1 ARE SET, THOSE EQUATED TO 0 ARE CLEARED. A FLAG NOT SPECIFIED IS CLEARED. IF THE FLAGS SWITCH IS NOT GIVEN ALL FLAGS ARE CLEARED.

'EOP-INCR' IS A DECIMAL NUMBER INDICATING HOW OFTEN (IN TERMS OF PASSES) IT IS DESIRED THAT THE END OF PASS MESSAGE BE PRINTED. THE DEFAULT IS AT THE END OF EVERY PASS.

\*\*\*\*\*  
RES(TART)/TEST:TEST-LIST/PASS:PASS-CNT/FLAGS:FLAG-LIST/EOP:EOP-INCR/UNITS:UNIT-LIST  
\*\*\*\*\*

THE DIAGNOSTIC IN CORE IS EXECUTED IN ACCORDANCE WITH THE SWITCHES SPECIFIED. HOWEVER, NEW P-TABLES ARE NOT BUILT. INSTEAD, THE ONES IN CORE ARE USED.

THE QUESTION 'CHANGE SW?' IS ASKED, AND THE ANSWERS IF GIVEN BECOME THE NEW DEFAULTS. THE COMMAND MAY BE ISSUED WHEN COMMAND MODE HAS BEEN ENTERED VIA A) DIAGNOSTIC IS FINISHED B) HALT ON ERROR C) CONTROL/C.

THE SWITCH ARGUMENTS ARE AS IN THE START COMMAND EXCEPT:

1. 'UNIT-LIST' IS A SEQUENCE OF LOGICAL UNIT NUMBERS RANGING FROM 1 THRU N (N = NUMBER OF UNITS BEING TESTED) SPECIFYING WHICH UNITS ARE TO BE TESTED. THE LOGICAL UNIT NUMBER DESIGNATES THE POSITION OF THE P-TABLE IN CORE, ACCORDING TO THE ORDER IN WHICH THEY WERE BUILT. THE UNITS SPECIFIED MUST NOT HAVE BEEN DROPPED BY THE OPERATOR DROP COMMAND. THE UNIT-LIST DEFAULTS TO 'ALL THAT HAVE NOT BEEN DROPPED BY OPERATOR COMMAND'. THE EFFECT OF THE UNIT-LIST LASTS UNTIL THE NEXT START (WHERE IT IS AUTOMATICALLY RESET TO 'ALL') OR THE NEXT RESTART.
2. ALL UNSPECIFIED FLAG SETTINGS ARE UNCHANGED.

\*\*\*\*\*  
CON(TINUE)/PASS:<PASS-CNT/FLAGS:<FLAG-LIST>  
\*\*\*\*\*

COMMAND MODE MUST HAVE BEEN ENTERED DUE TO A HALT ON ERROR OR A CONTROL/C. THE EFFECT OF THE COMMAND IS TO GO TO THE BEGINNING OF THE TEST THAT WAS BEING EXECUTED WHEN THE HALT OR CONTROL/C TOOK PLACE. SOFTWARE DIALOGUE MAY OPTIONALLY BE REEXECUTED. HARDWARE PARAMETERS MAY NOT BE CHANGED.

THE SWITCH ARGUMENTS ARE AS IN THE START COMMAND EXCEPT:

1. DEFALT FOR PASS-CNT IS THE UNSATISFIED PASS-CNT FROM THE PREVIOUS START OR RESTART
2. UNSPECIFIED FLAG SETTINGS ARE UNCHANGED

\*\*\*\*\*  
PRO(CCEED)/FLAGS:<FLAG-LIST>  
\*\*\*\*\*

COMMAND MODE MUST HAVE BEEN ENTERED VIA A HALT ON ERROR. THE EFFECT OF THE COMMAND IS TO BEGIN EXECUTION AT THE LOCATION FOLLOWING THE ERROR CALL. NEITHER HARDWARE NOR SOFTWARE PARAMETERS MAY BE ALTERED.

THE SWITCH ARGUMENTS ARE THE SAME AS THE START COMMAND EXCEPT:

1. UNSPECIFIED FLAG SETTINGS ARE UNCHANGED

\*\*\*\*\*  
CCI/TEST:TEST-LIST/PASS:PASS-CNT/FLAGS:FLAG-LIST/EOP:EOP-INCR  
\*\*\*\*\*

THE DIAGNOSTIC EXECUTES THRU ALL OPERATOR DIALOGUE AND HALTS AT THE HARDWARE TEST CODE. NOW THE OPERATOR CAN DUMP THE CORE IMAGE TO THE MEDIUM WITH A BIC EXTENSION.

THE BIC FILE MUST BE HANDLED DIFFERENTLY DEPENDING ON WHETHER IT IS RUN MANUALLY OR IN CHAIN MODE. IF RUN MANUALLY IT CAN BE INVOKED EITHER WITH A 'START' (IN WHICH CASE IT WILL BEHAVE LIKE THE BIN FILE: THE PRE-GENERATED ANSWERS TO OPERATOR QUESTIONS WILL BE IGNORED) OR WITH A 'RESTART' (IN WHICH CASE THE PRE-GENERATED OPERATOR ANSWERS WILL BE USED).

IF RUN IN CHAIN MODE, AUTOMATIC EXECUTION WILL COMMENCE IMMEDIATELY FROM THE XXDP COMMAND '.R DIAG'. THE COMMAND PROMPT 'DS-B>' WILL NOT BE ISSUED.

ANY SWITCHES SPECIFIED ON THE CCI COMMAND WILL CARRY OVER WHEN THE BIC FILE IS RUN IN CHAIN MODE (EXCEPT THAT UAM IS ALWAYS SET THERE) BUT WILL NOT CARRY OVER WHEN IT IS RUN MANUALLY.

TO DO A CCI ON A FULL SIZED DIAGNOSTIC (14.5K WORDS), A MACHINE SIZE LARGER THAN 16K IS REQUIRED. THE EXACT SIZE NEEDED DEPENDS ON WHICH UTILITY IS USED TO EXECUTE THE DIAGNOSTIC AT CCI TIME.

\*\*\*\*\*  
DRO(P)/UNITS:UNIT-LIST  
\*\*\*\*\*

THE UNITS SPECIFIED ARE DROPPED FROM TESTING UNTIL THEY ARE ADDED BACK OR UNTIL A START COMMAND IS GIVEN. A DROP CANNOT BE FOLLOWED BY A PROCEED.

THERE IS ALSO A 'DROP' MACRO INTERNAL TO THE DIAGNOSTIC, WHICH GIVES THE FACILITY OF AUTO-DROPPING. THE DURATION OF A PROGRAM DROP, HOWEVER, IS ONLY UNTIL THE NEXT START OR RESTART.

\*\*\*\*\*  
ADD/UNITS:UNIT-LIST  
\*\*\*\*\*

THE UNITS SPECIFIED ARE ADDED BACK (THEY MUST HAVE BEEN PREVIOUSLY DROPPED BY THE DROP COMMAND) TO THE TEST SEQUENCE. AN ADD CANNOT BE FOLLOWED BY A PROCEED.

\*\*\*\*\*  
PRI(NT)  
\*\*\*\*\*

ALL STATISTICS TABLES ACCUMULATED BY THE DIAGNOSTIC ARE PRINTED. THE ISR (INHIBIT STATISTICAL REPORTING) FLAG IS CLEARED.

\*\*\*\*\*  
DIS(PLAY)/UNITS:<UNIT-LIST>  
\*\*\*\*\*

THE HARDWARE P-TABLES FOR ALL UNITS UNDER TEST ARE PRINTED OUT IN THE FORMAT IN WHICH THEY WERE ENTERED. ANY UNITS THAT WERE DROPPED BY THE OPERATOR 'DROP' COMMAND ARE SO DESIGNATED.

\*\*\*\*\*  
FLA(GS)  
\*\*\*\*\*

THE CURRENT SETTINGS OF ALL FLAGS ARE PRINTED.

\*\*\*\*\*  
ZFL(AGS)  
\*\*\*\*\*

ALL FLAGS ARE CLEARED.

#### 2.4 EXTENDED P-TABLE DIALOGUE

THE FULL CAPABILITY OF THE HARDWARE DIALOGUE IS REVEALED BY THE FOLLOWING DISCUSSION OF WHAT HAPPENS INTERNALLY.

AS SOON AS THE QUESTION '# UNITS?' IS ANSWERED (WITH THE NUMBER N, SAY) SPACE IN CORE IS ALLOCATED FOR N P-TABLES. ALL OF THE P-TABLES ARE OF THE SAME FORMAT, AND THERE IS A ONE-TO-ONE CORRESPONDENCE BETWEEN THE HARDWARE PARAMETER QUESTIONS AND THE SLOTS IN THE P-TABLE FORMAT.

ON THE FIRST TRIP THRU THE QUESTIONS, ALL OF THE SLOTS IN ALL OF THE P-TABLES ARE FILLED. IF THE OPERATOR TYPES IN LESS THAN N EXPLICIT VALUES IN RESPONSE TO A PARTICULAR QUESTION, THESE VALUES ARE PLACED IN THE P-TABLES (ONE VALUE GOING INTO THE PROPER SLOT OF EACH P-TABLE BEGINNING WITH THE FIRST P-TABLE) UNTIL THE STRING OF VALUES IS EXHAUSTED. THE LAST VALUE THAT SLOT IN THE REMAINING P-TABLES.

ON SUBSEQUENT TRIPS THRU THE QUESTIONS, THE SAME PROCESS IS CARRIED OUT, EXCEPT THAT THE EARLIEST P-TABLE NOT TO HAVE RECEIVED AN EXPLICIT VALUE IN ANY OF ITS SLOTS NOW ASSUMES THE ROLE THAT TABLE NUMBER ONE PLAYED IN THE FIRST TRIP.

THE SERIES OF QUESTIONS IS REISSUED UNTIL AT LEAST ONE QUESTION HAS RECEIVED N EXPLICIT VALUES FROM THE OPERATOR.

IN GIVING A STRING OF VALUES, COMMAS WITHOUT INTERVENING VALUES MAY BE USED TO INDICATE A REPETITION OF THE LAST NAMED VALUE.

A STRING OF VALUES MAY BE GIVEN AS A RANGE (6-10 FOR EXAMPLE). IF THE VALUES REPRESENT PURE NUMERICAL DATA, THIS SAMPLE RANGE TRANSLATES TO THE STRING 6,7,8,9,10 (AN INCREMENT OF 1). IF THE VALUES ARE ADDRESSES, THE SAMPLE RANGE TRANSLATES TO THE STRING 6,8,10 (AN INCREMENT OF 2).

NOW LET US SEE HOW WE COULD USE THESE CAPABILITIES TO CONSTRUCT A SET OF P-TABLES. ASSUME THAT WE HAVE 64 UNITS, AND THAT THERE ARE THREE HARDWARE PARAMETERS FOR EACH (THREE SLOTS IN THE P-TABLE, THREE HARDWARE QUESTIONS IN THE DIALOGUE). LET THE DESIRED VALUE FOR THE FIRST PARAMETER BE THE NUMBER 75 FOR ALL 64 TABLES. LET THE DESIRED VALUE FOR THE SECOND PARAMETER BE EQUAL TO THE UNIT NUMBER (1,2,3,...,64) EXCEPT FOR UNIT 50, WHICH SHOULD RECEIVE THE VALUE 49. LET THE DESIRED VALUE FOR THE THIRD PARAMETER BE THE NUMBER 76 FOR THE FIRST 20 UNITS AND THE NUMBER 77 FOR THE LAST 44 UNITS.

THE FOLLOWING DIALOGUE WOULD ACCOMPLISH THIS GOAL:

# UNITS (D) ? 64

UNIT 1  
<QUESTION 1> ? 75  
<QUESTION 2> ? 1-20  
<QUESTION 3> ? 76

UNIT 21  
<QUESTION 1> ?  
<QUESTION 2> ? 21-49,,51-64  
<QUESTION 3> ? 77

THE FIRST TIME THE SERIES IS ASKED, SLOT ONE RECEIVES A 75 IN ALL 64 TABLES. SLOT TWO RECEIVES THE VALUES 1,2,3,...,20 IN TABLES 1 THRU 20 AND A CONSTANT 20 IN TABLES 21 THRU 64. SLOT THREE RECEIVES A CONSTANT 76 IN ALL 64 TABLES.

THE SECOND TIME THRU THE SERIES, TABLES 21 THRU THE END ARE GOING TO BE AFFECTED (NOTE THAT THIS PIECE OF INFORMATION IS PRINTED OUT FOR THE OPERATOR IN THE FORM 'UNIT XX' AT THE BEGINNING OF EACH SERIES). QUESTION 1 IS RESPONDED TO BY A <CR>, SO SLOT ONE STAYS A CONSTANT 75 IN TABLES 21 THRU 64, SINCE NO NEW EXPLICIT VALUES ARE TYPED IN. SLOT TWO GETS THE VALUES 21,22,23,...,49 IN TABLES 21 THRU 49, AND GETS A 49 IN SLOT 50, AND GETS THE VALUES 51,52,53,...,64 IN TABLES 51 THRU 64. SLOT THREE GETS THE VALUE 77 IN TABLES 21 THRU 64.

THE DIALOGUE IS TERMINATED WHEN THE SOFTWARE RECOGNIZES THAT 64 EXPLICIT VALUES HAVE BEEN GIVEN FOR AT LEAST ON QUESTION (NAMELY QUESTION 2).

## 2.5 HARDWARE PARAMETERS

THE FOLLOWING QUESTIONS WILL BE ASKED ON A START COMMAND. THE VALUE LOCATED TO THE LEFT OF THE QUESTION MARK IS THE DEFAULT VALUE THAT WILL BE TAKEN ON A CARRIAGE RETURN RESPONSE.

RL11 (L) Y?

ANSWER YES(Y) IF YOU HAVE AN RL11 CONTROLLER, NO(N) IF YOU HAVE AN RLV11 CONTROLLER.

BUS ADDRESS (O) 174400?

ANSWER WITH THE BUS ADDRESS OF THE CONTROLLER.

VECTOR (O) 330?

ANSWER WITH THE INTERRUPT VECTOR OF THE CONTROLLER.

BR LEVEL (O) 5?

ANSWER WITH THE INTERRUPT PRIORITY OF THE CONTROLLER.

DRIVE (O) 0?

ANSWER WITH THE DRIVE(S) CONNECTED TO THE CONTROLLER.

## 2.6 SOFTWARE PARAMETERS

THE FOLLOWING QUESTIONS ARE ASKED IF REQUESTED ON A START, RESTART, OR CONTINUE. THEY ALLOW FLEXIBILITY IN THE WAY THE PROGRAM BEHAVES. THE SOFTWARE PARAMETERS GIVE THE PROGRAM FLEXIBILITY IN THE WAY IT RUNS. THE PARAMETERS CAN BE MODIFIED ON A START, RESTART, OR CONTINUE BY ANSWERING (Y)ES TO THE FOLLOWING QUESTION:

CHANGE S.W. ?

A YES ANSWER WILL ASK THE FOLLOWING SOFTWARE PARAMETER QUESTIONS, WITH THE PRESENT DEFAULT VALUE PRINTED TO THE LEFT OF THE QUESTION MARK. (THE LAST ANSWER GIVEN IS THE DEFAULT) THE DEFAULT IS TAKEN ON A <CR>. CONTROL Z (^Z) WILL DEFAULT ALL REMAINING QUESTIONS AND START THE TEST.

USE ALL CYLINDERS (N)?

IF 'YES', THOSE TESTS THAT NORMALLY USE A SELECTED SET OF CYLINDERS WILL TEST EVERY CYLINDER ON THE CARTRIDGE.

USE ALL SECTORS (N)?

IF 'YES', THOSE TESTS THAT NORMALLY USE A SINGLE SECTOR TO TEST A GIVEN OPERATION (SUCH AS SEEK DESTINATION) WILL READ AND VERIFY EVERY SECTOR HEADER.

EXECUTE MANUAL INTERVENTION TESTS (N)?

IF 'YES', SEEK TIMING, ROTATIONAL TIMING, AND WRITE LOCK ERROR AND DATA PROTECTION TESTS ARE EXECUTED. THE ONLY TEST THAT ACTUALLY REQUIRES MANUAL INTERVENTION IS THE WRITE LOCK TEST AND THAT TEST WILL BYPASS AUTOMATICALLY AFTER WAITING 30 SECONDS FOR WRITE LOCK TO BE SET.

LOWER SEEK LIMIT (N)?

IF 'YES', THE NEXT PARAMETER IS REQUESTED.

ENTER VALUE (DECIMAL) (0)?

THIS LIMIT IS IMPOSED ON ALL SEEK OPERATIONS SUCH THAT TESTING IS NOT DONE BELOW THAT LIMIT. IN ADDITION, SETTING THIS LIMIT (OR THE UPPER LIMIT, SEE BELOW) CAUSES THE FORWARD AND REVERSE OSCILLATING SEEK TESTS TO PERFORM DIFFERENTLY (SEE TEST DESCRIPTION). TESTS THAT REQUIRE ACCESS TO A SPECIFIC CYLINDER THAT FALLS BELOW THE SPECIFIED LIMIT WILL IGNORE THE LIMIT (SEE WRITE/READ TEST PART 1).

UPPER SEEK LIMIT (N)?

IF 'YES', AN UPPER CYLINDER LIMIT IS IMPOSED IN THE SAME MANNER AS THE LOWER SEEK LIMIT. A 'YES' RESPONSE WILL CAUSE THE FOLLOWING PARAMETER REQUEST.

ENTER VALUE (DECIMAL) (255)?

USE ONLY ONE SURFACE (N)?

IF 'YES', THE NEXT PARAMETER IS REQUESTED.

SPECIFY SURFACE (0 OR 1) (DECIMAL) (0)?

WHICHEVER SURFACE IS SPECIFIED IS THE ONLY SURFACE TESTED IN THE ENTIRE PROGRAM. ANY TEST THAT IS DESIGNED TO TEST THE OTHER SURFACE IS AUTOMATICALLY BYPASSED. THE PROGRAM DOES NOT PRINT ANY INDICATION THAT A TEST IS BYPASSED IN THIS CASE.

SPECIFY ERROR LIMIT (DECIMAL) (20)?

THIS PARAMETER SPECIFIES THE MAXIMUM NUMBER OF ERRORS ALLOWED. THIS LIMIT IS ON A PER DRIVE BASIS IN A SINGLE PASS. IF THE ERROR LIMIT IS EXCEEDED, THE DRIVE IS DROPPED FROM FURTHER TESTING.

DATA COMPARE ERROR LIMIT (DECIMAL) (20)?

THIS PARAMETER SPECIFIES THE NUMBER OF DATA COMPARE ERRORS THAT WILL BE LISTED FOR A GIVEN COMPARE OPERATION. AFTER THE LIMIT IS REACHED, THE DATA ERRORS ARE NOT PRINTED BUT THE COMPARE CONTINUES UNTIL THE END OF THE DATA FIELD. A TOTAL IS REPORTED AT THE END OF THE COMPARE.

DROP DRIVE IF NO RESPONSE (N)?

IF THIS PARAMETER IS SPECIFIED AS YES, THE PROGRAM WILL CHECK IF THE DRIVE IS READY OR IF IT WILL RESPOND TO A GET STATUS BEFORE TESTING STARTS ON THAT DRIVE. IF IT IS NOT READY AND WILL NOT RESPOND TO A GET STATUS THE DRIVE IS DROPPED AND A MESSAGE IS PRINTED.

### 3.0 ERROR INFORMATION

ALL ERRORS ARE PRINTED VIA CONSOLE DEVICE. THE ERROR INCLUDES ERROR NUMBER, TYPE AND PROGRAM LOCATION. ERRORS INCLUDE REGISTERS BEFORE AND AT ERROR WITH RELEVANT DATA.

### 3.1 ERROR REPORTING

THE OPERATION MESSAGE (LINE 4) IS GENERATED IN A DYNAMIC MANNER BASED ON THE SUBSYSTEM FUNCTION BEING EXECUTED AT THE TIME OF THE ERROR AND THE STATE OF THE FLAGS IN THE LOCATION TAGGED 'OPFLAGS'. THE POSSIBLE OPERATION MESSAGES ARE GIVEN BELOW.

SEEK - FROM (CYL NUM) DIFF (CYL DIFF) SGN (0 OR 1) HD (0 OR 1) WHERE THE VALUES ARE GIVEN IN OCTAL. THIS MESSAGE IS THE RESULT OF A SEEK OPERATION THAT WAS VERIFIED BY A READ HEADER AND THE HEAD POSITION AFTER A SEEK IS IN ERROR. (THE ACTUAL HEAD POSITION IN THIS ERROR SITUATION IS GIVEN IN THE RESULT LINE, LINE 5.)

READ DATA - IS A READ DATA OPERATION WHERE SOME FORM OF ERROR WAS DETECTED IN THE ACTUAL READ OPERATION. THIS ERROR COULD BE HARDWARE DETECTED SUCH AS DATA CRC, HEADER CRC, HEADER NOT FOUND, ETC., OR A SOFTWARE DETECTED ERROR SUCH AS DRIVE READY RESET AFTER A READ DATA COMPLETED.

READ DATA WITH DATA COMPARE - IS AN ERROR THAT WAS DETECTED AS BAD DATA IN THE BUFFER AFTER

A READ DATA OPERATION. WHEN THIS OPERATION IS REPORTED IT INDICATES THE ACTUAL READ DATA OPERATION COMPLETED WITH NO DETECTED ERRORS BUT THE DATA WAS WRONG.

READ HEADER - READ HEADER FOR 40 HEADERS - READ HEADER FOR 40 HEADERS WITH HEADER COMPARE - HAVE THE SAME GENERAL MEANING AS THE READ DATA AND READ DATA WITH DATA COMPARE. MESSAGES HAVING THE OPERATION OF READ HEADER OR READ HEADER FOR 40 HEADERS ARE THE RESULT OF ERRORS DETECTED IN THE ACTUAL OPERATION WHILE THE READ HEADER FOR 40 HEADERS WITH HEADER COMPARE INDICATES NO ERROR IN THE ACTUAL OPERATION BUT THE HEADER DATA ITSELF WAS IN ERROR.

WRITE DATA - RESET - GET STATUS - GET STATUS WITH RESET - ARE ALL BASIC OPERATIONS. AS BEFORE, THE ERROR DETECTION CAN BE EITHER HARDWARE OR SOFTWARE. THE RESULT LINE (LINE 5) WILL DEFINE THE REASON FOR THE REPORT.

LD DRV - UNLD DRV - ARE OPERATION MESSAGES THAT WILL APPEAR IN THE REPORT WHEN THE DRIVE LOAD AND UNLOAD SEQUENCE IS BEING TESTED.

ANOTHER GROUP OF OPERATION QUALIFIERS WILL BE REPORTED FOR OPERATIONS THAT FAIL IN SPECIFIC TESTS. THESE TESTS ARE THE WRITE/READ TEST PART 2, OVERWRITE TEST, AND THE ADJACENT CYLINDER INTERFERENCE TEST.

<u>OPERATION</u>	<u>QUALIFIER</u>
READ DATA WITH DATA COMPARE	FOL 0 TO CC SEEK
READ DATA	FOL 255 TO CC SEEK
WRITE DATA	FOL WRITE (NO SEEK)
READ HEADER	ADJ. CYL WRITTEN AFTER FWD SK
	ADJ. CYL WRITTEN AFTER REV SK
	SK FWD, WRT-SK REV, OVERWRT
	SK REV, WRT-SK FWD, OVERWRT

THE ABOVE OPERATIONS CAN BE REPORTED WITH ANY OF THE QUALIFIERS. THE QUALIFIERS IN THESE TESTS ARE AN ATTEMPT TO MAKE THE REPORT MORE MEANINGFUL BY PROVIDING INFORMATION ABOUT THE SEQUENCE OF OPERATIONS BEING DONE.

THE QUALIFIERS "FOL 0 TO CC SEEK" AND "FOL 255 TO CC SEEK" INDICATE THAT THE SEQUENCE OF OPERATIONS INCLUDED A SEEK OF A GIVEN DIRECTION TO THE CYLINDER WHERE THE TEST IS BEING

PERFORMED.

THE "FOL WRITE (NO SEEK)" QUALIFIER MEANS THAT THE OPERATION WAS DONE AFTER A WRITE WITH NO HEAD MOVEMENT BETWEEN THE WRITE AND READ.

THE QUALIFIER "ADJ CYL WRITTEN AFTER FWD SK" AND "ADJ CYL WRITTEN AFTER REV SK" WILL BE REPORTED ONLY IN THE ADJACENT CYLINDER INTERFERENCE TEST. THESE QUALIFIERS ARE USED WHEN THE ERROR OCCURS ON THE CYLINDER UNDER TEST AND DEFINE THE DIRECTION THE HEADS WERE MOVED WHEN THE ADJACENT CYLINDER WAS WRITTEN.

THE QUALIFIERS 'SK FWD, WRT-SK REV, OVERWRT' AND 'SK REV, WRT-SK FWD, OVERWRT' WILL BE REPORTED ONLY IN THE OVERWRITE TEST. THESE QUALIFIERS DEFINE THE DIRECTION OF HEAD MOTION BEFORE THE INITIAL WRITE AND THE OVERWRITE.

THE QUALIFIER 'ON BAD SEC FILES' WILL BE REPORTED WITH THE WRITE DATA COMMAND IF THE PROGRAM ABORTS THAT COMMAND BECAUSE THE WRITE WOULD BE ON THE BAD SECTOR FILES.

### 3.1.2 SPECIFIC RESULT MESSAGES

-----

THE RESULT MESSAGE (LINE 5) IS GENERATED DYNAMICALLY BASED ON THE EXPECTED RESULT OF THE OPERATION BEING TESTED. SINCE OPERATIONS ARE MONITORED DURING EXECUTION THE RESULT MESSAGE MAY REPORT AN ERROR DETECTED DURING THE OPERATION AS WELL AS THE ERRORS SEEN AT THE END OF THE OPERATION. ONLY THE FIRST ERROR SEEN IS REPORTED IN ALL CASES.

THE GENERAL FORMAT FOR THE RESULT LINE IS

RESULT:(VAR 1) IS (VAR 2) SB (VAR 3) (OPTIONAL QUALIFIER)

WHERE VARIABLE 1 CAN BE ONE OF THE FOLLOWING:

CONT ERR	(CONTROLLER ERROR)
DRV ERR	(DRIVE ERROR)
NON-EXSTNT MEM	(NON-EXISTANT MEMORY)
HDR CRC	(HEADER CRC ERROR)
DATA CRC	
HDR NOT FND	(HEADER NOT FOUND)
DATA LATE	
HDR NOT FND/HDR (CRC/OPI	(ALL 3 BITS SET)
DRV RDY	(DRIVE READY)
SELECTED HEAD	
VOL CHK	(VOLUME CHECK)
COVER OPEN	
BRUSH HME	(BRUCH HOME)
WRT LCK	(WRITE LOCK)
HDS OUT	(HEADER OUT)
DRV SEL ERR	(DRIVE SELECT ERROR)
DRV STATE	(DRIVE STATE)
SPIN TIMEOUT	(SPINDLE TIMEOUT SPD ERROR)
WRT GAT ERR	(WRITE GATE ERROR)
SEEK TIMEOUT	(SKTO ERROR)
CUR HEAD ERR	(CURRENT IN HEAD ERROR)
WRT DAT ERR	(WRITE DATA ERROR)
OP INCOMPLETE	(OPI ERROR)
HDR/DAT ERR	(HEADER CRC OR DATA CRC ERROR BIT 11 OF CS REGISTER)
HDR NOT FND/DAT LATE	(HEADER NOT FOUND OR DATA LATE ERROR BIT 12 OF CS REGISTER)
CYL	(CYLINDER WHEN REPORTING A SEEK ERROR)

VARIABLE 2 WILL BE A VALUE THAT DEFINES WHAT THE RESULT ACTUALLY IS. THIS CAN BE A 1 OR 0 TO INDICATE A SET OF RESULT CONDITIONS, A NUMBER 0 TO 7 TO INDICATE THE DRIVE STATE, OR A NUMBER 0 TO 377 (OCTAL) TO IDENTIFY A CYLINDER NUMBER.

VARIABLE 3 DEFINES THAT THE VALUE GIVEN IS VARIABLE 2 SHOULD BE.

THE OPTIONAL QUALIFIER IS PROVIDED WHEN IT IS USEFUL TO KNOW WHEN THE ERROR WAS DETECTED IN THE OPERATION BEING PERFORMED. THIS QUALIFIER IS USED TO REPORT RESULTS SUCH AS:

```
BRUSH HME IS 1 SB 0 IN STATE 2
HEADS OUT IS 0 SB 1 IN STATE 3
DRV RDY IS 0 SB 1 IN DATA XFER
SELECTED HEAD IS 1 SB 0 IN CYCLE UP
DRV RDY IS 0 SB 1 IN STATE 5
DRV RDY IS 1 SB 0 IN SEEK W/O MOTION
DRV RDY IS 0 SB 1 IN 10MS
DRV RDY IS 0 SB 1 IN 500MS
DRV RDY IS 0 SB 1 IN 5SECONDS
```

THESE RESULTS, WHEN SEEN WITH THE OPERATION MESSAGE, WILL BE SELF EXPLANATORY.

OTHER RESULT MESSAGES THAT CAN BE PART OF AN ERROR REPORT ARE:

"INTERRUPT TO LATE" WHICH INDICATES THAT THE OPERATION BEING PERFORMED DID NOT COMPLETE IN THE EXPECTED AMOUNT OF TIME. THIS RESULT CAN BE CAUSED BY THE DRIVE LOSING READY BEFORE STARTING A READ HEADER AND THEREFORE NOT COMPLETING THE READ HEADER IN 1MS.

"FAIL TO RELOAD HEADS AFTER ERR CLEAR" IS REPORTED WHEN AN ERROR CAUSES HEADS TO UNLOAD AND AFTER THE ERROR IS CLEARED THE HEADS DO NOT RELOAD.

"UNKN DRV STATE-NO RDY, NO ERR, HDS OUT" IS REPORTED WHEN THE PROGRAM CANNOT DETERMINE THE DRIVE STATE OR STATUS.

"WRITE ABORTED" IS REPORTED WHEN THE PROGRAM ABORTS A WRITE TO PROTECT THE BAD SECTOR FILES.

"COULD NOT RETRIEVE DRIVE STATUS" IS REPORTED IF THE GET STATUS COMMAND DOES NOT COMPLETE SUCCESSFULLY WHEN THE STATUS IS REQUIRED TO REPORT AN ERROR.

"OPI SET-NO DRIVE RESPONSE" IS REPORTED AS THE RESULT WHEN THE GET STATUS COMMAND IS TIMED OUT (OPI SETS) WHEN THAT COMMAND IS BEING USED IN THE EARLY TESTS TO CHECK THE DRIVE INTERFACE.

"NO INTERRUPT ON CMND COMPLETE" IS REPORTED WHEN THE COMMAND SUCCESSFULLY COMPLETES BUT THE CONTROLLER HAS NOT GENERATED AN INTERRUPT.

'ERR DID NOT CLEAR' IS REPORTED WHEN THE RESET COMMAND DOES NOT CLEAR THE CONTROLLER ERRORS. THIS IS A CONTROLLER RELATED PROBLEM BUT IS REPORTED IF SEEN IN THE DRIVE TEST PROGRAMS.

'DRV ERR IS NOT CLEARED' IS REPORTED WHEN THE GET STATUS W/RESET COMMAND DOES NOT CLEAR ALL DRIVE ERRORS.

'UNEXPECTED ERR' IS REPORTED WHEN THE CONTROLLER SENSES AN ERROR BUT NO ERROR BITS ARE SET.

'BAD SEC FILE FMT ERR' IS REPORTED IF THE CONTENTS OF THE FILES DO NO CORRESPOND TO THE EXPECTED FORMAT. (REFER TO DEC STANDARD 144 FOR FORMAT SPECIFICS.)

### 3.1.3 OTHER MESSAGES

OTHER INFORMATION IS REPORTED UNDER VARIOUS CIRCUMSTANCES. THESE ARE:

'BAD SEC FILES NOT STRD. ALL SEC ASSUMED GOOD.' THIS MESSAGE IS PRINTED WHEN A PARTICULAR TEST REQUIRES THE BAD SECTOR FILES BUT THEY HAVE NOT BEEN STORED. THIS SITUATION WILL OCCUR IF THIS TEST IS STARTED OUT OF THE NORMAL PROGRAM SEQUENCE OR IF THE BAD SECTOR FILES COULD NOT BE READ.

'ERROR LIMIT EXCEEDED-UNIT DROPPED' IS REPORTED (WITH THE UNIT NUMBER) WHEN MORE THAN THE

SPECIFIED NUMBER OF ERRORS (DEFAULT 20) HAVE OCCURED IN ANY SINGLE PASS.

MOST ERROR REPORTS HAVE THE FOLLOWING FORMAT.

```

(1)  PROG NAME  ERR NUM  TEST NUM  SUBTEST NUM  ERR PC
(2)  ROUTINE TRACE SEQ (IN SEQ CALLED)
      (ADDRESS)
      (ADDRESS)
      .
      (ADDRESS)
(3)  TEST DESCRIPTION
(4)  OPERATION:
(5)  RESULT:
(6)  ADDRESS OF UNIT UNDER TEST
(7)  RLCS      RLDA      RLBA      RLMP      CYL      HD
(8)  OP INIT
(9)  OP DONE
(10) DRIVE STATUS
(11) WORD NUM IS (XXXXXX) SB (YYYYYY)
(12) TOTAL COMPARE ERRS: (ZZZ) OF (128)

```

THE ONLY EXCEPTION TO THE ABOVE FORMAT IS PURE DATA COMPARE ERRORS (NOT DETECTED BY READ ERROR). THEN THE FORMAT DOES NOT INCLUDE LINES 5 THROUGH 10.

LINE 1 IS THE ERROR HEADER AND IS PROVIDED BY THE SUPERVISOR. THE PROGRAM IS IDENTIFIED BY NAME WITH THE NUMBER OF TEST AND SUBTEST PRESENTLY BEING EXECUTED.

THE SUBTEST NUMBER IS UNIQUE IN THIS PROGRAM IN THAT IT DOES

NOT REFER TO A PHYSICAL SUBTEST WITHIN A GIVEN TEST. RATHER IT REFLECTS THE NUMBER OF TIMES A SUBTEST HAS BEEN EXECUTED WITHIN A TEST. CONSEQUENTLY, ON A TEST THAT TESTS AN INCREMENTAL TYPE OF OPERATION (SUCH A INCREMENTAL SEEKS, READ ALL HEADERS FROM BOTH SURFACES, ETC.) THE SUBTEST WILL BE DESCRIPTIVE OF WHERE IN THE TEST THE ERROR OCCURRED.

THE ERROR P.C. IS THE PHYSICAL MEMORY LOCATION WHERE THE ERROR REPORT WAS INITIATED. SINCE MANY FUNCTIONS ARE SUBROUTINED, AND ERRORS ARE REPORTED FROM SUBROUTINES, THE ERROR P.C. IS NOT SUFFICIENT TO IDENTIFY THE LOCATION OF THE ERROR CALL AND THE ROUTINE TRACE SEQUENCE IS PROVIDED.

LINE 2 IS THE ROUTINE TRACE SEQUENCE. IF THE ERROR CALL IS INITIATED FROM WITHIN THE TEST (AS OPPOSED TO WITHIN A ROUTINE), THIS PORTION OF THE REPORT IS OMITTED. IF THE CALL IS INITIATED FROM A ROUTINE (WHICH MAY BE CALLED BY ANOTHER ROUTINE, WHICH MAY BE CALLED BY ANOTHER ROUTINE, ETC. SEVERAL LEVELS DEEP) THE ROUTINE TRACE SEQUENCE PROVIDES A TRAIL TO THE ACTUAL LOCATION WITHIN THE TEST THAT CALLED THE FIRST ROUTINE. THE FIRST ENTRY LISTED IS THE LOCATION WHERE THE FIRST ROUTINE WAS CALLED.

LINE 3 IS THE TEST DESCRIPTION AND IS ROUGHLY IDENTICAL TO THE NAME OF THE TEST BEING PERFORMED.

LINE 4 IDENTIFIES THE ACTUAL HARDWARE FUNCTION THAT IS BEING PERFORMED. ADDITIONAL INFORMATION ON THIS LINE IS DESCRIPTIVE OF SPECIFIC USE OF THE FUNCTION. FOR EXAMPLE, THE OPERATION LINE WILL READ 'READ HEADERS FOR 40 HEADERS' WHEN ALL HEADERS ARE BEING READ FROM A TRACK.

LINE 5 IDENTIFIES THE ERROR THAT HAS BEEN DETECTED. THE CONTENT OF LINE 5 IDENTIFIES WHAT WAS BEING TESTED (SUCH AS DRIVE READY, CONTROLLER ERROR, DRIVE STATE, ETC.), WHAT IT IS AND WHAT IT SHOULD BE. LINE 5 MAY BE REPEATED IF MORE THAN ONE TESTED ITEM IS FOUND IN ERROR.

IN ADDITION LINE 5 WILL REPORT ANY HARDWARE DETECTED ERRORS SUCH AS OPERATION INCOMPLETE, HEADER CRC, ETC. IN THIS CASE THE FIRST LINE PRINTED AS RESULT WILL BE DETERMINED BY THE THREE ERROR BITS OPI, HNF/DLT, AND HCRC/DCRC. THE LINE WILL BE DETERMINED AS IN THE FOLLOWING TRUTH TABLE:

HNF/DLT	DCRC/HCRC	OPI	MESSAGE
1	1	1	HDR NOT FND/HDR CRC/OPI ERROR
0	1	1	HDR CRC ERROR
1	0	1	HDR NOT FND ERROR
0	1	0	DATA CRC ERROR
1	0	0	DATA LATE ERROR

LINE 6 IDENTIFIES THE PHYSICAL ADDRESS OF THE UNIT UNDER TEST. THIS ADDRESS IS BY UNIBUS ADDRESS OF THE CONTROLLER AND DRIVE NUMBER.

LINE 7 NAMES THE CONTROLLER REGISTERS (AND CYLINDER AND HEAD WHERE THESE ARE APPLICABLE IN THE REPORT) TO BE REPORTED.

LINE 8 PROVIDES THE CONTENTES OF CONTROLLER REGISTERS WHEN THE OPERATION WAS INITIATED.

LINE 9 PROVIDES THE CONTENTS OF THE CONTROLLER REGISTERS WHEN THE ERROR BEING REPORTED WAS DETECTED. FREQUENTLY THE REGISTER CONTENTS OF OP INIT AND OP DONE WILL BE DIFFERENT. OP INIT MAY INDICATE A SEEK WAS BEING PERFORMED BUT OP DONE MAY INDICATE THE ERROR WAS DETECTED BY A READ HEADER. THE REASON IS THAT A SEEK WAS EXECUTED AND DID NOT PROPERLY POSITION HEADS AND WHEN THE READ HEADER WAS DONE THE HEADS WERE ON THE WRONG CYLINDER.

LINE 10 IS THE DRIVE STATUS. THIS LINE IS ONLY REPORTED IF THE RLMP REGISTER DOES NOT CONTAIN THE ACTUAL DRIVE STATUS.

LINE 11 AND LINE 12 ARE REPORTED IF THE ERROR WAS DETECTED AS A COMPARE OPERATION, EITHER DATA OR HEADERS. IN ADDITION, GOOD AND BAD DATA IS REPORTED FOR ALL READ ERRORS.

### 3.2 ERROR HALTS

ERROR HALTS ARE SUPPORTED PER DESCRIBED IN THE PREVIOUS SECTION WITH /FLAG:HOE. THERE ARE NO OTHER HALTS.

### 4.0 PERFORMANCE AND PROGRESS REPORTS

#### 4.1 PERFORMANCE REPORTS

THIS PROGRAM WILL NOT GIVE ANY PERFORMANCE REPORTS.

#### 4.2 PROGRESS REPORTS

THIS PROGRAM WILL NOT GIVE ANY PROGRESS REPORTS.

### 5.0 DEVICE INFORMATION TABLES

THE RL11/RLV11 CONTROLLER HAS THE FOLLOWING FOUR(4) REGISTERS FOR CONTROL OF THE SUBSYSTEM.

RLCS - CONTROL AND STATUS REGISTER (XXXXX0)  
-----

BIT 15 - COMPOSITE ERROR

BIT 14 - DRIVE ERROR

BIT 13 - NON EXISTANT MEMORY ERROR

BIT 12 - HEADER NOT FOUND (WITH BIT 10 SET)  
          - DATA LATE (WITH BIT 10 CLEAR)  
BIT 11 - HEADER CRC (WITH BIT 10 SET)  
          - DATA CRC (WITH BIT 10 CLEAR)  
BIT 10 - OPERATION INCOMPLETE  
BIT 9/8 - DRIVE SELECT (0-3)  
BIT 7 - CONTROLLER READY  
BIT 6 - INTERRUPT ENABLE  
BIT 5 - EXTENDED BUS ADDRESS (BIT 17)  
BIT 4 - EXTENDED BUS ADDRESS (BIT 16)  
BIT 3-1 - FUNCTION CODE  
          0 - NOP (PDP-11) MAINT (LSI-11)  
          1 - WRITE CHECK  
          2 - GET DRIVE STATUS  
          3 - SEEK  
          4 - READ HEADER  
          5 - WRITE DATA  
          6 - READ DATA  
          7 - READ WITHOUT HEADER COMPARE

BIT 0 - DRIVE READY

RLBA - BUS ADDRESS REGISTER (XXXXX2)

BITS 15-1 BUS ADDRESS OF DATA TRANSFER  
BIT 0 SHOULD BE 0

RLDA - DISK ADDRESS REGISTER (XXXXX4)

FOR READ/WRITE FUNCTIONS

BIT 15 - MUST BE ZERO(0)  
BIT 14-7 - CYLINDER ADDRESS FOR TRANSFER  
BIT 6 - SURFACE FOR TRANSFER  
BIT 5-0 - SECTOR FOR TRANSFER (0-47)

FOR SEEK FUNCTION

BIT 15 - MUST BE ZERO(0)  
BIT 14-7 - DIFFERENCE TO NEW CYLINDER  
BIT 6-5 - MUST BE ZERO(0)  
BIT 4 - SURFACE  
BIT 3 - MUST BE ZERO  
BIT 2 - SEEK DIRECTION( 1 - IN / 0 - OUT )  
BIT 1 - MUST BE ZERO  
BIT 0 - MUST BE ONE(1)

FOR GET STATUS FUNCTION

BIT 15-4 - IGNORED SHOULD BE ZERO  
BIT 3 - DRIVE RESET  
BIT 2 - MUST BE ZERO  
BIT 1 - MUST BE ONE  
BIT 0 - MUST BE ONE

RLMP - MULTIPURPOSE REGISTER  
-----

FOR READ/WRITE FUNCTION  
-----

BIT 15 - 0 - WORD COUNT(TWO'S COMPLIMENT)

FOR READ HEADER FUNCTION  
-----

BIT 15-0 - DISK HEADER OF SECTOR (FIRST READ)  
- ZERO WORD (SECOND READ)  
- HEADER CRC (THIRD READ)

FOR GET STATUS FUNCTION  
-----

HAS DRIVE STATUS

BIT 15 - WRITE DATA ERROR  
BIT 14 - CURRENT HEAD ERROR(CHE)  
BIT 13 - WRITE LOCK STATUS(WL)  
BIT 12 - SEEK TIME OUT(SKTO)  
BIT 11 - SPIN ERROR(SPE)  
BIT 10 - WRITE GATE ERROR(WGE)  
BIT 9 - VOLUME CHECK(VC)  
BIT 8 - DRIVE SELECT ERROR(DSE)  
BIT 7 - RESERVED(O)  
BIT 6 - SURFACE  
BIT 5 - COVER OPEN  
BIT 4 - HEADS HOME  
BIT 3 - BRUSHES HOME  
BIT 2-0 - STATE BITS  
0 - LOAD STATE  
1 - SPIN UP  
2 - BRUSH CYCLE  
3 - LOAD HEADS  
4 - SEEK - TRACK COUNTING  
5 - SEEK - LINEAR MODE  
6 - UNLOAD HEADS  
7 - SPIN DOWN

## 6.0 TEST SUMMARIES

TEST 1 DIFFERENCE OF 1 SEEK TEST (PART 1)

DO READ HEADER, WAIT FOR INTERRUPT. STORE WORD 1 OF HEADER.

DO SEEK WITH DIFFERENCE OF 1, HEAD 0. IF CYLINDER OF STORED  
HEADER WORD IS NOT 255 THEN SIGN BIT 1, ELSE SIGN BIT 0. WAIT  
FOR INTERRUPT.

DO GET STATUS, WAIT FOR INTERRUPT. CHECK STATE IS 4. IF NOT:

DRIVE COMMAND SHIFT REGISTER BAD  
DIFFERENCE REGISTER DROPPED BIT  
STATE ROM FAILED

WAIT APPROX 5 MS. DO GET STATUS, WAIT FOR INTERRUPT. CHECK  
STATE IS 5. IF NOT:

DIFFERENCE REGISTER NOT COUNTING  
COUNT PULSE NOT GENERATED (COUNT LOGIC)  
SEEK ROM FAILED  
FAILURE IN DC SERVO  
NO TACH FEEDBACK

WAIT APPROX 5 MS LONGER. TEST DRIVE READY. IF SET:

FAILURE IN READY LATCH OR INTEGRATOR

WAIT APPROX 5 MS LONGER. TEST READY. IF RESET:

FAILURE IN INTEGRATOR  
UNEXPECTED GUARD BAND DETECTED

DO SEEK WITH DIFFERENCE 1, OPPOSITE SIGN, HEAD 0. REPEAT ALL  
TESTS AS ABOVE.

REPEAT TEST USING HEAD 1.

NOTE: THIS TEST IS PERFORMED AT THE CYLINDER POSITION FOUND  
IN THE DRIVE WHEN THE TEST EXECUTES. CHOOSING A  
SINGLE SURFACE WILL LIMIT TESTING TO THAT SURFACE.

TEST 2 DIFFERENCE OF 1 SEEK TEST (PART 2)

DO READ HEADER, WAIT FOR INTERRUPT. STORE WORD 1 OF HEADER.

DO SEEK WITH DIFFERENCE OF 1, HEAD 0. IF CYLINDER OF STORED  
HEADER WORD IS NOT 255 THEN SIGN BIT 1, ELSE SIGN BIT 0. WAIT  
FOR INTERRUPT, WAIT FOR DRIVE READY.

DO READ HEADER, WAIT FOR INTERRUPT. COMPARE CYLINDER OF THIS  
HEADER WITH CYLINDER OF STORED HEADER FOR DIFFERENCE OF ONE.  
IF NOT:

COUNT LOGIC BAD  
INTERGRATOR FAILED

CHECK THAT HEADS MOVED FORWARD OR REVERSE AS EXPECTED. IF

NOT:

SEEK ROM FAILED

DO SEEK WITH DIFFERENCE OF 1, OPPOSITE SIGN, HEAD 0. REPEAT ALL TESTS AS ABOVE.

REPEAT TEST USING HEAD 1.

NOTE: THIS TEST IS PERFORMED AT THE CYLINDER POSITION FOUND IN THE DRIVE WHEN THE TEST EXECUTES. CHOOSING A SINGLE SURFACE WILL LIMIT TESTING TO THAT SURFACE.

TEST 3 OUTER GUARD BAND DETECTION TEST

DO READ HEADER, WAIT FOR INTERRUPT. CHECK IF AT CYLINDER 0. IF NOT, SEEK REVERSE 1 CYLINDER AT A TIME UNTIL CYLINDER 0 IS REACHED. IF ANY REVERSE SEEK FAILS TO MOVE THE HEADS IN 10 TRIES:

DETECTION OF GUARD BAND PREMATURE.

WHEN AT CYLINDER 0, DO SEEK DIFFERENCE OF 1, SIGN 0, HEAD 0. WAIT FOR INTERRUPT, WAIT FOR READY. READY SHOULD SET IN 20MS > T > 15MS. IF NOT:

FAILED TO DETECT GUARD BAND

DO READ HEADER. WAIT FOR INTERRUPT. CHECK FOR CYLINDER 0. IF NOT

FAILED TO SEEK BACK TO ZERO

DO SEEK WITH DIFFERENCE OF 1, SIGN 0, HEAD 1. DO SAME TESTS AS ABOVE WITH REGARD TO READY VS TIME AND CYLINDER FOUND IN HEADER.

NOTE: CHOOSING A SINGLE SURFACE WILL LIMIT THE TESTING TO THAT SURFACE.

TEST 4 INCREMENTAL FORWARD SEEK HEAD 0 TEST

POSITION HEADS AT CYLINDER 'LOLIMIT' USING SEEKS WITH DIFFERENCE OF ONE, HEAD 0.

DO SEEK WITH DIFFERENCE OF 1, SIGN 1, HEAD 0. WAIT FOR INTERRUPT, WAIT FOR DRIVE READY. CHECK READY IS SET IN 15 MS. IF NOT:

POSITIONING PROBLEM AT A SPECIFIC CYLINDER  
MECHANICAL OBSTRUCTION

DO READ HEADER, WAIT FOR INTERRUPT. CHECK THAT THIS CYLINDER IS OLD CYLINDER + 1. IF NOT:

DIFFERENCE REGISTER OR COUNT LOGIC FAILURE  
TRACK CROSSING DETECTION FAILURE

REPEAT SEEKS AND READS UNTIL CYLINDER READ IS 'HILIMIT'.

NOTE 1: IF THE 'USE ALL SECTORS' PARAMETER IS SPECIFIED AS 'Y', THE TEST WILL READ AND TEST ALL 40 HEADERS (CARTRIDGE VERIFY).

NOTE 2: TESTING WILL BE DONE BETWEEN UPPER AND LOWER CYLINDER LIMITS. CHOOSING A SINGLE SURFACE WILL LIMIT TESTING TO THAT SURFACE. THIS TEST WILL BE BYPASSED IF SURFACE 1 IS CHOSEN.

TEST 5 INCREMENTAL REVERSE SEEK HEAD 0 TEST

POSITION HEADS AT CYLINDER 'HILIMIT' USING SEEKS WITH DIFFERENCE OF 1, HEAD 0.

DO SEEK WITH DIFFERENCE OF 1, SIGN 0, HEAD 0. WAIT FOR INTERRUPT, WAIT FOR DRIVE READY. CHECK READY SET IN 15 MS:

POSITIONING PROBLEM AT A SPECIFIC CYLINDER  
DO READ HEADER, WAIT FOR INTERRUPT. CHECK THAT THIS CYLINDER IS OLD CYLINDER - 1. IF NOT:

DIFFERENCE REGISTER OR COUNT LOGIC FAILURE  
TRACK CROSSING DETECTION FAILURE

REPEAT SEEK AND CHECKS UNTIL CYLINDER IS 'LOLIMIT'.

NOTE: IF THE 'USE ALL SECTORS' PARAMETER IS SPECIFIED AS 'Y', THE TEST WILL READ AND TEST ALL 40 HEADERS (CARTRIDGE VERIFY).

NOTE: TESTING WILL BE DONE BETWEEN UPPER AND LOWER CYLINDER LIMITS. CHOOSING A SINGLE SURFACE WILL LIMIT TESTING TO THAT SURFACE. THIS TEST WILL BE BYPASSED IF SURFACE 1 IS CHOSEN.

TEST 6 INCREMENTAL FORWARD SEEK HEAD 1 TEST

POSITION HEADS AT CYLINDER 'HILIMIT' USING SEEKS WITH DIFFERENCE OF ONE, HEAD 0.

DO SEEK WITH DIFFERENCE OF 1, SIGN 1, HEAD 1. WAIT FOR INTERRUPT, WAIT FOR DRIVE READY. CHECK READY IS SET IN 15 MS. IF NOT:

POSITIONING PROBLEM AT A SPECIFIC CYLINDER

DO READ HEADER, WAIT FOR INTERRUPT. CHECK THAT THIS CYLINDER IS OLD CYLINDER + 1. IF NOT:

DIFFERENCE REGISTER OR COUNT LOGIC FAILURE  
TRACK CROSSING DETECTION FAILURE

REPEAT SEEKS AND READS UNTIL CYLINDER READ IS 'HILIMIT'.

NOTE 1: IF THE 'USE ALL SECTORS' PARAMETER IS SPECIFIED AS 'Y', THE TEST WILL READ AND TEST ALL 40 HEADERS (CARTRIDGE VERIFY).

NOTE 2: TESTING WILL BE DONE BETWEEN UPPER AND LOWER CYLINDER LIMITS. CHOOSING A SINGLE SURFACE WILL LIMIT TESTING TO THAT SURFACE. THIS TEST WILL BE BYPASSED IF SURFACE 0 IS CHOSEN.

TEST 7 INNER GUARD BAND DETECTION TEST

POSITION HEADS AT CYLINDER 255 USING SEEK WITH DIFFERENCE OF 1, HEAD 0.

WHEN AT CYLINDER 255, DO SEEK WITH DIFFERENCE OF 1, SIGN 1, HEAD 0. WAIT FOR INTERRUPT, WAIT FOR DRIVE READY. READY SHOULD SET IN 20MS>T>15MS. IF NOT:

FAILED TO DETECT GUARD BAND

DO READ HEADER. WAIT FOR INTERRUPT. CHECK FOR CYLINDER 255. IF NOT:

FAILED TO SEEK BACK TO CYLINDER 255

DO SEEK WITH DIFFERENCE OF 1, SIGN 1, HEAD 1. DO SAME TESTS AS ABOVE.

NOTE: CHOOSING A SINGLE SURFACE WILL LIMIT THE TESTING TO THAT SURFACE.

TEST 8 INCREMENTAL REVERSE SEEK HEAD 1 TEST

POSITION HEADS AT CYLINDER 'HILIMIT' USING SEEKS WITH DIFFERENCE OF 1, HEAD C.

DO SEEK WITH DIFFERENCE OF 1, SIGN 0, HEAD 1. WAIT FOR INTERRUPT, WAIT FOR DRIVE READY. CHECK READY SET IN 15 MS:

POSITIONING PROBLEM AT A SPECIFIC CYLINDER

DO READ HEADER, WAIT FOR INTERRUPT. CHECK THAT THIS CYLINDER IS OLD CYLINDER - 1. IF NOT:

DIFFERENCE REGISTER OR COUNT LOGIC FAILURE  
TRACK CROSSING DETECTION FAILURE

REPEAT SEEK AND CHECKS UNTIL CYLINDER IS 'LOLIMIT'.

NOTE 1: IF PROGRAM MODE 2 IS USED AND THE 'USE ALL SECTORS' PARAMETER IS SPECIFIED AS 'Y', THE TEST WILL READ AND TEST ALL 40 HEADERS (CARTRIDGE VERIFY).

NOTE 2: TESTING WILL BE DONE BETWEEN UPPER AND LOWER CYLINDER LIMITS. CHOOSING A SINGLE SURFACE WILL LIMIT TESTING TO THAT SURFACE. THIS TEST WILL BE BYPASSED IF SURFACE 0 IS CHOSEN.

TEST 9 SEEK TESTS

POSITION HEADS AT CYLINDER 'LOLIMIT' USING SEEKS WITH DIFFERENCE OF 1, HEAD 0.

DO READ HEADER, RECORD POSITION. DO SEEK WITH DIFFERENCE OF 2 (MAX DISTANCE AT 3 IPS), SIGN 1, HEAD 0. DO READ HEADER, CHECK NEW CYLINDER IS OLD CYLINDER + DISTANCE. IF NOT:

TRACK CROSSING DETECTION FAILURE  
DIFFERENCE COUNTER FAILURE  
COUNT PULSE GENERATION FAILURE  
VELOCITY ROM FAILURE

REPEAT ABOVE UNTIL OLD CYLINDER + DISTANCE > 255. POSITION AT 255.

DO READ HEADER, RECORD POSITION. DO SEEK WITH DIFFERENCE OF 2 (MAX DISTANCE AT 3 IPS), SIGN 0, HEAD 0. DO READ HEADER, CHECK NEW CYLINDER IS OLD CYLINDER - DISTANCE. IF NOT:

TRACK CROSSING DETECTION FAILURE

REPEAT UNTIL OLD CYLINDER - DISTANCE < 0. REPEAT ALL OF THE ABOVE USING HEAD 1.

REPEAT ALL OF THE ABOVE TESTS USING THE FOLLOWING DISTANCES: 6, 9, 12, 17, 22, 27, 34, 41, 128, 256. THESE DISTANCES ARE SPECIFIED BECAUSE THEY REPRESENT THE MAXIMUM DISTANCE FOR EACH VELOCITY LEVEL USED IN THE DRIVE.

NOTE: TESTING WILL BE DONE BETWEEN UPPER AND LOWER CYLINDER LIMITS. CHOOSING A SINGLE SURFACE WILL LIMIT TESTING TO THAT SURFACE.

TEST 10 FORWARD OSCILLATING SEEK TEST

POSITION HEADS AT CYLINDER 0.

DO OSCILLATING SEEK USING HEAD 0 (SEEK FROM 0 TO 1 TO 0, 0 TO 2 TO 0, 0 TO 3 TO 0, ..... 0 TO 255 TO 0). AFTER EACH SEEK READ HEADER AND VERIFY POSITION.

REPEAT TEST USING HEAD 1.

NOTE: IF EITHER CYLINDER LIMIT IS SPECIFIED, THE TEST WILL SEEK BETWEEN UPPER AND LOWER LIMITS FOR EACH SURFACE. CHOOSING A SINGLE SURFACE WILL LIMIT TESTING TO THAT SURFACE. NOTE THAT LOOPING ON TEST THEN PROVIDES A FIXED DISTANCE SEEK LOOP.

TEST 11 REVERSE OSCILLATING SEEK TEST

POSITION HEADS AT CYLINDER 255. DO OSCILLATING SEEK USING HEAD 0. (SEEK FROM 255 TO 254 TO 255, 255 TO 253 TO 255, ... 255 TO 0 TO 255.) AFTER EACH SEEK READ HEADER AND VERIFY POSITION.

REPEAT TEST USING HEAD 1.

NOTE: IF EITHER CYLINDER LIMIT IS SPECIFIED, THE TEST WILL SEEK BETWEEN UPPER AND LOWER LIMITS FOR EACH SURFACE. CHOOSING A SINGLE SURFACE WILL LIMIT TESTING TO THAT SURFACE. NOTE THAT LOOPING ON TEST THEN PROVIDES A FIXED DISTANCE SEEK LOOP.

TEST 12 SEEK TIMING

POSITION HEADS AT CYLINDER 0.

DO 64 SEEKS FROM 0 TO 1 AND 1 TO 0, MEASURING THE SEEK TIME FOR EACH SEEK. AVERAGE THE SEEK TIMES (FORWARD AND REVERSE INDEPENDENTLY) AND REPORT.

REPEAT ABOVE SEEKING BETWEEN CYLINDER 127 TO 128 AND 254 TO 255.

REPEAT ABOVE SEEKING BETWEEN CYLINDER 0 TO 127 AND 128 TO 256.

REPEAT ABOVE SEEKING BETWEEN CYLINDER 0 AND 255.

THE SEEK TIMES WILL BE REPORTED AS SHOWN BELOW. THE TIME MEASURED IS FROM START OF SEEK COMMAND UNTIL INTERRUPT IS RECEIVED.

	INNER	MIDDLE	OUTER	EXPECTED
1 CYL FWD	X	X	X	X
1 CYL REV	X	X	X	X
128 CYL FWD	X		X	X
128 CYL REV	X		X	X
256 CYL FWD		X		X
256 CYL REV		X		X

THE X INDICATES WHERE TIME WILL BE REPORTED.

NOTE: THE ABOVE REPORT WILL BE PRINTED IN THE FIRST PASS FOR EACH DRIVE UNDER TEST IF MANUAL INTERVENTION TESTS WERE RUN. THE EXPECTED TIMES ARE FOR USER COMPARISON

ONLY. THE PROGRAM WILL NOT REPORT DEVIATION AS AN ERROR.

TEST 13 BASIC READ DATA TEST

POSITION HEADS AT CYLINDER 255.

DO READ DATA, HEAD 1. CHECK FOR ANY ERRORS AND REPORT. IF ERROR, READ SECTOR 1 THROUGH 19 UNTIL NO ERROR ON READ. REPORT ALL ERRORS BUT DO NOT INCREMENT ERROR COUNT. IF NONE CAN BE READ, SUCCESSFULLY, REPORT THAT FACTORY BAD SECTOR FILE CANNOT BE READ, INCREMENT ERROR COUNT AND PROCEED WITH READ OF SECTOR 20.

ON SECTOR WITH NO CRC ERROR, VERIFY DATA FORMAT (WORD 0 AND 1 ARE NOT 0, WORD 2 AND 3 ARE 0, LOCATE FIRST WORD OF ALL ONE'S AND THAT WORD TO WORD 127 ARE ALL ONE'S.) STORE BAD SECTOR DATA.

READ DATA, HEAD ONE, SECTOR 20. CHECK FOR ANY ERRORS AND REPORT. IF ERROR, READ SECTOR 21 THROUGH 39 UNTIL NO ERROR ON READ. REPORT ALL ERRORS BUT DO NOT INCREMENT ERROR COUNT. IF NONE CAN BE READ SUCCESSFULLY, REPORT THAT SOFTWARE BAD SECTOR FILES CANNOT BE READ, INCREMENT ERROR COUNT AND EXIT TEST.

ON SECTOR WITH NO CRC ERROR, VERIFY DATA AS ABOVE. STORE BAD SECTOR DATA.

NOTE: IF SURFACE 0 IS SELECTED THIS TEST WILL BE BYPASSED.

TEST 14 WRITE/READ DATA TEST (PART 1)

POSITION HEADS AT CYLINDER 0

WRITE PATTERN 1 ON HEAD 0, SECTOR 0. CHECK FOR ANY ERROR.

READ HEAD 0, SECTOR 0. CHECK FOR CRC ERROR. COMPARE DATA.

REPEAT FOR OTHER DATA PATTERNS (2 THROUGH 8).

CHECK IF CYLINDER 0, TRACK 1, SECTOR 0 IS LISTED IN BAD SECTOR DATA. IF NOT, REPEAT ABOVE TEST AT CYLINDER 0, TRACK 1, SECTOR 0. IF IT IS LISTED AS BAD, LOCATE FIRST SECTOR 0, TRACK 1 THAT IS GOOD AND DO ABOVE TESTS.

NOTE: CYLINDER LIMITS ARE IGNORED, TESTING IS DONE AT CYLINDER 0. HOWEVER, CHOOSING A SINGLE SURFACE WILL LIMIT TESTING TO THAT SURFACE.

TEST 15 SPINDLE TIMING TEST

POSITION HEADS TO CYLINDER 0.

DO WRITE DATA TO CYLINDER 0, HEAD 0, SECTOR 0. WAIT FOR INTERRUPT.

DO WRITE DATA TO CYLINDER 0, HEAD 0, SECTOR 0. START TIMING. WHEN INTERRUPT OCCURS, STOP TIMING. RESULT IS SPINDLE ROTATION TIME.

REPEAT TEST 64 TIMES. REPORT THE AVERAGE AS SPINDLE ROTATION TIME. THE TIME REPORTED IS IN 100'S OR MICROSECONDS.

NOTE: THIS TEST WILL BE RUN ONLY IN THE FIRST PASS AND ONLY IF MANUAL INTERVENTION TESTS WERE RUN.

TEST 16 WRITE/READ TEST (PART 2)

CC IS CURRENT CYLINDER SELECTED FROM SET.  
LET SELECTED CYLINDER SET BE AS DEFINED IN PARAGRAPH 4.3.

SEEK FORWARD TO CC. WRITE PATTERNS 1 THROUGH 8 REPEATED 5 TIMES ON HEAD 0. READ/COMPARE ALL DATA.

SEEK REVERSE TO 'LOLIMIT'. SEEK FORWARD TO CC. READ/COMPARE ALL DATA. SEEK FORWARD TO 'HILIMIT'. SEEK REVERSE TO CC. READ/COMPARE ALL DATA. REWRITE DATA PATTERNS 1 THROUGH 8 REPEATED 5 TIMES ON HEAD 0. READ COMPARE ALL DATA.

SEEK FORWARD TO 'HILIMIT'. SEEK REVERSE TO CC. READ/COMPARE ALL DATA. SEEK REVERSE TO 'LOLIMIT'. SEEK FORWARD TO CC. READ/COMPARE ALL DATA.

REPEAT ABOVE TEST FOR HEAD 1.

REPEAT ABOVE TESTS FOR ALL CYLINDERS IN SELECTED CYLINDER SET.

NOTE 1: IF ANY OF THE SECTORS IN THE SELECTED CYLINDER SET ARE LISTED AS BAD, THAT SECTOR WILL BE BYPASSED.

NOTE 2: IF THE 'USE ALL CYLINDERS' PARAMETER IS SPECIFIED AS 'Y', THE TEST WILL INCLUDE ALL CYLINDERS IN THE SELECTED PARAMETER SET.

NOTE 3: IN THE FIRST PASS OF THE PROGRAM THIS TEST IS EXECUTED ON ONLY 6 OF THE CYLINDERS LISTED IN THE CYLINDER SET. THOSE USED WILL BE EVERY 8TH ENTRY IN THE TABLE. ON THE SECOND AND SUBSEQUENT PASSES ALL ENTRIES IN THE SELECTED CYLINDER SET ARE USED.

NOTE 4: TESTING WILL BE DONE BETWEEN UPPER AND LOWER LIMITS. CYLINDERS IN THE CYLINDER SET BEYOND THESE LIMITS WILL NOT BE TESTED. CHOOSING A SINGLE SURFACE WILL LIMIT TESTING TO THAT SURFACE.

TEST 17 WRITE LOCK ERROR AND DATA PROTECTION TEST

DO WRITE DATA PATTERN 0 AT SECTOR 0. READ DATA AND VERIFY.

ASK OPERATOR TO WRITE LOCK DRIVE. DO GET STATUS LOOP UNTIL WRITE LOCK IS SET. IF NOT SET IN 30 SECONDS, ABORT THE TEST.

WHEN WRITE LOCK IS SET, DO WRITE DATA PATTERN 1 AT SECTOR 0. REPORT FAILURE IF DRIVE ERROR DOES NOT SET OR IF ANY OTHER ERROR SETS. CLEAR ERROR AND READ DATA AT SECTOR 0. CHECK THAT DATA HAS NOT BEEN DISTURBED.

REQUEST OPERATOR TO RESET WRITE LOCK. DO GET STATUS LOOP UNTIL WRITE LOCK IS RESET. IF NOT RESET IN 30 SECONDS, REPEAT THE REQUEST.

NOTE: THIS TEST IS EXECUTED ONLY IF THE PROGRAM OPERATION MODE 2 IS SELECTED, MANUAL INTERVENTION TESTING IS REQUESTED, AND IS RUN IN FIRST PASS ONLY.

#### TEST 18 ADJACENT CYLINDER INTERFERENCE TEST

CC IS CURRENT CYLINDER SELECTED FROM SET  
LET SELECTED CYLINDER SET BE AS DEFINED IN PARAGRAPH 4.3.  
DATA PATTERN IS 155555.

SEEK FORWARD TO CYLINDER CC. WRITE PATTERN ON TRACK 0, ALL SECTORS. READ/COMPARE DATA.

SEEK FORWARD TO 'HILIMIT'. SEEK REVERSE TO CC-1. WRITE PATTERN. SEEK FORWARD TO 'HILIMIT'. SEEK REVERSE TO CC. WRITE PATTERN. (THIS HAS BRACKETED ORIGINAL WRITE WITH WRITES IN ADJACENT CYLINDERS. NOTE ADJACENT CYLINDERS WERE WRITTEN AFTER HEADS CAME ON CYLINDER IN REVERSE DIRECTION WHICH IS OPPOSITE OF CENTER CYLINDER.)

SEEK REVERSE TO 'LOLIMIT'. SEEK FORWARD TO CC. READ/COMPARE DATA FROM ALL SECTORS. ANY ERRORS (READ OR COMPARE) ARE ATTRIBUTED TO ADJACENT CYLINDER INTERFERENCE.

SEEK FORWARD TO 'HILIMIT'. SEEK REVERSE TO CC. WRITE DATA PATTERN. SEEK REVERSE TO 'LOLIMIT'. SEEK FORWARD TO CC-1. WRITE PATTERN. SEEK REVERSE TO 'LOLIMIT'. SEEK FORWARD TO CC+1. WRITE PATTERN. SEEK FORWARD TO 'HILIMIT'. SEEK REVERSE TO CC. READ/COMPARE DATA IN ALL SECTORS. ANY ERRORS (READ OR COMPARE) ARE ATTRIBUTED TO ADJACENT CYLINDER INTERFERENCE.

REPEAT ABOVE TESTS ON HEAD 1.

NOTE 1: IF ANY SECTOR ON A SELECTED CYLINDER IS LISTED BAD, THAT SECTOR WILL BE BYPASSED.

NOTE 2: IF THE 'USE ALL CYLINDERS' PARAMETER IS SPECIFIED AS 'Y', THE TEST WILL INCLUDE ALL CYLINDERS (EXCEPT 0 AND 255) IN THE SELECTED PARAMETER SET.

NOTE 3: IN THE FIRST PASS OF THE PROGRAM THIS TEST IS EXECUTED ON ONLY 3 OF THE CYLINDERS LISTED IN THE CYLINDER SET. THOSE USED WILL BE THE FIRST, TWENTYFIRST, AND FORTYFIRST ENTRIES IN THE TABLE. ON SECOND AND SUBSEQUENT PASSES EVERY FOURTH CYLINDER SET ENTRY WILL BE TESTED.

NOTE 4: TESTING WILL BE DONE BETWEEN UPPER AND LOWER LIMITS. CYLINDERS IN THE CYLINDER SET BEYOND THESE LIMITS WILL NOT BE TESTED. CHOOSING A SINGLE SURFACE WILL LIMIT TESTING TO THAT SURFACE.

#### TEST 19 OVERWRITE TEST

CC IS CURRENT CYLINDER SELECTED FROM SET  
SELECTED CYLINDER SET DEFINED IN PARAGRAPH 4.3.  
PATTERN A = 125252  
PATTERN B = 000000

SEEK FORWARD TO CC. WRITE DATA OF PATTERN A IN ALL SECTORS,  
HEAD 0. READ/COMPARE DATA.

SEEK FORWARD TO 'HILIMIT', SEEK REVERSE TO CC. WRITE PATTERN  
B. SEEK REVERSE TO 'LOLIMIT', SEEK FORWARD TO CC,  
READ/COMPARE DATA.

SEEK FORWARD TO 'HILIMIT', SEEK REVERSE TO CC. WRITE DATA  
PATTERN A. READ/COMPARE DATA. SEEK REVERSE TO 'LOLIMIT',  
SEEK FORWARD TO CC. WRITE PATTERN B. SEEK FORWARD TO  
'HILIMIT' SEEK REVERSE TO CC. READ/COMPARE DATA.

ANY FAILURES (READ OR COMPARE) ARE ATTRIBUTED TO OVERWRITE  
PROBLEM.

REPEAT ABOVE TESTS ON HEAD 1.

NOTE 1: IF ANY SECTOR ON A SELECTED CYLINDER IS LISTED AS BAD,  
THAT SECTOR WILL BE BYPASSED.

NOTE 2: IF THE 'USE ALL CYLINDERS' PARAMETER IS SPECIFIED AS  
'Y', THE TEST WILL INCLUDE ALL CYLINDERS IN THE  
SELECTED PARAMETER SET.

NOTE 3: IN THE FIRST PASS OF THE PROGRAM THIS TEST IS  
EXECUTED ON ONLY 3 OF THE CYLINDERS LISTED IN THE  
CYLINDER SET. THOSE USED WILL BE THE FIRST,  
TWENTYFIRST, AND FORTYFIRST ENTRIES IN THE TABLE. ON  
SECOND AND SUBSEQUENT PASSES EVERY FOURTH CYLINDER SET  
ENTRY WILL BE TESTED.

NOTE 4: TESTING WILL BE DONE BETWEEN UPPER AND LOWER LIMITS.  
CYLINDERS IN THE CYLINDER SET BEYOND THESE LIMITS WILL

NOT BE TESTED. CHOOSING A SINGLE SURFACE WILL LIMIT  
TESTING TO THAT SURFACE.

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4076	DIAGNOSTIC SUPERVISOR -- LOW CORE SET UP	

1			.NLIST	CND,MD,ME
2			.ENABL	ABS,AMA
3		002000	.=2000	
4				
5				
6	002000		SVC	
7		000001	SVCTST=1	
8		000001	SVCSUB=1	
9		000001	SVCBGL=1	
10		000000	SVCINS=0	
11		000000	SVCTAG=0	
12	002000		POINTER	BGNSW,BGNSFT,BGNDU
13				
14	002000		BGNMOD	MDHEDR
19	002000		HEADER	CZRLD,B,0,30000,30000,300,RL01
(4)	002000	103	.ASCII	/C/
(4)	002001	132	.ASCII	/Z/
(4)	002002	122	.ASCII	/R/
(4)	002003	114	.ASCII	/L/
(4)	002004	104	.ASCII	/D/
(6)	002005	000	.BYTE	0
(6)	002006	000	.BYTE	0
(5)	002007	000	.BYTE	0
(4)	002010	102	.ASCII	/B/
(4)	002011	060	.ASCII	/O/
(4)	002012	000000	.WORD	0
(4)	002014	000300	.WORD	300
(4)	002016	037604	.WORD	L\$HARD
(4)	002020	037730	.WORD	L\$SOFT
(4)	002022	013356	.WORD	L\$HW
(4)	002024	013372	.WORD	L\$SW
(4)	002026	040514	.WORD	L\$LAST
(4)	002030	000000	.WORD	0
(4)	002032	000000	.WORD	0
(4)	002034	000000	.WORD	0
(4)	002036	000000	.WORD	0
(4)	002040	013410	.WORD	L\$DISPATCH
(4)	002042	000000	.WORD	0
(4)	002044	000000	.WORD	0
(4)	002046	000000	.WORD	0
(4)	002050	002	.BYTE	C\$REVISION
(3)	002051	002	.BYTE	C\$EDIT
(4)	002052	030000	.WORD	30000
(4)	002054	030000	.WORD	30000
(4)	002056	000000	.WORD	0
(5)	002060	000000	.WORD	0
(4)	002062	000000	.WORD	0
(4)	002064	002114	.WORD	L\$DV'TYP
(4)	002066	000000	.WORD	0
(4)	002070	002112	.WORD	L\$DR
(4)	002072	002112	.WORD	L\$DRST
(4)	002074	000000	.WORD	0
(4)	002076	014564	.WORD	L\$DU
(5)	002100	000014	.WORD	14
(4)	002102	000000	.WORD	0
(4)	002104	013456	.WORD	L\$INIT
(4)	002106	014444	.WORD	L\$CLEAN

```

21 002110          ENDMOD
22 002110          DEVREG
(5) 002110 000000  .WORD 0
(2) 002112 000001  .BLKW
23 002114          DEVTYP <RL01>
(3) 002114 046122 030460 000 .ASCIZ /RL01/
(2) 002114 002122  .EVEN
24
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43
44 002122          BGNMOD  GLBEQAT
45
46 002122          EQUALS
47                :
48                :OFFSETS FOR HARDWARE P-TABLE
49                CSR =0                :BUS ADDRESS
50                VECT =2                :VECTOR ADDRESS
51                PRIOR =4                :PRIORITY
52                DRSB =6                :DRIVE SELECT BIT
53                CNT =10                :CONTROLLER TYPE
54
55                :
56                :OFFSET FOR SOFTWARE P-TABLE
57                MISWI =0                :SOFTWARE PARAMETERS SWITCHES
58                LOLIM =2                :CYLINDER LOWER LIMIT
59                HILIM =4                :CYLINDER HIGH LIMIT
60                HEAD =6                :SELECTED HEAD FOR RUNNING TESTS
61                ERLIM =10               :ERROR LIMIT
62                DCLIM =12               :DATA COMPARE ERROR LIMIT
63
64                :
65                :BIT ASSIGNMENT FOR SOFTWARE P-TABLE SWITCHES
66                ALLCYL =BIT00           :USE ALL CYLINDERS
67                ALLSEC =BIT01           :USE ALL SECTORS
68                DRSELT =BIT02           :EXECUTE DRIVE SELECT TEST
69                HDALIGN =BIT03          :EXECUTE HEAD ALIGNMENT TEST
70                AUTOSZ =BIT04           :AUTO SIZE FOR DRIVE-DROP IF NO RESPONSE
71                HEADLM =BIT12           :HEAD LIMIT SPECIFIED FLAG
72                HICYL =BIT13           :HI LIMIT SPECIFIED FLAG
73                LOCYL =BIT14           :LO LIMIT SPECIFIED
74                MITEST =BIT15          :EXECUTE MANUAL INTERVENTION TESTS
75
76                :
77                :SUBSYSTEM FUNCTIONS
78                CKDATA =102            :WRITE CHECK
    
```

```

73      000104      GTSTAT  =104      ;GET STATUS
74      000106      SEEK      =106      ;SEEK
75      000110      RDHEAD   =110      ;READ HEADER
76      000112      WTDATA   =112      ;WRITE DATA
77      000114      RDDATA   =114      ;READ DATA
78      000116      RDNOHR   =116      ;READ DATA, IGNORE HEADERS
79      000100      NOOP      =100      ;NO OPERATION
80
81      ;           OPERATION FLAGS
82      007777      COMPOP   =7777      ;COMPOSITE OPERATION FLAGS
83      000002      HDRCMP   =BIT01     ;HEADER COMPARE OPERATION
84      000001      DATACMP  =BIT00     ;DATA COMPARE OPERATION
85      000004      CYLUP    =BIT02     ;CYCLE UP OPERATION
86      000010      ULOAD    =BIT03     ;UNLOAD OPERATION
87      000020      INOUTS   =BIT04     ;IN-OUT SEEK OPERATION
88      000040      OUTINS   =BIT05     ;OUT-IN SEEK OPERATION
89      000100      FOLWRT   =BIT06     ;FOLLOWING WRITE OPERATION
90      000200      REVSKS   =BIT07     ;REV SEEK SEQ (ADJ INTERFERENCE)
91      000400      FWDSKS   =BIT08     ;FWD SEEK SEQ (ADJ INTERFERENCE)
92      001000      REVSKO   =BIT09     ;REV SEEK SEQ (OVERWRITE)
93      002000      FWDSKO   =BIT10     ;FWD SEEK SEQ (OVERWRITE)
94      004000      BADADD   =BIT11     ;BAD DISK ADDRESS
95      010000      SEEKOP   =BIT12     ;SEEK OPERATION
96      020000      RORWOP   =BIT13     ;READ OR WRITE OPERATION
97      040000      RELDWT   =BIT14     ;RELOAD WAIT
98      100000      HDR40    =BIT15     ;40 HEADER OPERATION
99      003760      MQUALS   =OUTINS!INOUTS!FOLWRT!REVSKS!FWDSKS!REVSKO!FWDSKO
100                                     ;MESSAGE QUALIFIER BITS
101
102      ;           ERROR FLAGS FROM SUBROUTINES
103      000001      TOSLOW   =BIT00     ;OPERATION TOOK TO LONG
104      000002      NOIRPT   =BIT01     ;NO INTERRUPT FROM OPERATION
105      000004      CONHNG   =BIT02     ;CONTROLLER HUNG
106      000010      NOCLR    =BIT03     ;BAD CONTROLLER CLEAR
107
108      000000      RLCS     =0          ;CONTROL AND STATUS REGISTER
109      000002      RLBA     =2          ;BUS ADDRESS REGISTER
110      000004      RLDA     =4          ;DISK ADDRESS REGISTER
111      000006      RLMP     =6          ;MULTI-PURPOSE REGISTER
112
113      ;           REGISTER BIT DEFINITIONS - CONTROL STATUS REGISTER
114      000000      RLCSR    =0          ;CONTROL AND STATUS REGISTER
115      100000      ANYERR   =100000    ;ANY ERROR BIT
116      040000      DRVERR   =40000    ;DRIVE ERROR BIT
117      020000      NXMERR   =20000    ;NON-EXISTANT MEMORY ERROR
118      010000      DLTERR   =10000    ;DATA LATE ERROR
119      010000      HNFERR   =10000    ;HEADER NOT FOUND ERROR
120      004000      DCKERR   =4000     ;DATA CHECK ERROR
121      004000      HCRCERR  =4000     ;HEADER CHECK ERROR
122      002000      OPIERR   =2000     ;OPERATION INCOMPLETE ERROR
123      001400      DSMSK    =1400     ;DRIVE SELECT MASK
124      000200      CRDYMSK  =200      ;CONTROLLER READY MASK
125      000100      INTEBL   =100      ;INTERRUPT ENABLE MASK
126      000060      BAMSK    =60       ;BUS ADDRESS UPPER MASK
127      000001      DRDYMSK  =1        ;DRIVE READY MASK
128

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129      : REGISTER BIT DEFINITIONS - DISK ADDRESS FOR DATA XFER
130      000077      :SAMSK =77      :SECTOR ADDRESS MASK
131      000100      HSMSK =100     :HEAD SELECT MASK
132      077600      CAMSK =77600    :CYLINDER ADDRESS MASK
133
134      : REGISTER BIT DEFINITIONS - DISK ADDRESS FOR SEEK
135      000001      MBSETO =1      :MUST BE SET, BIT 0
136      000004      DIRBIT =4      :DIRECTION BIT
137      000020      HDSEL  =20     :HEAD SELECT BIT
138      077600      DIRMSK =77600    :CYLINDER DIFFERENCE MASK
139
140      : REGISTER BIT DEFINITIONS - DISK ADDRESS FOR GET STATUS
141      000003      GETSTAT =3      :GET STATUS SETUP
142      000010      DRSET  =10     :DRIVE RESET MASK
143
144      : REGISTER BIT DEFINITIONS - MP FOR DATA XFER
145      017777      WCMSK  =17777    :WORD COUNT MASK
146      160000      WCRNG  =160000   :WORD COUNT RANGE MASK
147
148      : REGISTER BIT DEFINITIONS - MP FOR READ HEADER
149      077600      HDCYL  =077600   :CYLINDER MASK
150      000077      HDSEC  =77      :SECTOR MASK
151      000100      HDHSEL =100     :HEAD SELECT MASK
152
153      : REGISTER BIT DEFINITIONS - MP FOR GET STATUS
154      000007      STAMSK =7      :STATE MASK
155      000010      BHSTAT =10     :BRUSH HOME STATUS
156      000020      HOSTAT =20     :HEADS OUT STATUS
157      000040      COSTAT =40     :COVER OPEN STATUS
158      000100      HSSTAT =100    :HEAD SELECT STATUS
159      000400      DSESTAT =400   :DRIVE SELECT ERROR STATUS
160      001000      VCSTAT =1000   :VOLUME CHECK STATUS
161      002000      WGESTAT =2000  :WRITE GATE ERROR STATUS
162      004000      SPDSTAT =4000  :SPIN ERROR STATUS
163      010000      STOSTAT =10000 :SEEK TIMEOUT ERROR STATUS
164      020000      WLSTAT  =20000 :WRITE LOCK STATUS
165      040000      HCESTAT =40000 :HEAD CURRENT ERROR STATUS
166      100000      WDESTAT =100000:WRITE DATA ERROR STATUS
167
168 002122      ENDMOD
169 002122      BGNMOD  GLBDAT
170
171      : TABLE OF OPERATION MESSAGES
172 002122 000000      OPMSGs: .WORD 0      :FILLER
173 002124 005002      .WORD MWRCHK      :MESSAGE FOR WRITE CHECK
174 002126 005032      .WORD MGTSTA      :GET STATUS
175 002130 004744      .WORD MSEEK      :SEEK
176 002132 004765      .WORD MREADH     :READ HEADER
177 002134 005016      .WORD MWRITE     :WRITE DATA
178 002136 004752      .WORD MREAD      :READ DATA
179 002140 005127      .WORD MWRSET     :WITH RESET
180 002142 005046      .WORD MDATCP     :WITH DATA COMPARE
181 002144 005071      .WORD MHDRCP     :WITH HEADER COMPARE
182 002146 005204      .WORD MCYLUP     :LOAD HEADS
183 002150 005173      .WORD MLOAD     :UNLOAD HEADS
184 002152 005235      .WORD MINOUT     :IN-OUT SEQ

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185 002154 005214      .WORD  MOUTIN      :
186 002156 005260      .WORD  MFOLWRT     :
187 002160 005304      .WORD  MREVSK      :
188 002162 005337      .WORD  MFWDSK      :
189 002164 005426      .WORD  MRESKO      :
190 002166 005372      .WORD  MFWSKO      :
191 002170 005462      .WORD  MBADAD      :
192 002172 005113      .WORD  M4OHDR      :
193
194
195 002174 007554      : RESTBL: TABLE OF RESULT NAME MESSAGE ADDRESSES
196 002176 007665      .WORD  MCERR       :CONTROLLER ERROR
197 002200 010114      .WORD  MDRERR      :DRIVE ERROR
198 002202 010066      .WORD  MNEERR      :NON-EXISTANT MEMORY ERROR
199 002204 010051      .WORD  MFLERR      :HEADER NOT FOUND-DATA LATE
200 002206 010032      .WORD  MHDERR      :HEADER OR DATA ERROR
201 002210 010141      .WORD  MOPERR      :OPERATION INCOMPLETE
202 002212 000000      .WORD  MNDRST      :NO DRIVE STATUS AVAILABLE
203 002214 010015      .WORD  0
204 002216 007777      .WORD  MWDERR      :WRITE DATA ERROR
205 002220 000000      .WORD  MHCERR      :HEAD CURRENT ERROR
206 002222 007761      .WORD  0
207 002224 007726      .WORD  MSTERR      :SEEK TIMEOUT ERROR
208 002226 007744      .WORD  MSPERR      :SPINDLE ERROR
209 002230 000000      .WORD  MWGERR      :WRITE GATE ERROR
210 002232 007676      .WORD  0
211
212
213 002234 004466      : PATTBL: PATTERN TABLE
214 002236 004470      .WORD  PAT1
215 002240 004530      .WORD  PAT2
216 002242 004570      .WORD  PAT3
217 002244 004630      .WORD  PAT4
218 002246 004636      .WORD  PAT5
219 002250 004676      .WORD  PAT6
220 002252 004700      .WORD  PAT7
221 002254 004740      .WORD  PAT8
222 002256 004742      .WORD  PAT9
223
224
225
226 002260 000000      : SUBSTK: SUBROUTINE CALLING STACK ;STACK IS 12 WORDS LONG
227 002262 000000      .WORD  0
228 002264 000000      .WORD  0
229 002266 000000      .WORD  0
230 002270 000000      .WORD  0
231 002272 000000      .WORD  0
232 002274 000000      .WORD  0
233 002276 000000      .WORD  0
234 002300 000000      .WORD  0
235 002302 000000      .WORD  0
236
237 002304 000002      : T25TBL: .WORD  2 ;TABLE OF DIFFERENCES TO BE USED
238 002306 000006      .WORD  6 ;IN TEST 25
239 002310 000011      .WORD  9
240 002312 000014      .WORD  12.

```

241	002314	000021	.WORD	17.
242	002316	000026	.WORD	22.
243	002320	000033	.WORD	27.
244	002322	000042	.WORD	34.
245	002324	000051	.WORD	41.
246	002326	000200	.WORD	128.
247	002330	000377	.WORD	255.

248  
249  
250 : TABLE TO BE USED IN TEST 33 AND 34 TO BUILD AND STORE THE  
CYLINDERS TO BE USED IN THE TEST.

251 002332 000010  
252  
253 :33TBL: .BLKW 10

253	002352	002	CYLTBL: .BYTE	2	:TABLE OF DEFAULT CYLINDERS
254	002353	007	.BYTE	7.	
255	002354	016	.BYTE	14.	
256	002355	024	.BYTE	20.	
257	002356	033	.BYTE	27.	
258	002357	041	.BYTE	33.	
259	002360	046	.BYTE	38.	
260	002361	055	.BYTE	45.	
261	002362	064	.BYTE	52.	
262	002363	072	.BYTE	58.	
263	002364	101	.BYTE	65.	
264	002365	110	.BYTE	72.	
265	002366	115	.BYTE	77.	
266	002367	124	.BYTE	84.	
267	002370	133	.BYTE	91.	
268	002371	141	.BYTE	97.	
269	002372	146	.BYTE	102.	
270	002373	154	.BYTE	108.	
271	002374	161	.BYTE	113.	
272	002375	170	.BYTE	120.	
273	002376	177	.BYTE	127.	
274	002377	206	.BYTE	134.	
275	002400	213	.BYTE	139.	
276	002401	222	.BYTE	146.	
277	002402	230	.BYTE	152.	
278	002403	235	.BYTE	157.	
279	002404	244	.BYTE	164.	
280	002405	252	.BYTE	170.	
281	002406	261	.BYTE	177.	
282	002407	270	.BYTE	184.	
283	002410	275	.BYTE	189.	
284	002411	303	.BYTE	195.	
285	002412	312	.BYTE	202.	
286	002413	317	.BYTE	207.	
287	002414	326	.BYTE	214.	
288	002415	334	.BYTE	220.	
289	002416	343	.BYTE	227.	
290	002417	352	.BYTE	234.	
291	002420	361	.BYTE	241.	
292	002421	367	.BYTE	247.	
293	002422	375	.BYTE	253.	
294	002423	000	.BYTE	0	

295  
296 002424 000000 SSINDX: .WORD 0 ;SUBROUTINE STACK INDEX POINTER

```

297
298
299 002426 000000      ; OPERATIONAL FLAGS
300 002430 000000      OPFLAG: .WORD 0      ; OPERATION FLAGS
301 002432 000000      DONE: .WORD 0        ; OPERATION COMPLETE FLAG
302 002434 000000      HADONE: .WORD 0     ; HEAD ALIGNMENT DONE FLAG
303 002436 000000      ERHEAD: .WORD 0    ; ADDRESS OF ERROR HEADER
304 002440 000000      MORECE: .WORD 0   ; MORE THAN 1 COMPARE ERROR
305 002442 000000      ERRSWI: .WORD 0   ; ERROR RETURN SWITCH
306 002444 000000      BSFLAG: .WORD 0   ; BAD SECTOR FLAGS
307 002446 000000      WRTSWI: .WORD 0   ; WRITE SWITCH
308                                TBLSTR: .WORD 0   ; TABLE STORAGE
309 002450 000000      RLBAS: .WORD 0    ; RL11 BASE ADDRESS
310 002452 000000      RLVEC: .WORD 0    ; RL11 VECTOR ADDRESS
311 002454 000000      RLDRV: .WORD 0    ; DRIVE NUMBER UNDER TEST
312
313 002456 000000      L.CS: .WORD 0     ; CONTROLLER REGISTER STORAGE
314 002460 000000      L.BA: .WORD 0     ; BEFORE OPERATION
315 002462 000000      L.DA: .WORD 0
316 002464 000000      L.MP: .WORD 0
317 002466 000000      T.CS: .WORD 0     ; CONTROLLER REGISTER STORAGE
318 002470 000000      T.BA: .WORD 0     ; AFTER OPERATION
319 002472 000000      T.DA: .WORD 0
320 002474 000000      T.MP:
321 002474 000000      HDWRD1: .WORD 0   ; HEADER WORD STORAGE
322 002476 000000      HDWRD2: .WORD 0
323 002500 000000      HDWRD3: .WORD 0
324
325 002502 000000      T.STAT: .WORD 0   ; DRIVE STATE STORAGE
326
327 002504 000000      RESPARM: .WORD 0  ; PARAM BLOCK FOR REASON REPORT
328 002506 000000      .WORD 0
329 002510 000000      .WORD 0
330 002512 000000      .WORD 0
331 002514 000000      .WORD 0
332
333 002516 000000      DRVCNT: .WORD 0   ; DRIVE COUNT FOR DRIVES UNDER TEST
334 002520 000000      DIFAug: .WORD 0   ; DIFFERENCE AUGMENT FOR SEEK
335 002522 000000      OLDcYL: .WORD 0   ; OLD CYLINDER
336 002524 000000      NEWcYL: .WORD 0   ; NEW CYLINDER
337 002526 000000      CURcYL: .WORD 0   ; CURRENT CYLINDER
338 002530 000000      DESDIF: .WORD 0   ; DESIRED DIFFERENCE
339 002532 000000      DESSGN: .WORD 0   ; DESIRED SIGN
340 002534 000000      DESHD: .WORD 0    ; DESIRED HEAD
341 002536 000000      DESSEC: .WORD 0   ; DESIRED SECTOR
342 002540 000000      TEMP0: .WORD 0    ; TEMPORARY STORAGE
343 002542 000000      TEMP1: .WORD 0    ; TEMPORARY STORAGE
344 002544 000000      TEMP2: .WORD 0    ; TEMPORARY STORAGE
345 002546 000000      TEMP3: .WORD 0    ; TEMPORARY STORAGE
346 002550 000000      TEMP4: .WORD 0    ; TEMPORARY STORAGE
347 002552 000000      TEMP5: .WORD 0    ; TEMPORARY STORAGE
348 002554 000000      TEMP6: .WORD 0    ; TEMPORARY STORAGE
349 002556 000000      TEMP7: .WORD 0    ; TEMPORARY STORAGE
350 002560 000000      TEMP8: .WORD 0    ; TEMPORARY STORAGE
351
352                                ; TIMER STORAGE

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```

738      :          RESULT: (R3) IS 1 SB 0 (R4)
739      :
740      :          ERR5  R3 POINTS TO RESULT NAME
741      :          R4 POINTS TO RESULT CONDITIONS
742      :          RESULT: (R3) IS 0 SB 1 (R4)
743      :
744      :          ERR6  RESULT ROUTINE DETERMINES WHICH ERROR(S) ARE SET AND
745      :          REPORTS ALL
746      :          RESULT: 'ERROR' IS 1 SB 0
747      :
748      :          ERR7  DRIVE STATE ERROR REPORT
749      :          R3 CONTAINS EXPECTED STATE
750      :          T.STAT CONTAINS BAD STATE
751      :          RESULT: DRIVE STATE IS (T.STAT) SB (R3)
752      :
753      :          ERR8  HEAD POSITIONING ERROR REPORT
754      :          NEWCYL CONTAINS EXPECTED CYLINDER
755      :          HDWRD1 CONTAINS BAD CYLINDER
756      :          RESULT: CYLINDER IS (HDWRD1) SB (NEWCYL)
757      :
758      :          ERR9  UTILITY RESULT REPORT
759      :          R3 POINTS TO RESULT NAME
760      :          R4 POINTS TO VALUE 1
761      :          R5 POINTS TO VALUE 2
762      :          RESULT: (R3-NAME) IS (R4-VALUE 1) SB (R5-VALUE 2)
763      :
764      :          ERR10 COMPARE ERROR REPORT
765      :          R3 CONTAINS THE BAD WORD NUMBER
766      :          R4 POINTS TO BAD WORD
767      :          R5 POINTS TO GOOD WORD
768      :          RESULT: WORD (R3) IS (R4) SB (R5)
769      :
770      :

```

```

771 011554      BGNMSG  ERR1
772 011554 105737 003067      TSTB  NOERCT          ;TEST IF ERROR COUNTING INHIBITED
773 011560 001002          BNE  1$              ;YES - SKIP
774 011562 005277 171072      INC  @ERRPOINT      ;ELSE BUMP ERROR COUNT
775 011566 010146          1$: MOV  R1, -(SP)        ;STORE R1
776 011570 004737 023244      JSR  PC, RPTOP      ;REPORT OPERATION
777 011574 012721 000001      MOV  #1, (R1)+      ;SET PARAM NUMBER
778 011600 010321          MOV  R3, (R1)+      ;INSERT MESSAGE ADDRESS POINTER
779 011602 004737 024032      JSR  PC, RPTRES     ;REPORT RESULTS
780 011606 004737 024240      JSR  PC, RPTREM     ;REPORT REMAINDER
781 011612 012601          MOV  (SP)+, R1      ;RESTORE R1
782 011614 004737 014634      JSR  PC, CKERLM     ;GO CHECK IF ERROR COUNT EXCEEDED
783 011620      ENDMSG
(3) 011620      L10000:
(3) 011620 104023      EMT  C$MSG
784
785 011622      BGNMSG  ERR2
786 011622 005277 171032      INC  @ERRPOINT      ;BUMP ERROR COUNT
787 011626 010146          MOV  R1, -(SP)        ;STORE R1
788 011630 004737 023244      JSR  PC, RPTOP      ;REPORT OPERATION
789 011634 012721 000003      MOV  #3, (R1)+      ;SET PARAM NUMBER
790 011640 010321          MOV  R3, (R1)+      ;INSERT NAME ADD POINTER
791 011642 012721 000001      MOV  #1, (R1)+      ;SET IS VALUE

```



















```

(8) 014220 012746 005640      MOV      #DRVNAV,-(SP)
(7) 014224 012746 011463      MOV      #FMT24,-(SP)
(6) 014230 012746 000002      MOV      #2,-(SP)
(3) 014234 010600      MOV      SP,R0
(4) 014236 104017      EMT      C$PNTF
(4) 014240 062706 000006      ADD      #6,SP
1149 014244      10$: PRINTF  #FMT5,#BASADD,RLBAS,#DRVNAM,<B,RLDRV+1>
(11) 014244 005046      CLR      -(SP)
(11) 014246 153716 002455      BISB     RLDRV+1,(SP)
(10) 014252 012746 005633      MOV      #DRVNAM,-(SP)
(9) 014256 013746 002450      MOV      RLBAS,-(SP)
(8) 014262 012746 005622      MOV      #BASADD,-(SP)
(7) 014266 012746 010657      MOV      #FMT5,-(SP)
(6) 014272 012746 000005      MOV      #5,-(SP)
(3) 014276 010600      MOV      SP,R0
(4) 014300 104017      EMT      C$PNTF
(4) 014302 062706 000014      ADD      #14,SP
1150 014306      PRINTF  #FMT3
(7) 014306 012746 010643      MOV      #FMT3,-(SP)
(6) 014312 012746 000001      MOV      #1,-(SP)
(3) 014316 010600      MOV      SP,R0
(4) 014320 104017      EMT      C$PNTF
(4) 014322 062706 000004      ADD      #4,SP
1151 014326      DODU     PSETNM          ;DROP DRIVE
(3) 014326 013700 003064      MOV      PSETNM,R0
(3) 014332 104053      EMT      C$DODU
1152 014334      DOCLN
(3) 014334 104044      EMT      C$DCLN
1153 014336      20$: CLRVEC  ERRVEC
(3) 014336 013700 002652      MOV      ERRVEC,R0
(3) 014342 104036      EMT      C$CVEC
1154 014344      22$:
1165      ;CHECK IF POWER FAILURE WAIT IS NEEDED
1166
1167 014344 005737 003072      4$: TST     PWRFLG          ;NEEDED???
1168 014350 001434      BEQ      8$              ;NO, SKIP
1169
1170 014352 013705 002454      MOV      RLDRV,R5          ;DRIVE SELECT
1171 014356 052705 000200      BIS      #CRDYMSK,R5      ;SET CRDY
1172 014362 010562 000000      MOV      R5,RLCS(R2)      ;SELECT DRIVE
1173 014366 012701 000074      MOV      #60.,R1         ;SIXTY SECOND TIMER
1174 014372 032762 000001 000000 9$: BIT     #DRDYMSK,RLCS(R2) ;DRIVE UP YET
1175 014400 001020      BNE      8$              ;YES START TEST
1176
1177 014402      WAITMS  #10.            ;WAIT A SECOND
(3) 014402 012700 000012      MOV      #10.,R0
(3) 014406 104026      EMT      C$WTM
1178 014410 005301      DEC      R1              ;SIXTY GONE BY
1179 014412 001367      BNE      9$              ;NO
1180 014414      PRINTF  #FMT24,#NOPWR
(8) 014414 012746 005673      MOV      #NOPWR,-(SP)
(7) 014420 012746 011463      MOV      #FMT24,-(SP)
(6) 014424 012746 000002      MOV      #2,-(SP)
(3) 014430 010600      MOV      SP,R0
(4) 014432 104017      EMT      C$PNTF
(4) 014434 062706 000006      ADD      #6,SP
  
```

```

1181 014440 000701          BR      10$
1182
1183 014442          8$:
1184
1185 014442          ENDINIT
(3) 014442          L10014:
(3) 014442 104011      EMT      C$INIT
1186 014444          ENDMOD
1187
1188 014444          BGNMOD  CLNCODE
1189 014444          BGNCLN
1190
1191 014444          SETVEC  ERRVEC,#TRPHAN,#340
(7) 014444 012746 000340  MOV     #340,-(SP)
(6) 014450 012746 014570  MOV     #TRPHAN,-(SP)
(5) 014454 013746 002652  MOV     ERRVEC,-(SP)
(4) 014460 012746 000003  MOV     #3,-(SP)
(3) 014464 104037      EMT      C$SVEC
(2) 014466 062706 000010  ADD     #10,SP
1192
1193 014472          SETPRI  #7          ;SET PRORITY TO 7
(3) 014472 012700 000007  MOV     #7,R0
(3) 014476 104041      EMT      C$SPRI
1194 014500 032762 000200 000000 2$:  BIT     #CRDYMSK,RLCS(R2) ;TEST IF CONTROLLER READY
1195 014506 001407      BEQ     3$          ;NO LOOP UNTIL READY
1196 014510 053762 002454 000000  BIS     RLDRV,RLCS(R2) ;SET DRIVE NUMBER
1197 014516 032762 000001 000000  BIT     #DRDYMSK,RLCS(R2) ;TEST IF DRIVE BUSY
1198 014524 001003      BNE     5$          ;NO - SKIP
1199 014526          WAITMS  #3          ;WAIT 300 MS
(3) 014526 012700 000003  MOV     #3,R0
(3) 014532 104026      EMT      C$WTM
1200 014534          5$:  CLRVEC  RLVEC          ;RELEASE VEC
(3) 014534 013700 002452  MOV     RLVEC,R0
(3) 014540 104036      EMT      C$CVEC
1201 014542 005737 003072  TST     PWRFLG ;PWR FAIL SET
1202 014546 001402      BEQ     7$          ;NO
1203 014550 005337 003072  DEC     PWRFLG
1204 014554          7$:  CLRVEC  ERRVEC
(3) 014554 013700 002652  MOV     ERRVEC,R0
(3) 014560 104036      EMT      C$CVEC
1205 014562          ENDCLN
(3) 014562          L10015:
(3) 014562 104012      EMT      C$CLEAN
1206
1207 014564          BGNDU
1208 014564 000240      NOP
1209 014566          ENDDU
(3) 014566          L10016:
(3) 014566 104055      EMT      C$DU
1210
1211 014570          ENDMOD
1212 014570          BGNMOD  GLBSUB
1213
1214 014570 005237 003070  TRPHAN: INC     TRPFLG
1215 014574 000002      RTI
1216

```



































































2631 025756  
2632 025756  
(3) 025756  
(3) 025756 104001

T2065\$:  
ENDTST  
L10026:  
EMT C\$ETST



2681 026166  
(3) 026166  
(3) 026166 104001

ENDTST  
L10030: EMT C\$ETST



(3) 026410  
(3) 026410 104003  
2732 026412  
2733 026412  
(3) 026412  
(3) 026412 104001

L10033:  
EMT C\$ESUB  
T2265\$:  
ENDTST  
L10032:  
EMT C\$ETST







(3)	027036	104003		
2829	027040		EMT	C\$ESUB
2830	027040		T2465\$:	
(3)	027040		ENDTST	
(3)	027040	104001	L10036:	
			EMT	C\$ETST







```

(3) 027600 000032
2940 027602 032737 020000 013372
2941 027610 001004
2942 027612 005205
2943 027614 020527 000400
2944 027620 001311
2945 027622 004737 017414
2946 027626 027632
2947 027630 000654
2948 027632
2949 027632
(3) 027632
(3) 027632 104001

```

```

      .WORD L10042-
      BIT #HICYL,MISWIW :TEST IF UPPER LIMIT SPEC'D
      BNE 20$ :YES - SKIP
      INC R5 :BUMP R5
      CMP R5,#256. :ALL CYLINDERS DONE
      BNE T267$ :NO - GO DO NEXT CYLINDER
      JSR PC,SWAPHD :GO SWAP TO HEAD 1 OR END TEST
      T2665$ :ABORT RETURN
      BR T266$ :GO DO TESTS

```

```

T2665$:
ENDTST
L10042:
      EMT C$ETST

```



















```
3277 .SBTTL *TEST 14 **WRITE/READ DATA (PART 1)
3278 BGNTST ;TEST 14
(3) 032410
3279 032410 012737 006517 002434 MOV #P2T14E,ERHEAD ;SET ERROR HEADER T14::
3280 032416 004737 017500 JSR PC,CKBSVD ;GO CHECK IF BAD SECTOR FILES VALID
3281 032422 004737 015160 JSR PC,TSTINT ;INITIALIZE TEST
3282 032426 004737 015176 JSR PC,GSTATR ;CLEAR DRIVE
3283 032432 032622 T3065$
3284 032434 004737 017370 JSR PC,CHOSHD ;GO CHOSE HEAD
3285 032440 005037 002536 CLR DESSEC ; SECTOR 0
3286 032444 005037 002524 CLR NEWCYL ; CYLINDER 0
3287 032450 005037 032514 CLR T310$ ;CLEAR PATTERN SELECT
3288 032454 004737 016070 T306$: JSR PC,XSEEK ;POSITION HEADS
3289 032460 032622 T3065$
3290 032462 012701 005670 MOV #3000.,R1 ;SET WAIT COUNT FOR 300 MS
3291 032466 004737 020650 JSR PC,RDYWAIT ;WAIT FOR READY
3292 032472 032622 T3065$
3293 032474 004737 021244 JSR PC,VERPOS ;VERIFY POSITION
3294 032500 032622 T3065$
3295 032502 005037 032514 CLR T310$ ;CLEAR PATTERN SELECTOR
3296 032506 T307$:
3297 032506 BGNSUB
(3) 032506 T14.1:
(3) 032506 104002
3298 032510 004537 021726 EMT C$BSUB
3299 032514 000000 T310$: JSR R5,DATGEN ;GENERATE DATA
3300 032516 004737 022354 .WORD 0 ;PATTERN SELECT WORD
3301 032522 032540 JSR PC,XWRITE ;DO WRITE DATA
3302 032524 004737 022414 60$
3303 032530 032540 JSR PC,XREAD ;DO READ DATA
3304 032532 004737 022066 60$
3305 032536 032540 JSR PC,DATCOM ;COMPARE DATA
3306 032540 012737 000002 002440 60$: MOV #2,ERRSWI ;INIT ERROR SWITCH
3307 032546 ENDSUB
(3) 032546 L10051:
(3) 032546 104003 EMT C$ESUB
3308 032550 ESCAPE TST ;EXIT TEST IF ERROR
(3) 032550 104010 EMT C$ESCAPE
(3) 032552 000050 .WORD L10050-
3309 032554 022737 000010 032514 CMP #8.,T310$ ;WAS DATA PAT 8 USED?
3310 032562 001403 BEQ 10$ ;YES - SKIP
3311 032564 005237 032514 INC T310$ ;ELSE BUMP TO NEXT PATTERN
3312 032570 000746 BR T307$ ;DO TEST WITH NEW PATTERN
3313 032572 004737 017414 10$: JSR PC,SWAPHD ;GO SWAP TO HEAD 1 OR END TEST
3314 032576 032622 T3065$ ;ABORT RETURN
3315 032600 005037 032514 CLR T310$ ;SET PATTERN SELECT TO 0
3316 032604 004737 023074 11$: JSR PC,BSCHK ;CHECK IF SECTOR BAD
3317 032610 032614 13$ ;YES RETURN - SKIP TO 13$
3318 032612 000720 BR T306$ ;NO RETURN - DO TEST THIS SECTOR
3319 032614 005237 002524 13$: INC NEWCYL ;BUMP TO NEXT CYLINDER
3320 032620 000771 BR 11$ ;CHECK IF THIS ONE BAD
3321 032622 T3065$:
3322 032622 ENDTST
(3) 032622 L10050:
(3) 032622 104001 EMT C$ETST
3323
```







```

3404 .SBTTL *TEST 16 **WRITE/READ DATA (PART 2)
3405 BGNTST ;TEST 16
(3) 033350
3406 033350 012737 006572 002434 MOV #P2T16E,ERHEAD ;SET ERROR HEADER T16::
3407 033356 004737 017500 JSR PC,CKBSVD ;GO CHECK IF BAD SECTOR FILES VALID
3408 033362 004737 015160 JSR PC,TSTINT ;INITIALIZE TEST
3409 033366 004737 015176 JSR PC,GSTATR ;CLEAR DRIVE
3410 033372 034370 T3165$
3411 033374 005037 002654 CLR PASCNT ;CLEAR PASS TO 0
3412 033400 012705 177777 MOV #-1,R5 ;SET R5
3413 033404 005737 003062 TST PASNUM ;TEST IF FIRST PASS (QUICK VERIFY)
3414 033410 001006 BNE 1$ ;NO - SKIP
3415 033412 032737 000001 013372 BIT #ALLCYL,MISWIW ;TEST IF USE ALL CYLINDERS
3416 033420 001002 BNE 1$ ;YES - SKIP
3417 033422 012705 177770 MOV #-8.,R5 ;ELSE SET R5 TO NEG 8
3418 033426 1$:
3419 033426 012701 002332 MOV #T33TBL,R1 ;GET ADDRESS OF WORK TABLE
3420 033432 012703 000010 MOV #10,R3 ;SET CLEAR COUNT
3421 033436 013721 013374 2$: MOV LOLIMW,(R1)+ ;CLEAR LOCATIONS TO LO LIMIT
3422 033442 005303 DEC R3 ;DEC COUNT
3423 033444 001374 BNE 2$ ;LOOP UNTIL 0
3424 033446 113737 013376 002336 MOV B HILIMW,T33TBL+4 ;INSERT HILIMIT
3425 033454 113737 013376 002340 MOV B HILIMW,T33TBL+6 ;INTO APPROPRIATE LOCATIONS
3426 033462 113737 013376 002342 MOV B HILIMW,T33TBL+10
3427 033470 005205 T3100$: INC R5 ;BUMP R5
3428 033472 032737 000001 013372 BIT #ALLCYL,MISWIW ;TEST IF USE ALL CYLINDERS
3429 033500 001017 BNE 5$ ;YES - SKIP
3430 033502 005737 003062 TST PASNUM ;TEST IF FIRST PASS (QUICK VERIFY)
3431 033506 001002 BNE 3$ ;NO - SKIP
3432 033510 062705 000007 ADD #7,R5 ;ELSE BUMP CYLINDER POINTER BY 7
3433 033514 020527 000051 3$: CMP R5,#41. ;TEST IF PAST TABLE
3434 033520 103005 BHS 4$ ;YES - GO TO EXIT
3435
3436 033522 116503 002352 MOV B CYLTBL(R5),R3 ;GET NEXT TABLE ENTRY
3437 033526 042703 177400 BIC #177400,R3 ;CLEAR UPPER BYTE
3438 033532 001006 BNE 8$ ;SKIP IF NOT 0
3439 033534 000137 034370 4$: JMP T3165$ ;EXIT TEST
3440 033540 022705 000377 5$: CMP #255.,R5 ;TEST IF ALL CYLINDERS USED
3441 033544 001773 BEQ 4$ ;YES - EXIT TEST
3442 033546 010503 MOV R5,R3 ;USE R5 AS NEXT CYLINDER
3443 033550 020337 013374 8$: CMP R3,LOLIMW ;CHECK IF LOWER THAN LOLIMIT
3444 033554 103745 BLO T3100$ ;YES - SKIP
3445 033556 020337 013376 CMP R3,HILIMW ;CHECK IF HIGHER THAN HILIMIT
3446 033562 101342 BHI T3100$ ;YES - SKIP
3447 033564 012704 002332 MOV #T33TBL,R4 ;GET ADDRESS OF SEEK TABLE
3448 033570 110364 000001 MOV B R3,1(R4) ;INSERT CC IN APPROPRIATE TABLE
3449 033574 110364 000003 MOV B R3,3(R4) ;LOCATIONS FOR TEST SEEK SEQUENCE
3450 033600 110364 000005 MOV B R3,5(R4)
3451 033604 110364 000007 MOV B R3,7(R4)
3452 033610 110364 000011 MOV B R3,11(R4)
3453 033614 110364 000013 MOV B R3,13(R4)
3454 033620 010437 002446 MOV R4,TBLSTR ;STORE TABLE ADDRESS
3455 033624 004737 017370 JSR PC,CHOSHD ;GO CHOSE HEAD
3456 033630 T3101$:
3457 033630 BGNSUB
(3) 033630

```

T16.1:



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3513 034156 005037 002536      33$: CLR      DESSEC      :CLEAR DESIRED SECTOR
3514 034162 005037 002444      CLR      WRTSWI      :CLEAR WRITE/READ SWITCH
3515 034166 005237 002654      INC      PASCNT      :BUMP PASS COUNT
3516 034172 042737 003760 002426 BIC      #MQUALS,OPFLAG :CLEAR ALL QUALIFIERS
3517 034200 023727 002654 000003 CMP      PASCNT,#3     :TEST IS PASS 3
3518 034206 001435      BEQ      60$         :YES - SKIP
3519 034210 023727 002654 000006 CMP      PASCNT,#6     :TEST IF PASS 6
3520 034216 001431      EEQ      60$         :YES - SKIP
3521 034220 012737 000002 002444 MOV      #BIT1,WRTSWI  :SET READ REQUIRED BIT
3522 034226 023727 002654 000001 CMP      PASCNT,#1     :TEST IF PASS 1
3523 034234 001415      BEQ      40$         :YES - SKIP
3524 034236 023727 002654 000005 CMP      PASCNT,#5     :TEST IF PASS 4
3525 034244 001411      BEQ      40$         :YES - SKIP
3526 034246 000404      BR       39$         :SKIP
3527 034250 052737 002000 002426 37$: BIS      #FWDSCO,OPFLAG :SET FWD QUALIFIER
3528 034256 000407      BR       36$         :GO DO NEXT PASS
3529 034260 052737 000020 002426 39$: BIS      #INOUTS,OPFLAG :SET QUALIFIER
3530 034266 000403      BR       36$         :SKIP
3531 034270 052737 000040 002426 40$: BIS      #OUTINS,OPFLAG :SET MESSAGE QUALIFIER
3532 034276 000137 033722      36$: JMP      15$         :GO DO NEXT PASS
3533 034302 012737 000002 002440 60$: MOV      #2,ERRSWI    :INIT ERROR SWITCH
3534 034310      ENDSUB
(3) 034310      L10054:
(3) 034310 104003      EMT      C$ESUB
3535 034312      ESCAPE   TST              :EXIT TEST IF ERROR
(3) 034312 104010      EMT      C$ESCAPE
(3) 034314 000054      .WORD   L10053-
3536 034316 012737 000003 002444 MOV      #3,WRTSWI    :SET FOR READ AND WRITE REQ.
3537 034324 023727 002654 000003 CMP      PASCNT,#3     :TEST IF PASS 3
3538 034332 001004      BNE     45$         :NO - SKIP
3539 034334 012737 002340 002446 MOV      #T33TBL+6,TBLSTR :STORE MID POINT IN TABLE
3540 034342 000410      BR      48$         :GO START PASS 4
3541 034344 005037 002654      45$: CLR      PASCNT      :CLEAR TO PASS 0
3542 034350 004737 017414 JSR      PC,SWAPHD    :GO SWAP TO HEAD 1 OR END TEST
3543 034354 033470      T3100$ :ABORT RETURN
3544 034356 012737 002332 002446 MOV      #T33TBL,TBLSTR :STORE START OF TABLE
3545 034364 000137 033630      48$: JMP      T3101$    :GO DO HEAD 1
3546 034370      T3165$:
3547 034370      ENDTST
(3) 034370      L10053:
(3) 034370 104001      EMT      C$ETST

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3549          .SBTTL *TEST 17          **WRITE LOCK ERROR AND DATA PROTECTION
3550 034372   BGNSTST                ;TEST 17
(3) 034372
3551 034372   005737 003062          TST     PASNUM          ;TEST IF FIRST PASS
3552 034376   001003                    BNE     2$              ;NO - SKIP
3553 034400   005737 013372          TST     MISWIW         ;TEST IF RUN MANUAL INTERVENTION
3554 034404   100402                    BMI     3$              ;YES - SKIP
3555 034406   000137 035362          JMP     T3265$         ;EXIT TST
3556 034412   2$:
3557 034412   3$:
(3) 034412   BGNSUB
(3) 034412   104002                    EMT     C$BSUB          T17.1:
3558 034414   012737 006615 002434   MOV     #P2T17E,ERHEAD ;SET ERROR HEADER
3559 034422   004737 015160          JSR     PC,TSTINT      ;INITIALIZE TEST
3560 034426   004737 015176          JSR     PC,GSTATR      ;CLEAR DRIVE
3561 034432   035234                    60$
3562 034434   005037 002534          CLR     DESHD          ;SET TO HEAD 0
3563 034440   005037 002536          CLR     DESSEC         ;SET TO SECTOR 0
3564 034444   005037 002524          CLR     NEWCYL         ;CLEAR TO CYLINDER 0
3565 034450   004737 016070          JSR     PC,XSEEK       ;DO SEEK
3566 034454   035234                    60$
3567 034456   012701 005670          MOV     #3000.,R1      ;SET WAIT FOR 300 MS
3568 034462   004737 020650          JSR     PC,RDYWAIT     ;WAIT FOR READY
3569 034466   035234                    60$
3570 034470   004737 021244          JSR     PC,VERPOS      ;VERIFY POSITION
3571 034474   035234                    60$
3572 034476   032737 020000 002474   BIT     #WLSTAT,T.MP   ;TEST IF WRITE LOCK SET
3573 034504   001114                    BNE     7$              ;YES - SKIP
3574 034506   004537 021726          JSR     R5,DATGEN      ;GENERATE DATA
3575 034512   000007                    7$
3576 034514   004737 022354          JSR     PC,XWRITE      ;WRITE DATA
3577 034520   035234                    60$
3578 034522   004737 022414          JSR     PC,XREAD       ;READ DATA
3579 034526   035234                    60$
3580 034530   004737 022066          JSR     PC,DATCOM      ;CHECK DATA
3581 034534   035234                    60$
3582 034536   PRINTF #FMTOP1,#OPR004,#OPR1A,#BASADD,RLBAS,#DRVNAM,<B,RLDRV+1> ;REQUEST SET WR
(13) 034536   005046          CLR     -(SP)
(13) 034540   153716 002455          BISB   RLDRV+1,(SP)
(12) 034544   012746 005633          MOV     #DRVNAM,-(SP)
(11) 034550   013746 002450          MOV     RLBAS,-(SP)
(10) 034554   012746 005622          MOV     #BASADD,-(SP)
(9) 034560   012746 007250          MOV     #OPR1A,-(SP)
(8) 034564   012746 007277          MOV     #OPR004,-(SP)
(7) 034570   012746 010532          MOV     #FMTOP1,-(SP)
(6) 034574   012746 000007          MOV     #7,-(SP)
(3) 034600   010600          MOV     SP,R0
(4) 034602   104017          EMT     C$PNTF
(4) 034604   062706 000020          ADD     #20,SP
3583 034610   012701 000006          MOV     #6.,R1        ;SET WAIT COUNT FOR 30 SECONDS
3584 034614   5$: WAITMS #50.          ;CALL WAIT
(3) 034614   012700 000062          MOV     #50.,R0
(3) 034620   104026          EMT     C$WTM
3585 034622   004737 015176          JSR     PC,GSTATR      ;GET STATUS
3586 034626   035234                    60$
3587 034630   032737 020000 002474   BIT     #WLSTAT,T.MP   ;CHECK IF WRITE LOCK SET
  
```

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3588 034636 001037      BNE      7$           ;YES - SKIP
3589 034640              PRINTF   #FMT2,#BELL      ;RING BELL
      (8) 034640 012746 010377      MOV      #BELL,-(SP)
      (7) 034644 012746 010640      MOV      #FMT2,-(SP)
      (6) 034650 012746 000002      MOV      #2,-(SP)
      (3) 034654 010600              MOV      SP,R0
      (4) 034656 104017              EMT      C$PNTF
      (4) 034660 062706 000006      ADD      #6,SP
3590 034664 005301      DEC      R1           ;DEC COUNT
3591 034666 001352      BNE      5$           ;SKIP IF NOT 0
3592 034670              PRINTF   #FMT23,#P2T17E,#BYPSNM,#OPR1A,<B,RLDRV+1> ;RPT BYPASSED
      (11) 034670 005046              CLR      -(SP)
      (11) 034672 153716 002455      BISB     RLDRV+1,(SP)
      (10) 034676 012746 007250      MOV      #OPR1A,-(SP)
      (9) 034702 012746 007353      MOV      #BYPSNM,-(SP)
      (8) 034706 012746 006615      MOV      #P2T17E,-(SP)
      (7) 034712 012746 011447      MOV      #FMT23,-(SP)
      (6) 034716 012746 000005      MOV      #5,-(SP)
      (3) 034722 010600              MOV      SP,R0
      (4) 034724 104017              EMT      C$PNTF
      (4) 034726 062706 000014      ADD      #14,SP
3593 034732              EXIT     TST
      (3) 034732 104032              EMT      C$EXIT
      (3) 034734 000426              .WORD    L10055-
3594 034736 004537 021726      7$:     JSR      R5,DATGEN      ;GENERATE DATA
3595 034742 000001              1           ;PATTERN 1
3596 034744 012705 002456      MOV      #L.CS,R5      ;GET ADDRESS OF L REGS
3597 034750 012715 000112      MOV      #WTDATA,(R5)  ;LOAD WRITE COMMAND
3598 034754 053715 002454      BIS      RLDRV,(R5)    ;INSERT DRIVE NUMBER
3599 034760 042725 002000      BIC      #BIT10,(R5)+  ;CLEAR FOR DRIVE 4 - 7 SPEC'D
3600 034764 012725 004066      MOV      #OBUFF,(R5)+ ;LOAD BUS ADDRESS
3601 034770 005025              CLR      (R5)+         ;CYL 0, HD 0, SECTOR 0
3602 034772 012725 177600      MOV      #177600,(R5)+ ;128 WORDS
3603 034776 012701 000454      MOV      #300.,R1      ;SET WAIT COUNT FOR 30 MS
3604 035002 005037 002430      CLR      DONE          ;CLEAR INTERRUPT FLAG
3605 035006 014562 000006      MOV      -(R5),RLMP(R2);LOAD RL REGS
3606 035012 014562 000004      MOV      -(R5),RLDA(R2)
3607 035016 014562 000002      MOV      -(R5),RLBA(R2)
3608 035022 014562 000000      MOV      -(R5),RLCS(R2)
3609 035026              10$:    WAITUS   #1
      (3) 035026 012700 000001      MOV      #1,R0
      (3) 035032 104027              EMT      C$WTU
3610 035034 005737 002430      TST      DONE          ;CHECK IF INTERRUPT
3611 035040 001012              BNE      14$          ;YES - SKIP
3612 035042 005301              DEC      R1           ;DEC WAIT COUNT
3613 035044 001370              BNE      10$         ;LGOP IF NOT 0
3614 035046 004737 015026      JSR      PC,WAITIN     ;WAIT FOR INTERRUPT
3615 035052 012603              MOV      (SP)+,R3      ;GET RESULT MESSAGE
3616 035054              ERRHRD  17C1...ERR1
      (3) 035054 104443              TRAP     T$ERCODE
      (5) 035056 003245              .WORD    1701
      (5) 035060 011554              .WORD    ERR1
3617 035062              EXIT     SUB
      (3) 035062 104032              EMT      C$EXIT
      (3) 035064 000156              .WORD    L10056-
3618 035066 004737 015226      14$:    JSR      PC,GSTAT      ;GET STATUS

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3619 035072 035234          60$
3620 035074 032737 040000 002466   BIT      #DRVERR,T.CS      :TEST IF ANY ERROR SET
3621 035102 001005          BNE      15$              :YES - SKIP
3622 035104 012703 007665   MOV      #MDRERR,R3      :SET RESULT MESSAGE POINTER
3623 035110 1702...ERR3      ERRHRD   1702...ERR3     :REPORT ERROR NOT SET
(3) 035110 104443          TRAP     T$ERCODE
(5) 035112 003246          .WORD   1702
(5) 035114 011670          .WORD   ERR3
3624 035116 032737 002000 002474 15$:  BIT      #WGESTAT,T.MP    :TEST IF WGE SET
3625 035124 001005          BNE      18$              :YES - SKIP
3626 035126 012703 007744   MOV      #MWGERR,R3      :SET MESSAGE FOR WGE NOT SET
3627 035132 1704...ERR3      ERRHRD   1704...ERR3
(3) 035132 104443          TRAP     T$ERCODE
(5) 035134 003250          .WORD   1704
(5) 035136 011670          .WORD   ERR3
3628 035140 042737 040000 002466 18$:  BIC      #DRVERR,T.CS    :CLEAR DRIVE ERROR BIT
3629 035146 042737 002000 002474   BIC      #WGESTAT,T.MP    :CLEAR WGE BIT
3630 035154 032737 157400 002474   BIT      #157400,T.MP     :TEST IF ANY OTHER ERRORS
3631 035162 001004          BNE      16$              :YES - GO REPORT
3632 035164 032737 036000 002466   BIT      #36000,T.CS     :TEST ANY ERRORS IN CS REG
3633 035172 001404          BEQ      17$              :NO - SKIP
3634 035174 1703...ERR6      ERRHRD   1703...ERR6     :REPORT ERRORS
(3) 035174 104443          TRAP     T$ERCODE
(5) 035176 003247          .WORD   1703
(5) 035200 012056          .WORD   ERR6
3635 035202 000414          BR       60$              :EXIT TEST
3636 035204 004737 015176          JSR      PC,GSTATR        :GET STATUS AND RESET ERROR
3637 035210 035234          60$
3638 035212 004537 021726          JSR      R5,DATGEN        :GO GENERATE DATA
3639 035216 000007          7              :PATTERN 7
3640 035220 004737 022414          JSR      PC,XREAD        :READ DATA
3641 035224 035234          60$
3642 035226 004737 022066          JSR      PC,DATCOM        :COMPARE DATA
3643 035232 035234          60$
3644 035234 012737 000002 002440 60$:  MOV      #2,ERRSWI        :INIT ERROR SWITCH
3645 035242          ENDSUB
(3) 035242          L10056:
(3) 035242 104003          EMT      C$ESUB
3646 035244 012737 000002 002440 T3204$: MOV      #2,ERRSWI        :INIT ERROR SWITCH
3647 035252          PRINTF  #FMTOP1,#OPR12,#OPR1A,#BASADD,RLBAS,#DRVNAM,<B,RLDRV+1> ;REQ RESET WRT L
(13) 035252 005046          CLR      -(SP)
(13) 035254 153716 002455          BISB    RLDRV+1,(SP)
(12) 035260 012746 005633          MOV     #DRVNAM,-(SP)
(11) 035264 013746 002450          MOV     RLBAS,-(SP)
(10) 035270 012746 005622          MOV     #BASADD,-(SP)
(9) 035274 012746 007250          MOV     #OPR1A,-(SP)
(8) 035300 012746 007231          MOV     #OPR12,-(SP)
(7) 035304 012746 010532          MOV     #FMTOP1,-(SP)
(6) 035310 012746 000007          MOV     #7,-(SP)
(3) 035314 010600          MOV     SP,R0
(4) 035316 104017          EMT      C$PNTF
(4) 035320 062706 000020          ADD     #20,SP
3648 035324 012701 000454          MOV     #300.,R1        :SET WAIT FOR 30 SEC
3649 035330          WAITMS #1
(3) 035330 012700 000001          MOV     #1,R0
(3) 035334 104026          EMT      C$WTM

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3650	035336	004737	015176		JSR	PC,GSTATR	:GET STATUS
3651	035342	035244			T3204\$		
3652	035344	032737	020000	002474	BIT	#WLSTAT,T.MP	:CHECK IF WRITE LOCK RESET
3653	035352	001403			BEQ	T3265\$	
3654	035354	005301			DEC	R1	:DEC WAIT COUNT
3655	035356	001364			BNE	16\$	:LOOP IF NOT 0
3656	035360	000731			BR	T3204\$	:ELSE REPEAT MESSAGE
3657	035362						
3658	035362						
(3)	035362						
(3)	035362	104001					
3659					EMT	C\$ETST	

T3265\$:  
 ENDTST  
 L10055:































