

TSU05

TSU05 DIAG PART 1
CZTSAAO

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IDENTIFICATION

PRODUCT ID: AC-T716A-MC
PRODUCT TITLE: CZTSAAO TSU05 DIAG PART 1
DEPARTMENT: COMPUTER SPECIAL SYSTEMS/PPG
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1.0 GENERAL INFORMATION

1.1 PROGRAM ABSTRACT

THIS IS A PDP-11 RESIDENT DIAGNOSTIC WHICH CHECKS THE FUNCTIONALITY OF A TSU05 MAGTAPE SUBSYSTEM WHILE CONNECTED TO A PDP-11/23 SYSTEM (UNIBUS). THE PROGRAM PROVIDES ERROR MESSAGES WHICH IDENTIFY FAILING FUNCTIONS THAT AID IN THE REPAIR OF THE DEVICE. THIS DIAGNOSTIC CONSIST OF ELEVEN TEST WHICH ARE EXECUTED IN SEQUENCE.

THIS DIAGNOSTIC HAS BEEN WRITTEN FOR USE WITH THE DIAGNOSTIC RUNTIME SERVICES SOFTWARE (SUPERVISOR). THESE SERVICES PROVIDE THE INTERFACE TO THE OPERATOR AND TO THE SOFTWARE ENVIRONMENT. THIS PROGRAM CAN BE USED WITH XXDP., ACT, APT, SLIDE AND PAPER TAPE. FOR A COMPLETE DESCRIPTION OF THE RUNTIME SERVICES, REFER TO THE XXDP. USER'S MANUAL. THERE IS A BRIEF DESCRIPTION OF THE RUNTIME SERVICES IN SECTION 2 OF THIS DOCUMENT.

1.2 SYSTEM REQUIREMENTS

PDP-11 PROCESSOR AND MEMORY
CAUTION:DIAGNOSTIC REQUIRES 32K WORDS OF MEMORY
(28K USEABLE I.E. 4K FOR I/O PAGE)
TSU05 MAGTAPE SUBSYSTEM (DRIVE AND CONTROLLER)
CONSOLE TERMINAL
PDP-11 DIAGNOSTIC SUPERVISOR (HSAAA.SYS VERSION 34 OR LATER)
PDP-11 DIAGNOSTIC LOADER/MONITOR (XXDP.)

1.3 RELATED DOCUMENTS AND STANDARDS

DIGITAL EQUIPMENT CORPORATION DOCUMENTS:

1. CHQUS XXDP. USERS GUIDE; DOCUMENT NUMBER AC-F348E-MC
DATE: 14 JULY 1980.
2. TSU05 TRANSPORT SUBSYSTEM USER'S GUIDE; DOCUMENT NUMBER EK-TSU05-UG-001
DATE: AUGUST 1983
3. TSU05 TRANSPORT SUBSYSTEM TECHNICAL MANUAL; DOCUMENT NUMBER EK-TSU05-TM-001
DATE: AUGUST 1983
4. TSU05 TRANSPORT SUBSYSTEM INSTALLATION MANUAL; DOCUMENT NUMBER EK-TSU05-IN-001
DATE: AUGUST 1983

1.4 DIAGNOSTIC HIERARCY PREREQUISITES

FUNCTIONAL PDP-11 CENTRAL PROCESSOR AND MEMORY
FUNCTIONAL CONSOLE TERMINAL
FUNCTIONAL STANDALONE DIAGNOSTIC SUPERVISOR
FUNCTIONAL DIAGNOSTIC LOADER/MONITOR (XXDP.)

1.5 ASSUMPTIONS

ALL HARDWARE EXCEPT THE HARDWARE UNDER TEST IS ASSUMED TO WORK PROPERLY OR FALSE ERRORS CAN BE REPORTED.
THE TAPE BEING USED ON THE TS05 TRANSPORT IS A KNOWN GOOD REEL OF TAPE.

2.0 OPERATING INSTRUCTIONS

THIS SECTION CONTAINS A BRIEF DESCRIPTION OF THE RUNTIME SERVICES. FOR DETAILED INFORMATION, REFER TO THE XXDP. USER'S MANUAL (CHQUS).

2.1 COMMANDS

THERE ARE ELEVEN LEGAL COMMANDS FOR THE DIAGNOSTIC RUNTIME SERVICES (SUPERVISOR). THIS SECTION LISTS THE COMMANDS AND GIVES A VERY BRIEF DESCRIPTION OF THEM. THE XXDP. USER'S MANUAL HAS MORE DETAILS.

COMMAND	EFFECT
START	START THE DIAGNOSTIC FROM AN INITIAL STATE
RESTART	START THE DIAGNOSTIC WITHOUT INITIALIZING
CONTINUE	CONTINUE AT TEST THAT WAS INTERRUPTED (AFTER ^C)
PROCEED	CONTINUE FROM AN ERROR HALT
EXIT	RETURN TO XXDP. MONITOR (XXDP. OPERATION ONLY!)
ADD	ACTIVATE A UNIT FOR TESTING (ALL UNITS ARE CONSIDERED TO BE ACTIVE AT START TIME)
DROP	DEACTIVATE A UNIT
PRINT	PRINT STATISTICAL INFORMATION (IF IMPLEMENTED BY THE DIAGNOSTIC - SECTION 4.0)
DISPLAY	TYPE A LIST OF ALL DEVICE INFORMATION
FLAGS	TYPE THE STATE OF ALL FLAGS (SEE SECTION 2.3)
ZFLAGS	CLEAR ALL FLAGS (SEE SECTION 2.3)

A COMMAND CAN BE RECOGNIZED BY THE FIRST THREE CHARACTERS. SO YOU MAY, FOR EXAMPLE, TYPE "STA" INSTEAD OF "START".

2.1.1 OPERATOR COMMANDS

THE TSU05 DIAGNOSTIC IS A PDP-11 DIAGNOSTIC SUPERVISOR COMPATIBLE PROGRAM. ALL LOADING AND RUNTIME INSTRUCTIONS CAN BE REFERENCED IN THE CHQUS XXDP. USERS GUIDE, DOCUMENT NUMBER AC-F348E-MC. THE USER ENTRY IS IN QUOTES.

BOOT THE DIAGNOSTIC MEDIA

```
.R VTSA??
DIAG. RUN-TIME SERVICES REV D. APR 79
CZTSA-A-0
****TSU05 LOGIC DIAGNOSTIC****
UNIT IS TSU05
```

>DR

2.2 SWITCHES

THERE ARE SEVERAL SWITCHES WHICH ARE USED TO MODIFY SUPERVISOR OPERATION. THESE SWITCHES ARE APPENDED TO THE LEGAL COMMANDS. ALL OF THE LEGAL SWITCHES ARE TABULATED BELOW WITH A BRIEF DESCRIPTION OF EACH. IN THE DESCRIPTIONS BELOW, A DECIMAL NUMBER IS DESIGNATED BY "DDDD".

SWITCH	EFFECT
/TESTS:LIST	EXECUTE ONLY THOSE TESTS SPECIFIED IN THE LIST. LIST IS A STRING OF TEST NUMBERS, FOR EXAMPLE - /TESTS:1:5:7-10. THIS LIST WILL CAUSE TESTS 1,5,7,8,9,10 TO BE RUN. ALL OTHER TESTS WILL NOT BE RUN.
/PASS:DDDD	EXECUTE DDDDD PASSES (DDDD = 1 TO 64000)
/FLAGS:FLGS	SET SPECIFIED FLAGS. FLAGS ARE DESCRIBED IN SECTION 2.3.
/EOP:DDDD	REPORT END OF PASS MESSAGE AFTER EVERY DDDDD PASSES ONLY. (DDDD = 1 TO 64000)
/UNITS:LIST	TEST/ADD/DROP ONLY THOSE UNITS SPECIFIED IN THE LIST. LIST EXAMPLE - /UNITS:0:5:10-12 USE UNITS 0,5,10,11,12 (UNIT NUMBERS = 0-63)

EXAMPLE OF SWITCH USAGE:

START/TESTS:1-5/PASS:1000/EOP:100

THE EFFECT OF THIS COMMAND WILL BE: 1) TESTS 1 THROUGH 5 WILL BE EXECUTED, 2) ALL UNITS WILL TESTED 1000 TIMES AND 3) THE END OF PASS MESSAGES WILL BE PRINTED AFTER EACH 100 PASSES ONLY. A SWITCH CAN BE RECOGNIZED BY THE FIRST THREE CHARACTERS. YOU MAY, FOR EXAMPLE, TYPE "/TES:1-5" INSTEAD OF "/TESTS:1-5".

BELOW IS A TABLE THAT SPECIFIES WHICH SWITCHES CAN BE USED BY EACH COMMAND.

	TESTS	PASS	FLAGS	EOP	UNITS
START	X	X	X	X	X
RESTART	X	X	X	X	X
CONTINUE		X	X	X	
PROCEED			X		
DROP					X
ADD					X
PRINT					
DISPLAY					X
FLAGS					
ZFLAGS					
EXIT					

2.3 FLAGS

FLAGS ARE USED TO SET UP CERTAIN OPERATIONAL PARAMETERS SUCH AS LOOPING ON ERROR. ALL FLAGS ARE CLEARED AT STARTUP AND REMAIN

CLEARED UNTIL EXPLICITLY SET USING THE FLAGS SWITCH. FLAGS ARE ALSO CLEARED AFTER A START COMMAND UNLESS SET USING THE FLAG SWITCH. THE ZFLAGS COMMAND MAY ALSO BE USED TO CLEAR ALL FLAGS. WITH THE EXCEPTION OF THE START AND ZFLAGS COMMANDS, NO COMMANDS AFFECT THE STATE OF THE FLAGS; THEY REMAIN SET OR CLEARED AS SPECIFIED BY THE LAST FLAG SWITCH.

FLAG	EFFECT
----	-----
MOE	HALT ON ERROR - CONTROL IS RETURNED TO RUNTIME SERVICES COMMAND MODE
LOE	LOOP ON ERROR
IER*	INHIBIT ALL ERROR REPORTS
IBR*	INHIBIT ALL ERROR REPORTS EXCEPT FIRST LEVEL (FIRST LEVEL CONTAINS ERROR TYPE, NUMBER, PC, TEST AND UNIT)
IXE*	INHIBIT EXTENDED ERROR REPORTS (THOSE CALLED BY PRINTX MACRO'S)
PRI	DIRECT MESSAGES TO LINE PRINTER
PNT	PRINT TEST NUMBER AS TEST EXECUTES
BOE	"BELL" ON ERROR
UAM	UNATTENDED MODE (NO MANUAL INTERVENTION)
ISR	INHIBIT STATISTICAL REPORTS (DOES NOT APPLY TO DIAGNOSTICS WHICH DO NOT SUPPORT STATISTICAL REPORTING)
IDR	INHIBIT PROGRAM DROPPING OF UNITS
ADR	EXECUTE AUTODROP CODE
LOT	LOOP ON TEST

*ERROR MESSAGES ARE DESCRIBED IN SECTION 3.1

SEE THE XXDP* USER'S MANUAL FOR MORE DETAILS ON FLAGS. YOU MAY SPECIFY MORE THAN ONE FLAG WITH THE FLAG SWITCH. FOR EXAMPLE, TO CAUSE THE PROGRAM TO LOOP ON ERROR, INHIBIT ERROR REPORTS AND TYPE A "BELL" ON ERROR, YOU MAY USE THE FOLLOWING STRING:

```
/FLAGS:LOE:IER:BOE
```

2.4 HARDWARE QUESTIONS

WHEN A DIAGNOSTIC IS STARTED, THE RUNTIME SERVICES WILL PROMPT THE USER FOR HARDWARE INFORMATION BY TYPING "CHANGE HW (L) ?" YOU MUST ANSWER "Y" AFTER A START COMMAND UNLESS THE HARDWARE INFORMATION HAS BEEN "PRELOADED" USING THE SETUP UTILITY (SEE CHAPTER 14 OF THE XXDP* USER'S MANUAL). WHEN YOU ANSWER THIS QUESTION WITH A "Y", THE RUNTIME SERVICES WILL ASK FOR THE NUMBER OF UNITS (IN DECIMAL).

AFTER INITIAL STARTING OF THE PROGRAM (START COMMAND TO THE DIAGNOSTIC SUPERVISOR), THE PROGRAM WILL ISSUE THE "CHANGE HW?" QUESTION TO ASK IF THE HARDWARE PARAMETERS ARE TO BE CHANGED (BY THE OPERATOR).

ON A "N" (NO) RESPONSE TO THE "CHANGE HW?" QUESTION, THE DIAGNOSTIC WILL

RUN USING THE DEFAULT VALUES FOR ALL QUESTIONS. THE DEFAULT ADDRESS AND VECTOR ARE:

TSBA/TSDB = 172520, VECTOR = 224

ON A "Y" (YES) RESPONSE TO THE QUESTION, THE FOLLOWING QUESTIONS WILL THEN BE ASKED TO ALLOW THE OPERATOR TO SELECT THE UNITS TO BE TESTED. A VALUE, IF PRESENT, LOCATED TO THE LEFT OF THE QUESTION MARK IS THE DEFAULT VALUE THAT WILL BE TAKEN IF ONLY A CARRIAGE RETURN IS TYPED AS A RESPONSE. A "(D)" IN A QUESTION INDICATES THAT A DECIMAL NUMBER IS REQUIRED AS A RESPONSE. AN "(O)" INDICATES AN OCTAL NUMBER IS BEING SOLICITED. AN "(L)" INDICATES THAT A LOGICAL RESPONSE IS TO BE MADE: "Y" FOR YES, "N" FOR NO.

• # UNITS (D) ? <ENTER THE NUMBER OF M7455 CONTROLLERS
PRESENT TO BE TESTED>

UNIT 0

DEVICE ADDRESS (O) 172520 ? <ENTER THE ADDRESS OF THE
TSBA/TSDB REGISTER>

VECTOR (O) 224 ? <ENTER ADDRESS OF INTERRUPT
VECTOR>

THE ADDRESS AND VECTOR QUESTIONS WILL BE ASKED FOR EACH OF THE NUMBER OF UNITS (CONTROLLERS) SPECIFIED IN THE "# UNITS?" QUESTION. LOGICAL UNIT NUMBERS ARE ASSIGNED IN ORDER, BEGINNING AT 0. UP TO FOUR UNITS CAN BE SELECTED FOR TESTING AS FOLLOWS:

UP TO 4 TSU05 CONTROLLERS PER PDP-11 AND UP TO 2 DRIVES PER CONTROLLER

2.5 SOFTWARE QUESTIONS

AFTER YOU HAVE ANSWERED THE HARDWARE QUESTIONS OR AFTER A RESTART OR CONTINUE COMMAND, THE RUNTIME SERVICES WILL ASK FOR SOFTWARE PARAMETERS. THESE PARAMETERS WILL GOVERN SOME DIAGNOSTIC SPECIFIC OPERATION MODES. YOU WILL BE PROMPTED BY "CHANGE SW (L) ?" IF YOU WISH TO CHANGE ANY PARAMETERS, ANSWER BY TYPING "Y". THE SOFTWARE QUESTIONS AND THE DEFAULT VALUES ARE DESCRIBED IN THE NEXT PARAGRAPH(S).

THE FOLLOWING QUESTIONS ARE ASKED ON A START, RESTART, OR CONTINUE. THEY ALLOW FLEXIBILITY IN THE WAY THE PROGRAM BEHAVES.

CHANGE SW (L) ? <TYPE Y TO CAUSE THE FOLLOWING
QUESTIONS TO BE ASKED>

INHIBIT ITERATIONS (L) N ? <TYPE "Y" TO PREVENT MULTIPLE
ITERATIONS OF CERTAIN TESTS.
THIS CAUSES EACH TEST PASS TO
RUN AS QUICKLY AS POSSIBLE.
ONLY QUICK-RUNNING LOGIC
TESTS USE MULTIPLE
ITERATIONS.>

2.6 EXTENDED P-TABLE DIALOGUE

WHEN YOU ANSWER THE HARDWARE QUESTIONS, YOU ARE BUILDING ENTRIES IN A TABLE THAT DESCRIBES THE DEVICES UNDER TEST. THE SIMPLEST WAY TO BUILD THIS TABLE IS TO ANSWER ALL QUESTIONS FOR EACH UNIT TO BE TESTED. IF YOU HAVE A MULTIPLEXED DEVICE SUCH AS A MASS STORAGE CONTROLLER WITH SEVERAL DRIVES OR A COMMUNICATION DEVICE WITH SEVERAL LINES, THIS BECOMES TEDIOUS SINCE MOST OF THE ANSWERS ARE REPETITIOUS.

TO ILLUSTRATE A MORE EFFICIENT METHOD, SUPPOSE YOU ARE TESTING A DEVICE, THE XY11. SUPPOSE THIS DEVICE CONSISTS OF A CONTROL MODULE WITH EIGHT UNITS (SUB-DEVICES) ATTACHED TO IT. THESE UNITS ARE DESCRIBED BY THE OCTAL NUMBERS 0 THROUGH 7. THERE IS ONE HARDWARE PARAMETER THAT CAN VARY AMONG UNITS CALLED THE Q-FACTOR. THIS Q-FACTOR MAY BE 0 OR 1. BELOW IS A SIMPLE WAY TO BUILD A TABLE FOR ONE XY11 WITH EIGHT UNITS.

UNITS (D) ? 8<CR>

UNIT 1
CSR ADDRESS (0) ? 160000<CR>
SUB-DEVICE # (0) ? 0<CR>
Q-FACTOR (0) 0 ? 1<CR>

UNIT 2
CSR ADDRESS (0) ? 160000<CR>
SUB-DEVICE # (0) ? 1<CR>
Q-FACTOR (0) 1 ? 0<CR>

UNIT 3
CSR ADDRESS (0) ? 160000<CR>
SUB-DEVICE # (0) ? 2<CR>
Q-FACTOR (0) 0 ? <CR>

UNIT 4
CSR ADDRESS (0) ? 160000<CR>
SUB-DEVICE # (0) ? 3<CR>
Q-FACTOR (0) 0 ? <CR>

UNIT 5
CSR ADDRESS (0) ? 160000<CR>
SUB-DEVICE # (0) ? 4<CR>
Q-FACTOR (0) 0 ? <CR>

UNIT 6
CSR ADDRESS (0) ? 160000<CR>
SUB-DEVICE # (0) ? 5<CR>
Q-FACTOR (0) 0 ? <CR>

UNIT 7
CSR ADDRESS (0) ? 160000<CR>
SUB-DEVICE # (0) ? 6<CR>
Q-FACTOR (0) 0 ? 1<CR>

UNIT 8
CSR ADDRESS (0) 160000<CR>
SUB-DEVICE # (0) ? 7<CR>
Q-FACTOR (0) 1 ? <CR>

NOTICE THAT THE DEFAULT VALUE FOR THE Q-FACTOR CHANGES WHEN A NON-DEFAULT RESPONSE IS GIVEN. BE CAREFUL WHEN SPECIFYING MULTIPLE UNITS!

AS YOU CAN SEE FROM THE ABOVE EXAMPLE, THE HARDWARE PARAMETERS DO NOT VARY SIGNIFICANTLY FROM UNIT TO UNIT. THE PROCEDURE SHOWN IS NOT VERY EFFICIENT.

THE RUNTIME SERVICES CAN TAKE MULTIPLE UNIT SPECIFICATIONS HOWEVER. LET'S BUILD THE SAME TABLE USING THE MULTIPLE SPECIFICATION FEATURE.

```

# UNITS (0) ? 8<CR>

UNIT 1
CSR ADDRESS (0) ? 160000<CR>
SUB-DEVICE # (0) ? 0,1<CR>
Q-FACTOR (0) 0 ? 1,0<CR>

UNIT 3
CSR ADDRESS (0) ? 160000<CR>
SUB-DEVICE # (0) ? 2-5<CR>
Q-FACTOR (0) 0 ? 0<CR>

UNIT 7
CSR ADDRESS (0) ? 160000<CR>
SUB-DEVICE # (0) ? 6,7<CR>
Q-FACTOR (0) 0 ? 1<CR>

```

AS YOU CAN SEE IN THE ABOVE DIALOGUE, THE RUNTIME SERVICES WILL BUILD AS MANY ENTRIES AS IT CAN WITH THE INFORMATION GIVEN IN ANY ONE PASS THROUGH THE QUESTIONS. IN THE FIRST PASS, TWO ENTRIES ARE BUILT SINCE TWO SUB-DEVICES AND Q-FACTORS WERE SPECIFIED. THE SERVICES ASSUME THAT THE CSR ADDRESS IS 160000 FOR BOTH SINCE IT WAS SPECIFIED ONLY ONCE. IN THE SECOND PASS, FOUR ENTRIES WERE BUILT. THIS IS BECAUSE FOUR SUB-DEVICES WERE SPECIFIED. THE "-" CONSTRUCT TELLS THE RUNTIME SERVICES TO INCREMENT THE DATA FROM THE FIRST NUMBER TO THE SECOND. IN THIS CASE, SUB-DEVICES 2, 3, 4 AND 5 WERE SPECIFIED. (IF THE SUB-DEVICE WERE SPECIFIED BY ADDRESSES, THE INCREMENT WOULD BE BY 2 SINCE ADDRESSES MUST BE ON AN EVEN BOUNDARY.) THE CSR ADDRESSES AND Q-FACTORS FOR THE FOUR ENTRIES ARE ASSUMED TO BE 160000 AND 0 RESPECTIVELY SINCE THEY WERE ONLY SPECIFIED ONCE. THE LAST TWO UNITS ARE SPECIFIED IN THE THIRD PASS.

THE WHOLE PROCESS COULD HAVE BEEN ACCOMPLISHED IN ONE PASS AS SHOWN BELOW.

```

# UNITS (0) ? 8<CR>

UNIT 1
CSR ADDRESS (0) ? 160000<CR>
SUB-DEVICE # (0) ? 0-7<CR>
Q-FACTOR (0) 0 ? 0,1,0,...,1,1<CR>

```

AS YOU CAN SEE FROM THIS EXAMPLE, NULL REPLIES (COMMAS ENCLOSING

A NULL FIELD) TELL THE RUNTIME SERVICES TO REPEAT THE LAST REPLY.

2.7 QUICK START-UP PROCEDURE (XXDP*)

TO START-UP THIS PROGRAM:

1. BOOT XXDP*
2. TYPE "R NAME", WHERE NAME IS THE NAME OF THE BIN OR BIC FILE FOR THIS PROGRAM
3. TYPE "START"
4. ANSWER THE "CHANGE HW" QUESTION WITH "Y"
5. ANSWER ALL THE HARDWARE QUESTIONS
6. ANSWER THE "CHANGE SW" QUESTION WITH "N"

WHEN YOU FOLLOW THIS PROCEDURE YOU WILL BE USING ONLY THE DEFAULTS FOR FLAGS AND SOFTWARE PARAMETERS. THESE DEFAULTS ARE DESCRIBED IN SECTIONS 2.3 AND 2.5.

3.0 ERROR INFORMATION

3.1 TYPES OF ERROR MESSAGES

THERE ARE THREE LEVELS OF ERROR MESSAGES THAT MAY BE ISSUED BY A DIAGNOSTIC: GENERAL, BASIC AND EXTENDED. GENERAL ERROR MESSAGES ARE ALWAYS PRINTED UNLESS THE "IER" FLAG IS SET (SECTION 2.3). THE GENERAL ERROR MESSAGE IS OF THE FORM:

```
NAME TYPE NUMBER ON UNIT NUMBER TST NUMBER PC:XXXXXX
ERROR MESSAGE
```

.WHERE; NAME = DIAGNOSTIC NAME
 TYPE = ERROR TYPE (SYS FATAL, DEV FATAL, HARD OR SOFT)
 NUMBER = ERROR NUMBER
 UNIT NUMBER = 0 - N (N IS LAST UNIT IN PTABLE)
 TST NUMBER = TEST AND SUBTEST WHERE ERROR OCCURRED
 PC:XXXXXX = ADDRESS OF ERROR MESSAGE CALL

BASIC ERROR MESSAGES ARE MESSAGES THAT CONTAIN SOME ADDITIONAL INFORMATION ABOUT THE ERROR. THESE ARE ALWAYS PRINTED UNLESS THE "IER" OR "IBR" FLAGS ARE SET (SECTION 2.3). THESE MESSAGES ARE PRINTED AFTER THE ASSOCIATED GENERAL MESSAGE.

EXTENDED ERROR MESSAGES CONTAIN SUPPLEMENTARY ERROR INFORMATION SUCH AS REGISTER CONTENTS OR GOOD/BAD DATA. THESE ARE ALWAYS PRINTED UNLESS THE "IER", "IBR" OR "IXE" FLAGS ARE SET (SECTION 2.3). THESE MESSAGES ARE PRINTED AFTER THE ASSOCIATED GENERAL ERROR MESSAGE AND ANY ASSOCIATED BASIC ERROR MESSAGES.

3.2 SPECIFIC ERROR MESSAGES

BELOW ARE SAMPLE ERROR MESSAGES. EACH ERROR MESSAGE REPRESENTS DIFFERENT TYPES

OF ERRORS DETECTED BY THIS DIAGNOSTIC.

ERROR MESSAGE EXAMPLE 1

THIS ERROR IS INDICATIVE OF AN INCORRECT REGISTER OR STATUS WORD RETURNED TO THE DIAGNOSTIC. THE FIRST PART DEFINES THE TEST FUNCTION AND UNIT THAT FAILED. THE SECOND PART PROVIDES THE REGISTER BITS AND THEIR MNEMONICS FOR THE INCORRECT REGISTER OR STATUS WORDS. THE THIRD PART IS THE EXPECTED AND RECEIVED DATA.

TST: 016 FIFO EXERCISER TEST
CZTSA HRD ERR 01610 ON UNIT 00 TST 016 SUB 002 PC: 040624
FIFO STATUS (IN WORD 9) INCORRECT AFTER WRITE FIFO

TAPE BUS SIGNALS IN WORD #8: - DESIGNATOR <BIT #>
PARERR<15> IEOT <12> IFMK <9> IRDY<6> IRWD<2>
IRESV2<14> IIDENT<11> IHER <8> IONL<5> IFBY<1>
IRESV1<13> ICER <10> ISPEED<7> ILDP<4> IFPT<0>

TAPE BUS SIGNALS IN WORD #9:
DATMIS<7> ILW<6> OUTRDY<5> INRDY<4>

MESSAGE BUFFER ADDRESS = 047352

MESSAGE BUFFER CONTENTS:

WORD #0	EXPD: 100020	RECV: 100020	XOR: 000000
WORD #1	EXPD: 000012	RECV: 000012	XOR: 000000
WORD #2	EXPD: 000000	RECV: 000000	XOR: 000000
WORD #3	EXPD: 000010	RECV: 000010	XOR: 000000
WORD #4	EXPD: 000000	RECV: 000000	XOR: 000000
WORD #5	EXPD: 000000	RECV: 000000	XOR: 000000
WORD #6	EXPD: 000000	RECV: 000000	XOR: 000000
WORD #7	EXPD: 000000	RECV: 000000	XOR: 000000
WORD #8	EXPD: 070217	RECV: 070217	XOR: 000000
WORD #9	EXPD: 000074	RECV: 000034	XOR: 000040

ERROR MESSAGE EXAMPLE 2

THIS ERROR SHOWS A FATAL FUNCTION ERROR FROM THE TAPE DRIVE. IN THIS INSTANCE A UNRECOVERABLE ERROR OCCURED WHICH INDICATES THAT THE CONTROLLER MAY BE DEFECTIVE.

CZTSA HRD ERR 00159 ON UNIT 00 TST 001 SUB 005 PC: 026202
TSSR NOT CORRECT AFTER SPACE RECORDS COMMAND

TSSR = 100214

TSSR BITS SET: SC,SSR

TERMINATION CLASS CODE = UNRECOVERABLE ERROR

PACKET ADDRESS = 026420

PACKET WORD # = 140010

PACKET WORD # = 000010

PACKET WORD # = 000000

PACKET WORD # = 000024

ERROR MESSAGE EXAMPLE 3

THIS ERROR SHOWS THAT THE MOTION BIT DID NOT GET SET WHILE DOING A REWIND WITH EXTENDED FEATURES MODE ENABLED.

CVTS HRD ERR 00121 ON UNIT 00 TST 001 SUB 002 PC: 023306
MOT BIT (XST0) NOT SET DURING REWIND (EXTENDED FEATURES MODE)
EXPD: 000312 RECV: 000112 XOR: 000200

4.0 PERFORMANCE AND PROGRESS REPORTS

AT THE END OF EACH PASS, THE PASS COUNT IS GIVEN ALONG WITH THE TOTAL NUMBER OF ERRORS REPORTED SINCE THE DIAGNOSTIC WAS STARTED. THE "EOP" SWITCH CAN BE USED TO CONTROL HOW OFTEN THE END OF PASS MESSAGE IS PRINTED. SECTION 2.2 DESCRIBES SWITCHES.

SUCCESSFUL RUN EXAMPLE (PDP-11)

DR>STA/FLA:PNT:HOE

UNITS (D) ? 1

UNIT 0

DEVICE ADDRESS (0) 172520 ? <CR>

VECTOR (0) 224 ? <CR>

CHANGE SW (L) ? N<CR>

THE ABOVE COMMAND WILL START THE DIAGNOSTIC. THE COMMAND HAS TWO SWITCHES ON WHICH ARE "PRINT EACH TEST NBR AS EXECUTED" AND "HALT ON ERROR".

TST: 001 INITIALIZE #1
TST: 002 WRAP DATA HIGH BYTE TEST
TST: 003 WRAP DATA LOW BYTE TEST
TST: 004 RAM TEST
TST: 005 INITIALIZE 2 TEST
TST: 006 COMMAND REJECT TEST
TST: 007 WRITE CHARACTERISTICS TEST
TST: 008 VOLUME CHECK
TST: 009 COMPLETION INTERRUPT TEST
TST: 010 BASIC PACKET PROTOCOL TEST
TST: 011 NON-TAPE-MOTION COMMANDS TEST

0 ERRORS

NOTE: THE DIAGNOSTIC WILL RUN CONTINUOUSLY UNLESS A PASS NUMBER LIMIT HAS BEEN SPECIFIED WITH THE "/PASS:" SWITCH.

PROGRAM RUN TIMES

THE AVERAGE RUN TIMES OF THE PROGRAM ARE LISTED BELOW. THESE FIGURES ARE TO BE USED AS A GUIDE. THE TIMING WAS DONE ON A PDP-11

PROCESSOR WITH A LA34 CONSOLE.

THE PROGRAM RUNS IN TWO MODES; NO ITERATIONS AND DEFAULT MODE. IN THE NO ITERATIONS MODE, EACH TEST IS RUN ONCE, WITH NO ITERATIONS. IN THE DEFAULT MODE EACH TEST IS REPEATED BY THE NUMBER OF TIMES INDICATED BY THE ITERATION COUNT. NO ITERATIONS MODE IS SELECTED BY ANSWERING THE INHIBIT ITERATIONS QUESTION WITH A "Y" (YES).

TEST NUMBER	N/I SECS.	ITER SECS	DEF SECS.
1	1	30	29
2	1	10	9
3	1	8	7
4	25	120	95
5	5	140	135
6	25	475	450
7	20	20	0
8	1	10	9
9	20	20	0
10	1	2	1
11	8	11	3

THE TIMES REQUIRED TO RUN TESTS 1 THROUGH 12 IN ONE COMMAND:

Q.V.	1 MIN 57 SECONDS
DEFAULT	12 MINS

5.0 DEVICE INFORMATION TABLES

WHENEVER THE PROGRAM IS STARTED, VIA THE STA(RT) COMMAND, THE SUPERVISOR REQUESTS THE FOLLOWING P-TABLES PARAMETER CHANGES:

CHANGE HW (L) ?

UNITS (D) ? <ENTER THE NUMBER OF M7455 CONTROLLERS
PRESENT TO BE TESTED>

UNIT 0

DEVICE ADDRESS (O) 172520 ? <ENTER THE ADDRESS OF THE
TSBA/TSDB REGISTER>

VECTOR (O) 224 ? <ENTER ADDRESS OF INTERRUPT
VECTOR>

THE ADDRESS AND VECTOR QUESTIONS WILL BE ASKED FOR EACH OF THE NUMBER OF UNITS (CONTROLLERS) SPECIFIED IN THE "# UNITS?" QUESTION. LOGICAL UNIT NUMBERS ARE ASSIGNED IN ORDER, BEGINNING AT 0. UP TO FOUR UNITS CAN BE SELECTED FOR TESTING.

IN ADDITION, ON A START, RESTART OR CONTINUE THE SUPERVISOR REQUESTS CHANGES TO THE SOFTWARE OPERATING PARAMETERS, AS

FOLLOWS:

CHANGE SW (L) ?

INHIBIT ITERATIONS (L) N ?

6.0 TEST SUMMARIES

TEST 1: BUS RESET TEST

THIS TEST VERIFIES THAT THE M7455 MODULE'S DEVICE REGISTERS ARE ACCESSIBLE ON THE BUS (SUBTEST 1) AND THEN CHECKS THAT THE BUILT-IN INITIALIZATION SELF-TEST MICRODIAGNOSTIC DID NOT FIND ANY BASIC PROBLEMS IN THE MODULE. AREAS OF LOGIC TESTED BY THE SELF-TEST SEQUENCE ARE AS FOLLOWS: ROM AND PIPELINE REGISTER, SEQUENCER, INTERNAL BUSES, 2901 MICROPROCESSOR, AND, RAM. THIS TEST INITIALIZES THE CONTROLLER BY ISSUING THE BUS INIT SIGNAL VIA A RESET INSTRUCTION, OR BY WRITING INTO THE TSSR REGISTER, WAITS A PERIOD OF TIME (TO ALLOW THE CONTROLLER'S INITIALIZATION MICRODIAGNOSTIC SEQUENCE TO BE COMPLETED), AND THEN CHECKS THE CONTENTS OF THE TSSR REGISTER. SUCCESSFUL INITIALIZATION IS INDICATED BY SUBSYSTEM READY (SSR) AND NEED BUFFER ADDRESS (NBA) BITS BEING SET (1) AND ALL OTHER BITS (EXCEPT A17 AND A16 AND OFL, WHICH ARE IGNORED FOR THIS TEST) BEING CLEAR (0). IF THE CONTENTS OF TSSR ARE NOT AS EXPECTED, AN ERROR REPORT IS ISSUED LISTING THE EXPECTED DATA, ACTUAL DATA, AND THE DISCREPANCIES. THE ERROR REPORT ANALYZES THE TSSR CONTENTS AND DISCERNS AND REPORTS ONE OF THREE POSSIBILITIES:

1. TSSR CONTENTS ARE AMBIGUOUS (ANY OF BITS 11-14 ARE SET, OR STATES OF SSR AND SC BITS DO NOT CORRESPOND TO THE APPARENT ERROR CODE IN BITS 0-5): INDICATES THAT THE TSSR CONTENT CANNOT BE TRUSTED. INDICATES A CATASTROPHIC CONTROLLER MALFUNCTION. THIS IS A FATAL ERROR (EXECUTION IS ABORTED). FIELD ACTION WOULD BE TO REPLACE THE M7455. IF THE M7455 ITSELF IS BEING DEBUGGED, THE PROGRAM SHOULD BE RESTARTED WITH LOOP ON ERROR ENABLED IN ORDER TO PROBE FOR THE PROBLEM.
2. SSR = 0, SC = 0 AND THE ERROR CODE IN BITS 0-5 IS IN THE RANGE 17-13: THIS IS A FATAL ERROR. THE ERROR CODE IS DECODED AND THE APPROPRIATE DESCRIPTION GIVEN. INDICATES THAT A SERIOUS PROBLEM EXISTS.

TEST 2: WRAP DATA - HIGH BYTE

THIS TEST VERIFIES OPERATION OF:

1. PART OF THE PDP-11 BUS INTERFACE SECTION OF THE M7455 MODULE: PART OF THE INPUT FILE (TSD: HIGH BYTE), PART

OF THE OUTPUT FILE (TSSR HIGH BYTE AND TSBA, BOTH BYTES), PART OF THE DCO05 TRANSCEIVER CIRCUITS (ADDRESS DECODER, BDAL DRIVERS, HIGH BYTE OF INTERNAL DAL BUS DRIVERS), AND BASIC PROGRAMMED I/O CONTROL SEQUENCES AND LOGIC;

2. PART OF 2901 MICROPROCESSOR ELEMENTS (Q-REGISTER, REGISTER 0, ROTATE AND NEGATE FUNCTIONS
3. Y AND SOURCE BUSES;
4. BASIC MICROPROGRAM SEQUENCES.

THE PROGRAM WRITES A TEST DATA BYTE INTO THE HIGH BYTE OF TSDB, WAITS FOR THE SSR BIT IN TSSR TO SET, THEN CHECKS THE CONTENTS OF BOTH TSBA AND TSSR. THE MODULE IS FUNCTIONING CORRECTLY IF DATA WRITTEN APPEARS IN BOTH BYTES OF TSBA AND THE FINAL CONTENT OF TSSR IS CORRECT (SAME AS AFTER INITIALIZATION EXCEPT FOR BITS 8 AND 9, WHICH SHOULD CONTAIN BITS 8 AND 9 OF THE DATA PATTERN WRITTEN. AN ERROR IS REPORTED AND A DESCRIPTIVE ANALYSIS GIVEN IF A DISCREPANCY IN TSBA OR TSSR IS DETECTED. THE ANALYSIS LISTS LIKELY FAULTY CANDIDATES FROM THE LOGIC ELEMENTS LISTED ABOVE. THE TEST IS REPEATED FOR ALL COMBINATIONS OF TEST DATA BYTES (0-377 OCTAL).

TEST 3: WRAP DATA - LOW BYTE

THIS TEST FURTHER VERIFIES OPERATION OF MANY OF THE SAME ELEMENTS TESTED IN TEST 2, AND ADDITIONALLY VERIFIES:

1. LOW BYTE OF THE TSDB INPUT FILE REGISTER,
2. LOW BYTE OF INTERNAL DAL BUS DRIVERS ON THE DCO05 TRANSCEIVER CIRCUITS,
3. BASIC FUNCTIONING OF PARTS OF THE RAM.

THE PROGRAM WRITES A TEST DATA BYTE INTO THE LOW BYTE OF TSDB, WAITS FOR THE SSR BIT IN TSSR TO SET, THEN CHECKS THE CONTENTS OF BOTH TSBA AND TSSR. THE MODULE IS FUNCTIONING CORRECTLY IF DATA WRITTEN APPEARS IN BOTH BYTES OF TSBA AND THE FINAL CONTENT OF TSSR IS CORRECT (SAME AS AFTER INITIALIZATION EXCEPT FOR BITS 8 AND 9, WHICH SHOULD CONTAIN BITS 8 AND 9 OF THE DATA PATTERN WRITTEN. AN ERROR IS REPORTED AND A DESCRIPTIVE ANALYSIS GIVEN IF A DISCREPANCY IN TSBA OR TSSR IS DETECTED. THE ANALYSIS LISTS LIKELY FAULTY CANDIDATES FROM THE LOGIC ELEMENTS LISTED ABOVE. THE TEST IS REPEATED FOR ALL COMBINATIONS OF TEST DATA BYTES (0-377 OCTAL).

TEST 4: RAM TEST

THIS TEST VERIFIES THAT ALL LOCATIONS OF THE RAM ON THE M7455 CAN PROPERLY STORE AND READ BACK ALL DATA PATTERNS, AND THAT EACH RAM LOCATION IS UNIQUELY ADDRESSED (I.E., THAT ONE AND ONLY ONE LOCATION IS ACCESSED BY ANY PARTICULAR ADDRESS). THE BYPRODUCT OF THESE TESTS IS A VERIFICATION OF TWO REGISTERS IN THE 2901 AND THE CAPABILITY OF THE 2901 TO CORRECTLY PERFORM AN ADD.

TEST 5: SECOND INITIALIZATION TEST

THIS TEST VERIFIES THE SAME ELEMENTS AS DID INITIALIZATION TEST #1 AND ALSO CHECKS THAT CERTAIN PARTS OF RAM IS CLEARED TO ZERO AND THAT 2901 REGISTERS 10 AND 11 ARE ALSO CLEARED TO ZERO. THIS IS A CONFIDENCE CHECK OF A PART OF THE SELF-TEST SEQUENCE (I.E., THAT IT IS REALLY BEING EXECUTED). FOR EACH OF TWO SUBTESTS (ONE FOR INITIALIZING VIA A BUS INIT, THE OTHER FOR INITIALIZING BY WRITING INTO THE TSSR), THE FOLLOWING SEQUENCE IS PERFORMED:

1. EACH RAM LOCATION AND 2901 REGISTERS 10 AND 11 ARE SET TO ALL 1'S BY USING WRITES INTO THE TSDB REGISTER (LOW BYTE AND MAINTENANCE MODE WORD WRITES).
2. THE CONTROLLER IS INITIALIZED AND THE VARIOUS CHECKS ON THE TSSR DESCRIBED IN INITIALIZATION TEST #1 ARE PERFORMED.
3. #1'S (377 OCTAL) ARE WRITTEN INTO THE LOW BYTE OF TSDB, WHICH SHOULD CAUSE RAM LOCATION 0 TO BE WRITTEN TO ALL 1'S SINCE 2901 REGISTERS 10 AND 11, SPECIFYING THE RAM ADDRESS, SHOULD BE 0. RAM LOCATION 0 IS VERIFIED BY WRITING A WORD OF ZEROS INTO THE TSDB. THE RESULTING LOW BYTE OF TSBA SHOULD CONTAIN ALL 1'S.
4. THE ENTIRE RAM IS SCANNED. LOCATION 0 SHOULD CONTAIN ALL 1'S AND THE REMAINING LOCATIONS, EXCEPT FOR THE MESSAGE BUFFER IMAGE AREA, SHOULD CONTAIN 0. DISCREPANCIES ARE REPORTED. AN ERROR AT THIS POINT IS MOST LIKELY DUE TO A ROM, PIPELINE OR SEQUENCER PROBLEM OR A TIMING PROBLEM.

TEST 6: COMMAND REJECT

THIS TEST VERIFIES THAT ALL COMMANDS OTHER THAN WRITE CHARACTERISTICS ARE REJECTED DUE TO THE NEED BUFFER ADDRESS (NBA) BIT BEING SET IN TSSR, AND THAT THE TSBA AND TSSR REGISTERS ARE LEFT IN THE PROPER STATE AFTER EACH COMMAND IS REJECTED. THIS TEST CHECKS MICROPROCESSOR SEQUENCING, BASIC COMMAND DECODING AND DATA DMA HANDLING. THIS TEST CONTAINS TWO SUBTESTS: SUBTEST 1 SEQUENCES THROUGH ALL COMMAND WORDS (OTHER THAN WRITE CHARACTERISTICS) WITH THE INTERRUPT ENABLE (IE) BIT CLEAR AND VERIFIES THAT AN INTERRUPT IS NOT GENERATED BY THE

REJECTED COMMAND; SUBTEST 2 PERFORMS SIMILARLY TO SUBTEST 1 BUT SETS THE IE BIT IN EACH COMMAND WORD AND VERIFIES THAT AN INTERRUPT IS GENERATED WHEN THE COMMAND IS REJECTED.

TEST 7: WRITE CHARACTERISTICS

THIS TEST VERIFIES BASIC OPERATION OF THE WRITE CHARACTERISTICS COMMAND. IT VERIFIES THAT THE COMMAND BLOCK AND CHARACTERISTICS DATA BLOCK ARE FETCHED PROPERLY FROM CPU MEMORY, THE NEED BUFFER ADDRESS (NBA) BIT IN TSSR IS HANDLED PROPERLY, AND THAT A PROPER MESSAGE PACKET IS STORED, WHERE APPROPRIATE. THIS TEST DOES NOT CHECK THAT THE VARIOUS FUNCTIONS ENABLED BY CHARACTERISTIC MODE DATA BITS OPERATE PROPERLY; THE FUNCTIONING OF THESE BITS IS VERIFIED IN SUBSEQUENT TESTS. ALL COMMANDS EXECUTED IN THIS TEST HAVE THE INTERRUPT ENABLE (IE) BIT CLEARED TO ZERO, SO NO INTERRUPTS SHOULD BE GENERATED. HOWEVER, THE PROGRAM RUNS AT PROCESSOR PRIORITY 0, WITH THE INTERRUPT SERVICE ROUTINE SET UP TO FLAG UNEXPECTED INTERRUPTS. IF AN INTERRUPT OCCURS, A PROBLEM EXISTS IN EITHER THE PDP-11 BUS INTERFACE SECTION OR IN THE ROM OR PIPELINE.

TEST 8: VOLUME CHECK

THIS TEST VERIFIES THAT THE VOLUME CHECK (VCK) BIT, A FLAG HELD WITHIN THE M7455 AND APPEARING IN XSTO, IS SET BY INITIALIZE AND CLEARED BY EXECUTING A WRITE CHARACTERISTICS COMMAND WITH THE CVC BIT SET. IT IS ALSO VERIFIED THAT A WRITE CHARACTERISTICS COMMAND WITH THE CVC BIT CLEAR DOES NOT AFFECT THE STATE OF THE VOLUME CHECK BIT. THE ACTUAL FUNCTION OF VOLUME CHECK, THAT OF PREVENTING OR ALLOWING A TAPE MOTION COMMAND DEPENDING UPON WHETHER VOLUME CHECK IS SET OR CLEAR, IS NOT CHECKED BY THIS TEST; THIS FUNCTIONALITY IS CHECKED IN THE INDIVIDUAL TESTS OF TAPE MOTION COMMANDS.

TEST 9: COMPLETION INTERRUPT

THIS TEST VERIFIES THAT AN INTERRUPT IS GENERATED AT THE COMPLETION OF THE WRITE CHARACTERISTICS COMMAND IF THE INTERRUPT ENABLE (IE) BIT IN THE COMMAND HEADER WORD IS SET. THIS TEST CHECKS THE FUNCTIONING OF THE INTERRUPT LOGIC AND BASIC PROCESSING OF THE IE BIT.

THE SEQUENCES OF TEST 7 ARE REPEATED, EXCEPT THAT THE INTERRUPT SERVICE ROUTINE IS SET UP TO EXPECT INTERRUPTS AND EACH WRITE CHARACTERISTICS COMMAND IS ISSUED WITH THE IE BIT SET (1). IT IS VERIFIED, WHERE APPROPRIATE, THAT THE IE STATUS BIT IN XSTO OF ANY MESSAGE PACKET IS SET AND THAT A COMPLETION INTERRUPT IS GENERATED. FINALLY, A SEQUENCE OF TWO COMMANDS ARE ISSUED, THE FIRST WITH IE=1 AND THE SECOND WITH IE=0. IT IS VERIFIED THAT NO INTERRUPT IS GENERATED AFTER THE SECOND COMMAND AND THAT THE IE BIT IN XSTO IS 0.

TEST 10: BASIC PACKET PROTOCOL

THIS TEST VERIFIES BASIC OPERATION OF THE MESSAGE BUFFER RELEASE COMMAND, THE FUNCTION OF THE ACK BIT IN THE COMMAND HEADER WORD.

AND THE REGISTER MODIFICATION REFUSED (RMR) LOGIC.

TEST 11: NON-TAPE MOTION COMMANDS

THIS TEST VERIFIES PROPER OPERATION OF THE INITIALIZE COMMAND. TWO SUBTESTS ARE USED. THE FIRST VERIFIES THAT THE COMMAND RUNS TO COMPLETION AND STORES A VALID MESSAGE PACKET. THE SECOND VERIFIES THAT NON-ZERO VALUES IN THE COMMAND MODE FIELD CAUSES COMMAND REJECT.

7.0 MAINTENANCE HISTORY

REVISION A - MARCH 1982

```

1
2
3
4
5 000000
6
12
13 000000
14
15
21
22 002000'
23
24 002000
002000
25
26
27
28
29
30
31
32 002000
33 002000
002000
002000 103
002001 132
002002 124
002003 123
002004 101
002005 000
002006 000
002007 000
002010
002010 101
002011
002011 060
002012
002012 000000
002014
002014 001217
002016
002016 045444'
002020
002020 045576'
002022
002022 002154'
002024
002024 002164'
002026
002026 045672'
002030
002030 000000
002032
002032 000000
002034
002034 000000

.TITLE TSV2 - PROGRAM HEADER
.SBTTL PROGRAM HEADER
.PSECT ABS

.MCALL SVC
SVC ; INITIALIZE SUPERVISOR MACROS
.ENABLE LC
.NLIST BEX,CND
.ENABL AMA
.=.+2000
.=2000
BGNMOD TSV2

TSV2::

; **
; THE PROGRAM HEADER IS THE INTERFACE BETWEEN
; THE DIAGNOSTIC PROGRAM AND THE SUPERVISOR.
; --

POINTER BGNSW,BGNSFT,BGNAU,BGNDU,BGNRPT
HEADER CZTSA,A,0,655.,0
L$NAME:: ;DIAGNOSTIC NAME
.ASCII /C/
.ASCII /Z/
.ASCII /T/
.ASCII /S/
.ASCII /A/
.BYTE 0
.BYTE 0
.BYTE 0

L$REV:: ;REVISION LEVEL
.ASCII /A/

L$DEPO:: ;0
.ASCII /0/

L$UNIT:: ;NUMBER OF UNITS
.WORD 0

L$TIML:: ;LONGEST TEST TIME
.WORD 655.

L$HPCP:: ;POINTER TO H.W. QUES.
.WORD L$HARD

L$SPCP:: ;POINTER TO S.W. QUES.
.WORD L$SOFT

L$HPTP:: ;PTR. TO DEF. H.W. PTABLE
.WORD L$HW

L$SPTP:: ;PTR. TO S.W. PTABLE
.WORD L$SW

L$LADP:: ;DIAG. END ADDRESS
.WORD L$LAST

L$STA:: ;RESERVED FOR APT STATS
.WORD 0

L$CO::
.WORD 0

L$DTYP:: ;DIAGNOSTIC TYPE
.WORD 0

```

002036		L\$APT::		;APT EXPANSION
002036	000000	.WORD	0	
002040		L\$DTP::		;PTR. TO DISPATCH TABLE
002040	002124	.WORD	L\$DISPATCH	
002042		L\$PRIO::		;DIAGNOSTIC RUN PRIORITY
002042	000000	.WORD	0	
002044		L\$ENVI::		;FLAGS DESCRIBE HOW IT WAS SETUP
002044	000000	.WORD	0	
002046		L\$EXP1::		;EXPANSION WORD
002046	000000	.WORD	0	
002050		L\$MREV::		;SVC REV AND EDIT #
002050	003	.BYTE	C\$REVISION	
002051	003	.BYTE	C\$EDIT	
002052		L\$EF::		;DIAG. EVENT FLAGS
002052	000000	.WORD	0	
002054	000000	.WORD	0	
002056		L\$SPC::		
002056	000000	.WORD	0	
002060		L\$DEVP::		; POINTER TO DEVICE TYPE LIST
002060	003400	.WORD	L\$DVTYP	
002062		L\$REPP::		;PTR. TO REPORT CODE
002062	022434	.WORD	L\$RPT	
002064		L\$EXP4::		
002064	000000	.WORD	0	
002066		L\$EXP5::		
002066	000000	.WORD	0	
002070		L\$AUT::		;PTR. TO ADD UNIT CODE
002070	022122	.WORD	L\$AU	
002072		L\$DUT::		;PTR. TO DROP UNIT CODE
002072	022220	.WORD	L\$DU	
002074		L\$LUN::		;LUN FOR EXERCISERS TO FILL
002074	000000	.WORD	0	
002076		L\$DESP::		;PTR. TO DIAG. DESCRIPTION
002076	003406	.WORD	L\$DESC	
002100		L\$LOAD::		;GENERATE SPECIAL AUTOLOAD EMT
002100	104035	EMT	E\$LOAD	
002102		L\$ETP::		;PTR. TO ERR TBL
002102	000000	.WORD	0	
002104		L\$ICP::		;PTR. TO INIT CODE
002104	021326	.WORD	L\$INIT	
002106		L\$CCP::		;PTR. TO CLEAN-UP CODE
002106	022406	.WORD	L\$CLEAN	
002110		L\$ACP::		;PTR. TO AUTO CODE
002110	022326	.WORD	L\$AUTO	
002112		L\$PRT::		;PTR. TO PROTECT TABLE
002112	021316	.WORD	L\$PROT	
002114		L\$TEST::		;TEST NUMBER
002114	000000	.WORD	0	
002116		L\$DLY::		;DELAY COUNT
002116	000000	.WORD	0	
002120		L\$HIME::		;PTR. TO HIGH MEM
002120	000000	.WORD	0	

.SBTTL DISPATCH TABLE

34
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39
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42
43
44 002122          DISPATCH 11
    002122 000013  .WORD 11
    002124          L$DISPATCH::
    002124 023216'  .WORD T1
    002126 023436'  .WORD T2
    002130 024134'  .WORD T3
    002132 024626'  .WORD T4
    002134 026162'  .WORD T5
    002136 027266'  .WORD T6
    002140 030544'  .WORD T7
    002142 034132'  .WORD T8
    002144 035036'  .WORD T9
    002146 040162'  .WORD T10
    002150 043274'  .WORD T11

45
46
47
48          .SBTTL  DEFAULT HARDWARE P-TABLE
49
50
51
52
53
54
55 002152          ;***
    002152 000003  ; THE DEFAULT HARDWARE P-TABLE CONTAINS DEFAULT VALUES OF
    002154          ; THE TEST-DEVICE PARAMETERS.  THE STRUCTURE OF THIS TABLE
    002154          ; IS IDENTICAL TO THE STRUCTURE OF THE RUN-TIME P-TABLE.
                    ; --
                    BGNHW  DFPTBL          ;DEFAULT HARD-P-TABLE
                    .WORD  L10000-L$HW/2
    L$HW::
    DFPTBL::
56
57 002154 172520   .WORD 172520          ; 1ST (OF 2) REGISTERS.
58 002156 000224   .WORD 224            ; INTERRUPT VECTOR
59 002160 000200   .WORD PRI04         ; INTERRUPT PRIORITY.
60 002162          ENDPHW
    002162          L10000:

61
62
63          .SBTTL  SOFTWARE P-TABLE
64
65
66
67
68
69 002162          ;***
    002162 000004  ; THE SOFTWARE P-TABLE CONTAINS THE VALUES OF THE PROGRAM
    002164          ; PARAMETERS THAT CAN BE CHANGED BY THE OPERATOR.
    002164          ; --
                    BGNSW  SFPTBL
                    .WORD  L10001-L$SW/2
    L$SW::
    SFPTBL::
70
71 002164 000000  TRANSTST:: .WORD 0          ; ENABLE TEST OF TRANSPORT(S) IF =1
72 002166 000000  NOITS:: .WORD 0          ; INHIBIT ITERATION OPTION.
73          ; ... 0 = ITERATE.
74          ; ...NZ = INHIBIT ITERATE.
75 002170 000017  LERRMAX:: .WORD 15.      ; LOCAL (PER TEST) ERROR LIMIT

```

J2

TSV2 - PROGRAM HEADER MACRO M1113 07-FEB-84 10:58
SOFTWARE P-TABLE

SEQ 022

76	002172	000310	GERRMAX::	.WORD	200.	; GLOBAL (PER UNIT) ERROR LIMIT
77	002174		ENDSW			
	002174		L10001:			
78						
79	002174		ENDMOD			
80						
81						
84						
85						

7
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13
19
20 002174
002174
21
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33 002174

.TITLE TSV3 - GLOBAL AREAS
.SBTTL GLOBAL EQUATES SECTION

BGNMOD TSV3
TSV3::

.SBTTL GLOBAL EQUATES SECTION

;++
; THE GLOBAL EQUATES SECTION CONTAINS PROGRAM EQUATES THAT
; ARE USED IN MORE THAN ONE TEST.
;--

EQUALS ; GET STANDARD EQUATES.

; BIT DIFINITIONS

100000	BIT15== 100000
040000	BIT14== 40000
020000	BIT13== 20000
010000	BIT12== 10000
004000	BIT11== 4000
002000	BIT10== 2000
001000	BIT09== 1000
000400	BIT08== 400
000200	BIT07== 200
000100	BIT06== 100
000040	BIT05== 40
000020	BIT04== 20
000010	BIT03== 10
000004	BIT02== 4
000002	BIT01== 2
000001	BIT00== 1

001000	BIT9== BIT09
000400	BIT8== BIT08
000200	BIT7== BIT07
000100	BIT6== BIT06
000040	BIT5== BIT05
000020	BIT4== BIT04
000010	BIT3== BIT03
000004	BIT2== BIT02
000002	BIT1== BIT01
000001	BIT0== BIT00

; EVENT FLAG DEFINITIONS
; EF32:EF17 RESERVED FOR SUPERVISOR TO PROGRAM COMMUNICATION

000040	EF.START== 32.	; START COMMAND WAS ISSUED
000037	EF.RESTART== 31.	; RESTART COMMAND WAS ISSUED
000036	EF.CONTINUE== 30.	; CONTINUE COMMAND WAS ISSUED
000035	EF.NEW== 29.	; A NEW PASS HAS BEEN STARTED
000034	EF.PWR== 28.	; A POWER-FAIL/POWER-UP OCCURRED

; ;


```

; PRIORITY LEVEL DEFINITIONS
;
000340 PRI07== 340
000300 PRI06== 300
000240 PRI05== 240
000200 PRI04== 200
000140 PRI03== 140
000100 PRI02== 100
000040 PRI01== 40
000000 PRI00== 0

```

```

; OPERATOR FLAG BITS
;
000004 EVL== 4
000010 LOT== 10
000020 ADR== 20
000040 IDU== 40
000100 ISR== 100
000200 UAM== 200
000400 BOE== 400
001000 PNT== 1000
002000 PRI== 2000
004000 IXE== 4000
010000 IBE== 10000
020000 IER== 20000
040000 LOE== 40000
100000 HOE== 100000

```

34
35 002174

```

KT11
.SBTTL MEMORY MANAGEMENT DEFINITIONS ;DEFINE MEMORY MANAGEMENT REGISTERS
;*KT11 VECTOR ADDRESS
MMVEC= 250
;*KT11 STATUS REGISTER ADDRESSES
SR0= 177572
SR1= 177574
SR2= 177576
SR3= 172516
.IF NB
;*USER "I" PAGE DESCRIPTOR REGISTERS
UIPDR0= 177600
UIPDR1= 177602
UIPDR2= 177604
UIPDR3= 177606
UIPDR4= 177610
UIPDR5= 177612
UIPDR6= 177614
UIPDR7= 177616
.IF NB
;*USER "D" PAGE DESCRIPTOR REGISTORS
UDPDR0= 177620
UDPDR1= 177622
UDPDR2= 177624
UDPDR3= 177626
UDPDR4= 177630
UDPDR5= 177632
UDPDR6= 177634
UDPDR7= 177636

```

000250
177572
177574
177576
172516

```
.ENDC
;*USER "I" PAGE ADDRESS REGISTERS
UIPAR0= 177640
UIPAR1= 177642
UIPAR2= 177644
UIPAR3= 177646
UIPAR4= 177650
UIPAR5= 177652
UIPAR6= 177654
UIPAR7= 177656
.IF NB
;*USER "D" PAGE ADDRESS REGISTERS
UDPAR0= 177660
UDPAR1= 177662
UDPAR2= 177664
UDPAR3= 177666
UDPAR4= 177670
UDPAR5= 177672
UDPAR6= 177674
UDPAR7= 177676
.ENDC
.ENDC
.IF NB
;*SUPERVISOR "I" PAGE DESCRIPTOR REGISTERS
SIPDR0= 172200
SIPDR1= 172202
SIPDR2= 172204
SIPDR3= 172206
SIPDR4= 172210
SIPDR5= 172212
SIPDR6= 172214
SIPDR7= 172216
.IF NB
;*SUPERVISOR "D" PAGE DESCRIPTOR REGISTERS
SDPDR0= 172220
SDPDR1= 172222
SDPDR2= 172224
SDPDR3= 172226
SDPDR4= 172230
SDPDR5= 172232
SDPDR6= 172234
SDPDR7= 172236
.ENDC
;*SUPERVISOR "I" PAGE ADDRESS REGISTERS
SIPAR0= 172240
SIPAR1= 172242
SIPAR2= 172244
SIPAR3= 172246
SIPAR4= 172250
SIPAR5= 172252
SIPAR6= 172254
SIPAR7= 172256
.IF NB
;*SUPERVISOR "D" PAGE ADDRESS REGISTERS
SDPAR0= 172260
SDPAR1= 172262
SDPAR2= 172264
```

172300 SDPAR3= 172266
172302 SDPAR4= 172270
172304 SDPAR5= 172272
172306 SDPAR6= 172274
172310 SDPAR7= 172276
172312 .ENDC
172314 .ENDC
172316 ;*KERNEL "I" PAGE DESCRIPTOR REGISTERS
KIPDR0= 172300
KIPDR1= 172302
KIPDR2= 172304
KIPDR3= 172306
KIPDR4= 172310
KIPDR5= 172312
KIPDR6= 172314
KIPDR7= 172316

.IF NB
;*KERNEL "D" PAGE
DESCRIPTOR REGISTERS
KDPDR0= 172320
KDPDR1= 172322
KDPDR2= 172324
KDPDR3= 172326
KDPDR4= 172330
KDPDR5= 172332
KDPDR6= 172334
KDPDR7= 172336
.ENDC

172340 ;*KERNEL "I" PAGE ADDRESS REGISTERS
172342 KIPAR0= 172340
172344 KIPAR1= 172342
172346 KIPAR2= 172344
172350 KIPAR3= 172346
172352 KIPAR4= 172350
172354 KIPAR5= 172352
172356 KIPAR6= 172354
KIPAR7= 172356

.IF NB
;*KERNEL "D" PAGE ADDRESS REGISTERS
KDPAR0= 172360
KDPAR1= 172362
KDPAR2= 172364
KDPAR3= 172366
KDPAR4= 172370
KDPAR5= 172372
KDPAR6= 172374
KDPAR7= 172376
.ENDC

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000004

.SBTTL TSU05 REGISTER AND PACKET DEFINITIONS

;
; SOME GENERAL EQUATES.
;

ERRVEC== 4 ; POINTER TO ERROR VECTOR FOR BUS TIME OUT.

TSU05 REGISTER AND PACKET DEFINITIONS

```

48      000060      TTIVEC==      60      ; INTERRUPT VECTOR FOR CONSOLE INPUT
49      177560      TTICSR==      177560    ; BUS ADDRESS OF CONSOLE INPUT
50      177562      TTIBFR==      177562    ; CONSOLE INPUT DATA BUFFER
51      177520      BDVPCR==      177520    ; BDV11 PAGE CONTROL REGISTER
52
53      ;*
54      ;BIT DEFINITIONS FOR TSSR REGISTER
55      ;-
56
57      100000      SC=      BIT15      ;SPECIAL CONDITION
58      040000      BIE=      BIT14      ;BUS INTERFACE ERROR
59      020000      SCE=      BIT13      ;SANITY CHECK ERROR
60      010000      RMR=      BIT12      ;MODIFICATION REFUSED
61      004000      NXM=      BIT11      ;NONEXISTANT MEMORY ERROR
62      002000      NBA=      BIT10      ;NEED BUFFER ADDRESS
63      001400      MIADDR= BIT9!BIT8    ;EXTENDED ADDRESS BITS
64      000200      SSR=      BIT7      ;SUB SYSTEM READY
65      000100      OFL=      BIT6      ;OFF LINE BIT
66      000060      FATERR= BIT4!BIT5    ;FATAL TERMINATION ERROR CODES
67      000016      TERCLS= BIT3!BIT2!BIT1 ;TERMINATION CODES
68
69
70      ;*
71      ;
72      ;BIT DEFINITIONS FOR EXTENDED STATUS REGISTER 0
73      ;(XST0)
74      ;
75      ;-
76
77      100000      XSOTMK= BIT15      ;TAPE MARK DETECTED
78      040000      XSORLS= BIT14      ;RECORD LENGTH SHORT
79      020000      XSOLET= BIT13      ;LOGICAL END OF TAPE
80      010000      XSORLL= BIT12      ;RECORD LENGTH LONG
81      004000      XSOWLE= BIT11      ;WRITE LOCK ERROR
82      002000      XSONEF= BIT10      ;NON EXECUTABLE FUNCTION
83      001000      XSOILC= BIT9      ;ILLEGAL COMMAND
84      000400      XSOILA= BIT8      ;ILLEGAL ADDRESS
85      000200      XSOMOT= BIT7      ;TAPE IN MOTION
86      000100      XSOONL= BIT6      ;TRANSPORT ON LINE
87      000040      XSOIE=  BIT5      ;INTERRUPT ENABLE
88      000020      XSOVCK= BIT4      ;VOLUME CHECK BIT
89      000010      XSOPED= BIT3      ;PHASE ENCODED DRIVE
90      000004      XSOWLK= BIT2      ;WRITE LOCKED
91      000002      XSOBOT= BIT1      ;BEGINNING OF TAPE
92      000001      XSOEOT= BIT0      ;END OF TAPE
93
94
95      ;*
96      ;BIT DEFINITIONS FOR EXTENDED STATUS REGISTER 1
97      ;(XST1)
98      ;-
99      100000      X1.DLT = BIT15      ;DATA LATE
100     040000      X1.SPARE= BIT14      ;NOT USED
101     020000      X1.COR = BIT13      ;CORRECTABLE DATA ERROR
102     017375      X1.MBZ = BIT12,BIT11,BIT10,BIT9,BIT7,BIT6,BIT5,BIT4,BIT3,BIT2,BIT0 ;ALWAYS 0
103     000400      X1.RBP = BIT8      ;READ BUS PARITY ERROR
104     000002      X1.UNC = BIT1      ;UNCORRECTABLE DATA OR HARD ERROR

```

```

105
106
107      ;*
108      ;BIT DEFINITIONS FOR EXTENDED STATUS REGISTER 2
109      ;(XST2)
110      ;-
110      100000 X2.OPM = BIT15      ;OPERATION IN PROGRESS (TAPE MOVING)
111      040000 X2.RCE = BIT14      ;RAM CHECKSUM ERROR
112      035400 X2.SPARE= BIT13·BIT12·BIT11·BIT9·BIT8 ;NOT USED BY TSU05 (ALWAYS=0)
113      002000 X2.WCF = BIT10      ;WRITE CLOCK FAILURE (FIFO NOT EMPTIED BY TRANSPORT)
114      000200 X2.EXTF = BIT7      ;IF WRITE CHAR CMD THEN = EXTENDED FEATURES ENABLED
115      000100 X2.BUFE = BIT6      ;IF WRITE CHAR CMD THEN = BUFFERING ENABLED
116      000077 X2.REV = 000077    ;IF WRITE CHAR CMD THEN = MICROCODE REVISION LEVEL
117      000007 X2.UNIT = BIT2·BIT1·BIT0 ;IF GET STATUS THEN = CURRENTLY SELECTED UNIT NO.
118
119      ;*
120      ;BIT DEFINITIONS FOR EXTENDED STATUS REGISTER 3
121      ;(XST3)
122      ;-
123      177400 X3.MDE = 177400    ;MICRO-DIAGNOSTIC ERROR CODE
124      000200 X3.SPARE= BIT7      ;NOT USED BY TSU05
125      000100 X3.OPI = BIT6      ;OPERATION INCOMPLETE
126      000040 X3.REV = BIT5      ;REVERSE
127      000020 X3.TRF = BIT4      ;TRANSPORT RESPONSE FAILURE
128      000010 X3.DCK = BIT3      ;DENSITY CHECK
129      000006 X3.MBZ =BIT2·BIT1    ;NOT USED ALWAYS 0
130      000001 X3.RIB = BIT0      ;REVERSE INTO BOT
131
132      ;*
133      ;BIT DEFINITIONS FOR EXTENDED STATUS REGISTER 4
134      ;(XST4)
135      ;-
136      100000 X4.HSP = BIT15      ;HIGH SPEED
137      040000 X4.RCE = BIT14      ;RETRY COUNT EXCEEDED
138      020000 X4.TSM = BIT13      ;TRANSPORT SPECIAL MODE
139      017400 X4.MBZ = BIT12·BIT11·BIT10·BIT9·BIT8 ;NOT USED ALWAYS 0
140      000377 X4.WRC = 000377    ;WRITE RETRY COUNT FIELD
141
142
143      ;*
144      ;
145      ;TSSR TERMINATION CODES (BIT 0-2)
146      ;
147      ;-
148
149      000006 TSREJ= 3·2          ;COMMAND REJECTED
150      000006 UNREC= 6           ;UNRECOVERABLE ERROR
151
152      ;*
153      ;
154      ;DEVICE REGISTER OFFSETS
155      ;
156      ;-
157
158      000000 TSBA== 0
159      000000 TSDB== 0          ;TSDB/TSBA REGISTER
160      000001 TSBAH== 1
161      000001 TSDBH== 1        ;TSDB/TSBA REGISTER HIGH BYTE

```

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162      000002      TSSR== 2      ;TSSR REGISTER
163      000003      TSSRH== 3     ;TSSR REGISTER HIGH BYTE
164
165      ;*
166      ; TSDB ADDRESS BIT DEFINITIONS
167      ;-
168      000003      A1716 = BIT1+BIT0 ;ADDRESS BITS 17:16 ARE IN 1:0
169
170      ;*
171      ; COMMAND DEFINITIONS
172      ;-
173      000017      P.GETSTAT = 17 ;GET STATUS
174      000013      P.INIT = 13 ;INITIALIZE
175      000012      P.CONTROL = 12 ;CONTROL COMMANDS
176      000011      P.FORMAT = 11 ;FORMAT
177      000010      P.POSITION = 10 ;POSITION
178      000006      P.WRTSUB = 6 ;SUBSYSTEM WRITE
179      000005      P.WRITE = 5 ;WRITE
180      000004      P.WRTCHAR = 4 ;WRITE CHARACTERISTICS
181      000001      P.READ = 1 ;READ
182
183      ;*
184      ; COMMAND PACKET HEADER WORD BIT DEFINITIONS
185      ;-
186      100000      P.ACK = BIT15 ;BUFFER AVAIL FOR CONTROLLER
187      040000      P.CVC = BIT14 ;CLEAR VOLUME CHECK
188      020000      P.OPP = BIT13 ;REVERSE SEQUENCE OF DATA BITS
189      010000      P.SWB = BIT12 ;SWAP BYTES IN MEMORY
190      007400      P.MODE = BIT11:BIT10:BIT9:BIT8 ;EXTENDED COMMAND MODE FIELD
191      000200      P.IE = BIT7 ;INTERRUPT ENABLE
192      000140      P.FMT= BIT6:BITS ;PACKET HEADER TYPE (ALWAYS=0)
193      000037      P.CMD = 37 ;MAJOR COMMAND FIELD
194
195      ;*
196      ; CONTROL COMMAND MODE CODES
197      ;-
197      000000      PC.RELEASE = 0*256. ;RELEASE BUFFER
198      000400      PC.REWIND = 1*256. ;REWIND
199      001000      PC.NOOP = 2*256. ;NO-OP
200      002000      PC.IEREW = 4*256. ;REWIND IMMEDIATE INTERRUPT
201      002400      PC.ERASE = 5*256. ;SECURITY ERASE
202
203      ;*
204      ; CONTROLLER RAM DEFINITIONS
205      ;-
206      000167      RMCHBEG = 167 ;CHARACTERISTICS IO DATA BEGIN RAM ADDRESS
207      000200      RMCHEND = 200 ;CHARACTERISTICS IO DATA END RAM ADDRESS
208      000201      RMPKTBEG= 201 ;COMMAND PACKET BEGIN RAM ADDRESS
209      000210      RMPKTEND= 210 ;COMMAND PACKET END RAM ADDRESS
210      000215      RMMSGBEG= 215 ;MESSAGE BUFFER BEGIN RAM ADDRESS
211      000234      RMMSGEND= 234 ;MESSAGE BUFFER END RAM ADDRESS
212
213      ;*
214      ; REGISTER DEFINITIONS IN THE MESSAGE BUFFER
215      ;-
216
217      000006      XSTO== 6 ;EXTENDED STATUS REGISTER 0 (WORD 4)
218

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```

219      000010      XST1== 8.          ;EXTENDED STATUS REGISTER 1 (WORD 5)
220      000012      XST2== 10.         ;EXTENDED STATUS REGISTER 2 (WORD 6)
221      000014      XST3== 12.         ;EXTENDED STATUS REGISTER 3 (WORD 7)
222      000016      XST4== 14.         ;EXTENDED STATUS REGISTER 4 (WORD 8)
223
224
225      ;+
226      ;
227      ;OFFSETS TO WORD LOCATIONS IN PACKET DEFINITIONS
228      ;
229      ;-
230
231      000002      PKLOW   = 2          ;LOW ORDER CHARACTERISTIC DATA POINTER
232      000004      PKHI    = 4          ;HIGH ORDER CHARACTERISTIC DATA POINTER
233      000006      PKBCNT  = 6          ;NUMBER OF BYTES IN DATA PACKET
234
235      000010      EXBCNT=10          ;NUMBER OF BYTES IN EXTENDED DATA PACKET
236
237      ;+
238      ;DATA PACKET OFFSETS FOR WRITE SUBSYSTEM COMMAND
239      ;-
240      000000      BSELO   = 0          ;BYTE 0
241      000001      BSEL1   = 1          ;BYTE 1
242      000002      SEL2    = 2          ;WORD 2
243      000004      SELDATA = 4          ;WORD 3
244
245      ;+
246      ;BSELO SELECT CODES FOR WRITE SUBSYSTEM COMMAND
247      ;-
248      000000      PW.NOP   = 0          ;NO-OP
249      000001      PW.RDRAM = 1          ;READ RAM
250      000002      PW.WTRAM = 2          ;WRITE RAM
251      000003      PW.RFIFO = 3          ;READ FIFO
252      000004      PW.WFIFO = 4          ;WRITE FIFO
253      000005      PW.RDSTAT = 5         ;READ STATUS
254      000006      PW.WCTL   = 6          ;WRITE TAPE CONTROL
255      000007      PW.WFMT   = 7          ;WRITE TAPE FORMAT
256      000010      PW.WMISC  = 10         ;WRITE MISCELLANEOUS
257      000011      PW.WNPR   = 11         ;WRITE NPR CONTROL
258      000020      PW.D22   = 20         ;DO MICROTEST 22
259      000021      PW.D11   = 21         ;DO MICROTEST 11
260      000022      PW.D13   = 22         ;DO MICROTEST 13
261      000023      PW.NO1311 = 23        ;DISABLE MICROTEST 11 AND 13
262      000024      PW.RDEXT  = 24        ;READ EXT. TAPE STATUS (NOT SUPPORTED BY ALL TRANSPORTS)
263
264      ;+
265      ;BSEL1 CODES FOR WRITE TAPE CONTROL
266      ;-
267      000200      WC.IFAD   = BIT7       ;IFAD - FORMATTER ADDRESS
268      000100      WC.IOTAD  = BIT6       ;ITADO - TRANSPORT ADDRESS BIT 0
269      000040      WC.I1TAD  = BIT5       ;ITAD1 - TRANSPORT ADDRESS BIT 1
270      000020      WC.ISRESV  = BIT4       ;IRESV5 - RESERVED #5
271      000010      WC.IREW   = BIT3       ;IREW - REWIND
272      000004      WC.IRWU   = BIT2       ;IRWU - REWIND AND UNLOAD
273      000002      WC.IFEN   = BIT1       ;IFEN - FORMATTER ENABLE
274      000001      WC.IGO    = BIT0       ;GO
275

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```

276
277      ;*
278      ;BSEL1 CODES FOR WRITE FORMAT
279      ;-
280      000200      WF.IHISP      = BIT7      ;IHISP - HIGH SPEED
281      000100      WF.IWRT      = BIT6      ;IWRT  - WRITE
282      000040      WF.IREV      = BIT5      ;IREV  - REVERSE
283      000020      WF.IWFM      = BIT4      ;IWFM  - WRITE FILE MARK
284      000010      WF.IEDIT     = BIT3      ;IEDIT - EDIT
285      000004      WF.IERASE    = BIT2      ;IERASE - ERASE
286      000002      WF.I3RESV    = BIT1      ;IRESV3 - RESERVED #3
287      000001      WF.I4RESV    = BIT0      ;IRESV4 - RESERVED #4
288
289      ;*
290      ;BSEL1 CODES FOR WRITE MISCELLANEOUS SUBCOMMAND
291      ;-
292      000200      MS.EXT        = BIT7      ;INVERT SENSE OF EXTENDED FEATURES SWITCH
293      000020      MS.RSFIFO     = BIT4      ;RESET FIFO AND INPUT PARITY ERRORR
294      000010      MS.RSTAPE    = BIT3      ;RESET TAPE STATUS IN 2 FLIP-FLOPS
295      000006      MS.ATTN      = BIT2:BIT1 ;ATTENTION TRIGGER FIELD
296      000001      MS.RSD       = BIT0      ;RESET TIMER A,B THEN DELAY TIMES IN SEL2
297
298      ;*
299      ; MS.ATTN SUBCODES
300      ;-
301      000000      MSA.NOP      = 0*2      ;NO-OP (NOTHING TRIGGERED)
302      000002      MSA.VOL      = 1*2      ;SIMULATE ON-LINE/OFF-LINE TRANSISTION
303      000004      MSA.NRAM     = 2*2      ;FORCE NON-FATAL RAM ERROR (FORCES ERRCODE 54)
304      000006      MSA.FRAME    = 3*2      ;FORCE FATAL RAM ERROR (CAUSES SCE TO SET)
305
306      ;*
307      ; WRITE SUBSYSTEM WRITE NPR BSEL1 BIT DEFINITIONS
308      ;-
309      000200      NP.IR        = BIT7      ;INTERRUPT REQUEST (0-1 TRANSITION)
310      000100      NP.OUT       = BIT6      ;TAPE DATA DIRECTION OUT (0= IN)
311      000040      NP.LOOP      = BIT5      ;ENABLE TRANSPORT LOOPBACK
312      000020      NP.WRP       = BIT4      ;WRITE CORRECT PARITY (SET=0 TO WRITE WRONG)
313
314      ;*
315      ; READ STATUS MESSAGE BUFFER BIT DEFINITIONS
316      ;-
317      000200      S2.DIM        = BIT7      ;WORD #9 BYTE 2 DATA IN MISS
318      000100      S2.ILW       = BIT6      ;
319      000040      S2.OURDY      = BIT5      ;
320      000020      S2.INRDY     = BIT4      ;
321      000010      S2.ATIMR     = BIT3      ;
322      000004      S2.BTIMR     = BIT2      ;
323      000003      S2.UNDEF     = BIT1:BIT0 ;(UNDEFINED)
324      100000      S1.PARIN     = BIT15     ;WORD #8 BYTE 1 PARIN H
325      040000      S1.I2RESV    = BIT14     ;
326      020000      S1.I1RESV    = BIT13     ;
327      010000      S1.IEOT      = BIT12     ;
328      004000      S1.IIDENT    = BIT11     ;
329      002000      S1.ICER      = BIT10     ;
330      001000      S1.IFMK      = BIT9      ;
331      000400      S1.IHER      = BIT8      ;
332      000200      S0.ISPEED    = BIT7      ;WORD #8 BYTE 0 ISPEED H
333      000100      S0.IRDY      = BIT6      ;
334      000040      S0.IONL      = BIT5      ;

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333      000020      SO.ILDP      = BIT4      ;           ILDP L
334      000010      SO.IDBY      = BIT3      ;           IDBY L
335      000004      SO.IRWD      = BIT2      ;           IRWD L
336      000002      SO.IFBY      = BIT1      ;           IFBY L
337      000001      SO.IFPT      = BIT0      ;           IFPT L
338      ;*
339      ;UNIBUS MAP DEFINATIONS
340      ;-
341      170200      MMRO= 170200
342
343
344      .SBTTL  SPECIAL MACROS AND OPDEFS.
345
346
347      ;*
348      ;SAVE GENERAL REGS 1 TO 5
349      ;-
350
351      .MACRO  SAVREG
352      JSR    R5,REGSAV
353      .ENDM
354
355      ;*
356      ; MACRO TO FORCE AN ERROR
357      ;-
358      .MACRO  FORCERROR      TAG,NOTSSR
359      .NLIST
360      .IIF NDF LISTALL, .NLIST
361      .LIST
362      .IF B NOTSSR
363      MOV    TSSR(R5),R1      ;READ TSSR
364      .ENDC
365      MOV    FORCER,FORCER    ;IS FORCER SET? (LEAVE C BIT ALONE)
366      BNE   TAG              ;BR IF YES
367      .NLIST
368      .IIF NDF LISTALL, .LIST
369      .LIST
370      .ENDM
371
372      ;*
373      ; MACRO TO FORCE AN EXIT TO AVOID SECTION ITERATIONS
374      ; WILL EXIT TO A LABEL IF FORCER IS NEGATIVE
375      ; SO TO FORCE ERRORS AND EXIT ON 1 ERROR SET
376      ; FORCER TO 177777
377      ; TO FORCE ERRORS AND ITERATIONS SET FORCER TO 1.
378      ;-
379      .MACRO  FORCEEXIT      TAG
380      .NLIST
381      .IIF NDF LISTALL, .NLIST
382      .LIST
383      MOV    FORCER,FORCER    ;IS FORCER NEGATIVE?
384      BMI   TAG              ;BR IF YES
385      .NLIST
386      .IIF NDF LISTALL, .LIST
387      .LIST
388      .ENDM
389      ;*

```

```

390 ; MACRO TO INCREMENT ERROR COUNTS
391 ;-
392 .MACRO NEXT.ERRNO
393 .NLIST
394 ;;;.IIF NDF LISTALL, .NLIST
395 ERRNO=ERRNO+1
396 ;;;.IIF NDF LISTALL, .LIST
397 .LIST
398 .ENDM
399
400 ;+
401 ;MACRO TO PERFORM XOR
402 ;-
403
404 .MACRO XOR A,B
405 MOV A,-(SP)
406 BIC B,(SP)
407 BIC A,B
408 BIS (SP)+,B
409 .ENDM
410
411 000000 EN=0 ; INITIALIZE ERROR NUMBER
412 .SBTTL FORCER - FORCE ERROR FLAG
413
414 ;
415 ; THE FOLLOWING LOCATIONS MAY BE PATCHED BY THE USER
416 ; TO OBTAIN THE RESULTS DESCRIBED FOR EACH.
417 ;
418
419 002174 000000 FORCER:: 0 ; FORCE TYPE ALL HARD ERRORS (THE ONES CALLED -
420 ; - BY THE MACRO "IFERROR"). AN ERROR NEED NOT -
421 ; - EXIST, JUST ASSUME AND TYPE THE MESSAGE.
422
423
424
425 .SBTTL GLOBAL DATA SECTION
426
427 ;++
428 ;THE GLOBAL DATA SECTION CONTAINS DATA THAT ARE USED
429 ;IN MORE THAN ONE TEST.
430 ;--
431
432 ;
433 ;THE FOLLOWING DATA ARE SET FOR EACH UNIT AT INIT TIME.
434 ;SINGLE UNIT DEFAULTS (LISTED) ARE IN THE DEFAULT P-TABLE.
435 ;
436 002176 000000 EPRTSW:: .WORD 0 ;PRINT SWITCH
437 002200 000000 UNITN:: .WORD 0 ;UNIT # UNDER TEST.
438 002202 000000 QVP:: .WORD 0 ;QUICK VERIFY FLAG.
439 002204 000000 CSRADDR:: .WORD 0 ;ADDRESS OF CSR FOR CURRENT DEVICE
440 002206 000224 IVEC:: .WORD 224 ;INTERRUPT VECTOR
441 002210 000200 IPRI:: .WORD PRI04 ;INTERRUPT PRIORITY.
442 002212 000000 TSTCNT:: .WORD 0 ;NUMBER OF TESTS RUN IN THIS PASS
443 002214 000000 LOOPCNT:: .WORD 0 ;REMAINING ITERATION COUNT FOR TEST
444 002216 000000 DEVCNT:: .WORD 0 ;NUMBER OF DEVICE UNDER TEST
445 002220 000000 FATELG:: .WORD 0 ;SET IF FATAL ERROR IS DETECTED IN TEST
446 002222 000000 INTRECV:: .WORD 0 ;SET IF TAPE INTERRUPT WAS RECEIVED

```

```

447 002224 000000 EXTFEA:: .WORD 0 ;EXTENDED FEATURES SOFTWARE SW 0=OFF;1=ON
448 002226 000000 BENBSW:: .WORD 0 ;BUFFER ENABLE SWITCH SW 0=OFF;1=ON
449 002230 000000 EXPD:: .WORD 0 ;EXPECTED RAM DATA FOR PRAMPKT ROUTINE
450 002232 000000 RECV:: .WORD 0 ;RECEIVED RAM DATA FOR PRAMPKT ROUTINE
451 002234 000000 ERRHI:: .WORD 0 ;HIGH ADDRESS MEMORY ERROR
452 002236 000000 ERRLO:: .WORD 0 ;LOW ADDRESS MEMORY ERROR
453 002240 RAMDATA:: .BLKW 16. ;DATA READ FROM RAM PACKET OR MESSAGE BUF AREA
454 002300 000000 RAMSIZ:: .WORD 0 ;RAM DATA SIZE FOR PRAMPKT ROUTINE
455 002302 000000 RCVHIADD:: .WORD 0 ;RECEIVED BUFFER HIGH ADDRESS
456 002304 000000 RCVLOADD:: .WORD 0 ;RECEIVED BUFFER LOW ADDRESS
457 002306 000000 COUNT:: .WORD 0 ;TEST COUNT PATTERN
458 002310 000000 DATA:: .WORD 0 ;TEST DATA
459 002312 000000 TSTFLAG:: .WORD 0 ;TEST FLAG WORD
460 002314 000000 TSTPTR:: .WORD 0 ;TSTBLK POINTER
461 002316 000000 PRMNO:: .WORD 0 ;PRINT ROUTINE TEMP
462 002320 EXPMSG:: .BLKB 100. ;EXPECTED MESSAGE BUFFER DATA
463 002464 RECMMSG:: .BLKB 100. ;RECEIVED MESSAGE BUFFER DATA
464 002630 TMPBFR:: .BLKB 80. ;TEMPORARY STORAGE FOR PRINT
465
466
467
468
469
470
471
472
473
474
475
476
477
478
479
480
481
482

```

.SBTTL TSTBLK - TEST DATA TABLE

```

;*
;
; THIS TABLE CONTAINS TEST DATA USED IN SEVERAL TESTS
;
; IN SEQUENCE THE DATA IS:
;
;     ALL ZEROS
;     ALL ONES
;     WALKING ONES
;     WALKING ZEROS
;     ALTERNATING ONES AND ZEROS
;
; -

```

```

483 002750 TSTBLK:: .WORD 0 ;ALL ZEROS
484 002750 .WORD 177777 ;ALL ONES
485 002752 .WORD BIT0 ;DATA FOR WALKING ONES
486 002754 .WORD BIT1
487 002756 .WORD BIT2
488 002760 .WORD BIT3
489 002762 .WORD BIT4
490 002764 .WORD BIT5
491 002766 .WORD BIT6
492 002770 .WORD BIT7
493 002772 .WORD BIT8
494 002774 .WORD BIT9
495 002776 .WORD BIT10
496 003000 .WORD BIT11
497 003002 .WORD BIT12
498 003004 .WORD BIT13
499 003006 .WORD BIT14
500 003010 .WORD BIT15
501 003012 .WORD †CBIT0 ;DATA FOR WALKING ZEROS
502 003014 .WORD †CBIT1
503 003016

```

```

504 003020 177773 .WORD †CBIT2
505 003022 177767 .WORD †CBIT3
506 003024 177757 .WORD †CBIT4
507 003026 177737 .WORD †CBIT5
508 003030 177677 .WORD †CBIT6
509 003032 177577 .WORD †CBIT7
510 003034 177377 .WORD †CBIT8
511 003036 176777 .WORD †CBIT9
512 003040 175777 .WORD †CBIT10
513 003042 173777 .WORD †CBIT11
514 003044 167777 .WORD †CBIT12
515 003046 157777 .WORD †CBIT13
516 003050 137777 .WORD †CBIT14
517 003052 077777 .WORD †CBIT15
518 003054 125252 .WORD 125252 ;ALTERNATING ONES, ZEROS
519 003056 052525 .WORD 052525 ;ALTERNATING ONES, ZERO OPPOSITE FROM ABOVE
520          003060'
521
522
523          .SBTTL GLOBAL ENVIRONMENT STORAGE
524          ;
525          ;STORAGE FOR DEVICE REGISTERS
526          ;
527 003060 000000 100000 000000 DUMMY: 0,100000,0,0 ;DUMMY DEVICE REGISTERS...
528 003070 000000 000000 000000 0,0,0,0,0,0,0,0,0,0 ;...FOR MULTI-UNIT CHECKOUT.
529
530
531
532 003110 000000 DUFLG:: .WORD 0 ;"DROPPED UNIT" FLAG.
533          ;INHIBITS CODE IN "CLEAN-UP".
534 003112 000000 NODEV:: .WORD 0 ;FLAG TO SAY NO DEVICE.
535
536 003114 000000 TEMP1:: .WORD 0 ;SOME TEMP LOCATIONS.
537 003116 000000 TEMP2:: .WORD 0
538 003120 000000 XXCOMM:: .WORD 0 ;XXDP* COMM BLOCK POINTER.
539 003122 000000 FREE:: .WORD 0 ;1ST FREE MEMORY ADDRESS...
540 003124 000000 FRESIZ:: .WORD 0 ;...AND SIZE (IN WORDS).
541 003126 000000 FREEHI: .WORD 0 ;LAST WORD IN FREE SPACE
542 003170 000000 KTFLG:: .WORD 0 ;KT11, MEM AVAIL FLAG -
543          ;- .WORD 0 = <24K OR NO KT -
544          ;- NZ = >24K AND KT.
545 003132 000000 KTENABLE:: .WORD 0 ;SET BY TEST ROUTINES TO FLAG >28K UNDER TEST
546 003134 000000 NXMFLG:: .WORD 0 ;SET IF WE CAN TEST CLEARED OTHERWISE
547 003136 000000 NXMLO:: .WORD 0 ;NXM LO ADDRESS BITS
548 003140 000000 NXMHI:: .WORD 0 ;NXM HI ADDRESS BITS FOR DAL'S 16-21
549 003142 000000 T23A:: .WORD 0 ;PROCESSOR TYPE FLAG
550 003144 000000 T23B:: .WORD 0 ;PROCESSOR TYPE FLAG B
551 003146 000000 T3BFLG:: .WORD 0 ;TEST 3B FLAG †0
552 003150 002000 PST32W:: .WORD 2000 ;32W BLOCK ADDRESS FOR 32K START
553 003152 000000 SIFLAG:: .WORD 0
554 003154 000000 BADDAT:: .WORD 0 ;ACTUAL DATA
555 003156 000000 GDDAT:: .WORD 0 ;EXPECTED DATA
556 003160 000000 LOOPFL:: .WORD 0
557 003162          ;
558 003162 000000 CTAB:: .WORD 0 ;CONFIGURATION TABLES.
559 003164 000000 CTABM:: .WORD 0 ;CONFIG WORK.
560 003166 000000 .WORD 0
  
```

```

561 003170 000000          .WORD 0
562 003172 177777          .WORD -1          ;END OF MEM TABLE.
563 003174
564          CTABE::
565          ;ERROR STATISTICS TABLE (1 WORD PER UNIT), 64 UNITS MAX:
566          :
567          :          0          =          UNIT NOT TESTED
568          :          100000 =          UNIT ONLINE, NO ERRORS
569          :          10XXXX =          UNIT ONLINE, ENCOUNTERED XXXX ERRORS
570          :          160000 =          UNIT DROPPED, NON-EXISTENT DEVICE REGISTER
571          :          160001 =          UNIT DROPPED, NOT IDLE AT START
572          :          14XXXX =          UNIT DROPPED, ENCOUNTERED XXXX ERRORS
573 003174          ERTABL:          .BLKW 64.
574 003374 000000          ERTABE:          .WORD 0
575
576 003376 000000          SKIPT:          .WORD 0          ;1=SKIP SUBTEST 0=NO SKIP OF SUBTEST
577
578          .SBTTL GLOBAL TEXT MESSAGES
579          ;+
580          ; THE GLOBAL TEXT SECTION CONTAINS FORMAT STATEMENTS,
581          ; MESSAGES, AND ASCII INFORMATION THAT ARE USED IN
582          ; MORE THAN ONE TEST.
583          ;--
584
585
586
587          ;+
588          ;NAMES OF DEVICES SUPPORTED
589          ;-
590
591 003400          DEVTYP <TSU05>
592 003400          L$DVTYP::
593 003400          124          123          125          .ASCIZ /TSU05/
594          .EVEN
595
596          ;+
597          ;TEST DESCRIPTION
598          ;-
599          ; DESCRIPT <***** TSU05 DIAG PART 1 - REPLACE M7455 IF ERROR *****>
600 003406          L$DESC::
601 003406          052          052          052          .ASCIZ /***** TSU05 DIAG PART 1 - REPLACE M7455 IF ERROR *****/
602 003406          .EVEN
603
604
605
606          ;+
607          ;BIT TO ASCII CONVERSION FOR TSSR REGISTER
608          ;-
609
610
611
612
613
614
615
616
617
618
619
620
621
622
623
624
625 003474 003534' 003537' 003543' TSSRBIT::          .WORD 1$,2$,3$,4$,5$,6$,7$,8$
626 003514 003575' 003601' 003605'          .WORD 9$,10$,11$,12$,13$,14$,15$,16$
627 003534          123          103          000          1$:          .ASCIZ 'SC'
628 003537          102          111          105          2$:          .ASCIZ 'BIE'
629 003543          123          103          105          3$:          .ASCIZ 'SCE'
630 003547          122          115          122          4$:          .ASCIZ 'RMR'
631 003553          116          130          115          5$:          .ASCIZ 'NXM'
632 003557          116          102          101          6$:          .ASCIZ 'NBA'

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```

633 003563      102      111      124  7$:      .ASCIZ  'BIT9'
634 003570      102      111      124  8$:      .ASCIZ  'BIT8'
635 003575      123      123      122  9$:      .ASCIZ  'SSR'
636 003601      117      106      114 10$:      .ASCIZ  'OFL'
637 003605      102      111      124 11$:      .ASCIZ  'BIT5'
638 003612      102      111      124 12$:      .ASCIZ  'BIT4'
639 003617      102      111      124 13$:      .ASCIZ  'BIT3'
640 003624      102      111      124 14$:      .ASCIZ  'BIT2'
641 003631      102      111      124 15$:      .ASCIZ  'BIT1'
642 003636      102      111      124 16$:      .ASCIZ  'BIT0'
643              102      111      124 16$:      .ASCIZ  'BIT0'
643              .EVEN
644 003644      124      123      123 SFIERR: .ASCIZ  'TSSR ERROR AFTER SOFT INIT'
645 003677      124      123      123 SFHERR: .ASCIZ  'TSSR ERROR AFTER BUS RESET'
646 003732      040      040      116 NXR:    .ASCIZ  / NON-EXISTANT DEVICE REGISTER/
647 003771      045      101      040 NXR:    .ASCIZ  /NON-EXISTANT DEVICE REGISTER/
648 004012      045      101      040 NXR:    .ASCIZ  /NON-EXISTANT DEVICE REGISTER/
649 004052      045      101      040 NXR:    .ASCIZ  /NON-EXISTANT DEVICE REGISTER/
650 004111      045      116      045 FUSI:   .ASCIZ  /NON-EXISTANT DEVICE REGISTER/
651 004115      040      040      125 USI:    .ASCIZ  / UNEXPECTED INTERRUPT/
652 004144      040      040      111 NSI:    .ASCIZ  / INTERRUPT EXPECTED, NOT RECEIVED/
653 004207      045      116      045 FNOINTR: .ASCIZ  /NON-EXISTANT DEVICE REGISTER/
654 004213      040      040      116 NOINTR: .ASCIZ  / NO INTERRUPT WAS GENERATED/
655 004250      040      040      111 IFAULT: .ASCIZ  / INTERRUPT FAULT/
656 004272      045      101      040 INTX:   .ASCIZ  /NON-EXISTANT DEVICE REGISTER/
657 004327      040      040      042 NOINIT: .ASCIZ  / "BUS-INIT" DIDN'T INITIALIZE CONTROLLER/
658 004401      040      040      042 NSINIT: .ASCIZ  / "SOFT-INIT" DIDN'T INITIALIZE THE DPU/
659 004451      040      040      042 BRINIT: .ASCIZ  / "BUS-RESET" DIDN'T INITIALIZE THE DPU/
660
661 004521      000              NUL:    .ASCIZ  //
662 004522      045      116      000 NUL.CR: .ASCIZ  /NON-EXISTANT DEVICE REGISTER/
663 004525      045      101      040 EXPGOT: .ASCIZ  /NON-EXISTANT DEVICE REGISTER/
664 004561      045      116      045 EXPGT2: .ASCIZ  /NON-EXISTANT DEVICE REGISTER/
665 004635      045      101      040 DUAD12: .ASCIZ  /NON-EXISTANT DEVICE REGISTER/
666 004737      122      101      115 PKTRAM: .ASCIZ  'RAM Contents Do Not Match Packet Sent'
667 005005      040      040      103 SCME:   .ASCIZ  / CONFIG DOESN'T MATCH MFG. MASTER/
668 005050      127      122      111 WRTMSG: .ASCIZ  'WRITE CHARACTERISTICS Failed'
669 005105      124      123      123 WRTERR: .ASCIZ  'TSSR Incorrect After WRITE Command, More Bits Set Than SSR'
670 005200      124      123      123 RDERR:  .ASCIZ  'TSSR Incorrect After READ Command, More Bits Set Than SSR'
671 005272      106      101      124 SCHERR: .ASCIZ  'FATAL ERROR IN SUBTEST - CHECK TAPE,CABLES,TRANSPORT etc.'
672 005364      105      122      122 RETERR: .ASCIZ  'ERROR IN SUBTEST - WRITE DATA RETRY FIVE TIMES FAILED'
673 005452      045      116      045 NOMEM:  .ASCIZ  '#N#A ***** NO NXM ADDRESS--CANNOT TEST NXM TIMEOUT. *****N'
674              .EVEN
675
676              .SBTTL GLOBAL ERROR REPORT SECTION
677
678
679      ;**
680      ; THE GLOBAL ERROR REPORT SECTION CONTAINS THE PRINTB AND PRINTX
681      ; CALLS THAT ARE USED IN MORE THAN ONE TEST.
682      ; ASCII TEXT STRINGS ARE FOUND IN THE GLOBAL TEXT SECTION.
683      ;--
684 005546      BGNMSG  NXRERR      ;NON-EXISTANT DEVICE REGISTER.
685 005546      NXRERR:  PRINTX  #NXRX,NODEV ;NODEV = NEXM ADDRESS.
685 005546      MOV      NODEV, -(SP)
685 005552      MOV      #NXRX, -(SP)
685 005556      MOV      #2, -(SP)

```

```

005562 010600          MOV    SP,R0
005564 104415          TRAP   C$PNTX
686 005566 062706 000006  ADD    #6,SP
687 005572 004737 005600' JSR    PC,EXTEND      ; PRINT EXTENSION IF REQUIRED.
005576          ENDMSG
005576 104423          L10002: TRAP   C$MSG
688
689
690
691          ; THIS ROUTINE APPENDS A UNIQUE EXTENSION (IF REQUIRED)
692          ; TO ANY OF THE ABOVE ERROR SIGNATURES.
693
694 005600 005727          EXTEND: TST    (PC)+
695 005602 000000          EXTA:  0          ; 0 = NO EXTENSION.
696 005604 001402          BEQ    1$
697 005606 004777 177770  JSR    PC,@EXTA      ; APPEND EXTENSION TEXT.
698 005612          1$:  PRINTX #NULCR      ; PRINT A BLANK LINE
005612 012746 004522'    MOV    #NULCR,-(SP)
005616 012746 000001    MOV    #1,-(SP)
005622 010600          MOV    SP,R0
005624 104415          TRAP   C$PNTX
005626 062706 000004    ADD    #4,SP
699 005632 000207          RTS    PC
700
701          .SBTTL  PRITSSR - PRINT TSSR CONTENTS
702
703
704          ;+
705          ; ROUTINE TO DISPLAY THE CONTENTS, AND BIT DEFINITIONS, OF
706          ; THE TSSR REGISTER. THIS ROUTINE IS NORMALLY CALLED ONLY
707          ; BY A MESSAGE PRINTING ROUTINE
708
709          ; INPUTS:
710
711          ; R1      CONTENTS OF TSSR
712
713          ; SUBORDINATE ROUTINES:
714
715          ; CHKAMB  CHECK FOR AMBIGUOUS CONTENTS
716
717          ; -
718
719 005634          PRITSSR: SAVREG          ; SAVE GENERAL REGISTERS
720 005634          MOV    R1,R4      ; SAVE THE TSSR CONTENTS
721 005640 010104          PRINTB #TSSRFOR,R4 ; PRINT THE CONTENTS OF TSSR
722 005642          MOV    R4,-(SP)
005642 010446          MOV    #TSSRFOR,-(SP)
005644 012746 006225'    MOV    #2,-(SP)
005650 012746 000002    MOV    SP,R0
005654 010600          TRAP   C$PNTB
005656 104414          ADD    #6,SP
723 005664 010400          MOV    R4,R0      ; GET TSSR BACK FOR CHKAMB
724 005666 004737 015654' JSR    PC,CHKAMB    ; ARE CONTENTS AMBIGUOUS ?
725 005672 103410          BCS   5$          ; BRANCH IF NOT
726 005674          PRINTX #AMBTSSR ; SHOW CONTENTS ARE AMBIGUOUS

```

```

005674 012746 006445'      MOV      #AMBTSSR,-(SP)
005700 012746 000001      MOV      #1,-(SP)
005704 010600      MOV      SP,R0
005706 104415      TRAP     C$PNTX
005710 062706 000004      ADD      #4,SP
727 005714 010403      5$:     MOV      R4,R3      ;CONTENTS OF TSSR
728 005716 042703 001476      BIC      #HIADDR!FATERR!TERCLS,R3 ;CLEAR ALL MULTIPLE BIT FIELDS
729 005722 001434      BEQ      20$      ;NO BITS ARE SET
730 005724 012702 002630'      MOV      #TMPBFR,R2      ;TEMPORARY ASCII BUFFER
731 005730 012701 003474'      MOV      #TSSRBIT,R1      ;ASCII EQUIVALENT OF BITS
732 005734 005703      10$:    TST      R3      ;REMAINING BITS TO CONVERT
733 005736 001413      BEQ      15$      ;BRANCH WHEN ALL ARE DONE
734 005740 000241      CLC      ;CLEAR CARRY FOR SHIFT
735 005742 006103      ROL      R3      ;SHIFT NEXT BIT TO CARRY
736 005744 103006      BCC      13$      ;BRANCH IF BIT NOT SET
737 005746 011100      MOV      (R1),R0      ;POINTER TO BIT DEFINITION
738 005750 112022      11$:    MOVB     (R0)+,(R2)+      ;MOVE ASCII TO BUFFER
739 005752 001376      BNE      11$      ;MOVE ALL BITS
740 005754 112762 000054 177777      MOVB     #' ,,-1(R2)      ;INSERT A COMMA TO TERMINATE
741 005762 005721      13$:    TST      (R1)+      ;POINT TO NEXT DESCRIPTION
742 005764 000763      BR       10$      ;GET THE REMAINING BITS
743 005766 105042      15$:    CLRB     -(R2)      ;TERMINATE THE LINE
744 005770      PRINTX  #TSSDEF,#TMPBFR ;PRINT THE BIT DEFINITIONS
005770 012746 002630'      MOV      #TMPBFR,-(SP)
005774 012746 006416'      MOV      #TSSDEF,-(SP)
006000 012746 000002      MOV      #2,-(SP)
006004 010600      MOV      SP,R0
006006 104415      TRAP     C$PNTX
006010 062706 000006      ADD      #6,SP
745
746 006014 010403      20$:    MOV      R4,R3      ;GET THE TSSR CONTENTS
747 006016 042703 177761      BIC      #+CTERCLS,R3      ;CLEAR ALL BUT TERMINATION
748 006022 016303, 006506'      MOV      TCOCOD(R3),R3      ;GET THE TERMINATION CODE MEANING
749 006026      PRINTX  #TCOASC,R3      ;PRINT THE TERMINATION CODE
006026 010346      MOV      R3,-(SP)
006030 012746 006306'      MOV      #TCOASC,-(SP)
006034 012746 000002      MOV      #2,-(SP)
006040 010600      MOV      SP,R0
006042 104415      TRAP     C$PNTX
006044 062706 000006      ADD      #6,SP
750 006050 010403      MOV      R4,R3      ;TSSR CONTENTS AGAIN
751 006052 042703 177717      BIC      #+CFATERR,R3      ;CLEAR ALL BUT FATAL TERMINATION
752 006056 001416      BEQ      25$      ;DON'T PRINT IF ZERO
753 006060 006203      ASR      R3
754 006062 006203      ASR      R3
755 006064 006203      ASR      R3      ;ALINE TERMINATION CODE FOR INDEX
756 006066 016303 007046'      MOV      TSFCOD(R3),R3      ;GET THE FATAL TERMINATION CODE
757 006072      PRINTX  #TFCASC,R3      ;PRINT THE FATAL TERMINATION CODE
006072 010346      MOV      R3,-(SP)
006074 012746 006347'      MOV      #TFCASC,-(SP)
006100 012746 000002      MOV      #2,-(SP)
006104 010600      MOV      SP,R0
006106 104415      TRAP     C$PNTX
006110 062706 000006      ADD      #6,SP
758 006114 042704 176377      25$:    BIC      #+CHIADDR,R4      ;CLEAR ALL BUT EXTENDED ADDRESS
759 006120 001411      BEQ      30$      ;DON'T PRINT IF ZERO
760 006122      PRINTX  #TEXASC,R4      ;PRINT THE EXTENDED ADDRESS BITS

```



```

006122 010446          MOV     R4, -(SP)
006124 012746 006245'  MOV     @TEXASC, -(SP)
006130 012746 000002    MOV     @2, -(SP)
006134 010600          MOV     SP, R0
006136 104415          TRAP   C:PNTX
006140 062706 000006    ADD     @6, SP
761 006144 013703 002176' 301:    MOV     EPRTSW, R3          ;PRINT MEASGE BUFFER ADDRESS
762 006150          PRINTX R3                  ;PRINT PROPER MESSAGE
006150 010346          MOV     R3, -(SP)
006152 012746 000001    MOV     @1, -(SP)
006156 010600          MOV     SP, R0
006160 104415          TRAP   C:PNTX
006162 062706 000004    ADD     @4, SP
763 006166 000207          RTS     PC                  ;RETURN TO CALLER
764
766 006170          EPRT2:
767 006170          045    116    045    EPRT1: .ASCIZ '##A *****REPLACE M7455*****'
782 006225          045    116    045    TSSRFOR: .ASCIZ '##A TSSR = #06'
783 006245          045    116    045    TEXASC: .ASCIZ '##A Extended Address Bits = #06'
784 006306          045    116    045    TCOASC: .ASCIZ '##A Termination Class Code = #T'
785 006347          045    116    045    TFCASC: .ASCIZ '##A Fatal Termination Class Code = #T'
786 006416          045    116    045    TSSDEF: .ASCIZ '##A TSSR Bits Set: #T'
787 006445          045    116    045    AMBTSSR: .ASCIZ '##A TSSR Contents Are Ambiguous'
788
789 006506 006526' 006551' 006577' TCOCOD: .EVEN
790 006526          116    157    162    1#: .WORD 1#,2#,3#,4#,5#,6#,7#,8#
791 006551          124    145    162    1#: .ASCIZ 'Normal Termination'
792 006577          124    141    160    2#: .ASCIZ 'Termination Condition'
793 006621          106    165    156    3#: .ASCIZ 'Tape Status Alert'
794 006641          122    145    143    4#: .ASCIZ 'Function Reject'
795 006723          122    145    143    5#: .ASCIZ 'Recoverable Error - Tape Position One Record Down'
796 006772          125    156    162    6#: .ASCIZ 'Recoverable Error - Tape Was Not Moved'
797 007016          106    141    164    7#: .ASCIZ 'Unrecoverable Error'
798
799
800 007046 007056' 007112' 007123' TSFCOD: .EVEN
801 007056          111    156    164    1#: .WORD 1#,2#,3#,4#
802 007112          122    145    163    1#: .ASCIZ 'Internal Diagnostic Failure'
803 007123          102    165    163    2#: .ASCIZ 'Reserved'
804 007167          122    145    163    3#: .ASCIZ 'Bus Interface or Sanity Check Error'
805
806
807
808
809
810
811
812
813
814
815
816
817
818
819
820
821

```

.SBTTL PRIPKT - PRINT THE ADDRESS/CONTENTS OF COMMAND PACKET
 ;
 ; THIS ROUTINE PRINTS THE ADDRESS AND CONTENTS OF A COMMAND PACKET.
 ; THIS ROUTINE IS NORMALLY ONLY CALLED FROM A PRINT ROUTINE.
 ;
 ; INPUT:
 ;
 ; R0 NUMBER OF WORDS IN PACKET
 ; R3 HIGH ORDER COMMAND PACKET ADDRESS
 ; R4 ADDRESS OF COMMAND PACKET
 ;
 ; NOTE: R3 IS IGNORED IF THE KENABLE FLAG IS CLEAR.
 ;

```

822 007200
823 007200
824 007204 010005
825 007206 005737 003132'
826 007212 001001
827 007214 005003
828 007216 010301
829 007220 010400
830 007222 006100
831 007224 006101
832 007226
      007226 010446
      007230 010146
      007232 012746 007364'
      007236 012746 000003
      007242 010600
      007244 104414
      007246 062706 000010
833 007252 010300
834 007254 001404
835 007256 010401
836 007260 004737 017130'
837 007264 010004
838 007266 005001
839 007270 012402
840 007272
      007272 010246
      007274 010146
      007276 012746 007326'
      007302 012746 000003
      007306 010600
      007310 104414
      007312 062706 000010
841 007316 005201
842 007320 020105
843 007322 002762
844 007324 000207
845
846 007326 045 116 045 PKTFRM: .ASCIZ 'NNA Packet Word #D1#A = #06'
847 007364 045 116 045 PKTADD: .ASCIZ 'NNA Packet Address = #01#05'
848
849
850
851 .SBTTL PRIBXOR - PRINT EXPD, RECV AND XOR BYTE
852
853
854
855 ;*
856 ;PRINT EXPECTED DATA, RECEIVED DATA, AND XOR OF THE DATA BYTE
857 ;THIS ROUTINE IS NORMALLY CALLED ONLY FOR PRINT ROUTINES.
858 ;
859 ;INPUTS:
860 ; R1 RECEIVED DATA
861 ; R2 EXPECTED DATA
862 ;
863 ;OUTPUT:
864 ;
  
```

```

PRIPKT::
      SAVREG
      MOV R0,R5 ;SAVE THE REGISTERS
      TST KTENABLE ;SAVE NO. OF WORDS IN PACKET
      BNE 10$ ;ABOVE 28K UNDER TEST?
      CLR R3 ;BR IF YES
      MOV R3,R1 ;SET HIGH ORDER ADDRESS TO 0
      MOV R4,R0 ;COPY HIGH ORDER ADDRESS
      ROL R0 ;GET LOWER ADDRESS
      ROL R1 ;SHIFT BIT 15 INTO C BIT
      PRINTB @PKTADD,R1,R4 ;AND INTO HIGH ORDER.
      MOV R4,-(SP) ;PRINT PACKET ADDRESS
      MOV R1,-(SP)
      MOV @PKTADD,-(SP)
      MOV @3,-(SP)
      MOV SP,R0
      TRAP C:PNTB
      ADD @10,SP
15$: MOV R3,R0 ;GET HIGH ORDER ADDRESS
      BEQ 20$ ;BR IF NOT ABOVE 28K.
      MOV R4,R1 ;GET LOW ORDER ADDRESS
      JSR PC,SETMAP ;SETUP PAR6 MAPPING FOR 18 BIT ADDRESS
      MOV R0,R4 ;GET RETURNED PAR6 ADDRESS BIAS
20$: CLR R1 ;SAVE WORD NUMBER
25$: MOV (R4),R2 ;GET PACKET CONTENTS
      PRINTB @PKTFRM,R1,R2 ;PRINT THE DATA
      MOV R2,-(SP)
      MOV R1,-(SP)
      MOV @PKTFRM,-(SP)
      MOV @3,-(SP)
      MOV SP,R0
      TRAP C:PNTB
      ADD @10,SP
      INC R1 ;NEXT WORD NUMBER
      CMP R1,R5 ;DONE ALL PACKET WORDS?
      BLT 25$ ;LOOP TILL ALL DONE
      RTS PC ;RETURN
  
```

```

865          ;      RO      XOR OF EXPECTED/RECEIVED DATA
866          ;
867          ; -
868
869 007422    PRIBXOR::
870 007422    SAVREG          ;SAVE THE REGISTERS
871 007426    010203          MOV      R2,R3          ;EXPECTED DATA
872 007430    XOR      R1,R3  ;FORM THE EXCLUSIVE OR
873 007440    012700    177400 MOV      @C<377>,R0  ;BYTE MASK
874 007444    040001          BIC      RO,R1          ;SAVE LOW BYTE RECV
875 007446    040002          BIC      RO,R2          ;SAVE LOW BYTE EXPD
876 007450    040003          BIC      RO,R3          ;SAVE LOW BYTE XOR
877 007452    PRINTB @XORBFOR,R2,R1,R3 ;PRINT THE MESSAGE
      007452    010346          MOV      R3,-(SP)
      007454    010146          MOV      R1,-(SP)
      007456    010246          MOV      R2,-(SP)
      007460    012746    007504' MOV      @XORBFOR,-(SP)
      007464    012746    000004          MOV      @4,-(SP)
      007470    010600          MOV      SP,R0
      007472    104414          TRAP    C#PNTB
      007474    062706    000012          ADD      @12,SP
878 007500    010300          MOV      R3,R0          ;RO HAS XOR ON RETURN
879 007502    000207          RTS      PC          ;RETURN TO CALLER
880
881 007504    045      116    045  XORBFOR:  .ASCIZ 'N#A EXPD: #03#A RECV: #03#A XOR: #03'
882          .EVEN
883
884
885          .SBTTL  PRIBXOR - PRINT EXPD, RECV AND XOR
886
887          ;*
888          ;
889          ;PRINT EXPECTED DATA, RECEIVED DATA, AND XOR OF THE TWO
890          ;THIS ROUTINE IS NORMALLY CALLED ONLY FOR PRINT ROUTINES.
891          ;
892          ;INPUTS:
893          ;
894          ;      R1      RECEIVED DATA
895          ;      R2      EXPECTED DATA
896          ;
897          ;OUTPUT:
898          ;
899          ;      RO      XOR OF EXPECTED/RECEIVED DATA
900          ;
901          ; -
902
903 007552    PRIBXOR::
904 007552    SAVREG          ;SAVE THE REGISTERS
905 007556    010203          MOV      R2,R3          ;EXPECTED DATA
906 007560    XOR      R1,R3  ;FORM THE EXCLUSIVE OR
907 007570    PRINTB @XORFOR,R2,R1,R3 ;PRINT THE MESSAGE
      007570    010346          MOV      R3,-(SP)
      007572    010146          MOV      R1,-(SP)
      007574    010246          MOV      R2,-(SP)
      007576    012746    007622' MOV      @XORFOR,-(SP)
      007602    012746    000004          MOV      @4,-(SP)
      007606    010600          MOV      SP,R0

```

```

007610 104414
007612 062706 000012
908 007616 010300
909 007620 000207
910
911 007622 045 116 045 XORFOR: .ASCIZ 'N#A EXPD: #06#A RECV: #06#A XOR: #06'
912 .EVEN
913
914 .SBTTL PRIEQU - PRINT BIT NUMBERS AS ASCII EQUIVALENT
915
916 ;*
917 ;
918 ;ROUTINE TO CONVERT BIT VALUES TO ASCII AND PRINT THE STRING
919 ;THIS ROUTINE IS NORMALLY CALLED FROM A PRINT ROUTINE
920 ;
921 ;INPUTS:
922 ;
923 ; R0 OCTAL VALUE TO CONVERT
924 ; R1 TABLE OF POINTERS TO ASCII EQUIVALENT
925 ;
926 ;-
927
928 007670
929 007670
930 007674 000207
931
932
933
934
935 .SBTTL PRIRAM - PRINT RAM ADDRESS
936
937 ;*
938 ;
939 ;PRINT CONTROLLER RAM ADDRESS.
940 ;THIS ROUTINE IS NORMALLY CALLED ONLY FROM PRINT ROUTINES.
941 ;
942 ;INPUTS:
943 ;
944 ; R4 RAM ADDRESS
945 ;-
946 007676
947 007676
948 007702
007702 010446
007704 012746 007726'
007710 012746 000002
007714 010600
007716 104414
007720 062706 000006
949 007724 000207
950
951 007726 045 116 045 RAMFOR: .ASCIZ 'N#A CONTROLLER RAM ADDRESS = #06'
952 .EVEN
953
954
955 .SBTTL PRIADD - PRINT MEMORY ERROR ADDRESS
956 ;*

```

```

957
958 ;PRINT MEMORY ADDRESS
959 ;THIS ROUTINE IS NORMALLY CALLED ONLY FROM PRINT ROUTINES.
960
961 ; IMPLICIT INPUTS
962
963 ; ERRHI - HIGH ORDER ADDRESS
964 ; ERRLO - LOW ORDER ADDRESS
965
966 ;-
967 007770 PRIADD:
968 007770 SAVREG ;SAVE R1-R5 UNTIL NEXT RETURN
969 007774 013700 002234' MOV ERRHI,R0 ;GET HIGH ADDRESS
970 010000 013701 002236' MOV ERRLO,R1 ;GET LOW ADDRESS
971 010004 010102 MOV R1,R2 ;COPY LOW ADDRESS
972 010006 006101 ROL R1 ;SHIFT BIT 15 TO C BIT
973 010010 006100 ROL R0 ;SHIFT INTO HIGH ORDER
974 010012 PRINTB #PRIA0,R0,R2 ;PRINT MEMORY ADDRESS IN ERROR
    010012 010246 MOV R2,-(SP)
    010014 010046 MOV R0,-(SP)
    010016 012746 010040' MOV #PRIA0,-(SP)
    010022 012746 000003 MOV #3,-(SP)
    010026 010600 MOV SP,R0
    010030 104414 TRAP C#PNTB
    010032 062706 000010 ADD #10,SP
975 010036 000207 RTS PC ;RETURN
976
977 010040 045 116 045 PRIA0: .ASCIZ 'NONA MEMORY ERROR ADDRESS = #01#05'
978 .EVEN
979
980
981 .SBTTL PRITADD - PRINT MEMORY TEST ADDRESS
982
983 ;*
984 ;PRINT MEMORY ADDRESS
985 ;THIS ROUTINE IS NORMALLY CALLED ONLY FROM PRINT ROUTINES.
986
987 ; IMPLICIT INPUTS
988
989 ; ERRHI - HIGH ORDER ADDRESS
990 ; ERRLO - LOW ORDER ADDRESS
991
992 ;-
993 010104 PRITADD:
994 010104 SAVREG ;SAVE R1-R5 UNTIL NEXT RETURN
995 010110 013702 002234' MOV ERRHI,R2 ;GET HIGH ADDRESS
996 010114 013701 002236' MOV ERRLO,R1 ;GET LOW ADDRESS
997 ;MOV R1,R2 ;COPY LOW ADDRESS
998 ;ROL R1 ;SHIFT BIT 15 TO C BIT
999 ;ROL R0 ;SHIFT INTO HIGH ORDER
1000 010120 PRINTB #PRIT0,R1 ;PRINT MEMORY ADDRESS LOW IN ERROR
    010120 010146 MOV R1,-(SP)
    010122 012746 010166' MOV #PRIT0,-(SP)
    010126 012746 000002 MOV #2,-(SP)
    010132 010600 MOV SP,R0
    010134 104414 TRAP C#PNTB
    010136 062706 000006 ADD #6,SP
    
```

TSV3 GLOBAL AREAS MACRO M1113 07-FEB-84 10:58
 PRITADJ - PRINT MEMORY TEST ADDRESS

SEQ 045

```

1001 010142          PRINTB  @PRIT1,R2          ;PRINT MEMORY ADDRESS HIGH IN ERROR
      010142 010246  MOV      R2,-(SP)
      010144 012746 010231' MOV      @PRIT1,-(SP)
      010150 012746 000002 MOV      @2,-(SP)
      010154 010600  MOV      SP,R0
      010156 104414  TRAP    C$PNTB
      010160 062706 000006  ADD      @6,SP
1002 010164 000207  RTS      PC          ;RETURN
1003
1004 010166      045      116      045  PRIT0:  .ASCIZ  'NMA MEMORY TEST ADDRESS LOW = '06'
1005 010231      045      116      045  PRIT1:  .ASCIZ  'NMA MEMORY TEST ADDRESS HIGH = '06'
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1044 010276
1045 010276
1046 010302 012737 000764 010470'
1047 010310 012737 140010 010460'
1048 010316 005703
1049 010320 100403
1050 010322 010337 010462'
1051 010326 000407

      .SBTTL  SPACE - SPACE RECORDS (FORWARD AND REVERSE) COMMAND
      ;*
      ;
      ;ROUTINE TO ISSUE A SPACE RECORDS
      ;COMMAND (FORWARD OR REVERSE)
      ;
      ;INPUT:
      ;
      ;      R3      NUMBER OF RECORDS TO BE SPACED OVER
      ;              BIT15 CONTROLS DIRECTION
      ;              BIT15 = 0 IS FORWARD
      ;              BIT15 = 1 IS REVERSE
      ;
      ;      R5      FIRST DEVICE UNIBUS ADDRESS
      ;
      ;      REQUIRES A WRITE CHARACTERISTICS DONE PREVIOUSLY
      ;
      ;OUTPUT:
      ;
      ;      CARRY  SET - SPACE RECORDS COMMAND OK
      ;              CLR - SPACE RECORDS FAILED
      ;
      ;
      ;      R0      THE CONTENTS OF R4 IS MOVED TO R0
      ;
      ;
      ;IMPLICIT OUTPUT:
      ;
      ;      TAPE HAS BEEN MOVED
      ;
      ;SIDE EFFECTS:
      ;
      ;
      ;-
      ;
      SPACE::
      SAVREG          ;SAVE THE GENERAL REGISTERS
      MOV      @500.,SDELAY  ;SET UP DELAY
      MOV      @140010,80$  ;SET UP COMMAND, SPACE FORWARD
      TST      R3          ;CHECK FOR DIRECTION
      BMI      5$          ;BR, IF REVERSE INDICATED
      MOV      R3,90$      ;LOAD UP NUMBER OF RECORDS TO SPACE
      BR      10$         ;GO DO COMMAND

```

TSV3 - GLOBAL AREAS MACRO M1113 07-FEB-84 10:58
 SPACE - SPACE RECORDS (FORWARD AND REVERSE) COMMAND

SEQ 046

```

1052 010330 042703 100000      5$:   BIC      #BIT15,R3      ;CLEAR DIRECTION BIT
1053 010334 010337 010462'    MOV      R3,90$      ;LOAD UP NUMBER OF RECORDS TO SPACE
1054 010340 052737 000400 010460'  BIS      #BIT8,80$   ;SET REVERSE BIT IN COMMAND PACKET
1055 010346 012704 010460'    10$:   MOV      #80$,R4    ;SET UP R4 WITH PACKET ADDRESS
1056 010352 010465 000000      MOV      R4,TSDB(R5) ;SEND OUT COMMAND
1057 010356 004737 016060'    15$:   JSR      PC,WAITF  ;WAIT FOR SSR
1058 010362 103420      BCS      20$         ;BR, IF SSR IS SET AND OK
1059 010364      DELAY    250      ;DELAY ABOUT .25 SECONDS
      010364 012727 000250      MOV      #250,(PC)+
      010370 000000      .WORD    0
      010372 013727 002116'    MOV      L$DLY,(PC)+
      010376 000000      .WORD    0
      010400 005367 177772      DEC      -6(PC)
      010404 001375      BNE      .-4
      010406 005367 177756      DEC      -22(PC)
      010412 001367      BNE      .-20
1060 010414 005337 010470'    DEC      SDELAY      ;BUMP DELAY COUNTER DOWN
1061 010420 001356      BNE      15$         ;BR, IF MORE DELAY
1062 010422 000411      BR       60$         ;BR IF TROUBLE CARRY = CLEAR
1063 010424 016501 000002    20$:   MOV      TSSR(R5),R1 ;READ TSSR
1064 010430 012702 000200      MOV      #SSR,R2    ;SET UP EXPECTED
1065 010434 020201    25$:   CMP      R2,R1      ;ARE THEY OK
1066 010436 001401      BEQ     40$         ;BR, IF EQUAL = OK
1067 010440 000402      BR       60$         ;TROUBLE EXIT
1068 010442 000261    40$:   SEC              ;SET CARRY NO TROUBLE
1069 010444 000401      BR       70$         ;EXIT
1070 010446 000241    60$:   CLC              ;CARRY CLEAR = ERROR
1071 010450    70$:   MOV      R4,R0      ;PASS PACKET ADDRESS
1072 010450 010400      RTS     PC          ;RETURN
1073 010452 000207
1074
1075
1076
1077
1078      ;
1079      ;PACKET FOR SPACE COMMAND
1081 010454      ;
      .BLKB  10-<.-TSV2&7>
1083
1084      ;
1085 010460 000000    80$:   .WORD
1086      ;NUMBER OF RECORDS TO BE SPACED OVER WORD
1087 010462 000000    90$:   .WORD
1088 010464 000000      .WORD
1089 010466 000000      .WORD
1090 010470 000000    SDELAY: .WORD 0      ;DELAY COUNTER
1091      .EVEN
1092
1093
1094      .SBTTL  WRTCHR - WRITE CHARACTERISTICS COMMAND
1095
1096
1097
1098      ;
1099      ;ROUTINE TO ISSUE A WRITE CHARACTERISTICS
1100      ;COMMAND SO THAT OTHER COMMANDS WILL BE ACCEPTED
1101
1102      ;
1103      ;INPUT:
1104
1105      ;

```

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1103      :      R4      ADDRESS OF PACKET FROM TEST
1104      :      R5      FIRST DEVICE UNIBUS ADDRESS
1105      :      REQUIRES A CALL TO SOFINIT BE DONE PREVIOUSLY
1106      :
1107      :OUTPUT:
1108      :
1109      :      R0      TSSR CONTENTS
1110      :      CARRY   SET - WRITE CHARACTERISTICS COMMAND OK
1111      :            CLR - WRITE CHARACTERISTICS FAILED
1112      :
1113      :IMPLICIT OUTPUT:
1114      :
1115      :      MESSAGE BUFFER AND OTHER BUFFERS ALL SET UP
1116      :      SOFTWARE SWITCHES SET AS FOLLOWS:
1117      :            EXTFEA = EXTENDED FEATURES PRESENT
1118      :            BENBSW = BUFFER ENABLE SWITCH ON OR OFF
1119      :
1120      :
1121      :SIDE EFFECTS:
1122      :
1123      :
1124      :-
1125
1126 010472 WRTCHR::
1127 010472      SAVREG      ;SAVE THE GENERAL REGISTERS
1128 010476 005037 002226' CLR      BENBSW      ;CLEAR BUFFER ENABLE SWITCH
1129 010502 005037 002224' CLR      EXTFEA      ;CLEAR EXTENDED FEATURES SW SWITCH
1130 010506 010465 000000 10$: MOV      R4,TSDB(R5)  ;SEND OUT COMMAND
1131 010512 004737 016146' JSR      PC,CHKTSSR ;WAIT FOR SSR
1132 010516 103401      BCS      20$      ;BR, IF SSR IS SET AND OK
1133 010520 000435      BR       60$      ;BR IF TROUBLE CARRY = CLEAR
1134 010522 016501 000002 20$: MOV      TSSR(R5),R1 ;READ TSSR
1135 010526 012702 000200      MOV      @SSR,R2      ;SET UP EXPECTED
1136 010532 032701 000100      BIT      @OFL,R1      ;WAS OFF LINE SET IN TSSR
1137 010536 001402      BEQ      25$      ;BR, IF NO OFL SET
1138 010540 052702 000100      BIS      @OFL,R2      ;MAKE THEM LOOK ALIKE
1139 010544 020201 25$:  CMP      R2,R1      ;ARE THEY OK
1140 010546 001401      BEQ      40$      ;BR, IF EQUAL = OK
1141 010550 000421      BR       60$      ;TROUBLE EXIT
1142 010552 062704 000010 40$: ADD      @8.,R4      ;POINT TO WRT CHARA DATA PACKET
1143 010556 011403      MOV      (R4),R3      ;GET ADDRESS OF MESSAGE BUFFER
1144 010560 032763 000200 000012 BIT      @X2.EXTF,XST2(R3) ;EXTENDED FEATURES BIT SET?
1145 010566 001402      BEQ      45$      ;BR IF NO
1146 010570 005237 002224' INC      EXTFEA      ;SET EXTENDED FEATURES SW SWITCH
1147 010574      45$:
1148 010574 032763 000100 000012 BIT      @X2.BUFE,XST2(R3) ;BUFFER ENABLE SWITCH SET
1149 010602 001402      BEQ      50$      ;BR, IF SWITCH NOT SET
1150 010604 005237 002226' INC      BENBSW      ;SET SOFTWARE SWITCH FOR ENABLED
1151 010610      50$:
1152 010610 000261      SEC      ;SET CARRY NO TROUBLE
1153 010612 000401      BR       70$      ;EXIT
1154 010614 000241      60$: CLC      ;CARRY CLEAR = ERROR
1155 010616 016500 000002 70$: MOV      TSSR(R5),R0 ;RETURN TSSR CONTENTS
1156 010622 000207      RTS      PC      ;RETURN
1157
1158
1159      .SBTTL REWIND - POSITION TAPE (REWIND) COMMAND

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 1187 010624
 1188 010624
 1189 010630 012704 010720'
 1190 010634 010465 000000
 1191 010640 012703 000550
 1192 010644 004737 016060'
 1193 010650 103417
 1194 010652
 010652 012727 000372
 010656 000000
 010660 013727 002116'
 010664 000000
 010666 005367 177772
 010672 001375
 010674 005367 177756
 010700 001367
 1195 010702 005303
 1196 010704 001357
 1197 010706 000241
 1198 010710 010400
 1199 010712 000207
 1200
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 1203 010714
 1205 010720
 1206 010720 102010
 1207 010722 000000
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; *
; THIS ROUTINE WILL REWIND THE SELECTED TAPE.
;
; CAUTION: THE ROUTINE DOES NOT WAIT FOR BOT
; TO ARRIVE. ALSO THE CALLER MUST CHECK FOR
; SSR TO SET IN THE TSSR
;
; CALLING SEQUENCE:
;
; DO A SOFT INIT
; DO A WRITE CHARACTERISTICS
; JSR PC,REWIND
;
; INPUT:
;
; R5 FIRST DEVICE UNIBUS ADDRESS
;
; OUTPUT
;
; R0 THE CONTENTS OF R4 IS PASSED TO R0
;
; -
REWIND::
    SAVREG                                ;SAVE R1-R5 UNTIL NEXT RETURN
    MOV @RWPACK,R4                        ;GET PACKET ADDRESS
    MOV R4,TSDB(R5)                       ;SEND PACKET ADDRESS TO EXECUTE
    MOV #360.,R3                          ;ENOUGH TIME FOR 2400' REEL TO REWIND
    JSR PC,WAITF                          ;WAIT FOR SSR TO SET
    BCS 20$                               ;LEAVE WHEN SSR IS SET
    DELAY 250.                            ;WAIT FOR .25 SECONDS
    MOV #250.,(PC)
    .WORD 0
    MOV L$DLY,(PC)
    .WORD 0
    DEC -6(PC)
    BNE .-4
    DEC -22(PC)
    BNE .-20
    DEC R3                                ;BUMP COUNTER DOWN
    BNE 10$                               ;KEEP GOING
    CLC                                   ;CLEAR CARRY TO SET ERROR
    MOV R4,R0                             ;PASS THE PACKET ADDRESS
    RTS PC                                ;RETURN
;
RWPACK:
    .BLKB 10-<.-TSV2&7>
    .WORD 102010                          ;POSTION COMMAND (REWIND)
    .WORD 0                               ;NOT USED
;
.SBTTL CKRAM - COMPARE RAM TO I/O PACKET
    
```

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```

;*
;
;ROUTINE TO READ THE FIRST 8 BYTES FROM RAM
;MEMORY AND COMPARE THIS DATA TO A COMMAND PACKET.
;
;INPUT:
;
;      R4      ADDRESS OF THE COMMAND PACKET
;      R5      FIRST DEVICE UNIBUS ADDRESS
;
;OUTPUT:
;
;      CARRY   SET - RAM MATCHES PACKET
;             CLR - RAM DOES NOT MATCH PACKET
;
;IMPLICIT OUTPUT:
;
;      THE TABLE RAMDATA IS FILLED WITH THE
;      DATA HELD IN RAM.
;      RAMSIZ IS SET TO 8. FOR PRAMPKT ROUTINE
;
;SIDE EFFECTS:
;
;      THE SUBSYSTEM IS LEFT IN MAINTENANCE MODE
;
;-
  
```

```

CKRAM::
  SAVREG                                ;SAVE THE GENERAL REGISTERS
  MOV      @RAMDATA,R1                   ;ADDRESS TO SAVE THE RAM DATA
  MOV      @RMPKTBEGR,R2                 ;BYTE ADDRESS OF FIRST RAM DATA
  CLR      R3                             ;CLEAR THE ERROR FLAG
  JSR      PC,CHKTSSR                     ;WAIT FOR SSR
  MOV      #0,TSDB(R5)                   ;SET MAINTENANCE MODE
  JSR      PC,CHKTSSR                     ;WAIT FOR SSR TO SET
  MOV      R2,TSDB(R5)                   ;SELECT NEXT RAM ADDRESS
  JSR      PC,CHKTSSR                     ;WAIT FOR SSR TO SET
  MOV      TSBA(R5),(R1)                  ;READ THE RAM DATA
  CMP      (R1)*,(R4)*                    ;COMPARE TO EXPECTED
  BEQ      20$                             ;BRANCH IF OK
  INC      R3                              ;SET ERROR FLAG
  INC      R2                              ;ADDRESS OF NEXT RAM LOCATION
  CMP      R2,@RMPKTEND                   ;REACHED END YET ?
  BLE      10$                             ;BRANCH TILL ALL READ
  TST      R3                              ;WAS AN ERROR FOUND ?
  BEQ      30$                             ;BRANCH IF NOT
  CLC                                         ;CLEAR CARRY TO SHOW ERROR
  BR      50$                             ;AND EXIT
  SEC                                         ;SHOW GOOD COMPARE
  MOV      #8.,RAMSIZ                      ;SETUP RAMSIZ FOR PRAMPKT ROUTINE
  RTS      PC                              ;RETURN
  
```

.SBTTL CKRAM2 - COMPARE RAM TO I/O CHARACTERISTICS DATA

```

;*
;
  
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1268      ;ROUTINE TO READ THE FIRST 8 OR 10 BYTES FROM RAM
1269      ;MEMORY AND COMPARE THIS DATA TO A CHARACTERISTICS DATA BLOCK.
1270      ;
1271      ;INPUT:
1272      ;
1273      ;       R4      ADDRESS OF THE CHARACTERISTICS DATA
1274      ;       R5      FIRST DEVICE UNIBUS ADDRESS
1275      ;
1276      ;OUTPUT:
1277      ;
1278      ;       CARRY   SET - RAM MATCHES PACKET
1279      ;              CLR - RAM DOES NOT MATCH PACKET
1280      ;
1281      ;IMPLICIT OUTPUT:
1282      ;
1283      ;       THE TABLE RAMDATA IS FILLED WITH THE
1284      ;       DATA HELD IN RAM.
1285      ;       RAMSIZ IS SET TO 8. OR 10. FOR PRAMPKT ROUTINE
1286      ;
1287      ;SIDE EFFECTS:
1288      ;
1289      ;       THE SUBSYSTEM IS LEFT IN MAINTENANCE MODE
1290      ;
1291      ;-
1292
1293 011034  CKRAM2::
1294 011034      SAVREG
1295 011040 012701 002240'  MOV      @RAMDATA,R1      ;SAVE THE GENERAL REGISTERS
1296 011044 012702 000167'  MOV      @RMCHBEG,R2     ;ADDRESS TO SAVE THE RAM DATA
1297 011050 005003      CLR      R3              ;BYTE ADDRESS OF FIRST RAM DATA
1298 011052 004737 016146'  JSR      PC,CHKTSSR      ;CLEAR THE ERROR FLAG
1299 011056 112765 000000 000000  MOVB     #0,TSDB(R5)     ;WAIT FOR SSR
1300 011064 004737 016146'  JSR      PC,CHKTSSR      ;SET MAINTENANCE MODE
1301 011070 010265 000000 10$:  MOV      R2,TSDB(R5)     ;WAIT FOR SSR TO SET
1302 011074 004737 016146'  JSR      PC,CHKTSSR      ;SELECT NEXT RAM ADDRESS
1303 011100 116511 000000      MOVB     TSBA(R5),(R1)   ;WAIT FOR SSR TO SET
1304 011104 122124      CMPB     (R1)+,(R4)+    ;READ THE RAM DATA
1305 011106 001401      BEQ      20$           ;COMPARE TO EXPECTED
1306 011110 005203      INC      R3            ;BRANCH IF OK
1307 011112 005202      INC      R2            ;SET ERROR FLAG
1308 011114 012737 000010 002300' 20$:  MOV      #8.,RAMSIZ     ;ADDRESS OF NEXT RAM LOCATION
1309 011122 005737 002224'  TST      EXTFEA         ;ASSUME EXTFEA NOT SET
1310 011126 001407      BEQ      25$           ;IS THE SOFTWARE EXTENDED FEATURES SET
1311 011130 012737 000012 002300'  MOV      #10.,RAMSIZ    ;BR, IF NOT SET
1312 011136 020227 000200      CMP      R2,@RMCHEND    ;SET RAMSIZ FOR EXTEND FEATURES
1313 011142 003750      BLE     10$           ;AT END OF EXTENDED BUFFER
1314 011144 000403      BR      27$           ;BR, IF NOT AT END YET
1315 011146 020227 000176 25$:  CMP      R2,@RMCHEND-2  ;AT END BRANCH
1316 011152 003744      BLE     10$           ;REACHED END YET ?
1317 011154 005703 27$:  TST      R3            ;BRANCH TILL ALL READ
1318 011156 001402      BEQ     30$           ;WAS AN ERROR FOUND ?
1319 011160 000241      CLC              ;BRANCH IF NOT
1320 011162 000401      BR      50$         ;CLEAR CARRY TO SHOW ERROR
1321 011164 000261 30$:  SEC              ;AND EXIT
1322 011166 000207 50$:  RTS      PC          ;SHOW GOOD COMPARE
1323
1324

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1325          .SBTTL CKMSG - COMPARE WRITE CHAR. MESSAGE BUFFERS
1326          ;*
1327          ;
1328          ;ROUTINE TO COMPARE A WRITE CHARACTERISTICS EXPD AND RECV
1329          ;BUFFER. THE EXPECTED AND RECEIVED BUFFERS ARE STORED FOR
1330          ;ERROR PRINT ROUTINES.
1331          ;
1332          ;INPUT:
1333          ;
1334          ;      R0      RECV MESSAGE BUFFER HIGH ORDER ADDRESS
1335          ;      R1      RECV MESSAGE BUFFER LOW ORDER ADDRESS
1336          ;      R2      EXPD MESSAGE BUFFER ADDRESS
1337          ;OUTPUT:
1338          ;
1339          ;      CARRY   SET - MESSAGE BUFFERS MATCH
1340          ;      CLR    -MESSAGE BUFFERS DON'T MATCH
1341          ;
1342          ;IMPLICIT OUTPUT:
1343          ;
1344          ;      EXPMSG   BUFFER IS SET TO EXPD DATA
1345          ;      RECVMSG  BUFFER IS SET TO RECV DATA
1346          ;      RCVHIADD SET TO HIGH ORDER ADDRESS OF RECV
1347          ;      RCVLOADD SET TO LOW ORDER ADDRESS OF RECV
1348          ;
1349          ;-
1350          CKMSG::
1351          SAVREG          ;SAVE R1-R5 UNTIL NEXT RETURN
1352          MOV            R0,RCVHIADD ;SAVE RECV HIGH ADDRESS
1353          MOV            R1,RCVLOAD ;SAVE RECV LOW ADDRESS
1354          TST            KTENABLE   ;TESTING ABOVE 28K?
1355          BEQ            10$        ;BR IF NO
1356          JSR            PC,SETMAP  ;RETURN ADDRESS BIASED TO PAR6 IN R0
1357          MOV            R0,R1      ;GET RETURNED ADDRESS BIASED TO PAR6
1358          10$:          CLR            R4      ;WORD IN BUFFER
1359          CLR            R3        ;CLEAR ERROR SEEN FLAG
1360          MOV            R2,R5      ;GET EXPD BUFFER ADDRESS
1361          15$:          MOV            (R2),EXPMSG(R4) ;SAVE EXPD FOR ERROR REPORT
1362          MOV            (R1),RECVMSG(R4) ;SAVE RECV FOR ERROR REPORT
1363          CMP            (R2)+,(R1)+ ;EXPD EQUAL RECV?
1364          BEQ            25$        ;BR IF YES
1365          INC            R3        ;SET ERROR SEEN FLAG
1366          25$:          ADD            #2,R4   ;POINT TO NEXT WORD ADDRESS
1367          CMP            R4,#14    ;DONE FIRST 7 WORDS?
1368          BLE            15$        ;BR IF NO
1369          BIT            #X2.EXTF,XST2(R5);IS EXTENDED FEATURES SET IN EXPD?
1370          BEQ            50$        ;BR IF NO
1371          CMP            R4,#16    ;DONE EXTENDED FEATURES WORD?
1372          BLE            15$        ;BR IF NO
1373          50$:          TST            R3      ;ANY ERRORS SEEN?
1374          BEQ            55$        ;BR IF NO
1375          CLC            ;SET FAILURE
1376          BR            60$        ;
1377          55$:          SEC            ;SET SUCCESS
1378          60$:          RTS            PC     ;RETURN
1379
1380          .SBTTL CKMSG2 - COMPARE EXPD RECV MESSAGE BUFFERS
1381

```

```

1382      ;*
1383      ;
1384      ;ROUTINE TO COMPARE AN EXPECTED AND RECEIVED MESSAGE
1385      ;BUFFER. THE EXPECTED AND RECEIVED BUFFERS ARE STORED FOR
1386      ;ERROR PRINT ROUTINES.
1387      ;
1388      ;INPUT:
1389      ;
1390      ;      R0      RECV MESSAGE BUFFER HIGH ORDER ADDRESS
1391      ;      R1      RECV MESSAGE BUFFER LOW ORDER ADDRESS
1392      ;      R2      EXPD MESSAGE BUFFER ADDRESS
1393      ;      R3      NUMBER OF BYTES TO COMPARE
1394      ;
1395      ;OUTPUT:
1396      ;
1397      ;      CARRY   SET - MESSAGE BUFFERS MATCH
1398      ;      CLR    - MESSAGE BUFFERS DON'T MATCH
1399      ;
1400      ;IMPLICIT OUTPUT:
1401      ;
1402      ;      EXPMSG   BUFFER IS SET TO EXPD DATA
1403      ;      RECVMSG  BUFFER IS SET TO RECV DATA
1404      ;      RCVHIADD SET TO HIGH ORDER ADDRESS OF RECV
1405      ;      RCVLOADD SET TO LOW ORDER ADDRESS OF RECV
1406      ;
1407      ;
1408      CKMSG2::
1409      SAVREG          ;SAVE R1-R5 UNTIL NEXT RETURN
1410      CMP             R3,#RECVMSG-EXPMSG;@@D IS COUNT ABOVE MAX ALLOWED?
1411      BLE             5$ ;@@D BR IF NO
1412      MOV             #RECVMSG-EXPMSG,R3;@@D
1413      PRINTF          #DEBUGMSG ;@@D
1414      MOV             #DEBUGMSG,-(SP)
1415      MOV             #1,-(SP)
1416      MOV             SP,R0
1417      TRAP           C$PNTF
1418      ADD             #4,SP
1419      MOV             R0,RCVHIADD ;SAVE RECV HIGH ADDRESS
1420      MOV             R1,RCVLOADD ;SAVE RECV LOW ADDRESS
1421      TST             KTENABLE ;TESTING ABOVE 28K?
1422      BEQ             10$ ;BR IF NO
1423      JSR             PC,SETMAP ;RETURN ADDRESS BIASED TO PAR6 IN R0
1424      MOV             R0,R1 ;GET RETURNED ADDRESS BIASED TO PAR6
1425      CLR             R4 ;WORD IN BUFFER
1426      CLR             R5 ;CLEAR ERROR SEEN FLAG
1427      MOV            (R2),EXPMSG(R4) ;SAVE EXPD FOR ERROR REPORT
1428      MOV            (R1),RECVMSG(R4) ;SAVE RECV FOR ERROR REPORT
1429      CMPB           (R2)+,(R1)+ ;EXPD EQUAL RECV?
1430      BEQ             25$ ;BR IF YES
1431      INC             R5 ;SET ERROR SEEN FLAG
1432      ADD             #1,R4 ;POINT TO NEXT BYTE
1433      CMP             R4,R3 ;DONE ALL BYTES?
1434      BGE             50$ ;BR IF YES
1435      BR              15$ ;DO NEXT BYTE
1436      TST             R5 ;ANY ERRORS SEEN?
1437      BEQ             55$ ;BR IF NO
1438      CLC             ;SET FAILURE
    
```

```

1434 011434 000401
1435 011436 000261
1436 011440 000207
1437
1438 011442 120 122 117 DEBUGMSG: .ASCIZ 'PROGRAM INTERNAL ERROR -CKMSG2 MESSAGE BUFFER EXCEEDED-' ;@@D
1439 011532 045 116 045 FERCM: .ASCII /@NWA ***/
1440 011543 040 040 124 ERCM: .ASCIZ / TSSR ERROR CODE REC'D = /
1441 011576 056 056 056 SIMSG: .ASCIZ /... AFTER DOING SOFT INIT/
1442 011631 124 105 123 TINERR: .ASCIZ /TEST: .../
1443 .EVEN
1444
1445
1446 ;*
1447 ;
1448 ;PRINT ROUTINE TO FATAL SOFT INIT ERRORS
1449 ;
1450 ;INPUT:
1451 ;
1452 ; R1 CONTENTS OF TSSR AT ERROR
1453 ;
1454 ;SIDE EFFECTS:
1455 ;
1456 ; EXECUTES DROP UNIT TO CEASE TESTING
1457 ;
1458 ;-
1459
1460 011644 BGNMSG SFMSG
1461 011644 SFMSG:: JSR PC,PRITSSR ;PRINT CONTENTS OF TSSR REGISTER
1462 011650 004737 005634' JSR PC,CKDROP ;DROP UNIT, IF ALLOWED
1463 011654 ENDMSG
1464 011654 104423 L10003: TRAP CMSG
1465
1466 ;*
1467 ;PRINT ROUTINE TO PRINT THE CONTENTS OF
1468 ;TSSR AND A COMMAND PACKET OTHER THAN GET STATUS COMMAND PACKET.
1469 ;
1470 ;INPUTS:
1471 ;
1472 ; R1 TSSR CONTENTS
1473 ; R4 ADDRESS OF COMMAND PACKET
1474 ;
1475 ;-
1476 011656 BGNMSG PKTSSR
1477 011656 PKTSSR:: JSR PC,PRITSSR ;PRINT THE CONTENTS OF TSSR REGISTER
1478 011662 004737 005634' MOV @4,R0 ;NO. OF WORDS IN PACKET
1479 011666 004737 007200' JSR PC,PRIPKT ;PRINT THE CONTENTS OF COMMAND PACKET
1480 011672 ENDMSG
1481 011672 104423 L10004: TRAP CMSG
1482
1483 ;*
1484 ;PRINT ROUTINE TO PRINT THE CONTENTS OF
;TSSR AND A GET STATUS COMMAND PACKET.

```

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1485
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1491
1492
1493 011674
      011674
1494 011674 004737 005634'
1495 011700 012700 000002
1496 011704 004737 007200'
1497 011710
      011710
      011710 104423
1498
1499
1500
1501
1502
1503
1504
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1506
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1509 011712
      011712
1510 011712 004737 005634'
1511 011716
      011716
      011716 104423
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1526
1527 011720
      011720
1528 011720 004737 005634'
1529 011724 010200
1530 011726 010301
1531 011730 004737 014052'
1532 011734
      011734
      011734 104423

;
; INPUTS:
;
; R1 TSSR CONTENTS
; R4 ADDRESS OF COMMAND PACKET
;
;
; BGNMSG PKTGETS
PKTGETS::
; JSR PC,PRITSSR ;PRINT THE CONTENTS OF TSSR REGISTER
; MOV #2,R0 ;NO. OF WORDS IN GET STATUS PACKET
; JSR PC,PRIPKT ;PRINT THE CONTENTS OF COMMAND PACKET
; ENDMSG
L10005:
; TRAP C#MSG

;
; PRINT TSSR ERRORS FOR INITIALIZATION TESTS
;
; INPUTS:
;
; R1 TSSR CONTENTS
; R4 ADDRESS OF COMMAND PACKET
;
;
; BGNMSG SFFMSG
SFFMSG::
; JSR PC,PRITSSR ;PRINT CONTENTS OF TSSR REGISTER
; ENDMSG
L10006:
; TRAP C#MSG

;
; .SBTTL PKTMES - PRINT TSSR AND MESSAGE BUFFER
;
; PRINT ROUTINE TO PRINT THE CONTENTS OF TSSR AND MESSAGE
; BUFFER FOR ERROR REPORTS
;
; INPUTS:
;
; R1 CONTENTS OF TSSR
; R2 LOW ORDER MESSAGE BUFFER
; R3 HIGH ORDER MESSAGE BUFFER ADDRESS
; NOTE: R3 IS IGNORED IF KTENABLE FLAG IS CLEAR
;
;
; BGNMSG PKTMES
PKTMES::
; JSR PC,PRITSSR ;PRINT CONTENTS OF TSSR
; MOV R2,R0 ;LOW ORDER ADDRESS
; MOV R3,R1 ;HIGH ORDER ADDRESS
; JSR PC,PRMESS ;PRINT THE MESSAGE BUFFER
; ENDMSG
L10007:
; TRAP C#MSG

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1547 011736
      011736
1548 011736 004737 010104'
1549 011742 016501 000002'
1550 011746 004737 005634'
1551 011752
      011752
      011752 104423
1552
1553
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1555
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1562
1563
1564
1565
1566 011754
      011754
1567 011754 012700 000007'
1568 011760 005737 002224'
1569 011764 001402
1570 011766 012700 000010'
1571 011772 004737 014362'
1572 011776
      011776
      011776 104423
1573
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```

      .SBTTL ADDSSR - PRINT TEST ADDRESS AND TSSR
;
;PRINT ROUTINE TO PRINT THE CONTENTS OF
;TSSR AND A MEMORY TEST ADDRESS
;
;INPUTS:
;
;      R5      FIRST DEVICE UNIBUS ADDRESS
;      ERRHI   HIGH ORDER MEMORY TEST ADDRESS
;      ERRLO   LOW ORDER MEMORY TEST ADDRESS
;-
      BGNMSG  ADDSSR
ADDSSR::
      JSR     PC,PRITADD      ;PRINT MEMORY TEST ADDRESS
      MOV     TSSR(R5),R1    ;GET CURRENT TSSR
      JSR     PC,PRITSSR     ;PRINT THE CONTENTS OF TSSR REGISTER
      ENDMSG
L10010:
      TRAP    C#MSG

```

```

      .SBTTL MSGEXP - PRINT WRITE CHAR. EXPD-RCV MESSAGE BUFFERS
;
;PRINT ROUTINE TO PRINT WRITE CHARACTERISTIC MESSAGE BUFFER
;
;IMPLICIT INPUTS:
;
;      EXPMSG  - EXPECTED MESSAGE BUFFER
;      RECMSG  - RECEIVED MESSAGE BUFFER
;      RCVHIADD- RECEIVED MESSAGE BUFFER HIGH ORDER ADDRESS
;      RCVLOADD- RECEIVED MESSAGE BUFFER LOW ORDER ADDRESS
;-
      BGNMSG  MSGEXP
MSGEXP::
      MOV     #7,R0          ;ASSUME NO EXT FEATURES
      TST     EXTFEA        ;EXT FEATURES SET?
      BEQ     5$            ;BR IF NO
      MOV     #8.,R0        ;EXT FEATURE BUFFER IS 8 WORDS
      JSR     PC,PRMSGEXP   ;PRINT EXPD/RCV MESSAGE BUFFERS
      ENDMSG
L10011:
      TRAP    C#MSG

```

```

      .SBTTL FIFEXP - PRINT FIFO EXP/RCV DATA
;
;PRINT ROUTINE TO PRINT FIFO EXP/RCV DATA
;
;      R1      - BYTE COUNT
;
;IMPLICIT INPUTS:
;
;      EXPMSG  - EXPECTED MESSAGE BUFFER (CONTAINS FIFO DATA ONLY

```



```

1584          :      RECMMSG - RECEIVED MESSAGE BUFFER (CONTAINS FIFO DATA ONLY)
1585          :-
1586 012000    :      BGNMSG FIFEXP
          012000
1587 012000    FIFEXP::
          012000    010146    PRINTX #FIF1MSG,R1      ;PRINT BYTES TRANSFERRED
          012002    012746    012052'  MOV R1,-(SP)
          012006    012746    000002    MOV #FIF1MSG,-(SP)
          012012    010600    MOV #2,-(SP)
          012014    104415    MOV SP,R0
          012016    062706    000006    TRAP C#PNTX
1588 012022    ADD #6,SP
          012022    012746    012121'  PRINTX #FIF2MSG      ;PRINT HEADER MSG
          012026    012746    000001    MOV #FIF2MSG,-(SP)
          012032    010600    MOV #1,-(SP)
          012034    104415    MOV SP,R0
          012036    062706    000004    TRAP C#PNTX
1589 012042    ADD #4,SP
          012044    010100    MOV R1,R0      ;GET BYTE COUNT
1590 012044    004737    014732'  JSR PC,PRBYTEXP ;PRINT FIFO BYTES IN ERROR
1591 012050    ENDMMSG
          012050
          012050    L10012:
1592 012052    TRAP C#MSG
          012052    045      116      045 FIF1MSG: .ASCIZ '#N#A NUMBER OF BYTES TRANSFERRED = #D2'
1593 012121    045      116      045 FIF2MSG: .ASCIZ '#N#A FIFO DATA BYTES IN ERROR:'
1594          .EVEN
1595
1596          .SBTTL MSGSTAT - PRINT STATUS HEADER AND MESSAGE BUFFERS
1597          ;
1598          ;
1599          ;PRINT ROUTINE TO PRINT MESSAGE BUFFER EXPD/RCV
1600          ;
1601          ;
1602          ;IMPLICIT INPUTS:
1603          ;
1604          ; EXPMSG - EXPECTED MESSAGE BUFFER
1605          ; RECMMSG - RECEIVED MESSAGE BUFFER
1606          ; RCVHIADD- RECEIVED MESSAGE BUFFER HIGH ORDER ADDRESS
1607          ; RCVLOADD- RECEIVED MESSAGE BUFFER LOW ORDER ADDRESS
1608          ;
1609 012160    BGNMSG MSGSTAT
          012160
1610 012160    MSGSTAT::
          012160    012701    012222'  MOV #STATCOD,R1      ;ASCII ADDRESS TABLE
1611 012164    012100    MOV (R1),R0      ;DONE ALL MSG LINES?
1612 012166    001410    BEQ 20$          ;BR IF YES
1613 012170    PRINTX R0      ;PRINT STATUS BIT NAMES
          012170    010046    MOV R0,-(SP)
          012172    012746    000001    MOV #1,-(SP)
          012176    010600    MOV SP,R0
          012200    104415    TRAP C#PNTX
          012202    062706    000004    ADD #4,SP
1614 012206    BR 10$
          012210    000766
          012210    012700    000012    20$: MOV #10,R0      ;DO ANOTHER MSG LINE
1615 012214    004737    014362'  JSR PC,PRMSGEXP ;NUMBER OF WORDS IN A READ STATUS BUFFER
1616 012220    ENDMMSG ;PRINT EXPD/RCV MESSAGE BUFFERS
          012220
          012220    L10013:
1617          TRAP C#MSG
          012220    104423
1618

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```

1619 012222 012240' 012302' 012373' STATCOD: .WORD 1$,2$,3$,4$,5$,6$,0
1620 012240 045 116 045 1$: .ASCIZ '%N%A Tape Bus Signals in Word #8:'
1621 012302 045 116 045 2$: .ASCIZ '%N%A PARERR<15> IEOT <12> IFMK <9> IRDY<6> IRWD<2>'
1622 012373 045 116 045 3$: .ASCIZ '%N%A IRESV2<14> IIDENT<11> IHER <8> IONL<5> IFBY<1>'
1623 012464 045 116 045 4$: .ASCIZ '%N%A IRESV1<13> ICER <10> ISPEED<7> ILDP<4> IFPT<0>'
1624 012555 045 116 045 5$: .ASCIZ '%N%A Tape Bus Signals in Word #9:'
1625 012617 045 116 045 6$: .ASCIZ '%N%A DATMIS<7> ILW<6> OUTRDY<5> INRDY<4>'

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1629

.SBTTL MSGLOOP - PRINT LOOPBACK HEADER AND MESSAGE BUFFERS

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1632
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1641

```

; *
; PRINT ROUTINE TO PRINT MESSAGE BUFFER EXPD/RCV
; IMPLICIT INPUTS:
; EXPMSG - EXPECTED MESSAGE BUFFER
; RECMMSG - RECEIVED MESSAGE BUFFER
; RCVHIADD- RECEIVED MESSAGE BUFFER HIGH ORDER ADDRESS
; RCVLOADD- RECEIVED MESSAGE BUFFER LOW ORDER ADDRESS
; -
BGNMSG MSGLOOP
MSGLOOP:
MOV @LOOPCOD,R1 ;ASCII ADDRESS TABLE
10$: MOV (R1)+,RO ;DONE ALL MSG LINES?
BEQ 20$ ;BR IF YES
PRINTX RO ;PRINT STATUS BIT NAMES
MOV RO,-(SP)
MOV @1,-(SP)
MOV SP,RO
TRAP C$PNTX
ADD @4,SP
BR 10$ ;DO ANOTHER MSG LINE
20$: MOV @10,,RO ;NUMBER OF WORDS IN A READ STATUS BUFFER
JSR PC,PRMSGEXP ;PRINT EXPD/RCV MESSAGE BUFFERS
ENDMSG
L10014: TRAP C$MSG

```

```

1642 012674 012701 012736'
012674
1643 012674 012701 012736'
1644 012700 012100
1645 012702 001410
1646 012704 010046
012704 012746 000001
012712 010600
012714 104415
012716 062706 000004
1647 012722 000766
1648 012724 012700 000012
1649 012730 004737 014362'
1650 012734 104423
012734
012734

```

```

1651
1652 012736 012756' 013031' 013130' LOOPCOD: .WORD 1$,2$,3$,4$,5$,6$,7$,0
1653 012756 045 116 045 1$: .ASCIZ '%N%A Tape Bus Loopback Signals in Word #8:'
1654 013031 045 116 045 2$: .ASCIZ '%N%A PARERR<15> IRESV2<14> IRESV1<13>'
1655 013130 045 116 045 3$: .ASCIZ '%N%A IHISP=>IEOT<12> IWRT=>IIDENT<11> IREV =>ICER <10>'
1656 013227 045 116 045 4$: .ASCIZ '%N%A IWFM =>IFMK<09> IEDIT=>IHER <08> IFAD =>ISPEED<07>'
1657 013326 045 116 045 5$: .ASCIZ '%N%A ITADO=>IRDY<06> ITAD1=>IONL <05> IERASE=>ILDP <04>'
1658 013425 045 116 045 6$: .ASCIZ '%N%A IREW =>IDBY<03> IRWU =>IRWD <02> IFEN =>IFBY <01>'
1659 013524 045 116 045 7$: .ASCIZ '%N%A IGO =>IFPT<00>'

```

1660
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1667

.SBTTL MSGSUB - PRINT WRITE SUBSYSTEM MESSAGE BUFFER

```

; *
; PRINT ROUTINE TO PRINT MESSAGE BUFFER EXPD/RCV
;
;

```

```

1668      ;IMPLICIT INPUTS:
1669      ;
1670      ;     EXPMSG  - EXPECTED MESSAGE BUFFER
1671      ;     RECMSG  - RECEIVED MESSAGE BUFFER
1672      ;     RCVHIADD- RECEIVED MESSAGE BUFFER HIGH ORDER ADDRESS
1673      ;     RCVLOADD- RECEIVED MESSAGE BUFFER LOW ORDER ADDRESS
1674      ;
1675      ;-      BGNMSG  MSGSUB
1676      MSGSUB::      MOV      #10.,R0      ;SIZE OF WRITE SUBSYSTEM BUFFER
1677      ;           JSR      PC,PRMSGEXP ;PRINT EXPD/RCV MESSAGE BUFFERS
1678      ;           ENDMSG
1679      ;
1680      ;
1681      ;
1682      ;
1683      ;
1684      ;           .SBTTL  MEMADD - PRINT MEMORY ADDRESS DATA ERROR
1685      ;*
1686      ;
1687      ;PRINT ROUTINE TO PRINT MEMORY ADDRESS DATA COMPARE ERROR
1688      ;
1689      ;IMPLICIT INPUTS:
1690      ;
1691      ;     ERRHI   - MEMORY ERROR HIGH ORDER ADDRESS
1692      ;     ERRLO   - MEMORY ERROR LOW ORDER ADDRESS
1693      ;     EXP     - EXPECTED DATA
1694      ;     RECV    - RECEIVED DATA
1695      ;
1696      ;-      BGNMSG  MEMADD
1697      MEMADD::      JSR      PC,PRIADD ;PRINT MEMORY ADDRESS IN ERROR
1698      ;           MOV      EXPD,R1      ;GET EXPD DATA
1699      ;           MOV      RECV,R2      ;GET RECEIVED DATA
1700      ;           JSR      PC,PRIXOR    ;PRINT EXPD/RCV
1701      ;           ENDMSG
1702      ;
1703      ;
1704      ;
1705      ;
1706      ;           .SBTTL  PRAMPKT - PRINT RAM AND PACKET DATA
1707      ;*
1708      ;
1709      ;PRINT ROUTINE TO DISPLAY RAM/PACKET DATA
1710      ;WHEN THE RAM DATA DOES NOT MATCH.
1711      ;
1712      ;INPUTS:
1713      ;
1714      ;     R4      POINTER TO COMMAND PACKET
1715      ;
1716      ;IMPLICIT INPUTS:
1717      ;
1718      ;     RAMDATA  DATA AS READ FROM THE RAM
1719      ;     RAMSIZ  NUMBER OF BYTES IN PACKET
1720      ;             IF RAMSIZ=0 THEN DEFAULT TO 8.

```

```

1719                                     ;IMPLICIT OUTPUTS:
1720                                     ;
1721                                     ;   RAMSIZ  SET TO 0
1722                                     ;-
1723
1724 013606                                PRAMPKT:
1725 013606                                SAVREG                                ;SAVE R1-R5 UNTIL NEXT RETURN
1726 013612 012701 002240'                MOV   @RAMDATA,R1                    ;DATA FROM THE RAM
1727 013616 005002                        CLR   R2                                ;INIT BYTE NUMBER
1728 013620 122124                        5#:  CMPB  (R1),.(R4).                ;COMPARE EXPECTED, RECEIVED
1729 013622 001005                        BNE   7#                                ;BR IF NO MATCH
1730 013624                                FORCERROR      7#,NOTSSR
1731 013634 000436                        BR    10#                                ;@@D
1732 013636 116105 177777                7#:  MOVB -1(R1),R5                    ;GET RECV RAM DATA
1733 013642 116403 177777                MOVB -1(R4),R3                    ;GET EXPD PACKET DATA
1734 013646                                XOR   R5,R3                            ;XOR EXPD/RECV
1735 013656 042703 177400                BIC   @177400,R3                    ;LOW BYTE ONLY
1736 013662 116137 177777 002232'        MOVB -1(R1),RECV                    ;GET RECEIVED RAM DATA
1737 013670 116437 177777 002230'        MOVB -1(R4),EXPD                    ;GET EXPECTED RAM DATA
1738 013676                                PRINTB @RAMASC,R2,RECV,EXPD,R3
1739 013732 005202                        MOV   R3,-(SP)
1740 013734 005737 002300'                MOV   EXPD,-(SP)
1741 013740 001404                        MOV   RECV,-(SP)
1742 013742 020237 002300'                MOV   R2,-(SP)
1743 013746 003724                        MOV   @RAMASC,-(SP)
1744 013750 000403                        MOV   @5,-(SP)
1745 013752 020227 000010                MOV   SP,R0
1746 013756 002720                        TRAP  C#PNTB
1747 013760 005037 002300'                ADD   @14,SP
1748 013764 000207                        10#: INC   R2                                ;UPDATE BYTE COUNT
1749                                     ;
1750 013766      045      116      045 RAMASC: .ASCIZ 'N#A BYTE: #D#A RAM: #03#A Packet: #03#A XOR:#03'
1751                                     ;
1752                                     ;
1753                                     ;.SBTTL  PRMESS - PRINT CONTENTS OF MESSAGE BUFFER
1754                                     ;*
1755                                     ;
1756                                     ;THIS ROUTINE PRINTS THE CONTENTS OF
1757                                     ;THE 7 OR 8 WORD MESSAGE BUFFER RETURNED BY THE
1758                                     ;TSV-05.
1759                                     ;
1760                                     ;INPUT:
1761                                     ;
1762                                     ;   R0      LOW ORDER ADDRESS OF MESSAGE BUFFER
1763                                     ;   R1      HIGH ORDER ADDRESS OF MESSAGE BUFFER
1764                                     ;   NOTE: R1 IS IGNORED IF KTENABLE FLAG IS CLEAR
1765                                     ;
1766                                     ;THIS ROUTINE IS NORMALLY CALLED FROM A PRINT ROUTINE

```

```

1767      ;
1768      ; -
1769
1770      PRMESS:
1771      SAVREG          ;SAVE THE REGISTERS
1772      MOV            RO,R5          ;SAVE LOW ORDER ADDRESS
1773      TST            KTENABLE      ;ADDRESS ABOVE 28K?
1774      BNE            10$           ;BR IF YES
1775      CLR            R1             ;SET HIGH ORDER ADDRESS TO 0
1776      10$:          MOV            R1,R3          ;SAVE HIGH ORDER ADDRESS
1777      ROL            RO             ;SHIFT BIT15 TO C BIT
1778      ROL            R1             ;SHIFT TO HIGH ORDER FOR PRINTOUT
1779      PRINTX        @PROASC,R1,R5  ;PRINT MESSAGE BUFFER ADDRESS
1780      MOV            R5,-(SP)
1781      MOV            R1,-(SP)
1782      MOV            @PROASC,-(SP)
1783      MOV            @3,-(SP)
1784      MOV            SP,RO
1785      TRAP          C$PNTX
1786      ADD            @10,SP
1787      PRINTX        @PR1ASC          ;PRINT HEADER FOR CONTENTS
1788      MOV            @PR1ASC,-(SP)
1789      MOV            @1,-(SP)
1790      MOV            SP,RO
1791      TRAP          C$PNTX
1792      ADD            @4,SP
1793      CLR            R4             ;NUMBER OF THE NEXT WORD
1794      MOV            R5,R1          ;COPY LOW ORDER ADDRESS
1795      MOV            R3,RO          ;COPY HIGH ORDER ADDRESS
1796      BEQ            20$           ;BR IF NOT ABOVE 28K
1797      JSR            PC,SETMAP      ;SETUP PAR ADDRESS IN RO
1798      MOV            RO,R5          ;GET PAR FORMAT ADDRESS ABOVE 28K
1799      20$:          PRINTX        @PRASC,R4,(R5)* ;PRINT THE CONTENTS OF MEMORY BUFFER
1800      MOV            (R5)*,-(SP)
1801      MOV            R4,-(SP)
1802      MOV            @PRASC,-(SP)
1803      MOV            @3,-(SP)
1804      MOV            SP,RO
1805      TRAP          C$PNTX
1806      ADD            @10,SP
1807      INC            R4             ;NUMBER OF THE NEXT
1808      CMP            R4,@7          ;DONE ALL YET ?
1809      BGT            50$           ;BRANCH IF ALL DONE
1810      BLT            20$           ;PRINT FIRST 7 WORDS
1811      BIT            @X2.EXTF,XST2(R3);EXTENDED FEATUTES ON ?
1812      BNE            20$           ;PRINT EXTENDED STATUS WORD
1813      50$:          RTS            PC          ;RETURN
1814
1815      045 PROASC: .ASCIZ  'N$A Message Buffer Address = #01#05'
1816      045 PR1ASC: .ASCIZ  'N$A Message Buffer Contents:'
1817      045 PRASC:  .ASCIZ  'N$A Word#D1#A: #0'
1818      .EVEN
1819
1820      .SBTTL PRMSGEXP - PRINT EXPD/RCV MESSAGE BUFFERS
1821      ;*
1822      ;
1823      ;ROUTINE TO PRINT EXPECTED AND RECEIVED MESSAGE BUFFERS

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1815 014362
1816 014362
1817 014366 010005
1818 014370 013700 002304'
1819 014374 010004
1820 014376 013701 002302'
1821 014402 006100
1822 014404 006101
1823 014406
    014406 010446
    014410 010146
    014412 012746 014542'
    014416 012746 000003
    014422 010600
    014424 104415
    014426 062706 000010
1824 014432
    014432 012746 014607'
    014436 012746 000001
    014442 010600
    014444 104415
    014446 062706 000004
1825 014452 005004
1826 014454 012701 002320'
1827 014460 012702 002464'
1828 014464 011100
1829 014466 011203
1830 014470
1831 014500
    014500 010346
    014502 012246
    014504 012146
    014506 010446
    014510 012746 014645'
    014514 012746 000005
    014520 010600
    014522 104415
    014524 062706 000014
1832 014530 005204
1833 014532 020405
1834 014534 002001
1835 014536 000752
1836 014540 000207
1837
1838 014542 045 116 045 PRMSG0: .ASCIZ '#N#A Message Buffer Address = #01#05'
1839 014607 045 116 045 PRMSG1: .ASCIZ '#N#A Message Buffer Contents:'
1840 014645 045 116 045 PRMSG2: .ASCIZ '#N#A WORD #D2#A EXPD: #06#A RECV: #06#A XOR: #06#A'

:
: RO - NUMBER OF WORDS IN BUFFER
:
: IMPLICIT INPUTS:
:
: EXPMSG - EXPECTED MESSAGE BUFFER
: RECVMSG - RECEIVED MESSAGE BUFFER
: RCVHIADD - RECEIVED MESSAGE BUFFER HIGH ORDER ADDRESS
: RCVLOADD - RECEIVED MESSAGE BUFFER LOW ORDER ADDRESS
:
: -
PRMSGEXP:
    SAVREG ;SAVE R1-R5 UNTIL NEXT RETURN
    MOV RO,R5 ;SAVE NUMBER OF WORDS
    MOV RCVLOADD,RO ;GET RECV LOW ADDRESS
    MOV RO,R4 ;COPY LOW ADDRESS
    MOV RCVHIADD,R1 ;GET RECV HIGH ADDRESS
    ROL RO ;SHIFT BIT15 TO C BIT
    ROL R1 ;SHIFT TO HIGH ORDER FOR PRINTOUT
    PRINTX @PRMSG0,R1,R4 ;PRINT MESSAGE BUFFER ADDRESS
    MOV R4,-(SP)
    MOV R1,-(SP)
    MOV @PRMSG0,-(SP)
    MOV @3,-(SP)
    MOV SP,RO
    TRAP C:PNTX
    ADD @10,SP
    PRINTX @PRMSG1 ;PRINT HEADER FOR CONTENTS
    MOV @PRMSG1,-(SP)
    MOV @1,-(SP)
    MOV SP,RO
    TRAP C:PNTX
    ADD @4,SP
    CLR R4 ;NUMBER OF THE CURRENT WORD
    MOV @EXPMSG,R1 ;GET EXPD BUFFER ADDRESS
    MOV @RECVMSG,R2 ;GET RECV BUFFER ADDRESS
    20$: MOV (R1),RO ;GET EXPD
    MOV (R2),R3 ;GET RECV
    XOR RO,R3 ;XOR EXPD/RECV
    PRINTX @PRMSG2,R4,(R1)*,(R2)*,R3
    MOV R3,-(SP)
    MOV (R2)*,-(SP)
    MOV (R1)*,-(SP)
    MOV R4,-(SP)
    MOV @PRMSG2,-(SP)
    MOV @5,-(SP)
    MOV SP,RO
    TRAP C:PNTX
    ADD @14,SP
    INC R4 ;NUMBER OF THE NEXT
    CMP R4,R5 ;DONE ALL YET?
    BGE 50$ ;BR IF YES
    BR 20$ ;DO ANOTHER
    50$: RTS PC ;RETURN
    
```

```

1841 .EVEN
1842
1843 .SBTTL PRBYTEXP - PRINT ERROR BYTES IN EXP/REC MESSAGE BUFFER
1844
1845 ;*
1846 ;ROUTINE TO PRINT ERROR BYTES IN MESSAGE BUFFERS
1847 ; ONLY THE FIRST 8 ERRORS ENCOUNTERED ARE PRINTED DUE TO SCREEN SPACE
1848 ;
1849 ; R0 - NUMBER OF BYTES IN BUFFER
1850 ;
1851 ;IMPLICIT INPUTS:
1852 ;
1853 ; EXPMSG - EXPECTED MESSAGE BUFFER
1854 ; RECMSG - RECEIVED MESSAGE BUFFER
1855 ;-
1856 014732 PRBYTEXP::
1857 014732 SAVREG ;SAVE R1-R5 UNTIL NEXT RETURN
1858 014736 010005 MOV R0,R5 ;SAVE NUMBER OF BYTES
1859 014740 005037 002316' CLR PRMNO ;INIT ERROR COUNT
1860 014744 005004 CLR R4 ;NUMBER OF THE CURRENT BYTE
1861 014746 012701 002320' MOV #EXPMSG,R1 ;GET EXPD BUFFER ADDRESS
1862 014752 012702 002464' MOV #RECMSG,R2 ;GET RCV BUFFER ADDRESS
1863 014756 111100 20$: MOVB (R1),R0 ;GET EXPD BYTE
1864 014760 042700 177400 BIC #C<377>,R0 ;CLEAR UPPER BYTE
1865 014764 110037 015300' MOVB R0,PRBEXP ;SAVE FOR ERROR REPORT
1866 014770 111203 MOVB (R2),R3 ;GET RCV BYTE
1867 014772 042703 177400 BIC #C<377>,R3 ;CLEAR UPPER BYTE
1868 014776 110337 015302' MOVB R3,PRBREC ;FOR ERROR REPORT
1869 015002 XOR R0,R3 ;XOR EXPD/RCV
1870 015012 122122 CMPB (R1)+,(R2)+ ;EXPD = RCV?
1871 015014 001431 BEQ 30$ ;BR IF YES
1872 015016 005237 002316' INC PRMNO ;UPDATE ERROR COUNT
1873 015022 023727 002316' 000010 CMP PRMNO,#8. ;PRINTED 8?
1874 015030 101023 BHI 30$ ;BR IF YES
1875 015032 27$: PRINTX #PRBMSG,R4,PRBEXP,PRBREC,R3
015032 010346 MOV R3,-(SP)
015034 013746 015302' MOV PRBREC,-(SP)
015040 013746 015300' MOV PRBEXP,-(SP)
015044 010446 MOV R4,-(SP)
015046 012746 015146' MOV #PRBMSG,-(SP)
015052 012746 000005 MOV #5,-(SP)
015056 010600 MOV SP,R0
015060 104415 TRAP C$PNTX
015062 062706 000014 ADD #14,SP
1876 015066 FORCEEXIT 50$ ;@@D
1877 015076 000404 BR 35$ ;@@D
1878 015100 30$: FORCERROR 27$,NOTSSR ;@@D
1879 015100 35$: ;@@D
1880 015110 INC R4 ;NUMBER OF THE NEXT
1881 015110 005204 CMP R4,R5 ;DONE ALL YET?
1882 015112 020405 BGE 50$ ;BR IF YES
1883 015114 002001 BR 20$ ;DO ANOTHER
1884 015116 000717 50$: PRINTX #PRBTOT,PRMNO ;PRINT TOTAL ERROR COUNT
015120 013746 002316' MOV PRMNO,-(SP)
015124 012746 015233' MOV #PRBTOT,-(SP)
015130 012746 000002 MOV #2,-(SP)

```

TSV3 - GLOBAL AREAS MACRO M1113 07-FEB-84 10:58
 PRBYTEXP - PRINT ERROR BYTES IN EXP/REC MESSAGE BUFFER

SEQ 063

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015134 010600          MOV    SP,R0
015136 104415          TRAP   C$PNTX
015140 062706 000006   ADD    #6,SP
1886 015144 000207          RTS    PC                ;RETURN
1887
1888 015146    045    116    045  PRBMSG: .ASCIZ 'N#A BYTE #D2#A EXPD: #03#A RECV: #03#A XOR: #03'
1889 015233    045    116    045  PRBTOT: .ASCIZ 'N#A NUMBER OF BYTES IN ERROR = #D2'
1890
1891 015300 000000          PRBEXP: .WORD 0          ;EXPD
1892 015302 000000          PRBREC: .WORD 0          ;RECV
1893
1894                      .SBTTL  EXPREC - PRINT EXPD/RECV WORD DATA
1895                      ;*
1896                      ;
1897                      ;PRINT ROUTINE TO DISPLAY EXPD/RECV DATA
1898                      ;
1899                      ;INPUTS:
1900                      ;
1901                      ;    R1    RECEIVED DATA
1902                      ;    R2    EXPECTED DATA
1903                      ;
1904                      ;-
1905
1906 015304          BGNMSG  EXPREC
015304          EXPREC::
1907 015304 004737 007552'    JSR    PC,PRIXOR          ;PRINT THE DATA
1908 015310          ENDMSG
015310          L10017:
015310 104423          TRAP   C$MSG
1909
1910
1911
1912
1913                      .SBTTL  EXPBREC - PRINT EXPD/RECV BYTE DATA
1914                      ;*
1915                      ;
1916                      ;PRINT ROUTINE TO DISPLAY BYTE EXPD/RECV DATA
1917                      ;
1918                      ;
1919                      ;INPUTS:
1920                      ;
1921                      ;    R1    RECEIVED DATA BYTE
1922                      ;    R2    EXPECTED DATA BYTE
1923                      ;
1924                      ;-
1925
1926 015312          BGNMSG  EXPBREC
015312          EXPBREC::
1927 015312 004737 007422'    JSR    PC,PRIBXOR          ;PRINT THE DATA
1928 015316          ENDMSG
015316          L10020:
015316 104423          TRAP   C$MSG
1929
1930
1931
1932
1933                      .SBTTL  RAMERR - PRINT RAM AND PACKET DATA

```



```

1934      ;*
1935      ;
1936      ;PRINT ROUTINE TO DISPLAY RAM/PACKET DATA
1937      ;
1938      ;INPUTS:
1939      ;
1940      ;       R4       POINTER TO COMMAND PACKET
1941      ;
1942      ;IMPLICIT INPUTS:
1943      ;
1944      ;       RAMDATA   DATA AS READ FROM THE RAM
1945      ;       RAMSIZ   NUMBER OF BYTES IN PACKET
1946      ;                   IF RAMSIZ=0 THEN DEFAULT TO 8.
1947      ;
1948      ;IMPLICIT OUTPUTS:
1949      ;
1950      ;       RAMSIZ   SET TO 0
1951      ;-
1952
1953 015320      BGNMSG  RAMERR
1954 015320      RAMERR:: JSR    PC,PRAMPKT      ;PRINT RAM/PACKET DATA
1955 015324      ENDMSG
1956 015324      L10021: TRAP   C$MSG
1957
1958      .SBTTL  RAMTADD - PRINT TEST ADDRESS, RAM AND PACKET DATA
1959      ;*
1960      ;
1961      ;PRINT ROUTINE TO DISPLAY RAM/PACKET DATA
1962      ;
1963      ;INPUTS:
1964      ;
1965      ;       R4       POINTER TO COMMAND PACKET
1966      ;
1967      ;IMPLICIT INPUTS:
1968      ;
1969      ;       RAMDATA   DATA AS READ FROM THE RAM
1970      ;       RAMSIZ   NUMBER OF BYTES IN PACKET
1971      ;                   IF RAMSIZ=0 THEN DEFAULT TO 8.
1972      ;       ERRHI    HIGH ORDER TEST ADDRESS
1973      ;       ERRLO    LOW ORDER TEST ADDRESS
1974      ;
1975      ;IMPLICIT OUTPUTS:
1976      ;
1977      ;       RAMSIZ   SET TO 0
1978      ;-
1979
1980 015326      BGNMSG  RAMTADD
1981 015326      RAMTADD:: JSR    PC,PRITADD      ;PRINT TEST ADDRESS
1982 015332      004737 010104' JSR    PC,PRAMPKT      ;PRINT RAM/PACKET DATA
1983 015336      ENDMSG
1984 015336      L10022: TRAP   C$MSG
1985

```

```

1985
1986
1987
1988
1989
1990
1991
1992
1993
1994
1995
1996
1997
1998 015340
    015340
1999 015340 042701 177400
2000 015344 042702 177400
2001 015350 004737 007676'
2002 015354 004737 007552'
2003 015360
    015360
    015360 104423
2004
2005
2006
2007
2008
2009
2010
2011
2012
2013
2014
2015
2016
2017 015362
    015362
2018 015362
    015362 012746 015410'
    015366 012746 000001
    015372 010600
    015374 104415
    015376 062706 000004
2019 015402 004737 007552'
2020 015406
    015406
    015406 104423
2021
2022
2023 015410 045 116 045 TIMSGO: .ASCIZ 'N#A TIMER A STATUS IS IN BIT 3#N#A TIMER B STATUS IS IN BIT 2'
2024 .EVEN
2025
2026
2027
2028
2029
2030
    .SBTTL RAMEXP - PRINT RAM EXPD/RECV DATA
    ;*
    ;
    ;PRINT ROUTINE TO DISPLAY EXPD/RECV DATA
    ;
    ;INPUTS:
    ;
    ; R1 RECEIVED DATA
    ; R2 EXPECTED DATA
    ; R4 CONTROLLER RAM ADDRESS
    ;
    ;-
    BGNMSG RAMEXP
RAMEXP::
    BIC #C<377>,R1 ;SAVE EXPD RAM DATA BYTE
    BIC #C<377>,R2 ;SAVE EXPD RAM DATA BYTE
    JSR PC,PRIRAM ;PRINT THE RAM ADDRESS
    JSR PC,PRIXOR ;PRINT THE DATA
    ENDMSG
L10023:
    TRAP C$MSG
    .SBTTL TIMEXP - PRINT TIMER A,B AND EXP/REC
    ;*
    ;
    ;PRINT ROUTINE TO DISPLAY EXPD/RECV DATA
    ;AND TIMER A,B HEADER MESSAGE
    ;
    ;INPUTS:
    ;
    ; R1 RECEIVED DATA
    ; R2 EXPECTED DATA
    ;
    ;-
    BGNMSG TIMEXP
TIMEXP::
    PRINTX #TIMSGO ;PRINT HEADER
    MOV #TIMSGO,-(SP)
    MOV #1,-(SP)
    MOV SP,R0
    TRAP C$PNTX
    ADD #4,SP
    JSR PC,PRIXOR ;PRINT THE DATA
    ENDMSG
L10024:
    TRAP C$MSG
    .SBTTL BADSSR - PRINT TSSR ERRORS ON DATA TRANSFERS
    ;*
    ;
    
```

```

2031 ;PRINT ROUTINE FOR TSSR ERRORS ON DATA TRANSFERS
2032 ;
2033 ;INPUTS:
2034 ;
2035 ; R1 CONTENTS OF TSSR
2036 ; R2 DATA WRITTEN (8 BITS)
2037 ;
2038 ;
2039 ;
2040 015510 BGNMSG BADSSR
015510 BADSSR::
2041 015510 010246 MOV R2, -(SP) ;SAVE DATA TRANSFERRED
2042 015512 042702 177400 BIC #177400,R2 ;GET JUST ONE BYTE
2043 015516 PRINTB @XFERASC,R2
015516 010246 MOV R2, -(SP)
015520 012746 015550' MOV @XFERASC, -(SP)
015524 012746 000002 MOV #2, -(SP)
015530 010600 MOV SP,R0
015532 104414 TRAP C:PNTB
015534 062706 000006 ADD #6,SP
2044 015540 012602 MOV (SP),R2 ;RESTORE R2
2045 015542 004737 005634' JSR PC,PRITSSR ;DECODE TSSR CONTENTS
2046 015546 ENDMSG
015546 L10025:
015546 104423 TRAP C:MSG
2047 015550 045 116 045 XFERASC: .ASCIZ '#N#A Data Transferred = #03'
2048
2049
2050 .SBTTL GLOBAL SUBROUTINES SECTION
2051
2052 ;**
2053 ; THE GLOBAL SUBROUTINES SECTION CONTAINS THE SUBROUTINES
2054 ; THAT ARE USED IN MORE THAN ONE TEST.
2055 ;--
2056
2057 .SBTTL SOFINIT - SOFT INITIALIZE OF CONTROLLER
2058
2059 ;*
2060 ;
2061 ;ROUTINE TO DO A SOFT INITIALIZE OF THE CONTROLLER
2062 ;BY WRITING INTO THE TSSR REGISTER. AFTER THE INIT,
2063 ;THE TSSR REGISTER IS TESTED FOR ERRORS. ANY ERRORS
2064 ;DETECTED SHOULD BE TREATED AS DEVICE FATAL ERRORS.
2065 ;
2066 ;INPUTS:
2067 ;
2068 ; R5 ADDRESS OF FIRST REGISTER
2069 ;
2070 ;OUTPUTS:
2071 ;
2072 ; R0 CONTENTS OF TSSR, IF ERROR
2073 ; CARRY SET IF INIT WAS OKAY
2074 ; CLEAR IF FATAL ERROR
2075 ;
2076 ;CALLING SEQUENCE:
2077 ;
2078 ; MOV @ADDRESS,R5

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2079          ; JSR PC,SOFINIT
2080          ; BCS CONTINUE
2081          ; ERRDF ;REPORT FATAL ERROR
2082          ;
2083          ;-
2084
2085 015604      SOFINIT::
2086 015604      SAVREG ; SAVE THE REGISTERS
2087 015610 012765 000000 000002 MOV #0,TSSR(R5) ; DO THE INIT.
2088 015616 004737 016060' JSR PC,WAITF ; WAIT FOR SSR
2089 015622 016500 000002 MOV TSSR(R5),R0 ; GET THE TSSR REGISTER
2090 015626 010004 MOV R0,R4 ; TSSR CONTENTS
2091 015630 042704 176277 BIC #C<HIADDR!OFL>,R4
2092 015634 052704 002200 BIS #SSR!NBA,R4 ; R4 HAS EXPECTED CONTENTS
2093 015640 020400 CMP R4,R0 ; ONLY EXPECTED BITS SET ?
2094 015642 001402 BEQ 5# ; BRANCH IF OKAY
2095 015644 000241 CLC ; CLEAR THE CARRY FOR ERROR
2096 015646 000401 BR 10# ; GO TO EXIT
2097 015650 000261 5#: SEC ; SET THE CARRY BIT
2098 015652 000207 10#: RTS PC ; RETURN TO CALLER
2099
2100          .SBTTL CHKAMB - CHECK TSSR FOR AMBIGUITY
2101
2102          ;*
2103          ;
2104          ; THIS ROUTINE TESTS THE CONTENTS OF THE TSSR REGISTER
2105          ; FOR AMBIGUITY
2106          ;
2107          ; INPUT:
2108          ;
2109          ; R0 CONTENTS OF TSSR
2110          ;
2111          ; OUTPUT:
2112          ;
2113          ; R0 CONTENTS OF TSSR
2114          ;
2115          ; CARRY SET - NO AMBIGUITY
2116          ; CLR - AMBIGUOUS CONTENTS
2117          ;
2118          ;-
2119
2120          CHKAMB:
2121 015654      SAVREG ; SAVE THE GENERAL REGISTERS
2122 015654      MOV R0,R4 ; CONTENTS OF TSSR
2123 015660 010004 BIT #SC,R0 ; IS BIT 15 SET ?
2124 015662 032700 100000 BNE 5# ; BRANCH IF YES
2125 015666 001004 BIT #C<NBA!OFL!SSR!HIADDR>,R0 ; ANY OTHER BITS SET ?
2126 015670 032700 174077 BNE 40# ; MUST BE AN ERROR
2127 015674 001023 BR 45# ; RETURN WITH SUCCESS
2128 015676 000424 5#: BIT #SSR,R0 ; IS READY BIT SET ?
2129 015700 032700 000200 BNE 10# ; BRANCH IF READY BIT IS SET.
2130 015706 032700 000040 BIT #BITS,R0 ; IS FATAL ERROR BIT SET ?
2131 015712 001414 BEQ 40# ; ERROR IF NOT
2132 015714 042704 177761 BIC #CTERCLS,R4 ; CLEAR ALL BUT TERMINATION CODE
2133 015720 020427 000016 CMP R4,#16 ; ALL THREE BITS MUST BE SET
2134 015724 001007 BNE 40# ; ERROR IF NOT SET
2135 015726 000410 BR 45# ; OK IF ALL ARE SET

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2136 015730 032700 000040      10$: BIT    #BITS,RO      ;IS FATAL ERROR BIT SET ?
2137 015734 001405              BEQ    45$              ;ERROR IF BIT IS SET WITH SSR
2138 015736 032700 000006      BIT    #BIT2!BIT1,RO    ;IS THIS A FUNCTION REJECT
2139 015742 001002              BNE    45$              ;BR, IF TSSR IS OK
2140 015744 000241      40$: CLC                    ;AMBIGUOUS CONTENTS
2141 015746 000401              BR     50$
2142 015750 000261      45$: SEC                    ;SHOW SUCCESS - NO AMBIGUITY
2143 015752 000207      50$: RTS    PC           ;RETURN TO CALLER
2144
2145              .SBTTL ENAINT,DSBINT - ENABLE/DISABLE INTERRUPTS
2146
2147              ;
2148              ; DEFAULT DISPLAY INTERRUPT HANDLERS.
2149              ; IF DISPLAY TIME-OUT, REPORT DEV FATAL, AND ABORT PASS.
2150              ; OTHERWISE, SAVE DPU REGISTERS AND DISMISS.
2151              ;
2152              ; BIT DEFINITIONS FOR "INTMASK" AND "INTFLAG" BYTES:
2153              ;
2154              ;           IOKCKIN=BIT7      ; DON'T CHECK FOR BAD INTERRUPTS -- TEST WILL.
2155              ;           IOKSTP=BIT0      ; EXPECT "STOP" INTERRUPT.
2156              ;
2157              ; INTERRUPT MASK -- SAYS EXPECTING INTERRUPTS
2158 015754      000      INTMASK: .BYTE    0
2159              ; INTERRUPT FLAG -- SAYS WE GOT ONE (IF POSITIVE)
2160 015755      000      INTFLAG: .BYTE    0
2161
2162              ; SAVED INTERRUPT VECTOR:
2163 015756 000000      INTVEC: .WORD    0
2164              ; SAVE CPU PC
2165 015760 000000      INTCPC: .WORD    0
2166
2167              ; SUBROUTINE TO ENABLE INTERRUPTS:
2168 015762 010046      ENAINT: MOV    RO, -(SP)      ;SAVE RO
2169 015764 013700 002206'      MOV    IVEC,RO      ;GET POINTER TO VECTORS
2170 015770 012720 016026'      MOV    #INTR,(RO)   ;SET UP INTERRUPT VECTOR
2171 015774 012720 000340      MOV    #PRI07,(RO)  ;
2172 016000 012600      MOV    (SP)+,RO     ;RESTORE RO
2173 016002 011646      MOV    (SP),-(SP)
2174 016004 012766 000000 000002      MOV    #0,2(SP)    ;SET CPU TO LEVEL 0
2175 016012 000002      RTI
2176
2177              ; SUBROUTINE TO DISABLE INTERRUPTS (RAISE PRIORITY TO LEVEL 7)
2178 016014 011646      DSBINT: MOV    (SP),-(SP)
2179 016016 012766 000340 000002      MOV    #PRI07,2(SP)
2180 016024 000002      RTI
2181
2182              .SBTTL INTR - INTERRUPT HANDLERS
2183
2184 016026      BGNSRV INTR      ;DEFINE INTERRUPT ENTRY
2185 016026 012737 000001 002222'      INTR:: MOV    #1,INTRECV   ;SET FLAG TO SHOW INTERRUPT RECEIVED
2186 016034 105037 015755'      CLRB  INTFLAG      ;CLEAR FLAG TO SAY WE GOT INTERRUPT
2187 016040 132737 000001 015754'      BITB  #IOKSTP,INTMASK ;EXPECTING STOP INTERRUPT?
2188 016046 001003      BNE    1$          ;BR IF YES
2189 016050 152737 000001 015755'      BISB  #IOKSTP,INTFLAG ;NO, SET THE ERROR FLAG.
2190
2191              ;SAVE REGISTERS, MSG BUFFER, ETC.

```

```

2192 016056
2193 016056
      016056
      016056 000002
2194
2195
2196
2197
2198
2199
2200
2201
2202
2203
2204
2205
2206
2207
2208
2209 016060 000401
2210 016062
      016062 104422
2211 016064 012746 011000
2212 016070 016500 000002
2213 016074 105700
2214
2215 016076 100420
2216 016100
      016100 012727 000001
      016104 000000
      016106 013727 002116'
      016112 000000
      016114 005367 177772
      016120 001375
      016122 005367 177756
      016126 001367
2217 016130 005316
2218 016132 001356
2219 016134 000241
2220 016136 000401
2221 016140 000261
2222 016142 005326
2223 016144 000207
2224
2225
2226
2227
2228
2229
2230
2231
2232
2233
2234
2235
2236
2237

1$:
      ENDSRV
L10026:
      RTI
      .SBTTL WAITF - WAIT FOR SUBSYSTEM READY
;
; SUBROUTINE TO WAIT FOR THE SUBSYSTEM READY FLAG
;
; INPUTS:
;
;      R5      ADDRESS OF FIRST DEVICE REGISTER
;
; OUTPUTS:
;
;      R0      CONTENTS OF LAST TSSR READ
;      CARRY   SET - READY BIT SET
;             CLR - TIMEOUT WAITING FOR READY
;
; WAITF:: BR      1$          ;NOP WHEN SUPER FIXED
          BREAK    ; DO A SUPVSR BREAK FIRST.
          TRAP     C$BRK
21$: MOV     #11000,-(SP) ;25-APRIL-83 REV B - 1100 MSEC TIMER
22$: MOV     TSSR(R5),R0 ;READ THE TSSR REGISTER
          TSTB    R0      ;TEST FOR READY BIT SET
          BMI     3$          ; EXIT ON STOP FLAG.
          DELAY   1          ; WAIT 100 USEC
          MOV     #1,(PC)+
          .WORD   0
          MOV     L$DLY,(PC)+
          .WORD   0
          DEC     -6(PC)
          BNE     .-4
          DEC     -22(PC)
          BNE     .-20
          DEC     (SP)      ;REDUCE DELAY COUNT
          BNE     2$          ;RETRY UNTIL TIMER EXPIRES
          CLC
          BR      4$          ; C = 0, CONTROLLER STILL RUNNING...
;...OR HUNG-UP AFTER 300 MSEC.
3$: SEC
4$: DEC     (SP)+          ; C = 1, CONTROLLER IS STOPPED.
          RTS      PC      ;RESTORE STACK WITHOUT CHANGING CARRY BIT
      .SBTTL CHKTSSR - CHECK TSSR FOR READY
;
;
; THIS ROUTINE WAITS FOR READY IN THE TSSR
; AND TESTS FOR AMBIGUOUS BIT SETTINGS IN TSSR.
;
; INPUT:
;
;      R5      ADDRESS OF CSR REGISTERS
;
; OUTPUT:
;

```

```

2238      :      RO      CONTENTS OF TSSR
2239      :      CARRY    SET - OKAY
2240      :
2241      :
2242      : -
2243
2244 016146      CHKTSSR:
2245 016146 004737 016060'      JSR      PC, WAITF      ;WAIT FOR READY
2246 016152 103014      BCC      20$      ;BRANCH IF TIME OUT
2247 016154 004737 015654'      JSR      PC, CHKAMB      ;TSSR AMBIGUOUS?
2248 016160 103006      BCC      10$      ;BR IF YES
2249 016162 032700 100000      BIT      @SC, RO      ;SPECIAL CONDITION SET?
2250 016166 001405      BEQ      15$      ;BR IF NO
2251 016170 032700 074000      BIT      @<SCE!BIE!RMR!NXM>, RO ;ANY ERROR BITS SET?
2252 016174 001402      BEQ      15$      ;BR IF NO
2253 016176 000241      10$: CLC      ;SET FAILURE
2254 016200 000401      BR      20$      ;
2255 016202 000261      15$: SEC      ;SET SUCCESS
2256 016204 000207      20$: RTS      PC      ;RETURN TO CALLER
2257
2258      .SBTTL  XNXM      - CHECK FOR NONEXISTENT MEMORY
2259
2260      ;*
2261      ; ROUTINE TO TEST FOR A NEXM IN THE RANGE (R1) THRU (R2).
2262      ; ON RETURN, IF "C" = 1, (R1) = NEXM ADDRESS.
2263      ; "C" = 0, ALL ADDRESSES OK.
2264
2265      ;CALL:  MOV ADR1, R1
2266      ;        MOV ADR2, R2
2267      ;        JSR PC, NXM
2268      ;        RETURN      ;TEST "C" AND PROCEED.
2269 016206 012737 016242' 000004 XNXM:  MOV      @2$, @04      ; SET BUSERR VECTOR.
2270 016214 012737 000200 000006      MOV      @PRI04, @06
2271 016222 005003      CLR      R3      ;FLAG.
2272 016224 000241      CLC      ;CLEAR THE CARRY FOR NO NXM FOUND
2273 016226 005711      1$:  TST      (R1)      ;TEST THE ADDRESS(ES).
2274
2275      ;IF ANY TRAP, CONTINUE AT 2$.
2276      ;OTHERWISE, CONTINUE HERE.
2277 016234 062701 000002      CMP      R1, R2      ;BR IF FINISHED (NO NEXM'S).
2278 016240 000772      BEQ      3$      ;SET NEXT ADDRESS...
2279
2280      ;...AND CONTINUE.
2280 016242 005103      2$:  COM      R3      ;GOT ONE, SET FLAG...
2281 016244 012716 016252'      MOV      @3$, (SP)
2282 016250 000002      RTI      ;...AND DISMISS INTERRUPT...
2283 016252
2283 016252 012700 000004      3$:  CLRVEC  @4      ;...AND GIVE BACK THE VECTOR.
2284 016256 104436      MOV      @4, RO
2285 016260 005703      TRAP   C$CVEC
2286 016262 001401      TST      R3      ;DID WE CATCH ONE ??
2287 016264 000261      BEQ      .+4      ;NO, "C" = 0, SKIP NEXT.
2288 016266 000207      SEC      ;YES, "C" = 1, (R1) = NEXM ADDR.
2289      RTS      PC
2290
2291      .SBTTL  TSTLOOP - CHECK ITERATION COUNT
2292

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```

2293
2294
2295
2296
2297
2298
2299
2300 016270
2301 016270 005737 002166'
2302 016274 001006
2303 016276 005737 002202'
2304 016302 100403
2305 016304 005337 002214'
2306 016310 001002
2307 016312 000241
2308 016314 000401
2309 016316 000261
2310 016320 000207
2311
2312
2313
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2330
2331
2332
2333
2334
2335
2336
2337
2338 016322
2339 016322 010046
2340 016324 005037 003152'
2341 016330 005037 016570'
2342 016334 005037 005602'
2343 016340 105037 015754'
2344 016344 013700 002200'
2345 016350 006300
2346 016352 005737 003112'
2347 016356 001430
2348 016360 100010
2349 016362 052760 160000 003174'

;
; SUBROUTINE TO EXECUTE TEST ITERATIONS.
; EXIT WITH "C" SET IF LOOPS ALLOWED AND LOOP COUNT NON-ZERO.
; LOOP COUNTER IS SET BY "BEGIN.TEST" MACRO.
;
; CALL: LOOPTO ARG
;
TSTLOOP::
    TST      NOITS          ; ITERATIONS INHIBITED?
    BNE     1$             ; YES.
    TST     QVP            ; NO.
    BMI     1$            ; LOOPS DISALLOWED IN QUICK PASS.
    DEC     LOOPCNT       ; BUMP LOOP COUNTER.
    BNE     2$
1$:      CLC              ; LOOP DISALLOWED, OR DONE.
    BR     3$
2$:      SEC              ; LOOP ENABLED.
3$:      RTS      PC

        .SBTTL  TSTSETUP - PRINT TEST NAME AND INIT ERROR COUNTS
;
; PRINT THE NUMBER AND NAME OF EACH TEST AS WE GO ALONG.
; INCREMENT "TESTK" TO INDICATE THE NUMBER OF TESTS
; IN THE CURRENT RUN SEQUENCE.
; CLEAR THE ERROR COUNTER AND SIGNATURE EXTENSION FLAGS.
;
; INPUT:
;
;     R0      POINTER TO TEST ID ASCIZ STRING
;
; OUTPUT:
;
;     R5      ADDRESS OF FIRST DEVICE REGISTER
;
; IMPLICIT OUTPUTS:
;
;     TSTCNT  UPDATED TO COUNT TESTS PERFORMED SINCE START OR RESTART
;
; SIDE EFFECTS:
;
;     INTERRUPT LEVEL IS RASIED TO LEVEL OF
;     THE DEVICE UNDER TEST
;
; -
TSTSETUP::
    MOV     R0, -(SP)      ; SAVE THE TEST ID MESSAGE
    CLR     SIFLAG        ; CLEAR "SOFT INIT" FLAG
    CLR     ERRK          ; CLEAR LOCAL ERROR COUNTER.
    CLR     EXTA          ; CLEAR ERROR EXTENSION FLAG.
    CLRB   INTMASK       ; CLEAR INTERRUPT MASK (CHECK ERROR)
    MOV     UNITN, R0     ; GET THE UNIT NUMBER,
    ASL    R0             ; ... AND MAKE IT A WORD OFFSET.
    TST    NODEV         ; DID STARTUP FIND THE DEVICE?
    BEQ    4$            ; BR IF YES
    BPL    3$            ; BR IF NOT IDLE
    BIS    @160000,ERTABL(R0) ; FLAG ERROR IN THE ERROR TABLE

```



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2350 016370          ERRDF 1,NXR,NXRERR ; NO DEVICE HERE -- PRINT IT
      016370 104455 TRAP C#ERDF
      016372 000001 .WORD 1
      016374 003732' .WORD NXR
      016376 005546' .WORD NXRERR
2351 016400 000407 BR 2#
2352 016402 052760 160001 003174' 3# : BIS @160001,ERTABL(RO) ; FLAG ERROR IN THE ERROR TABLE
2353 016410          ERRDF 2,NOINIT ; DEVICE NOT IDLE
      016410 104455 TRAP C#ERDF
      016412 000002 .WORD 2
      016414 004327' .WORD NOINIT
      016416 000000 .WORD 0
2354 016420 012737 177777 003110' 2# : MOV @-1,DUFLG ; DROP THE UNIT
2355 016426          DODU UNITN
      016426 013700 002200' MOV UNITN,RO
      016432 104451 TRAP C#DODU
2356 016434          DOCLN ; ABORT THE PASS
      016434 104444 TRAP C#DOCLN
2357 016436 000423 BR 5#
2358
2359 016440          RFLAGS RO ; GET THE OPERATOR FLAGS.
      016440 104421 TRAP C#RFLA
2360 016442 032700 001000 BIT @PNT,RO ; PRINT THE TEST NUMBERS?
2361 016446 001412 BEQ 1# ; BR IF NO
2362 016450 011600 MOV (SP),RO ;GET THE ID MESSAGE
2363 016452          PRINTF @TNAM,RO ;DISPLAY THE TEST ID
      016452 010046 MOV RO,-(SP)
      016454 012746 016516' MOV @TNAM,-(SP)
      016460 012746 000002 MOV @2,-(SP)
      016464 010600 MOV SP,RO
      016466 104417 TRAP C#PNTF
      016470 062706 000006 ADD @6,SP
2364 016474 005237 002212' 1# : INC TSTCNT ; BUMP TEST COUNTER.
2365 016500          SETPRI IPRI ;PRIORITY THAT OF DEVICE
      016500 013700 002210' MOV IPRI,RO
      016504 104441 TRAP C#SPRI
2366 016506 005726 5# : TST (SP)+ ;FIX UP THE STACK
2367 016510 013705 002204' MOV CSRADDR,R5 ; ADDRESS OF TSV REGISTERS ON UNIBUS
2368 016514 000207 RTS PC
2369 016516 045 123 045 TNAM: .ASCIZ '#S#T#A Test'
2370          .EVEN
2371
2372          .SBTTL TSTEND - PRINT ERRORS RECEIVED
2373          ;
2374          ; AT END OF EACH TEST, PRINT THE NUMBER OF ERRORS RECEIVED
2375          ; IF NORMAL ERROR REPORTING IS DISABLED (FLA:IER).
2376          ;
2377 TSTEND: RFLAGS RO
      016532 104421 TRAP C#RFLA
2378 016534 030027 020000 BIT RO,@IER
2379 016540 001412 BEQ 1# ; BR IF "IER" NOT SET.
2380 016542          PRINTF @ESUM,ERRK ; PRINT ERROR COUNT.
      016542 013746 016570' MOV ERRK,-(SP)
      016546 012746 016572' MOV @ESUM,-(SP)
      016552 012746 000002 MOV @2,-(SP)
      016556 010600 MOV SP,RO
      016560 104417 TRAP C#PNTF
  
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2381 016562 062706 000006          ADD    #6,SP
2382 016566 000207          RTS    PC
2383 016570 000000          ERRK:  0                ; LOCAL ERROR COUNT.
2384 016572    045    101    040  ESUM:  .ASCIZ  /#A #D#A ERRORS/
2385 016611    105    122    122  EMAXDU: .ASCIZ  /ERROR LIMIT REACHED -- DROPPING UNIT/
2386                                     .EVEN
2387
2388                                     .SBTTL  INCERK  - INCREMENT LOCAL ERROR COUNT
2389
2390                                     ;*
2391                                     ; ROUTINES TO INCREMENT LOCAL ERROR COUNT AND CHECK FOR LIMIT:
2392                                     ;-
2392 016656 005237 016570'  INCERK: INC    ERRK                ; INCREMENT LOCAL ERROR COUNT
2393 016662 010046          MOV    RO,-(SP)              ; SAVE RO
2394 016664 013700 002200'  MOV    UNITN,RO             ; GET UNIT NUMBER,
2395 016670 006300          ASL    RO                    ; ... AND MAKE IT A WORD OFFSET.
2396 016672 062700 003174'  ADD    @ERTABL,RO           ; RO GETS ADDRESS OF ERROR TABLE ENTRY.
2397 016676 005210          INC    (RO)                  ; INCREMENT THE DEVICE ERROR COUNT
2398 016700 032710 007777  BIT    @7777,(RO)           ; DID WE OVERFLOW THE FIELD?
2399 016704 001001          BNE   1$                    ; BR IF NO.
2400 016706 005310          DEC    (RO)                  ; YES -- BACK IT UP TO 7777.
2401 016710 012600          1$:  MOV    (SP)+,RO           ; RESTORE RO
2402 016712 000207          RTS    PC                    ; RETURN TO CALLER.
2403
2404 016714 010046          CKEMAX: MOV   RO,-(SP)          ; SAVE RO
2405 016716 013700 002200'  MOV   UNITN,RO              ; GET UNIT NUMBER
2406 016722 006300          ASL   RO                     ; ... AND MAKE IT A WORD OFFSET
2407 016724 016000 003174'  MOV   ERTABL(RO),RO         ; GET ERROR TABLE ENTRY
2408 016730 042700 170000  BIC   @170000,RO            ; EXTRACT ERROR COUNT FIELD
2409 016734 020037 002172'  CMP   RO,GERRMAX            ; IS GLOBAL LIMIT EXCEEDED FOR THIS UNIT?
2410 016740 103004          BHIS  1$                     ; BR IF YES
2411 016742 023737 016570' 002170'  CMP   ERRK,LERRMAX          ; IS LOCAL LIMIT EXCEEDED FOR THIS TEST?
2412 016750 103417          BLO  2$                     ; BR IF NO
2413 016752          1$:  RFLAGS RO                    ; GET OPERATOR FLAGS
2414 016752 104421          TRAP  C#RFLA
2415 016754 032700 000040  BIT   @IDU,RO                ; IS DROPPING INHIBITED?
2416 016760 001013          BNE  2$                     ; BR IF YES.
2417 016762 012737 177777 003110'  MOV   @-1,DUFLG             ; NO -- DROP THE UNIT
2418 016770          ERRDF 4,EMAXDU
2419 016772 104455          TRAP  C#ERDF
2420 016774 000004          .WORD 4
2421 016776 016611'          .WORD EMAXDU
2422 016776 000000          .WORD 0
2423 017000          DODU  UNITN
2424 017000 013700 002200'  MOV   UNITN,RO
2425 017004 104451          TRAP  C#DODU
2426 017006          DOCLN
2427 017006 104444          TRAP  C#DCLN
2428 017010 012600          2$:  MOV   (SP)+,RO            ; RESTORE RO
2429 017012 000207          RTS   PC                    ; RETURN TO CALLER
2430
2431                                     .SBTTL  CKDROP  - CHECK IF UNIT SHOULD BE DROPPED
2432
2433                                     ;*
2434                                     ; CHECK IF UNIT SHOULD BE DROPPED
2435                                     ;-
2436 017014 010046          CKDROP: MOV   RO,-(SP)
2437 017016          FORCERROR 1$,NOT$SSR

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2429 017026          RFLAGS RO
      017026 104421    TRAP   C#RFLA
2430 017030 032700 000040    BIT   #IDU,RO
2431 017034 001010    BNE   1$
2432 017036 011600    MOV   (SP),RO
2433 017040 012737 177777 003110'  MOV   #-1,DUFLG
2434 017046          DODU   UNITN
      017046 013700 002200'  MOV   UNITN,RO
      017052 104451    TRAP   C#DODU
2435 017054          DOCLN          ;ABORT THE PASS
      017054 104444    TRAP   C#DCLN
2436 017056 012600 1$:    MOV   (SP)+,RO
2437 017060 000207    RTS   PC
2438
2439          .SBTTL  CONFIG - DETERMINE CONFIGURATION OF SYSTEM
2440          ;
2441          ; SUBROUTINE - DETERMINE CONFIGURATION OF TSUOS SYSTEM.
2442          ;
2443 017062          CONFIG:
2444 017062 004737 015604'    JSR   PC,SOFINIT
2445 017066 000207          RTS   PC
2446
2447          .SBTTL  KTON,KTOFF - ENABLE/DISABLE MEMORY MANAGEMENT
2448          ;
2449          ; SUBROUTINE - ENABLE MEM MGT.
2450          ;
2451 017070 005737 003130'    KTON:  TST   KTF LG          ; GOT KT?
2452 017074 001403          BEQ   1$          ; NO.
2453 017076 012737 000001 177572  MOV   #1,SRO          ; YES. ENABLE KT11.
2454 017104 000207 1$:    RTS   PC
2455
2456
2457
2458          ;
2459          ; SUBROUTINE - DISABLE MEM MGT.
2460          ;
2461 017106 005737 003130'    KTOFF: TST   KTF LG          ; GOT KT11?
2462 017112 001405          BEQ   1$          ; NO.
2463 017114 000240          NOP
2464 017116 000240          NOP
2465 017120 012737 000000 177572  MOV   #0,SRO          ; DISABLE KT.
2466 017126 000207 1$:    RTS   PC
2467
2468          .SBTTL  SETMAP - SETUP PAR6 MAPPING
2469
2470          ;*
2471          ;
2472          ; THIS ROUTINE SETS UP KERNEL PAR6 TP HANDLE
2473          ; AN 22 BIT ADDRESS. THE OFFSET INTO THE PAGE
2474          ; IS RETURNED BIASED TO PAR6.
2475          ;
2476          ; INPUTS:
2477          ;
2478          ;     R0     HIGH ORDER ADDRESS BITS
2479          ;     R1     LOW ORDER ADDRESS BITS
2480          ;
2481          ; OUTPUTS:

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2482
2483
2484
2485
2486
2487 017130
2488 017130
2489 017134 005737 003130'
2490 017140 001433
2491 017142 010102
2492
2493
2494
2495
2496 017174 042701 000177
2497 017200 020137 003130'
2498 017204 103011
2499 017206 010137 172354
2500 017212 042702 160000
2501 017216 062702 140000
2502 017222 010200
2503 017224 000261
2504 017226 000401
2505 017230 000241
2506 017232 000207
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2520
2521
2522
2523
2524 017234
2525 017234
2526 017240 004737 017106'
2527 017244 010003
2528 017246 013701 003122'
2529 017252 013702 003124'
2530 017256 010321
2531 017260 005302
2532 017262 003375
2533 017264 005737 003130'
2534 017270 001502
2535 017272 004737 017070'
2536 017276 005000
2537 017300 013701 003150'
2538
    
```

```

;
; RO OFFSET INTO BLOCK WITH PAR6 BIAS (I.E. THE ADDRESS)
; CARRY SET IF SUCCESS
; CLR IF ERROR
;
;--
SETMAP:
    SAVREG
    TST KTFLG
    BEQ 10$
    MOV R1,R2
    .REPT 6
    ASR R0
    ROR R1
    .ENDR
    BIC #177,R1
    CMP R1,KTFLG
    BHIS 10$
    MOV R1,#KIPAR6
    BIC #160000,R2
    ADD #140000,R2
    MOV R2,R0
    SEC
    BR 15$
    10$: CLC
    15$: RTS PC
;
;SAVE R1-R4 UNTIL NEXT RETURN
;SYSTEM HAVE ABOVE 28K?
;BR IF NO
;SAVE LOW ORDER BITS
;CONVERT WORD ADDRESS TO 32W BLOCKS
;MAKE IT DOUBLE PRECISION
;ALINE FOR LOWER 4K BOUNDARY
;HIGHER THAN EXISTING MEMORY?
;BR IF YES
;SETUP MAPPING REGISTER PAR6
;SETUP DISPLACEMENT IN PAGE
;ADD IN PAR6 BIAS
;RETURN IN R0
;SET SUCCESS
;SET FAILURE
;RETURN
    
```

```

;SBTTL FILLMEM - FILL MEMORY WITH BACKGROUND PATTERN
;
;*
; FILL MEMORY WITH A BACKGROUND PATTERN
;
; INPUTS:
;
; RO = BACKGROUND PATTERN
; FREE = FIRST LOCATION AVAILABLE TO DIAGNOSTIC
; KTFLG = SET TO HIGHEST MEMORY LOCATION IF > 28K.
;
; OUTPUTS:
;
; NONE
;
;--
;
; FILLMEM:
    SAVREG
    JSR PC,KTOFF
    MOV R0,R3
    MOV FREE,R1
    MOV FRESIZ,R2
    10$: MOV R3,(R1)+
    DEC R2
    BGT 10$
    TST KTFLG
    BEQ 55$
    JSR PC,KTON
    CLR R0
    MOV PST32W,R1
    .REPT 6
;SAVE R1-R5 UNTIL NEXT RETURN
;DISABLE KT.
;COPY TEST PATTERN
;GET FIRST FREE LOCATION
;SIZE OF FREE SPACE BELOW 28K.
;STORE A BACKGROUND WORD
;DONE ALL MEMORY IN FREE SPACE?
;BR IF NO
;GOT KT?
;NO. GET OUT.
;YES. ENABLE KT.
;HIGH ORDER ADDRESS START
;GET >28K START ADDRESS (IN 32W BLOCKS)
    
```



```

2596 017516 013702 003124'      MOV    FRESIZ,R2      ;SIZE OF FREE SPACE BELOW 28K.
2597 017522 020311      10$:  CMP    R3,(R1)      ;FREE SPACE LOCATION EQUAL TO EXPD?
2598 017524 001411      BEQ    15$            ;BR IF YES
2599 017526 010137 002236'      MOV    R1,ERRLO      ;SAVE ADDRESS IN ERROR
2600 017532 005037 002234'      CLR    ERRHI          ;NO HIGH ADDRESS
2601 017536 010337 002230'      MOV    R3,EXPD       ;SAVE EXPD FOR ERROR REPORT
2602 017542 011137 002232'      MOV    (R1),RECV     ;SAVE RECV FOR ERROR REPORT
2603 017546 000474      BR     50$           ;
2604 017550 005721      15$:  TST    (R1)+        ;POINT TO NEXT ADDRESS
2605 017552 005302      DEC    R2            ;DONE ALL MEMORY IN FREE SPACE?
2606 017554 003362      BGT    10$           ;BR IF NO
2607 017556 005737 003130'      TST    KTFLG         ; GOT KT?
2608 017562 001472      BEQ    55$           ; NO. GET OUT.
2609 017564 004737 017070'      JSR    PC,KTON        ; YES. ENABLE KT.
2610 017570 005000      CLR    R0            ;HIGH ORDER ADDRESS START
2611 017572 013701 003150'      MOV    PST32W,R1     ;GET >28K START ADDRESS (IN 32W BLOCKS)
2612      .REPT 6
2613      ROL    R1          ;CONVERT BLOCKS TO WORDS
2614      ROL    R0          ;MAKE IT DOUBLE PRECISION
2615      .ENDR
2616 017626 042701 000177      BIC    #177,R1       ;ALINE 4K BOUNDARY
2617 017632 010046      MOV    R0,-(SP)      ;SAVE HIGH ORDER
2618 017634 010146      MOV    R1,-(SP)      ;SAVE LOW ORDER
2619 017636 004737 017130'      JSR    PC,SETMAP     ;SETUP PAR6 MAPPING REGISTER
2620 017642 010004      MOV    R0,R4         ;COPY ADDRESS BIASED TO PAR6
2621 017644 012601      MOV    (SP)+,R1      ;RESTORE LOW ORDER IN NON PAR6 FORMAT
2622 017646 012600      MOV    (SP)+,R0      ;RESTORE HIGH ORDER IN NON PAR6 FORMAT
2623 017650 020314      30$:  CMP    R3,(R4)       ;ABOVE 28K LOCATION EQUAL EXPD?
2624 017652 001411      BEQ    32$           ;BR IF YES
2625 017654 010037 002234'      MOV    R0,ERRHI     ;SAVE HIGH ORDER IN ERROR
2626 017660 010137 002236'      MOV    R1,ERRLO     ;SAVE LOW ORDER IN ERROR
2627 017664 010337 002230'      MOV    R3,EXPD      ;SAVE EXPD FOR ERROR REPORT
2628 017670 011437 002232'      MOV    (R4),RECV    ;SAVE RECV FOR ERROR REPORT
2629 017674 000421      BR     50$           ;
2630 017676 062701 000002      32$:  ADD    #2,R1         ;UPDATE NON PAR6 ADDRESS
2631 017702 005500      ADC    R0            ;MAKE IT DOUBLE PRECISION ADD
2632 017704 062704 000002      ADD    #2,R4         ;UPDATE PAR FORMAT ADDRESS
2633 017710 020427 160000      CMP    R4,#160000   ;END OF PAR6 MAPPING AREA?
2634 017714 103755      BLO    30$           ;BR IF NO
2635 017716 162704 020000      SUB    #20000,R4     ;BACKUP INTO PAR6 MAPPING BEGIN
2636 017722 062737 000200 172354      ADD    #200,#KIPAR6 ;POINT TO NEXT 4K BLOCK >28K.
2637 017730 023737 172354 003130'      CMP    #KIPAR6,KTFLG ;END OF MEMORY?
2638 017736 101744      BLOS  30$           ;BR IF NO
2639 017740 004737 017106'      50$:  JSR    PC,KTOFF     ;TURN OFF MEMORY MAPPING
2640 017744 000241      CLC                    ;SET FAILURE
2641 017746 000403      BR     60$           ;
2642 017750 004737 017106'      55$:  JSR    PC,KTOFF     ;TURN OFF MEMORY MAPPING
2643 017754 000261      SEC                    ;SET SUCCESS
2644 017756 000207      60$:  RTS    PC
2645
2646      .SBTTL REGSAV - SAVE R1-R5 ON STACK
2647      ;+
2648      ;
2649      ;ROUTINE TO
2650      ;SAVE R1 THROUGH R5 ON THE STACK
2651      ;
2652      ;CALLING SEQUENCE:

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2653      ;
2654      ;       JSR      R5,REGSAV
2655      ;
2656      ; THIS IS A COOROUTINE WHICH TRANSFER CONTROL BACK TO
2657      ; THE CALLING ROUTINE. AT THE END OF THE CALLING ROUTINE,
2658      ; THE RTS PC RETURNS CONTROL TO THIS ROUTINE TO RESTORE
2659      ; REGISTERS.
2660      ;
2661      ; THIS ROUTINE SHOULD ONLY BE CALLED FROM ROUTINES WHICH ARE
2662      ; CALLED VIA A JSR PC INSTRUCTION
2663      ;
2664      ; -
2665
2666      REGSAV:
2667      MOV      R4,-(SP)
2668      MOV      R3,-(SP)
2669      MOV      R2,-(SP)
2670      MOV      R1,-(SP)
2671      MOV      R5,-(SP)
2672      MOV      10.(SP),R5
2673      JSR      PC,@(SP)+
2674      MOV      (SP)+,R1
2675      MOV      (SP)+,R2
2676      MOV      (SP)+,R3
2677      MOV      (SP)+,R4
2678      MOV      (SP)+,R5
2679      RTS      PC
2680
2681      .SBTTL  GETPAT - GET 8 BIT PATTERN FROM OPERATOR
2682      ;*
2683      ;
2684      ; ROUTINE TO REQUEST AN 8 BIT DATA PATTERN FROM THE OPERATOR
2685      ;
2686      ; INPUTS:
2687      ;
2688      ;     NONE.
2689      ;
2690      ; OUTPUTS:
2691      ;
2692      ;     R0     OCTAL NUMBER FROM THE OPERATOR
2693      ;
2694      ; CALLING SEQUENCE:
2695      ;
2696      ;     JSR      PC,GETPAT
2697      ;
2698      ; -
2699
2700      GETPAT::
2701      SAVREG      ;SAVE THE GENERAL REGISTERS
2702      1$:  GMANID  DATASC,PATDAT,0,377,0,377,NO
           TRAP    C$GMAN
           BR     10000$
           .WORD  PATDAT
           .WORD  T$CODE
           .WORD  DATASC
           .WORD  377
           .WORD  T$LOLIM
2703      020014 104443
2704      020014 000406
2705      020020 020050'
2706      020022 000022
2707      020024 020052'
2708      020026 000377
2709      020030 000000
2710      020032 000000
2711      020034 000000
    
```

```

020036 000377          .WORD  T$HILIM
020040
2703 020040          10000$: BNCOMPLETE 1$ ;RETRY IF ERROR
020040 103367          BCC 1$
2704 020042 013700 020050'  MOV PATDAT,RO ;DATA PATTERN FROM OPERATOR
2705 020046 000207          RTS PC ;RETURN TO CALLER
2706
2707
2708 ;LOCAL DATA AREA
2709 ;-
2710
2711 020050 000000          PATDAT: .WORD 0 ;TEMPORARY STORAGE FOR DATA
2712 020052 105 116 124 DATASC: .ASCIZ 'ENTER DATA PATTERN'
2713 ;EVEN
2714
2715 ;SBTTL GETSEL - ISSUE MENU AND GET OPERATOR RESPONSE
2716
2717 ;*
2718 ;ROUTINE TO ISSUE A MENU AND GET
2719 ;THE OPERATOR'S RESPONSE.
2720 ;
2721 ;INPUTS:
2722 ;
2723 ; R0 ADDRESS OF ASCIZ STRING OF MENU
2724 ; R1 MAXIMUM ALLOWABLE OPERATOR RESPONSE
2725 ;
2726 ;OUTPUTS:
2727 ;
2728 ; R0 NUMBER OF THE OPERATOR'S SELECTION
2729 ;
2730 ;-
2731
2732 020076
2733 020076
2734 020102 010002          SAVREG ;SAVE GENERAL REGISTERS
2735 020104 010203          MOV R0,R2 ;SAVE THE MENU ADDRESS
2736 020106 005713          1$: MOV R2,R3 ;START OF MENU STRING
2737 020110 001412          2$: TST (R3) ;END OF ASCII ?
2738 020112          BEQ 3$ ;BRANCH IF ALL LINES DISPLAYED
020112 012346          PRINTF @SELASC,(R3); ;DISPLAY THE MENU
020114 012746 020262'     MOV (R3),-(SP)
020120 012746 000002     MOV @SELASC,-(SP)
020124 010600          MOV @2,-(SP)
020126 104417          MOV SP,R0
020130 062706 000006     TRAP C$PNTF
2739 020134 000764          ADD @6,SP
2740 020136          BR 2$
020136 104443          3$: GMANID MENASC,MENRES,D,-1,0,-1,NO
020140 000406          TRAP C$GMAN
020142 020316'         BR 10001$
020144 000042          .WORD MENRES
020146 020267'         .WORD T$CODE
020150 177777          .WORD MENASC
020152 000000          .WORD -1
020154 177777          .WORD T$LOLIM
020156          .WORD T$HILIM
2741 020156          10001$: BNCOMPLETE 1$ ;RETRY IF ERROR
  
```



```

020156 103352
2742 020160 013700 020316'      BCC      1$
2743 020164 020001              MOV      MENRES,RO      ;GET THE OPERATOR'S REPLY
2744 020166 101411              CMP      RO,R1          ;COMPARE TO MAXIMUM ALLOWED
2745 020170              BLOS     5$            ;BRANCH IF OK
020170 012746 020214'      PRINTF  @MENERR        ;DISPLAY ERROR MESSAGE
020174 012746 000001      MOV      @MENERR,-(SP)
020200 010600              MOV      @1,-(SP)
020202 104417              MOV      SP,RO
020204 062706 000004      TRAP    C$PNTF
2746 020210 000735              ADD     @4,SP
2747 020212 000207              BR      1$            ;RETRY
2748 020214 045 116 045 5$:      RTS      PC          ;RETURN TO CALLER
2749 020262 045 116 045 MENERR: .ASCIZ '### Menu Selection Too Large ###'
2750 020267 105 156 164 SELASC: .ASCIZ '###'
2751              MENASC: .ASCIZ 'Enter Menu Selection: '
2752 020316 000000              .EVEN
2753              MENRES:      .WORD 0
2754              .SBTTL  CHKMAN - CHECK MANUAL INTERVENTION LEGALITY
2755              ;*
2756              ;
2757              ;ROUTINE TO TEST FOR MANUAL INTERVENTION LEGALITY.
2758              ;
2759              ;INPUT:
2760              ;
2761              ;      NONE.
2762              ;
2763              ;OUTPUT:
2764              ;
2765              ;      CARRY 0      MANUAL INTERVENTION NOT ALLOWED
2766              ;      CARRY 1      MANUAL INTERVENTION IS OK
2767              ;
2768              ;SIDE EFFECTS:
2769              ;
2770              ;      A MESSAGE IS DISPLAYED WARNING THAT TEST IS
2771              ;      NOT EXECUTED IF MANUAL INTERVENTION IS NOT
2772              ;      ALLOWED.
2773              ;
2774              ;-
2775
2776 020320              CHKMAN::
2777 020320              SAVREG          ;SAVE THE REGISTERS
2778 020324              MANUAL          ;SEE IF MANUAL INTERVENTION OK
2779 020326 104450      TRAP    C$MANI
2780 020330 103411      BCOMPLETE 1$      ;BRANCH IF ALLOWED
2781 020330 012746 020354'      BCS     1$
2782 020334 012746 000001      PRINTF  @NOMAN        ;PRINT THE WARNING MESSAGE
2783 020340 010600              MOV      @NOMAN,-(SP)
2784 020342 104417              MOV      @1,-(SP)
2785 020344 062706 000004      MOV      SP,RO
020350 000241              TRAP    C$PNTF
020352 000207              ADD     @4,SP
2781 020350 000241              CLC
2782 020352 000207              ;CLEAR CARRY FOR ERROR
2783              1$:      RTS      PC          ;RETURN
2784 020354 045 116 045 NOMAN: .ASCIZ '### Manual Intervention not Allowed - Test Aborted ###'
2785              .even

```

```

2786
2787             .SBTTL  ENVIRN  - SETUP FREE DIAGNOSTIC SPACE
2788             ;
2789             ; SUBROUTINE TO SET-UP VARIOUS ENVIRONMENTAL PARAMETERS.
2790             ;
2791             ; ENVIRN: MEMORY  R0
                TRAP  C$MEM
2792             020450  104431              MOV  R0, FREE      ; GET 1ST FREE ADDRESS...
2793             020452  010037  003122'    ADD  #2, FREE
2794             020456  062737  000002  003122'  MOV  (R0), FRESIZ  ; ...AND WORD COUNT.
2795             020464  011037  003124'    SUB  #4, FRESIZ
2796             020470  162737  000004  003124'  MOV  L$UNIT, R2   ; GET NUMBER OF UNITS
2797             020476  013702  002012'    SUB  #7, FRESIZ   ; TAKE AWAY 7 WORDS PER UNIT
2798             020502  162737  000007  003124'  10$: DEC  R2
2799             020510  005302              BNE  10$
2800             020512  001373              MOV  FREE, R0     ; GET FIRST FREE ADDRESS
2801             020514  013700  003122'    ADD  FRESIZ, R0   ; POINT TO LAST FREE ADDRESS
2802             020520  063700  003124'    SUB  #2, R0      ; BACKUP 1 WORD
2803             020524  162700  000002      MOV  R0, FREE;:T  ; STORE LAST FREE ADDRESS
2804             020530  010037  003126'    40$: MOV  RO, FREE;:T
2805             020534  000207      RTS  PC             ; RETURN
2806
2807             .SBTTL  KTINIT  - SETUP KT11 MEMORY MANAGEMENT REGISTERS
2808             ;
2809             ;
2810             ; ROUTINE TO INIT KT-11
2811             ;
2812             ; -
2813             ;
2814             ; KTINIT:
                CLR  KTFLG      ; INIT >28K MEMORY FLAG
2815             020536  005037  003130'    CLR  KTENABLE    ; INIT TEST >28K FLAG
2816             020542  005037  003132'    CMP  L$HIME, #1577 ; GOT ENOUGH MEMORY (>28K)?
2817             020546  023727  002120'  001577  9$      ; NO.
2818             020554  101453              BLOS 9$          ; NO.
2819             020556  023727  002120'  001777  CMP  L$HIME, #1777 ; GOT ENOUGH MEMORY (>32K)?
2820             020564  101447              BLOS 9$          ; NO.
2821             020566  013700  000004      MOV  @ERRVEC, R0  ; SAVE OLD ERR VEC PTR.
2822             020572  012737  020664'  000004  MOV  #2$, @ERRVEC ; SET ERR VEC PTR.
2823             020600  005737  177572      TST  @SRO        ; GOT KT11?
2824             020604  000240              NOP             ; (TRAP IF NO).
2825             020606  013737  002120'  003130'  MOV  L$HIME, KTFLG ; YES. SET KT FLAG.
2826             020614  042737  000177  003130'  BIC  #177, KTFLG
2827             020622  010037  000004      MOV  R0, @ERRVEC ; RESTORE OLD ERR VEC PTR.
2828             020626  005000              CLR  R0         ; R0 = AR DATA.
2829             020630  012701  172340      MOV  @KIPAR0, R1 ; R1 = KI REGS PTR.
2830             020634  012761  077406  177740  1$: MOV  #77406, -40(R1) ; SET DESCRIPTOR REG.
2831             020642  010021              MOV  R0, (R1),  ; SET KIPAR REG.
2832             020644  062700  000200      ADD  #200, R0    ; BUMP AR DATA BY "4K".
2833             020650  020027  002000      CMP  R0, #2000  ; AT "I/O"?
2834             020654  001367              BNE  1$        ; NO.
2835             020656  012741  177600      MOV  #177600, -(R1) ; YES. SET KTPAR7 FOR I/O.
2836             020662  000410              BR   9$
2837             020664  012716  020700'    2$: MOV  #6$, (SP) ; SET UP RETURN
2838             020670  000002      RTI             ; RTI TO NEXT LOCATION
2839
2840
2841             020672  012716  020726'    3$: MOV  #10$, (SP) ; SET UP RETURN

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2842 020676 000002          RTI          ; RTI TO NEXT LOCATION
2843
2844 020700 010037 000004    6$:      MOV      R0,@ERRVEC    ; RESTORE OLD ERR VEC PTR.
2845
2846 020704          9$:
2847 020704 013700 000004    MOV      @ERRVEC,R0    ; SAVE OLD ERR VEC PTR.
2848 020710 012737 020672' 000004    MOV      @3@,@ERRVEC  ; SET ERR VEC PTR.
2849 020716 042737 000001 170200    BIC      @BITO,@MMRO   ;BE SURE UNIBUS MAP IS OFF
2850 020724 000240
2851 020726 010037 000004    10$:    MOV      R0,@ERRVEC    ; RESET VECTOR BACK TO ERROR POINTER
2852 020732 000207          RTS      PC
2853
2854
2855          ;*
2856          ;      SUBROUTINE TO SET EXTENDED FEATURES SWITCH
2857          ;
2858          ;      Requires that SOFINIT and WRTCHR have been done previous to call.
2859          ;
2860          ;
2861          ; INPUTS:
2862          ;      R5      CURRENT UNIT NUMBER
2863          ; OUTPUTS:
2864          ;      The Extended Features Switch is set.
2865          ;
2866          ;-
2867
2868 020734          INVERT::
2869
2870 020734 005737 002224'          TST      EXTFEA          ; IS SWITCH SET?
2871 020740 001020          BNE      1$              ; YES,EXIT STAGE RIGHT!(or the next one outa town!)
2872 020742 012737 100206 021010'  MOV      @100206,CMDPKT ; WRT SUB-SYS MEM CMD
2873 020750 012737 021020' 021012'  MOV      @WSMBK,CMDPKT+2 ; MSG BUF ADDR
2874 020756 012737 000006 021016'  MOV      @6,CMDPKT+6    ; BYTE COUNT
2875 020764 012737 100010 021020'  MOV      @100010,WSMBK  ; INVERT THE SWITCH
2876 020772 012704 021010'          MOV      @CMDPKT,R4     ; SET CMDPKT INTO R4
2877 020776 004737 010472'          JSR      PC,WRTCHR      ; DO IT
2878 021002 000207          1$:      RTS      PC          ; RETURN
2879
2880
2881          ;      COMMAND PACKET.
2882
2884 021004          .BLKB  10-<.-TSV2&7>
2886
2887 021010 000000          CMDPKT:: 0              ;1ST WORD IS TS05 COMMAND.
2888 021012 000000          0              ;2ND WORD IS THE BUFFER LOW ADDRESS.
2889 021014 000000          0              ;3RD WORD IS THE BUFFER HIGH ADDRESS.
2890 021016 000000          0              ;4TH WORD IS THE BYTE/RECORD/FILE COUNT.
2891
2892
2893          ;      WRITE SUB-SYSTEM MEMORY CHARACTERISTIC BLOCK.
2894
2895 021020 000000          WSMBK:: 0              ;1ST WORD:: SEL 0
2896 021022 000000          0              ;2ND WORD:: SEL 2
2897 021024 000000          0              ;3RD WORD:: SEL 4
2898          .EVEN
2899
2900          ;*

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2901          ;          SUBROUTINE TO CHECK WETHER OR NOT WE'LL TEST NXM
2902          ;
2903          ;
2904          ;INPUTS:
2905          ;OUTPUTS:
2906          ;          The NXMFLG is set if we can test.
2907          ;          The NXMLO and NXMHI addresses are setup.
2908          ;
2909          ;
2910 021026    MEMCK::
2911
2912 021026    SAVREG          ;SAVE THE REGISTERS
2913 021032 005037 003134'   CLR          NXMFLG      ;CLEAR THE FLAG
2914 021036 005037 003136'   CLR          NXMLO       ;CLEAR THE TEST ADDRESS LO
2915 021042 005037 003140'   CLR          NXMHI       ;CLEAR THE TEST ADDRESS HI
2916 021046 032737 170000 002120' BIT          @170000,L$HIME ;CHECK FOR MORE THAN 18 BITS INDICATED
2917          ;FROM THE SUPERVISOR
2918 021054 001050          BNE          14$          ;BR, IF MAP BOX ETC.
2919 021056 005737 003144'   TST          T23B        ;IS IT A PROCESSOR TYPE B?
2920 021062 001407          BEQ          1$          ;NO
2921 021064 023727 002120' 007777   CMP          L$HIME,@7777 ; GREATER THAN 128K
2922 021072 103406          BLO          2$          ; NO
2923 021074 004737 021222'   JSR          PC,NXMTST    ;SETUP THE ADDRESS
2924 021100 000427          BR          13$         ;SET THE FLAG AND EXIT
2925 021102 005737 003142'   1$: TST          T23A        ;IS IT A PROCESSOR TYPE A?
2926 021106 001413          BEQ          4$          ;NO
2927 021110 023727 002120' 005777   2$: CMP          L$HIME,@5777 ;GREATER THAN 96K
2928 021116 101027          BHI          14$         ;YES,23A/23B WITH 128K MEMORY
2929 021120 023727 002120' 003777   CMP          L$HIME,@3777 ;GREATER THAN 64K BUT LESS THAN 92K?
2930 021126 103403          BLO          4$          ;NO, CHECK 24K
2931 021130 004737 021222'   JSR          PC,NXMTST    ;SETUP THE ADDRESS
2932 021134 000411          BR          13$         ;SET THE FLAG AND EXIT
2933 021136 023727 002120' 001577   4$: CMP          L$HIME,@1577 ;GREATER THAN 24K BUT LESS THAN 64K?
2934 021144 103414          BLO          14$         ;NO, TELL THEM AND EXIT WITH FLAG CLEAR
2935 021146 004737 021222'   JSR          PC,NXMTST    ;SETUP THE ADDRESS
2936 021152 062737 000077 003140'   ADD          @77,NXMHI    ;FOOL THE 11/02 & 11/03
2937 021160 032737 177774 003140' 13$: BIT          @177774,NXMHI ;ANY MORE THAN 18 BITS SET?
2938 021166 001014          BNE          15$         ;BR, IF MORE THAN 18 BITS SET
2939 021170 005237 003134'   INC          NXMFLG      ;SET THE FLAG
2940 021174 000411          BR          15$         ;EXIT
2941 021176 000410          14$: BR          15$         ;NOP FOR PRINTOUT
2942 021200          PRINTF      @NOMEM          ;TELL THEM & EXIT ***NO PRINT*****
2943 021200 012746 005452'   MOV          @NOMEM,-(SP)
2944 021204 012746 000001   MOV          @1,-(SP)
2945 021210 010600          MOV          SP,RO
2946 021212 104417          TRAP         C$PNTF
2947 021214 062706 000004   ADD          @4,SP
2948 021220 000207          15$: RTS          PC          ;RETURN
2949
2950          ;
2951          ;          SUBROUTINE TO SETUP THE NXM ADDRESS FOR TESTING
2952          ;
2953          ;
2954          ;OUTPUTS:NXMLO,NXMHI          ;SETUP WITH NXM ADDRESS
2955          ;
2956          ;

```

TSV3 - GLOBAL AREAS MACRO M1113 07-FEB-84 10:58
 KTINIT - SETUP KT11 MEMORY MANAGEMENT REGISTERS

SEQ 084

2953	021222	013701	002120'	NXMTST: MOV	L#HIME,R1	:GET TOP OF MEMORY
2954	021226	062701	000200	ADD	#200,R1	:MAKE IT I/O BLOCK OR OTHER NXM
2955	021232	042701	000177	BIC	#177,R1	
2956	021236	010102		MOV	R1,R2	:RESAVE RESULTS
2957		000006		.REPT	6	
2958				ASL	R1	:PUT IN PLACE FOR XFER
2959				.ENDR		
2960	021254	010137	003136'	MOV	R1,NXMLE	:SAVE TEST ADDRESS LOW
2961		000012		.REPT	10.	
2962				ASR	R2	:PUT IN PLACE FOR XFER
2963				.ENDR		
2964	021304	042702	177700	BIC	#177700,R2	:DON'T WANT ILA!
2965	021310	010237	003140'	MOV	R2,NXMMI	:SAVE TEST ADDRESS HIGH
2966	021314	000207		RTS	PC	:RETURN
2967						
2968						
2969						
2970						
2971	021316			ENDMOD		

```

6          .TITLE  TSV4 - MISCELLANEOUS SECTIONS
7
8 021316   BGNMOD  TSV4
9 021316   TSV4::
15
16          .SBTTL  PROTECTION TABLE
17 021316   BGNPROT
18 021316   177777 177777 177777 L$PROT::
19 021326   .WORD   -1, -1, -1, -1          ;NO DEVICE PROTECTION REQUIRED.
20          ENDPROT
21          .SBTTL  INITIALIZE SECTION
22
23          ;**
24          ;THE INITIALIZE SECTION CONTAINS THE CODING THAT IS PERFORMED
25          ;AT THE BEGINNING OF EACH PASS.
26          ;
27          ;IF "START" OR "RESTART", SET QUICK-PASS FLAG AND BUS-INIT.
28          ;IF "CONTINUE", NOTHING IS REQUIRED.
29          ;
30          ;--
31          ;*
32          ;INSERT TEMPORARY JUMP TO ODT
33          ;-
34 021326   BGNINIT
35 021326   L$INIT::
36 021332   005037 002224' 40$: CLR     EXTFEA
37 021336   012737 006170' 002176' CLR     NXMFLG
38 021344   005037 003152'      MOV     @EPRT1,EPRTSW          ;SET UP PRIMARY MESSAGE FOR REPLACEMENT
39 021350   005037 003132'      CLR     SIFLAG              ;CLEAR "SOFT INIT" FLAG
40 021354   005037 002300'      CLR     KTENABLE           ;CLEAR TEST ABOVE 28K FLAG
41 021360   012700 000036      CLR     RAMSIZ              ;CLEAR RAM SIZE FOR RAMERR ROUTINE
42 021366   103023 000036      READEF  @EF.CONTINUE
43 021370   023737 002200' 002012' MOV     @EF.CONTINUE,RO
44 021376   103070 000036      TRAP   C$REFG
45 021400   005737 003110'      BNCOMPLETE 1$
46 021404   100472 000036      BCC    1$
47 021406   013701 002200'      CMP    UNITN,L$UNIT          ;UNIT IN RANGE?
48 021412   006301 000036      BHIS  4$                    ;BR IF NO.
49 021414   005761 003174'      TST   DUFLG                 ;DROPPED UNIT?
50 021420   001516 000036      BMI   NXTU                  ;BR IF YES
51 021422   032761 040000 003174' MOV     UNITN,R1
52 021430   001060 000036      ASL   R1
53 021432   104432 000036      TST   ERTABL(R1)
54 021436   012700 000035      BEQ   SETU
55 021444   103052 000036      BIT   @BIT14,ERTABL(R1)    ;DROPPED?
56 021446   104447 000036      BNE   NXTU
57          EXIT              ;DO NOTHING IF "CONTINUE".
58          TRAP   C$EXIT
59          .WORD  L10030-.
60          READEF  @EF.NEW
61          MOV     @EF.NEW,RO
62          TRAP   C$REFG
63          BNCOMPLETE NXTU          ;TAKE NEXT UNIT IF NOT NEW PASS.
64          BCC    NXTU
65          READEF  @EF.START

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021446 012700 000040      MOV      @EF.START,RO
021452 104447      TRAP     C$REFG
57 021454      @COMPLETE 2$
021454 103404      BCS     2$
58 021456      READEF  @EF.RESTART
021456 012700 000037      MOV      @EF.RESTART,RO
021462 104447      TRAP     C$REFG
59 021464      @COMPLETE 31$
021464 103031      BCC     31$
60 021466      2$:
61 021466      BRESET
021466 104433      TRAP     C$RESET
62 021470 005037 002212'      CLR     TSTCNT      ;NUMBER OF TESTS RUN IN PASS
63 021474 005037 002220'      CLR     FATFLG      ;CLEAR FATAL ERROR COUNT
64 021500 005037 003142'      CLR     T23A        ;CLEAR PROCSSOR TYPE A FLAG
65 021504 005037 003144'      CLR     T23B        ;CLEAR PROCSSOR TYPE B FLAG
66
67      ;      MOV     @340,-(SP)      ;RETURN TO DEBUGGER
68      ;      MOV     @20$,-(SP)      ;:ENTER THE DEBUGGER
69 021510 005037 003376'      JMP     0.ODT      ;:CLEAR THE SUBTEST "SKIPPER"
70 021514      CLR     SKIPT
71 021514 012737 177777 002202' 20$:      MOV     @-1,QVP      ;...QUICK VERIFY...
72 021522 004737 020450'      JSR     PC,ENVIRN    ;SET ENVIRONMENT.
73 021526 004737 020536'      JSR     PC,KTINIT   ;INITIALIZE KT MEMORY MANAGEMENT
74 021532 012700 003174'      MOV     @ERTABL,RO
75 021536 005020 30$:      CLR     (RO)        ;CLEAR THE ERROR TABLE
76 021540 020027 003374'      CMP     RO,@ERTABE
77 021544 103774      BLO     30$
78 021546 000404      BR     4$
79 021550 005037 002202' 31$:      CLR     QVP
80 021554 000137 021624'      JMP     PASRPT      ;GO REPORT THE STATUS
81
82 021560      4$:
83 021560 012737 177777 002200' NEWPAS: MOV     @-1,UNITN    ;INIT UNIT NUMBER...
84 021566 005037 002216'      CLR     DEVCNT     ;CLEAR COUNT OF DEVICES RUNNING
85 021572      NXTU:      BREAK
021572 104422      TRAP     C$BRK
86 021574 005237 002200'      INC     UNITN      ;...AND SET NEXT UNIT NUMBER.
87 021600 023737 002200' 002012'      CMP     UNITN,L$UNIT
88 021606 103423      BLO     SETU
89 021610 012737 177777 003110'      MOV     @-1,DUFLG
90 021616 000401      BR     11$
91 021620      DOCLN      ;ABORT, NO MORE UNITS.
021620 104444      TRAP     C$DCLN
92 021622 000240      NOP
93 021624      11$:
94 021624 023727 002012' 000001 PASRPT: CMP     L$UNIT,@1    ;HOW MANY UNITS SELECTED?
95 021632 101752      BLOS    NEWPAS      ;BR IF ONLY 1
96 021634 005737 002216'      TST     DEVCNT     ;ARE ANY STILL RUNNING?
97 021640 001747      BEQ     NEWPAS     ;BR IF NO
98 021642      RFLAGS    RO
021642 104421      TRAP     C$RFLA
99 021644 032700 000100      BIT     @ISR,RO    ;SHOULD WE PRINT STATISTICS
100 021650 001343      BNE     NEWPAS     ;BR IF NO
101
102 021652      DORPT
021652 104424      TRAP     C$DRPT

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103 021654 000741          BR      NEWPAS
104 021656
105
106 021656 013700 002200'  10$:  SETU:  GPWARD  UNITN,R0          ;GET UNIT N P-TABLE POINTER.
      021656 013700 002200'  MOV      UNITN,R0
      021662 104442  TRAP    C$GPWRD
107 021664 103342          BNCOMPLETE NXTU          ;BR IF UNIT NOT AVAILABLE.
      021664 103342  BCC     NXTU
108 021666 005037 003110'  CLR     DUFLG          ;CLEAR "DROPPED" FLAG.
109 021672 005237 002216'  INC     DEVCNT
110 021676 012001          MOV     (R0)+,R1       ;GET 1ST REGISTER ADDRESS.
111 021700 010137 002204'  MOV     R1,CSRADDR     ;ADDRESS OF REGISTERS OF UNIT UNDER TEST
112
113 021704 012001          MOV     (R0)+,R1       ;GET VECTOR ADDRESS.
114          ;MOV    (R0),R2     ;GET INTERRUPT PRIORITY
115          ;MOV    R2,IPRI     ;SET INTERRUPT PRIORITY.
116 021706 010137 002206'  MOV     R1,IVC         ;SET INTERRUPT VECTOR POINTEP...
117 021712 012721 016026'  MOV     @INTR,(R1)+    ;...VECTOR...
118 021716 013721 002210'  MOV     IPRI,(R1)+    ;...AND PRIORITY.
119
120 021722          1$:
121          ;      TST    QVP          ;1ST PASS ??
122          ;      BEQ    5$          ;NO, SKIP THE PASS 1 STUFF.
123
124          ;
125          ;1ST PASS, CHECK THAT DEVICE ADDRESSES ARE VALID, AND
126          ;THAT THE DISPLAY STATUS IS PROPERLY INITIALIZED.
127          ;
128 021722 013701 002200'  MOV     UNITN,R1
129 021726 006301          ASL     R1
130 021730 052761 100000 003174'  BIS     @BIT15,ERTABL(R1) ;SAY DEVICE RUNNING
131 021736 005037 005602'  CLR     EXTA          ;CLEAR ERROR EXTENSION FLAG.
132 021742 023727 002012' 000001  CMP     L$UNIT,#1     ;ARE WE TESTING MULTIPLE UNITS?
133 021750 101416          BLOS   10$          ;BR IF NO.
134 021752          RFLAGS  R0          ;YES -- GET OPERATOR FLAGS.
      021752 104421  TRAP    C$RFLA
135 021754 032700 001000  BIT     @PNT,R0       ;SHOULD WE PRINT UNIT #?
136 021760 001412          BEQ    10$          ;BR IF NOT.
137 021762          PRINTF  @PUNIT,UNITN ;PRINT THE UNIT #
      021762 013746 002200'  MOV     UNITN,-(SP)
      021766 012746 022054'  MOV     @PUNIT,-(SP)
      021772 012746 000002  MOV     @2,-(SP)
      021776 010600          MOV     SP,R0
      022000 104417  TRAP    C$PNTF
      022002 062706 000006  ADD     @6,SP
138 022006          10$:
139 022006 005037 003112'  CLR     NODEV
140 022012 013701 002204'  MOV     CSRADDR,R1    ;ADDRESS OF FIRST REGISTER
141 022016 010102          MOV     R1,R2         ;START OF REGISTERS
142 022020 062702 000002  ADD     @TSSR,R2     ;ADDRESS OF TSSR REGISTER
143 022024 004737 016206'  JSR    PC,XNXM       ;TEST BOTH CONTROLLER REGISTERS...
144 022030 103005          BCC    2$          ;...AND BR IF ALL OK.
145 022032 010137 003112'  MOV     R1,NODEV     ;FLAG DEVICE AS NON-EXISTENT
146 022036 012737 177777 003110'  MOV     @-1,DUFLG    ;DROP THIS UNIT.
147 022044          2$:
148          ;
149          ;FINALLY, SET CPU PRIORITY AND WE'RE DONE.

```



```

150
151 022044          5$:   SETPRI  @PRI00          ;ENABLE INTERRUPTS.
    022044 012700 000000   MOV      @PRI00,RO
    022050 104441         TRAP    C$SPRI
152 022052         L10030:  ENDINIT
    022052         TRAP    C$INIT
    022052 104411
153
154 022054          045    116    045 PUNIT: .ASCIZ  /#N#N#A***** TESTING UNIT #D2#A *****/
155 .EVEN
156
157 .SBTTL  ADD AND DROP UNITS SECTIONS
158
159
160 ;**
161 ; THE ADD-UNIT SECTION CONTAINS THE CODING THAT CAUSES A DEVICE
162 ; TO BE (A) ADDED TO THE TEST LIST FOR THE FIRST TIME,
163 ; OR (B) RE-INSERTED IF IT HAD BEEN PREVIOUSLY DROPPED.
164 ;--
164 022122         BGNAU
    022122 L$AU::
165 022122 010001       MOV      RO,R1          ; GET UNIT TO BE ADDED (RO)
166 022124 006301       ASL      R1              ; MAKE IT A WORD INDEX
167 022126 052761 100000 003174'   BIS      @100000,ERTABL(R1)   ; SET THE "ACTIVE" BIT
168 022134 042761 040000 003174'   BIC      @40000,ERTABL(R1)   ; CLEAR THE "DROPPED" BIT
169 022142         PRINTF  @1$,RO
    022142 010046       MOV      RO,-(SP)
    022144 012746 022170'   MOV      @1$,-(SP)
    022150 012746 000002       MOV      @2$,-(SP)
    022154 010600       MOV      SP,RO
    022156 104417       TRAP    C$PNTF
    C22160 062706 000006       ADD      @6$,SP
170 022164         EXIT    AU
    022164 000167       .WORD   J$JMP
    022166 000026       .WORD   L10031-2-
171 022170          045    116    045 1$: .ASCIZ  /#N#A UNIT #D#A ADDED/
172 .EVEN
173
174 022216         ENDAU          ; UNUSED.
    022216 L10031:
    022216 104452       TRAP    C$AU
175
176 ;**
177 ; THE DROP-UNIT SECTION CONTAINS THE CODING THAT CAUSES A DEVICE
178 ; TO BE REMOVED FROM THE TEST LIST.
179 ;
180 ; SUPVSR DOES THE "DROPPING". THIS IS JUST TO TELL THE MAN.
181 ; "DROPPED" UNITS ARE RE-SELECTED ON OPERATOR "STA" OR "ADD"
182 ; COMMAND, OTHERWISE REMAIN INACTIVE. THE "DISPLAY" COMMAND
183 ; WILL PRINT ALL DROPPED UNITS, AND THE P-TABLES OF THOSE
184 ; WHICH ARE STILL ACTIVE.
185 ; UPON ENTRY, RO CONTAINS THE UNIT TO BE DROPPED.
186 022220         BGNDU
    022220 L$DU::
187 022220 012737 177777 003110'   MOV      @-1,DUFLG
188 022226 010001       MOV      RO,R1
189 022230 006301       ASL      R1
190 022232 052761 140000 003174'   BIS      @140000,ERTABL(R1)   ; SAY DROPPED

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```

191 022240 000240 000240 000240      240,240,240      ; ??????????
192 022246      010046      PRINTF #1$,R0
      022250 012746 022274'      MOV R0,-(SP)
      022254 012746 000002      MOV #1$,-(SP)
      022260 010600      MOV #2,-(SP)
      022262 104417      MOV SP,R0
      022264 062706 000006      TRAP C$PNTF
193 022270      EXIT DU
      022270 000167      .WORD J$JMP
      022272 000030      .WORD L10032-2-
194 022274      045      116      045 1$: .ASCIZ /%N%A UNIT %D%A DROPPED/
195      .EVEN
196 022324      ENDDU
      022324      L10032: TRAP C$DU
      022324 104453
197      ;**
198      ; AUTO-DROP CODE SECTION.
199      ;--
200 022326      BGNAUTO
      022326      L$AUTO::
201 022326 013705 002204'      MOV CSRADDR,R5      ;POINT TO DEVICE REGISTER
202 022332 012703 000550      MOV #360.,R3      ;ENOUGH TIME FOR 2400' REEL TO REWIND
203 022336 004737 016060'      10$: JSR PC,WAIF      ;WAIT FOR SSR TO SET
204 022342 103420      BCS 20$      ;LEAVE WHEN SSR IS SET
205 022344      DELAY 250.      ;WAIT FOR .25 SECONDS
      022344 012727 000372      MOV #250..(PC)+
      022350 000000      .WORD 0
      022352 013727 002116'      MOV L$DLY,(PC)+
      022356 000000      .WORD 0
      022360 005367 177772      DEC -6(PC)
      022364 001375      BNE -.4
      022366 005367 177756      DEC -22(PC)
      022372 001367      BNE -.20
206 022374 005303      DEC R3      ;BUMP COUNTER DOWN
207 022376 001357      BNE 10$      ;KEEP GOING
208 022400 004737 017014'      JSR PC,CKDROP      ;TRY AND DROP UNIT
209 022404
210 022404      20$: ENDAUTO      ; UNUSED.
      022404      L10033:
      022404 104461      TRAP C$AUTOJ
211
212
213      .SBTTL CLEAN-UP AND REPORT CODING SECTIONS
214
215      ;**
216      ; THE CLEANUP CODING SECTION CONTAINS THE CODING THAT IS
217      ; EXECUTED AT THE END OF EACH PASS (OR SUB-PASS).
218      ; USE TO RETURN DEVICE UNDER TEST TO A NEUTRAL STATE.
219      ;--
219 022406      BGNCLN
      022406      L$CLEAN::
220 022406 013705 002204'      MOV CSRADDR,R5      ;POINT TO DEVICE REGISTER
221 022412 005737 003110'      TST DUFLG      ;"DROPPED" FLAG IS SET ON...
222 022416 100405      BMI 1$      ;...AND GROSS CONTROLLER FAULT...
223      ;...DON'T TRY TO XCT CLEANUP CODE.
224
225 022420 012765 000000 000002      MOV #0,TSSR(R5)      ;DO SOFT INIT

```

226	022426	004737	016060'		JSR	PC, WAITF	
227	022432			1\$:			
228	022432			2\$:	ENDCLN		
	022432			L10034:			
	022432	104412			TRAP	C\$CLEAN	
229				***			
230				:	THE REPORT CODING SECTION CONTAINS THE		
231				:	"PRINTS" CALLS THAT GENERATE STATISTICAL REPORTS.		
232				:-			
233	022434				BGNRPT		
	022434			L\$RPT::			
234	022434				PRINTS	#DEVSUM	
	022434	012746	022676'		MOV	#DEVSUM, -(SP)	
	022440	012746	000001		MOV	#1, -(SP)	
	022444	010600			MOV	SP, R0	
	022446	104416			TRAP	C\$PNTS	
	022450	062706	000004		ADD	#4, SP	
235	022454	010246			MOV	R2, -(SP)	
236	022456	010346			MOV	R3, -(SP)	
237	022460	010446			MOV	R4, -(SP)	
238	022462	012704	003174'		MOV	#ERTABL, R4	: GET START OF ERROR TABLE.
239	022466	005003			CLR	R3	: CLEAR UNIT NUMBER
240	022470	011402		1\$:	MOV	(R4), R2	: GET ERROR TABLE ENTRY & TEST IT.
241	022472	001467			BEQ	4\$: ZERO IF UNIT NOT RUN
242	022474	100066			BPL	4\$	
243	022476	032702	040000		BIT	#BIT14, R2	: WAS UNIT DROPPED?
244	022502	001015			BNE	2\$: BR IF YES
245	022504	042702	170000		BIC	#C7777, R2	: GET ERROR COUNT FIELD
246	022510				PRINTS	#DEVONL, R3, R2	: PRINT
	022510	010246			MOV	R2, -(SP)	
	022512	010346			MOV	R3, -(SP)	
	022514	012746	022733'		MOV	#DEVONL, -(SP)	
	022520	012746	000003		MOV	#3, -(SP)	
	022524	010600			MOV	SP, R0	
	022526	104416			TRAP	C\$PNTS	
	022530	062706	000010		ADD	#10, SP	
247	022534	000446			BR	4\$	
248	022536	020227	160000	2\$:	CMP	R2, #160000	: WAS UNIT NON-EXISTENT?
249	022542	001012			BNE	3\$: BR IF NO
250	022544				PRINTS	#DEVN XR, R3	
	022544	010346			MOV	R3, -(SP)	
	022546	012746	023003'		MOV	#DEVN XR, -(SP)	
	022552	012746	000002		MOV	#2, -(SP)	
	022556	010600			MOV	SP, R0	
	022560	104416			TRAP	C\$PNTS	
	022562	062706	000006		ADD	#6, SP	
251	022566	000431			BR	4\$	
252	022570	020227	160001	3\$:	CMP	R2, #160001	: WAS UNIT NOT READY AT STARTUP?
253	022574	001012			BNE	30\$: BR IF NO.
254	022576				PRINTS	#DEVNRD, R3	
	022576	010346			MOV	R3, -(SP)	
	022600	012746	023065'		MOV	#DEVNRD, -(SP)	
	022604	012746	000002		MOV	#2, -(SP)	
	022610	010600			MOV	SP, R0	
	022612	104416			TRAP	C\$PNTS	
	022614	062706	000006		ADD	#6, SP	
255	022620	000414			BR	4\$	

```

256 022622 042702 170000      30$: BIC      #+C7777,R2
257 022626                PRINTS  #DEVDR0,R3,R2
      022626 010246          MOV      R2,-(SP)
      022630 010346          MOV      R3,-(SP)
      022632 012746 023146'  MOV      #DEVDR0,-(SP)
      022636 012746 000003   MOV      #3,-(SP)
      022642 010600          MOV      SP,R0
      022644 104416          TRAP    C#PNTS
      022646 062706 000010   ADD      #10,SP
258 022652 062704 000002      4$: ADD      #2,R4
259 022656 005203          INC      R3
260 022660 020427 003374'   CMP      R4,#ERTABE
261 022664 103701          BLO     1$
262 022666 012604          MOV      (SP)+,R4
263 022670 012603          MOV      (SP)+,R3
264 022672 012602          MOV      (SP)+,R2
265 022674                ENDRPT          ; UNUSED.
      022674                L10035:
      022674 104425          TRAP    C#RPT
266
267
268 022676      045      116      045  DEVSUM: .ASCIZ  /#N#ADEVICE STATUS SUMMARY:#N/
269 022733      045      101      040  DEVONL: .ASCIZ  /#A UNIT #D3#A ONLINE, ERRORS = #D#N/
270 023003      045      101      040  DEVNXL: .ASCIZ  /#A UNIT #D3#A DROPPED, NON-EXISTENT REGISTER#N/
271 023065      045      101      040  DEVNRD: .ASCIZ  /#A UNIT #D3#A DROPPED, NOT READY AT STARTUP#N/
272 023146      045      101      040  DEVDR0: .ASCIZ  /#A UNIT #D3#A DROPPED, ERRORS = #D#N/
273
274
275 023216                .EVEN
276                ENDMOD
277
278
  
```

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023216
023216

023216
023216
023216 012700 023414'
023222 004737 016322'
023226 012737 000024 002214'
023234
023234 005003
023236
023236
023236 104402

.TITLE TSV5A - HARDWARE TESTS
?
BGNMOD TSV5
TSV5::

.SBTTL TEST 1: BUS RESET TEST

: THIS TEST VERIFIES THAT THE M7455 MODULE'S DEVICE REGISTERS ARE
: ACCESSIBLE ON THE BUS (SUBTEST 1) AND THEN CHECKS THAT THE
: BUILT-IN INITIALIZATION SELF-TEST MICRODIAGNOSTIC DID NOT FIND
: ANY BASIC PROBLEMS IN THE MODULE. AREAS OF LOGIC TESTED BY THE
: SELF-TEST SEQUENCE ARE AS FOLLOWS: ROM AND PIPELINE REGISTER,
: SEQUENCER, INTERNAL BUSES, 2901 MICROPROCESSOR, AND, RAM. THIS
: TEST INITIALIZES THE CONTROLLER BY ISSUING THE BUS INIT SIGNAL
: VIA A RESET INSTRUCTION, OR BY WRITING INTO THE TSSR REGISTER,
: WAITS A PERIOD OF TIME (TO ALLOW THE CONTROLLER'S INITIALIZATION
: MICRODIAGNOSTIC SEQUENCE TO BE COMPLETED), AND THEN CHECKS THE
: CONTENTS OF THE TSSR REGISTER. SUCCESSFUL INITIALIZATION IS
: INDICATED BY SUBSYSTEM READY (SSR) AND NEED BUFFER ADDRESS (NBA)
: BITS BEING SET (1) AND ALL OTHER BITS (EXCEPT A17 AND A16 AND
: OFL, WHICH ARE IGNORED FOR THIS TEST) BEING CLEAR (0). IF THE
: CONTENTS OF TSSR ARE NOT AS EXPECTED, AN ERROR REPORT IS ISSUED
: LISTING THE EXPECTED DATA, ACTUAL DATA, AND THE DISCREPANCIES.
: THE ERROR REPORT ANALYZES THE TSSR CONTENTS AND DISCERNES AND
: REPORTS ONE OF THREE POSSIBILITIES:

- 1. TSSR CONTENTS ARE AMBIGUOUS (ANY OF BITS 11-14 ARE SET, OR STATES OF SSR AND SC BITS DO NOT CORRESPOND TO THE APPARENT ERROR CODE IN BITS 0-5): INDICATES THAT THE TSSR CONTENT CANNOT BE TRUSTED. INDICATES A CATASTROPHIC CONTROLLER MALFUNCTION. THIS IS A FATAL ERROR (EXECUTION IS ABORTED). FIELD ACTION WOULD BE TO REPLACE THE M7455. IF THE M7455 ITSELF IS BEING DEBUGGED, THE PROGRAM SHOULD BE RESTARTED WITH LOOP ON ERROR ENABLED IN ORDER TO PROBE FOR THE PROBLEM.
- 2. SSR = 0, SC = 0 AND THE ERROR CODE IN BITS 0-5 IS IN THE RANGE 17-13: THIS IS A FATAL ERROR. THE ERROR CODE IS DECODED AND THE APPROPRIATE DESCRIPTION GIVEN. INDICATES THAT A SERIOUS PROBLEM EXISTS.

BGNTST
MOV #TST1ID,RO ;ASCII MESSAGE TO IDENTIFY TEST
JSR PC,TSTSETUP ;DO INITIAL TEST SETUP
MOV #20,LOOPCNT ;PERFORM 20 ITERATIONS
T1LOOP: CLR R3 ;USE R3 AS FATAL ERROR FLAG
BGNSUB ;////////// BEGIN SUBTEST ///////////
T1.1: TRAP C1BSUB

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76
77 023240          BRESET          ;ISSUE A BUS RESET
    023240 104433          TRAP      C#RESET
78 023242 004737 016060'   JSR      PC,WAITF   ;WAIT FOR READY
79 023246 016501 000002   MOV      TSSR(R5),R1   ;GET THE CONTENTS OF TSSR
80 023252 010102          MOV      R1,R2        ;CONTENTS OF TSSR
81 023254 042702 176277   BIC      @+C<HIADDR!OFL>,R2 ;THESE BITS MAY BE SET
82 023260 052702 002200   BIS      @SSR!NBA,R2   ;READY AND NEW DATA SHOULD BE SET
83 023264 020102          CMP      R1,R2        ;COMPARE EXPECTED TO RECEIVED
84 023266 001405          BEQ      10#         ;BRANCH IF COMPARE
88 023270          ERRDF      ERRNO,SFHERR,SFFMSG ;REPORT A FATAL ERROR
    023270 104455          TRAP      C#ERDF
    023272 000145          .WORD    101
    023274 003677'       .WORD    SFHERR
    023276 011712'       .WORD    SFFMSG
89 023300 005203          INC      R3          ;SET THE FATAL ERROR FLAG
90          10#:          ENDSUB          ;////////////////// END SUBTEST ////////////////////
91 023302          L10037:          TRAP      C#ESUB
    023302 10-403
92
93 023304 005703          TST      R3          ;DID WE HAVE FATAL ERROR ?
94 023306 001402          BEQ      20#         ;BRANCH IF NOT
95 023310 004737 017014'   JSR      PC,CKDROP    ;GO DROP THIS UNIT, IF ALLOWED
96 023314 005003          CLR      R3          ;RESET FATAL ERROR FLAG
97
98
99 023316          BGNSUB          ;////////////////// BEGIN SUBTEST ////////////////////
    023316          T1.2:          TRAP      C#BSUB
    023316 104402
100
101 023320 005065 000002   CLR      TSSR(R5)     ;WRITE TO ISSUE A SOFT RESET
102 023324 004737 016060'   JSR      PC,WAITF   ;WAIT FOR READY TO SET
103 023330 016501 000002   MOV      TSSR(R5),R1   ;GET REGISTER TSSR DATA
104 023334 010102          MOV      R1,R2        ;CONTENTS OF TSSR
105 023336 042702 176277   BIC      @+C<HIADDR!OFL>,R2 ;THESE BITS MAY BE SET
106 023342 052702 002200   BIS      @SSR!NBA,R2   ;READY AND NEW DATA SHOULD BE SET
107 023346 020102          CMP      R1,R2        ;COMPARE EXPECTED TO RECEIVED
108 023350 001405          BEQ      10#         ;BRANCH IF COMPARE
112 023352          ERRDF      ERRNO,SFIERR,SFFMSG ;REPORT A FATAL ERROR
    023352 104455          TRAP      C#ERDF
    023354 000146          .WORD    102
    023356 003644'       .WORD    SFIERR
    023360 011712'       .WORD    SFFMSG
113 023362 005203          INC      R3          ;SET THE ERROR FLAG
114          10#:          ENDSUB          ;////////////////// END SUBTEST ////////////////////
115 023364          L10040:          TRAP      C#ESUB
    023364 104403
116
117
118 023366 005703          TST      R3          ;FATAL ERROR DETECTED ?
119 023370 001402          BEQ      20#         ;BRANCH IF NOT
120 023372 004737 017014'   JSR      PC,CKDROP    ;SEE IF TIME TO DROP UNIT
121 023376 004737 016270'   JSR      PC,TSTLOOP   ;SHOULD WE DO ITERATIONS ?
122 023402 103002          BCC     40#         ;BRANCH IF NOT
123 023404 000137 023234'   JMP      T1LOOP       ;LOOP UNTIL COUNT EXPIRED

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124 023410          401:  EXIT   TST           ;ALL DONE THIS TEST
    023410 104432
    023412 000022          TRAP   C#EXIT
                                .WORD  L10036-.
125
126
127          ;*
127          ;LOCAL TEXT MESSAGES FOR TEST
128          ;*
129
130 023414          111   156   151  TST1ID: .ASCIZ 'Initialization'
131          .EVEN
132 023434          .ENDTST
    023434
    023434 104401          L10036:  TRAP   C#ETST
133
134          .SBTTL  TEST  2:  WRAP DATA - HIGH BYTE
135
136
137          ; THIS TEST VERIFIES OPERATION OF:
138          ;
139          ;
140          ;
141          ; 1. PART OF THE PDP-11 BUS INTERFACE SECTION OF THE M7455
142          ;    MODULE: PART OF THE INPUT FILE (TSDB HIGH BYTE), PART
143          ;    OF THE OUTPUT FILE (TSSR HIGH BYTE AND TSBA, BOTH
144          ;    BYTES), PART OF THE DCO05 TRANSCEIVER CIRCUITS (ADDRESS
145          ;    DECODER, BDAL DRIVERS, HIGH BYTE OF INTERNAL DAL BUS
146          ;    DRIVERS), AND BASIC PROGRAMMED I/O CONTROL SEQUENCES
147          ;    AND LOGIC;
148          ;
149          ; 2. PART OF 2901 MICROPROCESSOR ELEMENTS (Q-REGISTER,
150          ;    REGISTER 0, ROTATE AND NEGATE FUNCTIONS;
151          ;
152          ; 3. Y AND SOURCE BUSES;
153          ;
154          ; 4. BASIC MICROPROGRAM SEQUENCES.
155          ;
156          ;
157          ; THE PROGRAM WRITES A TEST DATA BYTE INTO THE HIGH BYTE OF TSDB.
158          ; WAITS FOR THE SSR BIT IN TSSR TO SET, THEN CHECKS THE CONTENTS
159          ; OF BOTH TSBA AND TSSR. THE MODULE IS FUNCTIONING CORRECTLY IF
160          ; DATA WRITTEN APPEARS IN BOTH BYTES OF TSBA AND THE FINAL CONTENT
161          ; OF TSSR IS CORRECT (SAME AS AFTER INITIALIZATION EXCEPT FOR BITS
162          ; 8 AND 9, WHICH SHOULD CONTAIN BITS 8 AND 9 OF THE DATA PATTERN
163          ; WRITTEN. AN ERROR IS REPORTED AND A DESCRIPTIVE ANALYSIS GIVEN
164          ; IF A DISCREPANCY IN TSBA OR TSSR IS DETECTED. THE ANALYSIS
165          ; LISTS LIKELY FAULTY CANDIDATES FROM THE LOGIC ELEMENTS LISTED
166          ; ABOVE. THE TEST IS REPEATED FOR ALL COMBINATIONS OF TEST DATA
167          ; BYTES (0-377 OCTAL).
168          ;
169          ;
170          ;
171          ;
172          ;
173          ;
174 023436          012700 024104'  MOV    #TST2ID,R0          T2::
    023436          004737 016322'  JSR    PC,TSTSETUP      ;ASCII MESSAGE TO IDENTIFY TEST
175 023442          012737 000024 002214'  MOV    #20,LOOPCNT    ;DO INITIAL TEST SETUP
176 023446          012703 177777  T2LOOP: CLR    R4          ;PERFORM 20 ITERATIONS
177 023454          005004          MOV    #0-1,R3        ;STARTING DATA PATTERN
178 023456          005703          MOV    #0-1,R3        ;DO INIT ON FIRST TIME THROUGH
179 023462          005703          TST    R3          ;DO WE NEED SOFT INIT
    
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180 023464 001412 BEQ 10$ ;BRANCH IF NOT
181 023466 005003 CLR R3 ;DON'T NEED INIT NEXT TIME THRU
182 023470 004737 015604' JSR PC,SOFINIT ;DO SOFT INIT OF CONTROLLER
183 023474 103406 BCS 10$ ;BR IF SOFT INIT = OK
187 023476 010001 MOV R0,R1 ;SAVE CONTENTS OF TSSR
188 023500 ERRDF ERRNO,SFIERR,SFIMSG ;DEVICE FATAL ERROR DURING INIT
      023500 104455 TRAP C$ERDF
      023502 000311 .WORD 201
      023504 003644' .WORD SFIERR
      023506 011644' .WORD SFIMSG
189 023510 005203 INC R3 ;FORCE SOFT INIT ON NEXT PASS
190 023512 005037 002220' 10$: CLR FATFLG ;CLEAR FATAL ERROR FLAG
191
192 023516 BGNSEG ;>>>>>>>>> BEGIN SEGMENT >>>>>>>>>
      023516 104404 TRAP C$BSEG
193
194 023520 110465 000001 MOV R4,TSDBH(R5) ;SET MAINT MODE + WRITE DATA
195 023524 004737 016060' JSR PC,WAITF ;WAIT FOR SSR TO SET
196 023530 103411 BCS 15$ ;BR IF CARRY SET (GOOD RETURN)
197 023532 010001 MOV R0,R1 ;SAVE CONTENTS OF TSSR
198 023534 010402 MOV R4,R2 ;DATA THAT WAS WRITTEN
202 023536 ERRDF ERRNO,T2SSR,EXPREC ;DEVICE FATAL SSR FAILED TO SET
      023536 104455 TRAP C$ERDF
      023540 000312 .WORD 202
      023542 024032' .WORD T2SSR
      023544 015304' .WORD EXPREC
203 023546 005203 INC R3 ;FORCE SOFT INIT ON NEXT PASS
204 023550 005237 002220' INC FATFLG ;SET FATAL ERROR FLAG
205 023554 CKLOOP 15$: ;LOOP ON ERROR, IF FLAG SET
      023554 104406 TRAP C$CLP1
206 023556 005737 002220' TST FATFLG ;WAS FATAL ERROR RECEIVED ?
207 023562 001402 BEQ 20$ ;BRANCH IF NOT
208 023564 004737 017014' JSR PC,CKDROP ;SEE IF TIME TO DROP UNIT
209 023570 010402 MOV R4,R2 ;DATA PATTERN WRITTEN
210 023572 042702 177774 20$: BIC #C<BIT0!BIT1>,R2 ;CLEAR ALL BUT LOW 2 BITS
211 023576 000302 SWAB R2 ;BITS 8 AND 9 HAVE LOW DATA BITS
212 023600 052702 002200 BIS #SSR!NBA,R2 ;THESE BITS MUST BE SET ALSO
213 023604 016501 000002 MOV TSSR(R5),R1 ;GET THE CONTENTS OF TSSR
214 023610 032701 000100 BIT #OFL,R1 ;IS OFF-LINE BIT SET ?
215 023614 001402 BEQ 25$ ;BRANCH IF NOT OFF-LINE
216 023616 052702 000100 BIS #OFL,R2 ;SET OFF-LINE IN EXPECTED DATA
217 023622 020201 25$: CMP R2,R1 ;DOES EXPECTED MATCH RECEIVED ?
218 023624 001405 BEQ 30$ ;OKAY IF MATCH
222 023626 ERRHRD ERRNO,T2TSSR,EXPREC ;TSSR WASN'T CORRECT
      023626 104456 TRAP C$ERHRD
      023630 000313 .WORD 203
      023632 023765' .WORD T2TSSR
      023634 015304' .WORD EXPREC
223 023636 005203 INC R3 ;FORCE SOFT INIT ON NEXT PASS
224 023640 CKLOOP 30$: ;LOOP ON ERROR ?
      023640 104406 TRAP C$CLP1
225 023642 016501 000000 MOV TSBA(R5),R1 ;GET TSBA REGISTER CONTENTS
226 023646 005002 CLR R2 ;
227 023650 150402 BIS R4,R2 ;DATA PATTERN WRITTEN
228 023652 000302 SWAB R2 ;MOVE INTO TOP BYTE
229 023654 150402 BIS R4,R2 ;BOTH HALVES SHOULD BE SAME
230 023656 020102 CMP R1,R2 ;COMPARE EXPECTED TO RECEIVED

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421 024626
      024626
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423 024626
      024626
      024626 104402
424
429 024630 012700 026136'
430 024634 004737 016322'
431 024640 012737 000005 002214'
432 024646
433 024646 004737 015604'
434 024652 103405
438 024654 010001
439 024656
      024656 104455
      024660 000621
      024662 003644'
      024664 011644'
440 024666 005004
441 024670 004737 016146'
    
```

TEST 4 , SUBTEST 1: -

THIS SUBTEST VERIFIES EACH RAM LOCATION BY FIRST PLACING THE M7455 INTO MAINTENANCE MODE BY WRITING INTO THE LOW BYTE OF TSDB AND THEN PERFORMING THE FOLLOWING SEQUENCE FOR EACH ADDRESS 0-7777 (OCTAL):

1. THE ADDRESS TO BE TESTED IS LOADED INTO THE TSDB (VIA A WORD WRITE).
2. THE ADDRESSED RAM LOCATION IS WRITTEN, THEN READ INTO THE LOW BYTE OF TSBA, BY WRITING A DATA BYTE INTO THE LOW BYTE OF TSDB.
3. THE LOW BYTE OF TSBA IS CHECKED TO SEE IF IT CONTAINS THE DATA PATTERN ORIGINALLY WRITTEN; A DISCREPANCY IS REPORTED AS AN ERROR.
4. THE ADDRESS OF THE LOCATION BEING TESTED IS AGAIN WRITTEN INTO TSDB (WORD WRITE), TO CAUSE THE LOCATION UNDER TEST TO AGAIN BE READ INTO THE LOW BYTE OF TSBA. THE LOW BYTE OF TSBA IS AGAIN CHECKED AND DISCREPANCIES REPORTED.
5. THE HIGH BYTE OF TSBA IS CHECKED; IT SHOULD CONTAIN THE SUM OF THE HIGH AND LOW BYTES LAST WRITTEN INTO TSDB AS A WORD. A DISCREPANCY IS REPORTED AS A 2901 PROBLEM.
6. THE CONTENT OF TSSR IS CHECKED; SETTING OF THE SC BIT IS IGNORED. OTHER DISCREPANCIES IN TSSR ARE REPORTED.

BGNTST

T4::

BGNSUB

;/;;;;;;;;; BEGIN SUBTEST /;;;;;;;;;

T4.1:

TRAP C#BSUB

T4LOOP:

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MOV #TST4ID,R0
JSR PC,TSTSETUP
MOV #5,LOOPCNT
T4LOOP:
JSR PC,SOFINIT
BCS 20$
MOV R0,R1
ERRDF ERRNO,SFIERR,SFIMSG
    
```

```

;ASCII MESSAGE TO IDENTIFY TEST
;DO INITIAL TEST SETUP
;PERFORM 5 ITERATIONS
;DO INITIALIZE ON CONTROLLER
;BR IF INIT WAS OK
;CONTENTS OF TSSR REGISTER
;FATAL ERROR TSSR WAS NOT OK
    
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TRAP C#ERDF
.WORD 401
.WORD SFIERR
.WORD SFIMSG
    
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20\$:

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CLR R4
JSR PC,CHKTSSR
    
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;SET RAM ADDRESS AT ZERO
;WAIT FOR READY, NON-AMBIGUOUS
    
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537
538 025206 005304          35$: DEC    R4          ;SET BACK TO 7777
539 025210 005002          CLR    R2          ;SET TO ALL ZEROS
540 025212 004737 016146' 40$: JSR    PC,CHKTSSR ;WAIT FOR READY (SSR) TO SET
541 025216 010465 000000    MOV    R4,TSDB(R5) ;LOAD UP THE ADDRESS FOR RAM
542 025222 004737 016146'  JSR    PC,CHKTSSR ;WAIT FOR READY (SSR) TO SET
543 025226 016501 000000    MOV    TSBA(R5),R1 ;READ THE RAM CONTENTS BACK
544 025232 005002          CLR    R2          ;LOOKING FOR 000000 (EXPECTED)
545 025234 120102          CMPB   R1,R2       ;BOTH SHOULD BE 00000000 BINARY
546 025236 001404          BEQ    43$        ;BR, IF DATA IS GOOD
550 025240          ERRHRD ERRNO,TSBAM3,EXPREC ;CHARACTERISTICS DATA NOT CORRECT
      025240 104456          TRAP   C$ERHRD
      025242 000627          .WORD  407
      025244 026056'        .WORD  TSBAM3
      025246 015304'        .WORD  EXPREC
551 025250 012702 000377 43$: MOV    #000377,R2 ;SET ALL ONES WORD
552 025254 010465 000000    MOV    R4,TSDB(R5) ;LOAD UP RAM ADDRESS POINTER
553 025260 004737 016146'  JSR    PC,CHKTSSR ;WAIT FOR READY, NON-AMBIGUOUS
554 025264 110265 000000    MOVB   R2,TSDB(R5) ;WRITE DATA INTO RAM
555 025270 004737 016146'  JSR    PC,CHKTSSR ;WAIT FOR READY, NON-AMBIGUOUS
556 025274 016501 000000    MOV    TSBA(R5),R1 ;READ RAM CONTENTS BACK
557 025300 120102          CMPB   R1,R2       ;CHECK WITH DATA WRITTEN
558 025302 001404          BEQ    45$        ;BR IF OK, DATA IN = DATA OUT
562 025304          ERRHRD ERRNO,TSBAM2,EXPREC ;WRITTEN DATA NOT = TO READ
      025304 104456          TRAP   C$ERHRD
      025306 000630          .WORD  408
      025310 025774'        .WORD  TSBAM2
      025312 015304'        .WORD  EXPREC
563 025314          45$: CKLOOP ;SCOPE LOOP
      025314 104406          TRAP   C$CLP1
564 025316 004737 016146'  JSR    PC,CHKTSSR ;WAIT FOR READY, NON-AMBIGUOUS
565 025322 010465 000000    MOV    R4,TSDB(R5) ;WORD WRITE TO SET UP ADDRESS
566 025326 004737 016146'  JSR    PC,CHKTSSR ;WAIT FOR READY, NON-AMBIGUOUS
567 025332 116501 000001    MOVB   TSBAH(R5),R1 ;HIGH BYTE READ OF TSBA
568 025336 010403          MOV    R4,R3      ;DATA PATTERN WRITTEN
569 025340 000303          SWAB   R3          ;HIGH TO LOW
570 025342 060403          ADD    R4,R3      ;TOTAL OF BYTES IN LOW BYTE
571 025344 120103          CMPB   R1,R3      ;SUM OF BYTES WRITTEN TO TSDB = TSBAH
572 025346 001404          BEQ    50$        ;BR IF OK, THEY SHOULD BE
576 025350          ERRHRD ERRNO,M2901,EXPREC ;2901 PROBLEM ADDER
      025350 104456          TRAP   C$ERHRD
      025352 000631          .WORD  409
      025354 025704'        .WORD  M2901
      025356 015304'        .WORD  EXPREC
577 025360          50$: CKLOOP ;SCOPE LOOP
      025360 104406          TRAP   C$CLP1
578 025362 005304          DEC    R4          ;DROP RAM ADDRESS POINTER
579 025364 002312          BGE    40$        ;NOT AT LOC. ZERO YET
580
581 025366          ENDSUB ;////////////////// END SUBTEST ////////////////////
      025366          L10045:
      025366 104403          TRAP   C$ESUB
582
583
584 025370          BGNSUB ;////////////////// BEGIN SUBTEST ////////////////////
      025370          T4.3:
      025370 104402          TRAP   C$BSUB

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025550 104456
025552 000634
025554 026056'
025556 015304'
638 025560 005002
639 025562 010465 000000
640 025566 004737 016146'
641 025572 110265 000000
642 025576 004737 016146'
643 025602 016501 000000
644 025606 120102
645 025610 001404
649 025612
025612 104456
025614 000635
025616 025774'
025620 015304'
650 025622
025622 104406
651 025624 004737 016146'
652 025630 116501 000001
653 025634 010203
654 025636 000303
655 025640 060203
656 025642 120103
657 025644 001404
661 025646
025646 104456
025650 000636
025652 025704'
025654 015304'
662 025656
025656 104406
663 025660 005304
664 025662 001315
665
666 025664
025664
025664 104403
667
668 025666 004737 016270'
669 025672 103002
670 025674 000137 024646'
671 025700
025700 104432
025702 000256
672
673
674
675
676 025704 040 124 123 M2901: .ASCIZ ' TSBA High Byte Not Sum of Last TSDB Write (2901 Error)'
677 025774 040 127 162 TSBAM2: .ASCIZ ' Write to TSDB Not Equal to Read of TSBA Low Byte'
678 026056 127 162 151 TSBAM3: .ASCIZ 'Write To RAM Location Modified Another Location'
679 026136 122 101 115 TST4ID: .ASCIZ 'RAM Verification'
680
681 026160
026160

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43$: CLR R2 ;SET UP NEW EXPECTED
MOV R4,TSDB(R5) ;LOAD UP RAM ADDRESS POINTER
JSR PC,CHKTSSR ;WAIT FOR READY, NON-AMBIGUOUS
MOV R2,TSDB(R5) ;WRITE DATA INTO RAM
JSR PC,CHKTSSR ;WAIT FOR READY, NON-AMBIGUOUS
MOV TSBA(R5),R1 ;READ RAM CONTENTS BACK
CMPB R1,R2 ;CHECK WITH DATA WRITTEN
BEQ 45$ ;BR IF OK, DATA IN = DATA OUT
ERRHRD ERRNO,TSBAM2,EXPREC ;WRITTEN DATA NOT = TO READ

45$: CKLOOP ;SCOPE LOOP
JSR PC,CHKTSSR ;WAIT FOR READY, NON-AMBIGUOUS
MOV TSBAH(R5),R1 ;HIGH BYTE READ OF TSBA
MOV R2,R3 ;DATA PATTERN WRITTEN
SWAB R3 ;HIGH TO LOW
ADD R2,R3 ;TOTAL OF BYTES IN LOW BYTE
CMPB R1,R3 ;SUM OF BYTES WRITTEN TO TSDB = TSBAH
BCC 50$ ;BR IF OK, THEY SHOULD BE
ERRHRD ERRNO,M2901,EXPREC ;2901 PROBLEM ADDER

50$: CKLOOP ;SCOPE LOOP
DEC R4
BNE 40$ ;DROP RAM ADDRESS POINTER
;NOT AT LOC. ZERO YET

ENDSUB ;////////// END SUBTEST ////////////
L10046:

63$: EXIT TST ;DO WE NEED TO ITERATE TEST ?
;BRANCH IF NOT
;EXECUTE AGAIN
;ALL DONE THIS TEST

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TRAP C$ERHRD
.WORD 412
.WORD TSBAM3
.WORD EXPREC

TRAP C$ERHRD
.WORD 413
.WORD TSBAM2
.WORD EXPREC

TRAP C$CLP1

TRAP C$ERHRD
.WORD 414
.WORD M2901
.WORD EXPREC

TRAP C$CLP1

TRAP C$ESUB

TRAP C$EXIT
.WORD L10043-.

L10043:

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;+
;LOCAL TEXT MESSAGES FOR TEST
;-

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026160 104401

TRAP C1ETST

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.SBTTL TEST 5: SECOND INITIALIZATION TEST

THIS TEST VERIFIES THE SAME ELEMENTS AS DID INITIALIZATION TEST #1 AND ALSO CHECKS THAT CERTAIN PARTS OF RAM IS CLEARED TO ZERO AND THAT 2901 REGISTERS 10 AND 11 ARE ALSO CLEARED TO ZERO. THIS IS A CONFIDENCE CHECK OF A PART OF THE SELF-TEST SEQUENCE (I.E., THAT IT IS REALLY BEING EXECUTED). FOR EACH OF TWO SUBTESTS (ONE FOR INITIALIZING VIA A BUS INIT, THE OTHER FOR INITIALIZING BY WRITING INTO THE TSSR), THE FOLLOWING SEQUENCE IS PERFORMED:

1. EACH RAM LOCATION AND 2901 REGISTERS 10 AND 11 ARE SET TO ALL 1'S BY USING WRITES INTO THE TSDB REGISTER (LOW BYTE AND MAINTENANCE MODE WORD WRITES).
2. THE CONTROLLER IS INITIALIZED AND THE VARIOUS CHECKS ON THE TSSR DESCRIBED IN INITIALIZATION TEST #1 ARE PERFORMED.
3. 01'S (377 OCTAL) ARE WRITTEN INTO THE LOW BYTE OF TSDB, WHICH SHOULD CAUSE RAM LOCATION 0 TO BE WRITTEN TO ALL 1'S SINCE 2901 REGISTERS 10 AND 11, SPECIFYING THE RAM ADDRESS, SHOULD BE 0. RAM LOCATION 0 IS VERIFIED BY WRITING A WORD OF ZEROS INTO THE TSDB. THE RESULTING LOW BYTE OF TSBA SHOULD CONTAIN ALL 1'S.
4. THE ENTIRE RAM IS SCANNED. LOCATION 0 SHOULD CONTAIN ALL 1'S AND THE REMAINING LOCATIONS, EXCEPT FOR THE MESSAGE BUFFER IMAGE AREA, SHOULD CONTAIN 0. DISCREPANCIES ARE REPORTED. AN ERROR AT THIS POINT IS MOST LIKELY DUE TO A ROM, PIPELINE OR SEQUENCER PROBLEM OR A TIMING PROBLEM.

720 026162
026162

BGNTST

725 026162 012700 027134'

MOV #TST5ID,R0

T5::

;ASCII MESSAGE TO IDENTIFY TEST
;DO INITIAL TEST SETUP
;PERFORM 20 ITERATIONS

726 026166 004737 016322'

JSR PC,TSTSETUP

727 026172 012737 000024 002214'

MOV #20,LOOPCNT

728 026200

TSLOOP:

CLR FATFLG

;CLEAR THE FATAL ERROR FLAG

729 026200 005037 002220'

BGNSUB

;/TTTTTTTTTTTT BEGIN SUBTEST /TTTTTTTTTTTT

730

T5.1:

731 026204 104402

TRAP C1BSUB

732 026206 004737 015604'

JSR PC,SOFINIT

;DO A SOFT TO START

733 026212 103404

BCS 101

;BRANCH IF O.K.

738 026214 104455

ERRDF ERRNO,SFIERR,SFIMSG

;REPORT ERROR AND DROP DRIVE

026214 000765

TRAP C1ERDF

026216 003644'

.WORD 501

.WORD SFIERR


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783 026452 005002          CLR      R2          ;MEMORY EXPECTED SHOULD BE 000000
784 026454 004737 016146'  JSR      PC,CHKTSSR  ;WAIT FOR READY, NON-AMBIGUOUS
785 026460 010465 000000    30$:    MOV      R4,TSDB(R5) ;SELECT LOCATION SPECIFIED
786 026464 004737 016146'  JSR      PC,CHKTSSR  ;WAIT FOR READY, NON-AMBIGUOUS
787 026470 116501 000000    MOVB    TSBA(R5),R1 ;READ LOC CONTENTS
788 026474 120102          CMPB    R1,R2        ;CHECK MEMORY FOR 000000
789 026476 001406          BEQ     40$         ;BRANCH IF DATA OKAY
790 026500          ERRDF  ERRNO,TSMEM,SFFMSG ;MEMORY NOT ZERO AFTER INIT.
                                TRAP      C$ERDF
                                .WORD    502
                                .WORD    TSMEM
                                .WORD    SFFMSG
                                026500 104455
                                026502 000766
                                026504 027164'
                                026506 011712'
791 026510 005237 002220'    40$:    INC      FATFLG    ;SET THE FATAL ERROR FLAG
792 026514          CKLOOP
                                026514 104406
                                793 026516          ESCAPE  SUB        ;EXIT ON FATAL ERROR
                                026516 104410
                                026520 000012
                                TRAP      C$CLP1
                                .WORD    L10050-.
794 026522 005204          INC      R4          ;LOOK AT NEXT RAM LOC.
795 026524 020427 000400    CMP     R4,#400     ;AT TOP OF RAM ADDRESS SPACE
796 026530 001353          BNE    30$         ;BRANCH TILL ALL MEMORY TESTED
797
798 026532          ENDSUB    ;////////////////// END SUBTEST ////////////////////
                                026532
                                026532 104403
                                L10050: TRAP      C$ESUB
799
800 026534 005737 002220'    TST     FATFLG     ;IS FATAL ERROR FLAG SET ?
801 026540 001404          BEQ     50$         ;BRANCH IF NOT
802 026542 004737 017014'  JSR      PC,CKDROP  ;NO LOOP, TRY TO DROP DEVICE
803 026546 005037 002220'  CLR     FATFLG     ;CLEAR THE FATAL ERROR FLAG
804 026552
805
806 026552          BGNSUB    ;////////////////// BEGIN SUBTEST ////////////////////
                                026552
                                026552 104402
                                T5.2: TRAP      C$BSUB
807
808 026554 004737 015604'  JSR      PC,SOFINIT ;DO A SOFT TO START
809 026560 103404          BCS    10$         ;BRANCH IF O.K.
813 026562          ERRDF  ERRNO,SFIERR,SFIMSG ;REPORT ERROR AND DROP DRIVE
                                TRAP      C$ERDF
                                .WORD    503
                                .WORD    SFIERR
                                .WORD    SFIMSG
                                026562 104455
                                026564 000767
                                026566 003644'
                                026570 011644'
814 026572 012702 177777    10$:    MOV     #-1,R2   ;ALL ONE DATA PATTERN
815 026576 005004          CLR     R4         ;STARTING RAM ADDRESS
816 026600 004737 016146'  JSR      PC,CHKTSSR ;WAIT FOR READY, NON-AMBIGUOUS
817 026604 105065 000000    15$:    CLRB   TSDB(R5) ;SET MAINTENANCE MODE
818 026610 004737 016146'  JSR      PC,CHKTSSR ;WAIT FOR READY, NON-AMBIGUOUS
819 026614 010465 000000    MOV     R4,TSDB(R5) ;SET THE NEXT RAM ADDRESS
820 026620 004737 016146'  JSR      PC,CHKTSSR ;WAIT FOR READY, NON-AMBIGUOUS
821 026624 110265 000000    MOVB    R2,TSDB(R5) ;LOAD TEST DATA
822 026630 005204          INC     R4         ;NEXT ADDRESS TO TEST
823 026632 020427 007777    CMP     R4,#7777   ;COMPARE TO LAST ADDRESS
824 026636 003762          BLE    15$        ;BRANCH TILL ALL DATA WRITTEN
825 026640 005065 000002    CLR     TSSR(R5)   ;ISSUE A SOFT RESET
826 026644 004737 016146'  JSR      PC,CHKTSSR ;WAIT FOR READY, NON-AMBIGUOUS
827 026650 016501 000002    MOV     TSSR(R5),R1 ;GET THE CONTENTS OF TSSR

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027066 104410                                TRAP  C$ESCAPE
027070 000012                                .WORD L10051-.
869 027072 005204                                INC    R4                                ;LOOK AT NEXT RAM LOC.
870 027074 020427 000400                        CMP    R4,#400                            ;AT TOP OF RAM ADDRESS SPACE
871 027100 001353                                BNE    30$                                ;BRANCH TILL ALL MEMORY TESTED
872
873 027102                                ENDSUB                                    ;////////////////// END SUBTEST ////////////////////
027102                                L10051:                                TRAP  C$ESUB
027102 104403
874
875 027104 005737 002220'                        TST    FATFLG                            ;IS FATAL ERROR FLAG SET ?
876 027110 001402                                BEQ    50$                                ;BRANCH IF NOT
877 027112 004737 017014'                        JSR    PC,CKDROP                          ;NO LOOP, TRY TO DROP DEVICE
878 027116 004737 016270'                        50$:   JSR    PC,TSTLOOP                    ;SHOULD WE DO ITERATIONS ?
879 027122 103002                                BCC    60$                                ;BRANCH IF NOT
880 027124 000137 026200'                        JMP    T5LOOP                              ;LOOP UNTIL COUNT EXPIRED
881 027130 60$:   EXIT    TST                                ;ALL DONE THIS TEST
027130 104432                                TRAP  C$EXIT
027132 000132                                .WORD L10047-.
882
883
884 ;*
885 ;LOCAL TEXT MESSAGES FOR TEST
886 ;-
887 027134 105 170 164 TST5ID: .ASCIZ 'Extended Initialization'
888 027164 111 156 143 T5MEM: .ASCIZ 'Incorrect RAM Data After Init'
889 027222 111 156 143 T5ADDR: .ASCIZ 'Incorrect RAM Address After Init'
890 .EVEN
891 027264                                ENDTST
027264                                L10047:                                TRAP  C$ETST
027264 104401
892
893
894 .SBTTL TEST 6: COMMAND REJECT
895
896
897 ;
898 ; THIS TEST VERIFIES THAT ALL COMMANDS OTHER THAN WRITE
899 ; CHARACTERISTICS ARE REJECTED DUE TO THE NEED BUFFER ADDRESS
900 ; (NBA) BIT BEING SET IN TSSR, AND THAT THE TSBA AND TSSR
901 ; REGISTERS ARE LEFT IN THE PROPER STATE AFTER EACH COMMAND IS
902 ; REJECTED. THIS TEST CHECKS MICROPROCESSOR SEQUENCING, BASIC
903 ; COMMAND DECODING AND DATI DMA HANDLING. THIS TEST CONTAINS TWO
904 ; SUBTESTS: SUBTEST 1 SEQUENCES THROUGH ALL COMMAND WORDS (OTHER
905 ; THAN WRITE CHARACTERISTICS) WITH THE INTERRUPT ENABLE (IE) BIT
906 ; CLEAR AND VERIFIES THAT AN INTERRUPT IS NOT GENERATED BY THE
907 ; REJECTED COMMAND; SUBTEST 2 PERFORMS SIMILARLY TO SUBTEST 1 BUT
908 ; SETS THE IE BIT IN EACH COMMAND WORD AND VERIFIES THAT AN
909 ; INTERRUPT IS GENERATED WHEN THE COMMAND IS REJECTED. SUBTEST 1
910 ; SETS UP THE INTERRUPT SERVICE ROUTINE TO FLAG UNEXPECTED
911 ; INTERRUPTS. THE COMMAND WORD IN THE COMMAND BUFFER IS
912 ; INITIALIZED TO 100000 (OCTAL) AND THE REMAINING THREE WORDS IN
913 ; THE COMMAND BUFFER ARE SET TO KNOWN UNIQUE PATTERNS. THEN THE
914 ; FOLLOWING SEQUENCE IS PERFORMED:
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```


Line	Address	Offset	Code	Op	Opnd	Comment	Trap	Label
976	027340		ERRDF		ERRNO,SFIERR,SFIMSG	;DEVICE FATAL ERROR DURING INIT		
	027340	104455					TRAP	C\$ERDF
	027342	001131					.WORD	601
	027344	003644'					.WORD	SFIERR
	027346	011644'					.WORD	SFIMSG
977	027350	005037	10\$:	CLR	FATFLG	;CLEAR FATAL ERROR FLAG		
978	027354	005037		CLR	INTRECV	;CLEAR INTERRUPT RECEIVED FLAG		
979	027360	004737		JSR	PC,CHKTSSR	;WAIT FOR READY, NON-AMBIGUOUS		
980	027364	042714		BIC	#BIT7,(R4)	;DISABLE INTERRUPTS		
981	027370	010465		MOV	R4,TSDB(R5)	;SET THE PACKET ADDRESS		
982	027374	004737		JSR	PC,WAITF	;WAIT FOR SSR TO SET		
983	027400	103407		BCS	15\$;BR IF CARRY SET (GOOD RETURN)		
984	027402	010001		MOV	R0,R1	;SAVE CONTENTS OF TSSR		
988	027404			ERRDF	ERRNO,T6SSR,PKTSSR	;DEVICE FATAL SSR FAILED TO SET		
	027404	104455					TRAP	C\$ERDF
	027406	001132					.WORD	602
	027410	030235'					.WORD	T6SSR
	027412	011656'					.WORD	PKTSSR
989	027414	005237	002220'	INC	FATFLG	;SET FATAL ERROR FLAG		
990	027420		15\$:	CKLOOP		;LOOP ON ERROR, IF FLAG SET		
	027420	104406					TRAP	C\$CLP1
991	027422			ESCAPE	SUB	;BY-PASS SUBTEST IF FATAL ERROR		
	027422	104410					TRAP	C\$ESCAPE
	027424	000170					.WORD	L10053-
992	027426	005737	002222'	TST	INTRECV	;DID AN INTERRUPT OCCUR ?		
993	027432	001404		BEQ	22\$;BRANCH IF NOT		
997	027434			ERRHRD	ERRNO,T6INT,PKTSSR			
	027434	104456					TRAP	C\$ERHRD
	027436	001133					.WORD	603
	027440	030313'					.WORD	T6INT
	027442	011656'					.WORD	PKTSSR
998	027444	012702	102206	22\$:	MOV	#SC!NBA!SSR!TSREJ,R2		
999	027450	004737	016146'		JSR	PC,CHKTSSR		
1000	027454	016501	000002		MOV	TSSR(R5),R1		
1001	027460	032701	000100		BIT	#OFL,R1		
1002	027464	001402		BEQ	25\$;IS OFF-LINE BIT SET ?		
1003	027466	052702	000100		BIS	#OFL,R2		
1004	027472	020201		25\$:	CMP	R2,R1		
1005	027474	001404		BEQ	30\$;SET OFF-LINE IN EXPECTED DATA		
1009	027476			ERRHRD	ERRNO,T6NBA,PKTSSR	;DOES EXPECTED MATCH RECEIVED ?		
	027476	104456				;OKAY IF MATCH		
	027500	001134				;NBA NOT SET TO REJECT		
	027502	030210'					TRAP	C\$ERHRD
	027504	011656'					.WORD	604
							.WORD	T6NBA
							.WORD	PKTSSR
1010	027506		30\$:	CKLOOP		;LOOP ON ERROR ?		
	027506	104406					TRAP	C\$CLP1
1011	027510	004737	016146'		JSR	PC,CHKTSSR		
1012	027514	016501	000000		MOV	TSBA(R5),R1		
1013	027520	010402			MOV	R4,R2		
1014	027522	062702	000010		ADD	#10,R2		
1015	027526	020102			CMP	R1,R2		
1016	027530	001404			BEQ	35\$		
1020	027532			ERRHRD	ERRNO,T6TSBA,EXPREC	;EXPECT TSBA TO PACKET * 10		
	027532	104456				;COMPARE EXPECTED TO RECEIVED		
	027534	001135				;ERROR IF NOT EQUAL		
	027536	030451'				;PRINT THE ERROR & EXPD/RCV		
	027540	015304'					TRAP	C\$ERHRD
							.WORD	605
							.WORD	T6TSBA
							.WORD	EXPREC

1070	027730			ERRDF	ERRNO,T6SSR,PKTSSR		;DEVICE FATAL SSR FAILED TO SET	
	027730	104455					TRAP	C\$ERDF
	027732	001140					.WORD	608
	027734	030235'					.WORD	T6SSR
	027736	011656'					.WORD	PKTSSR
1071	027740	005237	002220'					
1072	027744			15\$:	INC	FATFLG		
	027744	104406			CKLOOP			
1073	027746				ESCAPE	SUB		
	027746	104410						
	027750	000170						
1074	027752	005737	002222'		TST	INTRECV		
1075	027756	001004			BNE	22\$		
1079	027760				ERRHRD	ERRNO,T6NINT,PKTSSR		
	027760	104456						
	027762	001141						
	027764	030371'						
	027766	011656'						
1080	027770	012702	102206		22\$:	MOV	#SC!NBA!SSR!TSREJ,R2	
1081	027774	004737	016146'			JSR	PC,CHKTSSR	
1082	030000	016501	000002			MOV	TSSR(R5),R1	
1083	030004	032701	000100			BIT	#OFL,R1	
1084	030010	001402				BEQ	25\$	
1085	030012	052702	000100			BIS	#OFL,R2	
1086	030016	020201			25\$:	CMP	R2,R1	
1087	030020	001404				BEQ	30\$	
1091	030022					ERRHRD	ERRNO,T6NBA,PKTSSR	
	030022	104456						
	030024	001142						
	030026	030210'						
	030030	011656'						
1092	030032				30\$:	CKLOOP		
	030032	104406						
1093	030034	004737	016146'			JSR	PC,CHKTSSR	
1094	030040	016501	000000			MOV	TSBA(R5),R1	
1095	030044	010402				MOV	R4,R2	
1096	030046	062702	000010			ADD	#10,R2	
1097	030052	020102				CMP	R1,R2	
1098	030054	001404				BEQ	35\$	
1102	030056					ERRHRD	ERRNO,T6TSBA,EXPREC	
	030056	104456						
	030060	001143						
	030062	030451'						
	030064	015304'						
1103								
1104								
1105	030066	004737	010724'		35\$:	JSR	PC,CKRAM	
1106	030072	103404				BCS	40\$	
1110	030074					ERRHRD	ERRNO,PKTRAM,RAMERR	
	030074	104456						
	030076	001144						
	030100	004737'						
	030102	015320'						
1111	030104				40\$:	ENDSEG		
	030104							
	030104	104405						
1112	030106	011300				MOV	(R3),R0	

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1113 030110 042700 177740      BIC    #177740,R0      ;GET BITS 0-4
1114 030114 020027 000004      CMP    RO,#4          ;DON'T TEST WRITE CHARACTERISTICS
1115 030120 001002              BNE    45$           ;BRANCH IF NOT WRITE CHARACTERISTICS
1116 030122 062703 000002      ADD    #2,R3         ;BY-PASS WRITE CHARACTERISTICS
1117 030126 020327 003060'    45$:  CMP    R3,#TBLEND ;HAVE WE COMPLETED DATA TABLE ?
1118 030132 103002              BHS    50$           ;BRANCH IF ALL TESTED
1119 030134 000137 027650'    JMP    5$            ;TEST WITH NEXT DATA
1120
1121 030140              50$:  ENDSUB          ;////////////////// END SUBTEST ////////////////////
      030140              L10054:              TRAP    C$ESUB
      030140 104403
1122 030142 065737 002220'    TST    FATFLG        ;ANY FATAL ERRORS ?
1123 030146 001402              BEQ    60$           ;BRANCH IF NOT
1124 030150 004737 017014'    JSR    PC,CKDROP     ;TRY TO DROP THE UNIT
1125 030154 004737 016270'    60$:  JSR    PC,TSTLOOP ;SHOULD WE DO ITERATIONS ?
1126 030160 103002              BCC    62$           ;BRANCH IF NOT
1127 030162 000137 027304'    JMP    T6LOOP        ;LOOP UNTIL COUNT EXPIRED
1128 030166              62$:  EXIT    TST      ;ALL DONE THIS TEST
      030166 104432              TRAP    C$EXIT
      030170 000352              .WORD  L10052-.
1129
1130
1131      ;*
1132      ;LOCAL STORAGE FOR THIS TEST
1133      ;-
1135 030172              .BLKB  10-<.-TSV2&7>
1137 030200      T6PACKET:          ;COMMAND PACKET FOR TEST
1138 030200 000000              .WORD  0             ;WILL CONTAIN VARIABLE COMMANDS
1139 030202 052525              .WORD  052525
1140 030204 125252              .WORD  125252
1141 030206 052525              .WORD  052525
1142
1143
1144      ;*
1145      ;LOCAL TEXT MESSAGES FOR TEST
1146      ;-
1147
1148 030210          103      157      155  T6NBA:  .ASCIZ  'Command Not Rejected'
1149 030235          103      157      156  T6SSR:  .ASCIZ  'Contents of TSSR Incorrect After Write Packet'
1150 030313          125      156      145  T6INT:  .ASCIZ  'Unexpected Interrupt Received On Write Packet'
1151 030371          105      170      160  T6NINT: .ASCIZ  'Expected Interrupt Not Received On Write Packet'
1152 030451          111      156      143  T6TSBA: .ASCIZ  'Incorrect TSBA Address After Packet Write'
1153 030523          103      157      155  TST6ID: .ASCIZ  'Command Reject'
1154
1155 030542              .EVEN
      030542              ENDTST
      030542 104401              L10052:              TRAP    C$ETST
1156
1157      .SBTTL  TEST 7: WRITE CHARACTERISTICS
1158
1159
1160      ;
1161      ; THIS TEST VERIFIES BASIC OPERATION OF THE WRITE CHARACTERISTICS
1162      ; COMMAND. IT VERIFIES THAT THE COMMAND BLOCK AND CHARACTERISTICS
1163      ; DATA BLOCK ARE FETCHED PROPERLY FROM CPU MEMORY, THE NEED BUFFER
1164      ; ADDRESS (NBA) BIT IN TSSR IS HANDLED PROPERLY, AND THAT A PROPER
1165      ; MESSAGE PACKET IS STORED, WHERE APPROPRIATE. THIS TEST DOES NOT
      ; CHECK THAT THE VARIOUS FUNCTIONS ENABLED BY CHARACTERISTIC MODE

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1315 031214 011656'
031216 104406
1316 031220
031220 104410
031222 000116
1317 031224 005737 002222'
1318 031230 001404
1322 031232
031232 104456
031234 001305
031236 033661'
031240 011656'
1323 031242 016501 000002
1324 031246 012702 102206
1325 031252 032701 000100
1326 031256 001402
1327 031260 052702 000100
1328 031264 020201
1329 031266 001414
1330 031270 010100
1331 031272
1332 031302 020027 002000
1333 031306 001404
1337 031310
031310 104456
031312 001306
031314 033113'
031316 011656'
1338 031320
031320 104406
1339 031322 032701 002000
1340 031326 001004
1344 031330
031330 104456
031332 001307
031334 032762'
031336 011656'
1345 031340
1346 031340
031340
031340 104405
1347
1348 031342 062703 000004
1349 031346 020327 032762'
1350 031352 103002
1351 031354 000137 031124'
1352
1353 031360
031360
031360 104403
1354
1355
1356
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1359

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15#: CKLOOP
ESCAPE SEG
TST INTRECV
BEQ 22#
ERRHRD ERRNO,T7INT,PKTSSR
22#: MOV TSSR(R5),R1
MOV #SC!SSR!TSREJ!NBA,R2
BIT #OFL,R1
BEQ 25#
BIS #OFL,R2
25#: CMP R2,R1
BEQ 30#
MOV R1,R0
XOR R2,R0
CMP R0,#NBA
BEQ 30#
ERRHRD ERRNO,T72REJ,PKTSSR
30#: CKLOOP
BIT #NBA,R1
BNE 35#
ERRHRD ERRNO,T72NBA,PKTSSR
35#: ENDSEG
57#: ENDSUB

```

```

;LOOP ON ERROR, IF FLAG SET
;BY-PASS CHECKS IF FATAL ERROR
;DID AN INTERRUPT OCCUR ?
;BRANCH IF NOT
TRAP C#ERHRD
WORD 709
WORD T7INT
WORD PKTSSR
;GET THE CONTENTS OF TSSR
;EXPECTED CONTENTS OF TSSR
;IS OFF-LINE BIT SET ?
;BRANCH IF NOT OFF-LINE
;SET OFF-LINE IN EXPECTED DATA
;DOES EXPECTED MATCH RECEIVED ?
;OKAY IF MATCH
;DATA FROM TSSR
;FIND BITS IN ERROR
;IS NBA ONLY BIT IN ERROR ?
;DON'T PRINT ERROR IF NBA ONLY BAD BIT
;COMMAND NOT REJECTED
TRAP C#ERHRD
WORD 710
WORD T72REJ
WORD PKTSSR
;LOOP ON ERROR ?
TRAP C#CLP1
;IS NBA BIT SET ?
;OKAY IF NBA SET
;NBA NOT SET
TRAP C#ERHRD
WORD 711
WORD T72NBA
WORD PKTSSR
;<<<<<<<<<<<<<<<< END SEGMENT 10000#
TRAP C#ESEG
;POINT TO NEXT DATA PAIR
;COMPARE TO END OF TEST DATA
;BRANCH IF ALL DATA TESTED
;BRANCH TILL BACK TO ZERO
;//////////////// END SUBTEST L10057
TRAP C#ESUB

```

```

;
;
;TEST 7, SUBTEST 3
;
;CHECK THE WRITE CHARACTERISTICS COMMAND IS REJECTED

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031664 001315
031666 003644'
031670 011644'
1454 031672 005037 002222' 10$: CLR INTRECV ;CLEAR INTERRUPT RECEIVED FLAG
1455 031676 052737 000001 032710' BIS #1,T7DATA ;MAKE ADDRESS ODD
1456 031704 010465 000000 MOV R4,TSDB(R5) ;SET THE PACKET ADDRESS
1457 031710 004737 016060' JSR PC,WAITF ;WAIT FOR SSR TO SET
1458 031714 103405 BCS 15$ ;BR IF CARRY SET (GOOD RETURN)
1459 031716 010001 MOV R0,R1 ;SAVE CONTENTS OF TSSR
1463 031720 ERRDF ERRNO,T7SSR,PKTSSR ;DEVICE FATAL SSR FAILED TO SET
031720 104455 TRAP C$ERDF
031722 001316 .WORD 718
031724 033501' .WORD T7SSR
031726 011656' .WORD PKTSSR
1464 031730 15$: CKLOOP ;LOOP ON ERROR, IF FLAG SET
031730 104406 TRAP C$CLP1
1465 031732 ESCAPE SUB ;BY-PASS SUBTEST IF FATAL ERROR
031732 104410 TRAP C$ESCAPE
031734 000116 .WORD L10061-.
1466 031736 005737 002222' TST INTRECV ;DID AN INTERRUPT OCCUR ?
1467 031742 001404 BEQ 22$ ;BRANCH IF NOT
1471 031744 ERRHRD ERRNO,T7INT,PKTSSR
031744 104456 TRAP C$ERHRD
031746 001317 .WORD 719
031750 033661' .WORD T7INT
031752 011656' .WORD PKTSSR
1472 031754 016501 000002 22$: MOV TSSR(R5),R1 ;GET THE CONTENTS OF TSSR
1473 031760 012702 102206 MOV #SC!SSR!TSREJ!NBA,R2 ;EXPECTED CONTENTS OF TSSR
1474 031764 032701 000100 BIT #OFL,R1 ;IS OFF-LINE BIT SET ?
1475 031770 001402 BEQ 25$ ;BRANCH IF NOT OFF-LINE
1476 031772 052702 000100 BIS #OFL,R2 ;SET OFF-LINE IN EXPECTED DATA
1477 031776 020201 25$: CMP R2,R1 ;DOES EXPECTED MATCH RECEIVED ?
1478 032000 001414 BEQ 30$ ;OKAY IF MATCH
1479 032002 010100 MOV R1,R0 ;DATA FROM TSSR
1480 032004 XOR R2,R0 ;FIND BITS IN ERROR
1481 032014 020027 002000 CMP R0,#NBA ;IS NBA ONLY BIT IN ERROR ?
1482 032020 001404 BEQ 30$ ;DON'T PRINT ERROR IF NBA ONLY BAD BIT
1486 032022 ERRHRD ERRNO,T74REJ,PKTSSR ;COMMAND NOT REJECTED
032022 104456 TRAP C$ERHRD
032024 001320 .WORD 720
032026 033305' .WORD T74REJ
032030 011656' .WORD PKTSSR
1487 032032 30$: CKLOOP ;LOOP ON ERROR ?
032032 104406 TRAP C$CLP1
1488 032034 032701 002000 BIT #NBA,R1 ;IS NBA BIT SET ?
1489 032040 001004 BNE 35$ ;OKAY IF NBA SET
1493 032042 ERRHRD ERRNO,T72NBA,PKTSSR ;NBA NOT SET
032042 104456 TRAP C$ERHRD
032044 001321 .WORD 721
032046 032762' .WORD T72NBA
032050 011656' .WORD PKTSSR
1494
1495 032052 35$: ENDSUB ;////////// END SUBTEST ////////////
032052 L10061: TRAP C$ESUB
032052 104403
1496
1497

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1498
1499
1500
1501
1502
1503
1504
1505
1506
1507 032054           BGNSUB                               ;////////// BEGIN SUBTEST ////////////
      032054                                     T7.5:              TRAP      C#BSUB
      032054 104402
1508
1509 032056           SETPRI  #PRI00                               ;LOWER PRIORITY TO ALLOW INTERRUPTS
      032056 012700 000000                                   MOV       #PRI00,R0
      032062 104441                                           TRAP      C#SPRI
1510 032064 012703 000001       5$:   MOV      #1,R3                               ;STARTING BUFFER LENGTH
1511 032070 012704 032700'     MOV      #T7PACKET,R4        ;GET THE ADDRESS OF COMMAND PACKET
1512 032074 004737 034062'     JSR      PC,T7REST          ;RESTORE PACKET TO STARTING VALUES
1513
1514 032100           BGNSEG                               ;>>>>>>>>>>>> BEGIN SEGMENT >>>>>>>>>>>>
      032100 104404                                           TRAP      C#BSEG
1515
1516 032102 004737 015604'     JSR      PC,SOFINIT        ;DO SOFT INIT OF CONTROLLER
1517 032106 103405                    BCS      10$               ;BR IF SOFT INIT = OK
1521 032110 010001             MOV      R0,R1             ;SAVE CONTENTS OF TSSR
1522 032112           ERRDF  ERRNO,SFIERR,SFIMSG         ;DEVICE FATAL ERROR DURING INIT
      032112 104455                                           TRAP      C#ERDF
      032114 001322                                           .WORD    722
      032116 003644'                                           .WORD    SFIERR
      032120 011644'                                           .WORD    SFIMSG
1523 032122 005037 002222'     10$:  CLR      INTRECV          ;CLEAR INTERRUPT RECEIVED FLAG
1524 032126 010337 032714'     MOV      R3,T7DATA+4        ;INSERT THE BAD MESSAGE LENGTH
1525 032132 010465 000000        MOV      R4,TSDB(R5)        ;SET THE PACKET ADDRESS
1526 032136 004737 016060'     JSR      PC,WAITF          ;WAIT FOR SSR TO SET
1527 032142 103405                    BCS      15$               ;BR IF CARRY SET (GOOD RETURN)
1528 032144 010001             MOV      R0,R1             ;SAVE CONTENTS OF TSSR
1532 032146           ERRDF  ERRNO,T7SSR,PKTSSR         ;DEVICE FATAL SSR FAILED TO SET
      032146 104455                                           TRAP      C#ERDF
      032150 001323                                           .WORD    723
      032152 033501'                                           .WORD    T7SSR
      032154 011656'                                           .WORD    PKTSSR
1533 032156           CKLOOP                               ;LOOP ON ERROR, IF FLAG SET
      032156 104406                                           TRAP      C#CLP1
1534 032160           ESCAPE  SEG                          ;BY-PASS SUBTEST IF FATAL ERROR
      032160 104410                                           TRAP      C#ESCAPE
      032162 000116                                           .WORD    10000$-.
1535 032164 005737 002222'     TST      INTRECV          ;DID AN INTERRUPT OCCUR ?
1536 032170 001404                    BEQ      22$               ;BRANCH IF NOT
1540 032172           ERRHRD  ERRNO,T7INT,PKTSSR         ;
      032172 104456                                           TRAP      C#ERHRD
      032174 001324                                           .WORD    724
      032176 033661'                                           .WORD    T7INT
      032200 011656'                                           .WORD    PKTSSR
1541 032202 016501 000002     22$:  MOV      TSSR(R5),R1        ;GET THE CONTENTS OF TSSR
1542 032206 012702 102206        MOV      #SC!SSR!TSREJ!NBA,R2 ;EXPECTED CONTENTS OF TSSR
1543 032212 032701 000100        BIT      #OFL,R1          ;IS OFF-LINE BIT SET ?

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1590
1591 032340          SETPRI  #PRI00          ;LOWER PRIORITY TO ALLOW INTERRUPTS
      032340 012700 000000          MOV      #PRI00,R0
      032344 104441          TRAP      C#SPRI
1592 032346 012703 002764'        MOV      #TSTBLK+12.,R3          ;START OF TEST DATA
1593 032352 012704 032700'        MOV      #T7PACKET,R4          ;GET THE ADDRESS OF COMMAND PACKET
1594 032356 012737 000200 032720' MOV      #200,T7SP          ;SPECIAL BIT SET FOR EXTFEA RAM RD
1595 032364 012764 000012 000006    MOV      #10.,PKBCNT(R4)      ;START WITH EXTENDED FEATURES VALUE
1596 032372
1597 032372          BGNSEG          ;>>>>>>>>>>>> BEGIN SEGMENT >>>>>>>>>>>>
      032372 104404          TRAP      C#BSEG
1598
1599 032374 004737 015604'        JSR      PC,SOFINIT          ;DO SOFT INIT OF CONTROLLER
1600 032400 103405          BCS     10$                ;BR IF SOFT INIT = OK
1604 032402 010001          MOV      R0,R1              ;SAVE CONTENTS OF TSSR
1605 032404          ERRDF     ERRNO,SFIERR,SFIMSG ;DEVICE FATAL ERROR DURING INIT
      032404 104455          TRAP      C#ERDF
      032406 001327          .WORD   727
      032410 003644'         .WORD   SFIERR
      032412 011644'         .WORD   SFIMSG
1606 032414 005037 002220'        CLR     FATFLG            ;CLEAR FATAL ERROR FLAG
1607 032420 005037 002222'        CLR     INTRECV          ;CLEAR INTERRUPT RECEIVED FLAG
1608 032424 010465 000000          MOV      R4,TSD8(R5)        ;SET THE PACKET ADDRESS
1609 032430 004737 016146'        JSR     PC,CHKTSSR         ;WAIT FOR SSR TO SET
1610 032434 103407          BCS     15$                ;BR IF CARRY SET (GOOD RETURN)
1611 032436 010001          MOV      R0,R1              ;SAVE CONTENTS OF TSSR
1615 032440          ERRDF     ERRNO,T7SSR,PKTSSR ;DEVICE FATAL SSR FAILED TO SET
      032440 104455          TRAP      C#ERDF
      032442 001330          .WORD   728
      032444 033501'         .WORD   T7SSR
      032446 011656'         .WORD   PKTSSR
1616 032450 005237 002220'        INC     FATFLG            ;SET FATAL ERROR FLAG
1617 032454          CKLOOP          ;LOOP ON ERROR, IF FLAG SET
      032454 104406          TRAP      C#CLP1
1618 032456          ESCAPE    SEG          ;BY-PASS SUBTEST IF FATAL ERROR
      032456 104410          TRAP      C#ESCAPE
      032460 000156          .WORD   10000$-.
1619 032462 005737 002222'        TST     INTRECV          ;DID AN INTERRUPT OCCUR ?
1620 032466 001404          BEQ     22$                ;BRANCH IF NOT
1624 032470          ERRHRD   ERRNO,T7INT,PKTSSR
      032470 104456          TRAP      C#ERHRD
      032472 001331          .WORD   729
      032474 033661'         .WORD   T7INT
      032476 011656'         .WORD   PKTSSR
1625 032500 016501 000002        22$:  MOV      TSSR(R5),R1      ;GET THE CONTENTS OF TSSR
1626 032504 012702 000200        MOV      #SSR,R2          ;EXPECTED CONTENTS OF TSSR
1627 032510 032701 000100        BIT     #OFL,R1          ;IS OFF-LINE BIT SET ?
1628 032514 001402          BEQ     25$                ;BRANCH IF NOT OFF-LINE
1629 032516 052702 000100        BIS     #OFL,R2          ;SET OFF-LINE IN EXPECTED DATA
1630 032522 020201          CMP     R2,R1              ;DOES EXPECTED MATCH RECEIVED ?
1631 032524 001404          BEQ     30$                ;OKAY IF MATCH
1635 032526          ERRHRD   ERRNO,T7NBA,PKTSSR ;NBA NOT ZERO
      032526 104456          TRAP      C#ERHRD
      032530 001332          .WORD   730
      032532 033040'         .WORD   T7NBA
      032534 011656'         .WORD   PKTSSR
1636 032536          30$:  CKLOOP          ;LOOP ON ERROR ?

```



```

1687 032706 000010          .WORD  8.          ;STARTING VALUE OF BLOCK SIZE
1688
1689 032710          T7DATA:          ;CHARACTERISTICS DATA BLOCK
1690 032710 032726'      .WORD  T7BFR      ;ADDRESS OF MESSAGE BUFFER
1691 032712 000000      .WORD  0
1692 032714 000016      .WORD  14.        ;LENGTH OF MESSAGE BUFFER
1693 032716 000000      .WORD  0
1694 032720 000000      T7SP:   .WORD  0          ;EXTFEA EXTRA WORD
1695
1696 032722 000000 000000 .WORD  0,0        ;SPACE
1697 032726          T7BFR:  .BLKW  8.        ;MESSAGE BUFFER
1698
1699
1700
1701          ;*
1702          ;TEST DATA FOR SUBTEST TWO
1703          ;
1704          ;DATA HAS FORMAT:
1705          ;
1706          ;      1ST WORD      OFFSET TO TEST WORD IN PACKET
1707          ;      2ND WORD      BITS TO SET FOR TEST
1708          ;
1709          ;-
1710 032746          T72DATA:
1711 032746 000000 037140 .WORD  0,BIT5!BIT6!BIT9!BIT10!BIT11!BIT12!BIT13
1712 032752 000002 000001 .WORD  2,BIT0
1713 032756 000004 100100 .WORD  4,BIT6!BIT15
1714          T72DONE=.
1715
1716
1717          ;*
1718          ;LOCAL TEXT MESSAGES FOR TEST
1719          ;-
1720
1721 032762          116      102      101  T72NBA: .ASCIZ  'NBA Not Set On Rejected WRITE CHARACTERISTICS'
1722 033040          127      122      111  T7NBA:  .ASCIZ  'WRITE CHARACTERISTICS Command Not Accepted'
1723 033113          127      122      111  T72REJ: .ASCIZ  'WRITE CHARACTERISTICS Not Rejected With Non-Zero Unused Fields'
1724 033212          127      122      111  T73REJ: .ASCIZ  'WRITE CHARACTERISTICS Not Rejected With Invalid Data Count'
1725 033305          127      122      111  T74REJ: .ASCIZ  'WRITE CHARACTERISTICS Not Rejected With Invalid Block Address'
1726 033403          127      122      111  T75REJ: .ASCIZ  'WRITE CHARACTERISTICS Not Rejected With Invalid Buffer Length'
1727 033501          103      157      156  T7SSR:  .ASCIZ  'Contents of TSSR Incorrect After WRITE CHARACTERISTICS'
1728 033570          105      170      160  T7NINT: .ASCIZ  'Expected Interrupt Not Received On WRITE CHARACTERISTICS'
1729 033661          125      156      145  T7INT:  .ASCIZ  'Unexpected Interrupt Received On WRITE CHARACTERISTICS'
1730 033750          111      156      143  T7TSBA: .ASCIZ  'Incorrect TSBA Address After WRITE CHARACTERISTICS'
1731 034033          127      162      151  TST7ID: .ASCIZ  'Write Characteristics'
1732          .EVEN
1733
1734
1735          ;*
1736          ;
1737          ;ROUTINE TO RESTORE COMMAND PACKET TO START-UP (DEFAULT) VALUES
1738          ;
1739          ;-
1740
1741 034062          T7REST:
1742 034062          SAVREG          ;SAVE THE REGISTERS
1743 034066 012701 032700'      MOV      #T7PACKET,R1      ;START OF THE PACKET
    
```

```

1744 034072 012721 100004      MOV    #100004,(R1)+ ;WRITE CHARACTERISTICS WITH ACK
1745 034076 012721 032710'    MOV    #T7DATA,(R1)+ ;ADDRESS OF CHAR DATA BLOCK
1746 034102 005021              CLR    (R1)+         ;EXTENDED ADDRESS
1747 034104 012721 000010      MOV    #8.,(R1)+    ;SIZE OF DATA BLOCK IN BYTES
1748 034110 012721 032726'    MOV    #T7BFR,(R1)+ ;ADDRESS OF MESSAGE BUFFER
1749 034114 005021              CLR    (R1)+
1750 034116 012721 000020      MOV    #16.,(R1)+  ;LENGTH OF MESSAGE BUFFER
1751 034122 005021              CLR    (R1)+
1752 034124 005011              CLR    (R1)
1753 034126 000207              RTS    PC            ;RETURN
1754 034130              ENDTST
                                L10055:
                                TRAP    C$ETST
034130 104401

```

```

1755
1756
1757      .SBTTL TEST 8: VOLUME CHECK
1758
1759

```

```

1760      ;
1761      ; THIS TEST VERIFIES THAT THE VOLUME CHECK (VCK) BIT, A FLAG HELD
1762      ; WITHIN THE M7455 AND APPEARING IN XSTO, IS SET BY INITIALIZE AND
1763      ; CLEARED BY EXECUTING A WRITE CHARACTERISTICS COMMAND WITH THE
1764      ; CVC BIT SET. IT IS ALSO VERIFIED THAT A WRITE CHARACTERISTICS
1765      ; COMMAND WITH THE CVC BIT CLEAR DOES NOT AFFECT THE STATE OF THE
1766      ; VOLUME CHECK BIT. THE ACTUAL FUNCTION OF VOLUME CHECK, THAT OF
1767      ; PREVENTING OR ALLOWING A TAPE MOTION COMMAND DEPENDING UPON
1768      ; WHETHER VOLUME CHECK IS SET OR CLEAR, IS NOT CHECKED BY THIS
1769      ; TEST; THIS FUNCTIONALITY IS CHECKED IN THE INDIVIDUAL TESTS OF
1770      ; TAPE MOTION COMMANDS.

```

```

1771      ; THE TEST PROCEEDS AS FOLLOWS:

```

1. THE CONTROLLER IS INITIALIZED BY WRITING INTO THE TSSR.
2. A WRITE CHARACTERISTICS COMMAND IS ISSUED (WITH CVC=0) AND XSTO IN THE RETURNED MESSAGE BUFFER IS EXAMINED; THE VCK BIT SHOULD BE CLEAR (0).
3. THE PREVIOUS STEP IS REPEATED TO VERIFY THAT VCK DOES NOT CHANGE (REMAINS AT 0).
4. A WRITE CHARACTERISTICS COMMAND IS ISSUED WITH CVC=1 AND THE VCK BIT IN XSTO IN THE MESSAGE BUFFER IS EXAMINED; THE VCK BIT SHOULD BE CLEAR (0).
5. A WRITE CHARACTERISTICS COMMAND IS ISSUED WITH CVC=0 AND THE VCK BIT IN XSTO IN THE MESSAGE BUFFER IS EXAMINED; THE VCK BIT SHOULD REMAIN CLEAR (0).

```

1791 034132              BGNTST
034132
1796 034132 012700 035017'    MOV    #T8ID,R0      ;ASCII MESSAGE TO IDENTIFY TEST
1797 034136 004737 016322'    JSR    PC,T8SETUP    ;DO INITIAL TEST SETUP
1798 034142 012737 000024 002214'  MOV    #20.,LOOPCNT ;PERFORM 20 ITERATIONS
1799 034150              T8LOOP:
1800
1801 034150 012704 034540'    MOV    #T8PACKET,R4 ;PACKET FOR WRITE CHARACTERISTICS

```


1802	034154	004737	015604'	5\$:	JSR	PC,SOFINIT	;DO SOFT INIT OF CONTROLLER		
1803	034160	103405			BCS	10\$;BR IF SOFT INIT = OK		
1807	034162	010001			MOV	RO,R1	;SAVE CONTENTS OF TSSR		
1808	034164				ERRDF	ERRNO,SFIERR,SFIMSG	;DEVICE FATAL ERROR DURING INIT		
	034164	104455					TRAP	C\$ERDF	
	034166	001441					.WORD	801	
	034170	003644'					.WORD	SFIERR	
	034172	011644'					.WORD	SFIMSG	
1809	034174	042714	040000	10\$:	BIC	#BIT14,(R4)	;CLEAR THE CVC BIT		
1810	034200	010465	000000		MOV	R4,TSDB(R5)	;SET THE PACKET ADDRESS FOR WRITE CHAR		
1811	034204	004737	016146'		JSR	PC,CHKTSSR	;WAIT FOR SSR TO SET		
1812	034210	103405			BCS	15\$;BR IF CARRY SET (GOOD RETURN)		
1813	034212	010001			MOV	RO,R1	;SAVE CONTENTS OF TSSR		
1817	034214				ERRDF	ERRNO,T8SSR,PKTSSR	;DEVICE FATAL SSR FAILED TO SET		
	034214	104455					TRAP	C\$ERDF	
	034216	001442					.WORD	802	
	034220	034730'					.WORD	T8SSR	
	034222	011656'					.WORD	PKTSSR	
1818	034224			15\$:	CKLOOP		;LOOP ON ERROR, IF FLAG SET		
	034224	104406					TRAP	C\$CLP1	
1819	034226				ESCAPE	TST	;EXIT IF FATAL ERROR		
	034226	104410					TRAP	C\$ESCAPE	
	034230	000604					.WORD	L10064--	
1820	034232	012702	034562'		MOV	#T8BFR,R2	;ADDRESS OF THE MESSAGE BUFFER		
1821	034236	032762	000020 000006		BIT	#XSOVCK,XSTO(R2)	;IS VOLUME CHECK SET IN XSTO ?		
1822	034244	001406			BEQ	20\$;OKAY IF VOLUME CHECK IS CLEAR		
1826	034246	016501	000002		MOV	TSSR(R5),R1	;CONTENTS OF TSSR FOR ERROR REPORT		
1827	034252				ERRHRD	ERRNO,T8NVCK,PKTMES	;VOLUME CHECK NOT CLEAR		
	034252	104456					TRAP	C\$ERHRD	
	034254	001443					.WORD	803	
	034256	034637'					.WORD	T8NVCK	
	034260	011720'					.WORD	PKTMES	
1828	034262			20\$:	CKLOOP		;LOOP ON ERROR ?		
	034262	104406					TRAP	C\$CLP1	
1829	034264	010465	000000		MOV	R4,TSDB(R5)	;SET THE PACKET ADDRESS FOR WRITE CHAR		
1830	034270	004737	016146'		JSR	PC,CHKTSSR	;WAIT FOR SSR TO SET		
1831	034274	103405			BCS	25\$;BR IF CARRY SET (GOOD RETURN)		
1832	034276	010001			MOV	RO,R1	;SAVE CONTENTS OF TSSR		
1836	034300				ERRDF	ERRNO,T8SSR,PKTSSR	;DEVICE FATAL SSR FAILED TO SET		
	034300	104455					TRAP	C\$ERDF	
	034302	001444					.WORD	804	
	034304	034730'					.WORD	T8SSR	
	034306	011656'					.WORD	PKTSSR	
1837	034310			25\$:	CKLOOP		;LOOP ON ERROR, IF FLAG SET		
	034310	104406					TRAP	C\$CLP1	
1838	034312				ESCAPE	TST	;EXIT IF FATAL ERROR		
	034312	104410					TRAP	C\$ESCAPE	
	034314	000520					.WORD	L10064--	
1839	034316	032762	000020 000006		BIT	#XSOVCK,XSTO(R2)	;IS VOLUME CHECK SET IN XSTO ?		
1840	034324	001406			BEQ	30\$;OKAY IF VOLUME CHECK IS SET		
1844	034326	016501	000002		MOV	TSSR(R5),R1	;CONTENTS OF TSSR FOR ERROR REPORT		
1845	034332				ERRHRD	ERRNO,T8NVCK,PKTMES	;VOLUME CHECK NOT SET		
	034332	104456					TRAP	C\$ERHRD	
	034334	001445					.WORD	805	
	034336	034637'					.WORD	T8NVCK	
	034340	011720'					.WORD	PKTMES	
1846	034342			30\$:	CKLOOP		;LOOP ON ERROR ?		

```

      034342 104406
1847 034344 052714 040000     BIS    #BIT14,(R4)      ;SET THE CVC BIT
      034350 010465 000000     MOV    R4,TSDB(R5)    ;SET THE PACKET ADDRESS FOR WRITE CHAR
1849 034354 004737 016146'    JSR    PC,CHKTSSR     ;WAIT FOR SSR TO SET
1850 034360 103405             BCS    35$            ;BR IF CARRY SET (GOOD RETURN)
1851 034362 010001             MOV    R0,R1          ;SAVE CONTENTS OF TSSR
1855 034364             ERRDF  ERRNO,T8SSR,PKTSSR ;DEVICE FATAL SSR FAILED TO SET
      034364 104455             TRAP  C$ERDF
      034366 001446             .WORD 806
      034370 034730'           .WORD T8SSR
      034372 011656'           .WORD PKTSSR
1856 034374             35$:  CKLOOP          ;LOOP ON ERROR, IF FLAG SET
      034374 104406             ESCAPE TST           ;EXIT IF FATAL ERROR
1857 034376             TRAP  C$CLP1
      034376 104410             TRAP  C$ESCAPE
      034400 000434             .WORD L10064-.
1858 034402 032762 000020 000006 BIT    #XSOVCK,XSTO(R2) ;IS VOLUME CHECK CLEAR IN XSTO ?
1859 034410 001406             BEQ    40$            ;OKAY IF VOLUME CHECK IS CLEARED
1863 034412 016501 000002     MOV    TSSR(R5),R1    ;CONTENTS OF TSSR FOR ERROR REPORT
1864 034416             ERRHRD ERRNO,T8VCK,PKTMES ;VOLUME CHECK NOT CLEARED
      034416 104456             TRAP  C$ERHRD
      034420 001447             .WORD 807
      034422 034602'           .WORD T8VCK
      034424 011720'           .WORD PKTMES
1865 034426             40$:  CKLOOP          ;LOOP ON ERROR ?
      034426 104406             TRAP  C$CLP1
1866 034430 042714 040000     BIC    #BIT14,(R4)    ;CLEAR THE CVC BIT
1867 034434 010465 000000     MOV    R4,TSDB(R5)    ;SET THE PACKET ADDRESS FOR WRITE CHAR
1868 034440 004737 016146'    JSR    PC,CHKTSSR     ;WAIT FOR SSR TO SET
1869 034444 103405             BCS    45$            ;BR IF CARRY SET (GOOD RETURN)
1870 034446 010001             MOV    R0,R1          ;SAVE CONTENTS OF TSSR
1874 034450             ERRDF  ERRNO,T8SSR,PKTSSR ;DEVICE FATAL SSR FAILED TO SET
      034450 104455             TRAP  C$ERDF
      034452 001450             .WORD 808
      034454 034730'           .WORD T8SSR
      034456 011656'           .WORD PKTSSR
1875 034460             45$:  CKLOOP          ;LOOP ON ERROR, IF FLAG SET
      034460 104406             ESCAPE TST           ;EXIT IF FATAL ERROR
1876 034462             TRAP  C$CLP1
      034462 104410             TRAP  C$ESCAPE
      034464 000350             .WORD L10064-.
1877 034466 032762 000020 000006 BIT    #XSOVCK,XSTO(R2) ;IS VOLUME CHECK CLEAR IN XSTO ?
1878 034474 001406             BEQ    50$            ;OKAY IF VOLUME CHECK IS CLEARED
1882 034476 016501 000002     MOV    TSSR(R5),R1    ;CONTENTS OF TSSR FOR ERROR REPORT
1883 034502             ERRHRD ERRNO,T8VCK,PKTMES ;VOLUME CHECK NOT CLEARED
      034502 104456             TRAP  C$ERHRD
      034504 001451             .WORD 809
      034506 034602'           .WORD T8VCK
      034510 011720'           .WORD PKTMES
1884 034512             50$:  CKLOOP          ;LOOP ON ERROR ?
      034512 104406             TRAP  C$CLP1
1885 034514 004737 016270'    60$:  JSR    PC,TSTLOOP ;SHOULD WE DO ITERATIONS ?
1886 034520 103002             BCC    62$            ;BRANCH IF NOT
1887 034522 000137 034150'    62$:  JMP    T8LOOP     ;LOOP UNTIL COUNT EXPIRED
1888 034526             EXIT  TST           ;ALL DONE THIS TEST
      034530 104432             TRAP  C$EXIT
      034530 000304             .WORD L10064-.

```

```

1889
1890
1891                ;*
1892                ;LOCAL STORAGE FOR THIS TEST
1893                ;-
1895 034532          .BLKB   10-<.-TSV2&7>
1897 034540          T8PACKET:                ;COMMAND PACKET FOR TEST
1898 034540 100004   .WORD   100004          ;WRITE CHARACTERISTICS COMMAND
1899 034542 034550'  .WORD   T8DATA         ;ADDRESS OF CHARACTERISTICS BLOCK
1900 034544 000000   .WORD   0
1901 034546 000010   .WORD   10            ;STARTING VALUE OF COUNTER
1902
1903 034550          T8DATA:                ;CHARACTERISTICS DATA BLOCK
1904 034550 034562'  .WORD   T8BFR         ;ADDRESS OF MESSAGE BUFFER
1905 034552 000000   .WORD   0
1906 034554 000020   .WORD   16.           ;LENGTH OF MESSAGE BUFFER
1907 034556 000000 000000 .WORD   0.0
1908
1909 034562          T8BFR: .BLKW  8.        ;MESSAGE BUFFER
1910
1911                ;*
1912                ;LOCAL TEXT MESSAGES FOR TEST
1913                ;-
1915
1916 034602          126     157     154  T8VCK:  .ASCIZ  'Volume Check Bit Not Cleared'
1917 034637          126     157     154  T8NVCK: .ASCIZ  'Volume Check Bit (VCK) Not Clear After Initialize (XST0)'
1918 034730          103     157     156  T8SSR:  .ASCIZ  'Contents of TSSR Incorrect After Write Characteristics'
1919 035017          126     157     154  TST8ID: .ASCIZ  'Volume Check'
1920
1921 035034          .EVEN
1921 035034          ENDTST
1921 035034 104401          L10064:          TRAP   C$ETST
1922
1923                .SBTTL  TEST  9: COMPLETION INTERRUPT
1924
1925
1926                ;
1927                ;   THIS TEST VERIFIES THAT AN INTERRUPT IS GENERATED AT THE
1928                ;   COMPLETION OF THE WRITE CHARACTERISTICS COMMAND IF THE INTERRUPT
1929                ;   ENABLE (IE) BIT IN THE COMMAND HEADER WORD IS SET. THIS TEST
1930                ;   CHECKS THE FUNCTIONING OF THE INTERRUPT LOGIC AND BASIC
1931                ;   PROCESSING OF THE IE BIT.
1932                ;
1933                ;   THE SEQUENCES OF TEST 7 ARE REPEATED, EXCEPT THAT THE INTERRUPT
1934                ;   SERVICE ROUTINE IS SET UP TO EXPECT INTERRUPTS AND EACH WRITE
1935                ;   CHARACTERISTICS COMMAND IS ISSUED WITH THE IE BIT SET (1). IT
1936                ;   IS VERIFIED, WHERE APPROPRIATE, THAT THE IE STATUS BIT IN XST0
1937                ;   OF ANY MESSAGE PACKET IS SET AND THAT A COMPLETION INTERRUPT IS
1938                ;   GENERATED. FINALLY, A SEQUENCE OF TWO COMMANDS ARE ISSUED, THE
1939                ;   FIRST WITH IE=1 AND THE SECOND WITH IE=0. IT IS VERIFIED THAT
1940                ;   NO INTERRUPT IS GENERATED AFTER THE SECOND COMMAND AND THAT THE
1941                ;   IE BIT IN XST0 IS 0.
1942
1942 035036          BGNTST
1942 035036
1947 035036 005037 002224' CLR      EXTFEA          ;CLEAR EXTENDED FEATURES SWITCH
1948 035042 012700 040061' MOV      #TST9ID,R0       ;ASCII MESSAGE TO IDENTIFY TEST

```



```

035714 104456
035716 001620
035720 037240'
035722 011656'
2137 035724
2138 035724
035724
035724 104405
2139
2140 035726 005203
2141 035730 020327 000006
2142 035734 002002
2143 035736 000137 035554'
2144
2145 035742
035742
035742 104403
2146
2147
2148
2149
2150
2151
2152
2153
2154
2155
2156 035744
035744
035744 104402
2157
2158 035746
035746 012700 000000
035752 104441
2159 035754 012703 037052'
2160 035760 012704 037010'
2161 035764 004737 040106'
2162
2163
2164 035770 004737 015604'
2165 035774 103405
2169 035776 010001
2170 036000
036000 104455
036002 001621
036004 003644'
036006 011644'
2171 036010 005037 002222'
2172 036014 052737 000001 037020'
2173 036022 010465 000000
2174 036026 004737 016060'
2175 036032 103405
2176 036034 010001
2180 036036
036036 104455
036040 001622
036042 037527'

30$:
ENDSEG
;NEXT BYTE COUNT
;TESTED ALL INVALID ?
;BRANCH IF TEST DONE
;BRANCH TILL BACK TO ZERO

59$:
ENDSUB
;NEXT BYTE COUNT
;TESTED ALL INVALID ?
;BRANCH IF TEST DONE
;BRANCH TILL BACK TO ZERO

5$:
MOV #T92DATA,R3
MOV #T9PACKET,R4
JSR PC,T9REST

10$:
CLR INTRECV
BIS #1,T9DATA
MOV R4,TSDB(R5)
JSR PC,WAITF
BCS 15$
MOV R0,R1
ERRDF ERRNO,T9SSR,PKTSSR

30$:
ENDSEG
;NEXT BYTE COUNT
;TESTED ALL INVALID ?
;BRANCH IF TEST DONE
;BRANCH TILL BACK TO ZERO

59$:
ENDSUB
;NEXT BYTE COUNT
;TESTED ALL INVALID ?
;BRANCH IF TEST DONE
;BRANCH TILL BACK TO ZERO

BGNSUB
;LOWER PRIORITY TO ALLOW INTERRUPTS
;START OF TEST DATA FOR SUBTEST
;GET THE ADDRESS OF COMMAND PACKET
;RESTORE PACKET TO STARTING VALUES

;DO SOFT INIT OF CONTROLLER
;BR IF SOFT INIT = OK
;SAVE CONTENTS OF TSSR
;DEVICE FATAL ERROR DURING INIT

10$:
CLR INTRECV
BIS #1,T9DATA
MOV R4,TSDB(R5)
JSR PC,WAITF
BCS 15$
MOV R0,R1
ERRDF ERRNO,T9SSR,PKTSSR

;TEST 9, SUBTEST 4
;SUBTEST TO VERIFY THAT A WRITE CHARACTERISTICS COMMAND IS
;REJECTED IF AN ILLEGAL DATA BLOCK ADDRESS IS ISSUED.

;***** BEGIN SUBTEST *****
T9.4:
TRAP C$BSUB

TRAP C$ERHRD
.WORD 912
.WORD T93REJ
.WORD PKTSSR

TRAP C$ESEG

TRAP C$ESUB

TRAP C$ERDF
.WORD 913
.WORD SFIERR
.WORD SFIMSG

TRAP C$ERDF
.WORD 914
.WORD T9SSR
    
```



```

2271
2272
2273
2274
2275
2276
2277
2278
2279
2280 036336          BGNSUB          ;////////// BEGIN SUBTEST ////////////
      036336          T9.6:
      036336 104402
2281 036340 005737 002224'      TST     EXTFEA      ;IS EXTENDED FEATURES SOFT. SW SET?
2282 036344 001002          BNE     4$          ;BR, IF SOFTWARE SWITCH IS SET (ON)
2283 036346 000137 036566'      JMP     55$         ;EXIT SUBTEST
2284 036352 004737 040106'      4$: JSR     PC,T9REST ;SET PACKET TO START-UP VALUES
2285
2286 036356          SETPRI   #PRI00      ;LOWER PRIORITY TO ALLOW INTERRUPTS
      036356 012700 000000          MOV     #PRI00,R0
      036362 104441          TRAP    C$SPRI
2287 036364 012703 002762'      MOV     #TSTBLK*10.,R3 ;START OF TEST DATA
2288 036370 012704 037010'      MOV     #T9PACKET,R4  ;GET THE ADDRESS OF COMMAND PACKET
2289 036374 012764 000012 000006 5$: MOV     #10.,PKBCNT(R4) ;START WITH EXTENDED FEATURES VALUE
2290 036402
2291 036402          BGNSEG          ;>>>>>>>>>>>> BEGIN SEGMENT >>>>>>>>>>>>
      036402 104404          TRAP    C$BSEG
2292
2293 036404 004737 015604'      JSR     PC,SOFINIT   ;DO SOFT INIT OF CONTROLLER
2294 036410 103405          BCS    10$         ;BR IF SOFT INIT = OK
2298 036412 010001          MOV     R0,R1       ;SAVE CONTENTS OF TSSR
2299 036414          ERRDF   ERRNO,SFIERR,SFIMSG ;DEVICE FATAL ERROR DURING INIT
      036414 104455          TRAP    C$ERDF
      036416 001631          .WORD  921
      036420 003644'          .WORD  SFIERR
      036422 011644'          .WORD  SFIMSG
2300 036424 005037 002220'      10$: CLR     FATFLG      ;CLEAR FATAL ERROR FLAG
2301 036430 005037 002222'      CLR     INTRECV     ;CLEAR INTERRUPT RECEIVED FLAG
2302 036434 010465 000000          MOV     R4,TSDB(R5) ;SET THE PACKET ADDRESS
2303 036440 004737 016146'      JSR     PC,CHKTSSR  ;WAIT FOR SSR TO SET
2304 036444 103407          BCS    15$         ;BR IF CARRY SET (GOOD RETURN)
2305 036446 010001          MOV     R0,R1       ;SAVE CONTENTS OF TSSR
2309 036450          ERRDF   ERRNO,T9SSR,PKTSSR ;DEVICE FATAL SSR FAILED TO SET
      036450 104455          TRAP    C$ERDF
      036452 001632          .WORD  922
      036454 037527'          .WORD  T9SSR
      036456 011656'          .WORD  PKTSSR
2310 036460 005237 002220'      15$: INC     FATFLG      ;SET FATAL ERROR FLAG
2311 036464          CKLOOP   ;LOOP ON ERROR, IF FLAG SET
      036464 104406          TRAP    C$CLP1
2312 036466          ESCAPE   SEG          ;BY-PASS SUBTEST IF FATAL ERROR
      036466 104410          TRAP    C$ESCAPE
      036470 000056          .WORD  10000$-.
2313 036472 005737 002222'      TST     INTRECV     ;DID AN INTERRUPT OCCUR ?
2314 036476 001004          BNE    22$         ;BRANCH IF YES
2318 036500          ERRHRD   ERRNO,T9NINT,PKTSSR
      036500 104456          TRAP    C$ERHRD
      036502 001633          .WORD  923

```



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2364 036644 004737 016146'      JSR    PC,CHKTSSR      ;WAIT FOR SSR TO SET
2365 036650 103405              BCS    15$            ;BR IF CARRY SET (GOOD RETURN)
2366 036652 010001              MOV    R0,R1         ;SAVE CONTENTS OF TSSR
2370 036654              ERRDF  ERRNO,T9SSR,PKTSSR ;DEVICE FATAL SSR FAILED TO SET
                                TRAP    C$ERDF
                                .WORD   926
                                .WORD   T9SSR
                                .WORD   PKTSSR
2371 036664              15$:  CKLOOP          ;LOOP ON ERROR, IF FLAG SET
                                TRAP    C$CLP1
2372 036666              ESCAPE  SUB           ;BY-PASS SUBTEST IF FATAL ERROR
                                TRAP    C$ESCAPE
                                .WORD   L10074-.
2373 036672 005737 002222'      TST    INTRECV
2374 036676 001004              BNE    22$            ;DID AN INTERRUPT OCCUR ?
2378 036700              ERRHRD ERRNO,T9NINT,PKTSSR ;BRANCH IF YES
                                TRAP    C$ERHRD
                                .WORD   927
                                .WORD   T9NINT
                                .WORD   PKTSSR
2379 036710              22$:  CKLOOP          ;LOOP ON ERROR ?
                                TRAP    C$CLP1
2380
2381 036712 005037 002222'      CLR    INTRECV
2382 036716 042714 000200      BIC    #BIT7,(R4)    ;CLEAR INTERRUPT RECEIVED FLAG
2383 036722 010465 000000      MOV    R4,TSDB(R5)  ;DISABLE INTERRUPTS
2384 036726 004737 016146'      JSR    PC,CHKTSSR   ;SET THE PACKET ADDRESS
2385 036732 103405              BCS    25$            ;WAIT FOR SSR TO SET
2386 036734 010001              MOV    R0,R1         ;BR IF CARRY SET (GOOD RETURN)
2390 036736              ERRDF  ERRNO,T9SSR,PKTSSR ;SAVE CONTENTS OF TSSR
                                TRAP    C$ERDF
                                .WORD   928
                                .WORD   T9SSR
                                .WORD   PKTSSR
2391 036746              25$:  CKLOOP          ;LOOP ON ERROR, IF FLAG SET
                                TRAP    C$CLP1
2392 036750              ESCAPE  SUB           ;BY-PASS SUBTEST IF FATAL ERROR
                                TRAP    C$ESCAPE
                                .WORD   L10074-.
2393 036754 005737 002222'      TST    INTRECV
2394 036760 001404              BEQ    30$            ;DID AN INTERRUPT OCCUR ?
2398 036762              ERRHRD ERRNO,T9INT,PKTSSR ;BRANCH IF NOT
                                TRAP    C$ERHRD
                                .WORD   929
                                .WORD   T9INT
                                .WORD   PKTSSR
2399 036772              30$:  ENDSUB
2400 036772              ENDSUB          ;////////// END SUBTEST //////////
                                .WORD   L10074:
                                TRAP    C$ESUB
2401
2402 036774              EXIT    TST         ;ALL DONE THIS TEST
                                TRAP    C$EXIT
                                .WORD   L10065-.
2403
2404
2405              ;*
                ;LOCAL STORAGE FOR THIS TEST

```

```

2406      ; -
2407
2409 037000      .BLKB  10-<.-TSV2&7>
2411 037010      T9PACKET:      ;COMMAND PACKET FOR TEST
2412 037010      .WORD  100204      ;WRITE CHAR COMMAND, WITH IE, ACK
2413 037012      .WORD  T9DATA      ;ADDRESS OF CHARACTERISTICS BLOCK
2414 037014      .WORD  0
2415 037016      .WORD  8.          ;STARTING VALUE OF BLOCK SIZE
2416
2417 037020      T9DATA:          ;CHARACTERISTICS DATA BLOCK
2418 037020      .WORD  T9BFR      ;ADDRESS OF MESSAGE BUFFER
2419 037022      .WORD  0
2420 037024      .WORD  14.         ;LENGTH OF MESSAGE BUFFER
2421 037026      .WORD  0,0
2422
2423 037032      T9BFR:  .BLKW  8.          ;MESSAGE BUFFER
2424
2425      ;*
2426      ;
2427      ;TEST DATA FOR SUBTEST TWO
2428      ;
2429      ;DATA HAS FORMAT:
2430      ;
2431      ;      1ST WORD      OFFSET TO TEST WORD IN PACKET
2432      ;      2ND WORD      BITS TO SET FOR TEST
2433      ;
2434      ; -
2435
2436 037052      T92DATA:
2437 037052      .WORD  0,BIT5!BIT6!BIT9!BIT10!BIT11!BIT12!BIT13
2438 037056      .WORD  2,BIT0
2439 037062      .WORD  4,BIT6!BIT15
2440
2441      T92DONE=.
2442
2443      ;*
2444      ;LOCAL TEXT MESSAGES FOR TEST
2445      ; -
2446
2447 037066      127      122      111      T9NBA:  .ASCIZ  'WRITE CHARACTERISTICS Command Not Accepted'
2448 037141      127      122      111      T92REJ: .ASCIZ  'WRITE CHARACTERISTICS Not Rejected With Non-Zero Unused Fields'
2449 037240      127      122      111      T93REJ: .ASCIZ  'WRITE CHARACTERISTICS Not Rejected With Invalid Data Count'
2450 037333      127      122      111      T94REJ: .ASCIZ  'WRITE CHARACTERISTICS Not Rejected With Invalid Block Address'
2451 037431      127      122      111      T95REJ: .ASCIZ  'WRITE CHARACTERISTICS Not Rejected With Invalid Buffer Length'
2452 037527      103      157      156      T95SR:  .ASCIZ  'Contents of TSSR Incorrect After WRITE CHARACTERISTICS'
2453 037616      105      170      160      T9NINT: .ASCIZ  'Expected Interrupt Not Received On WRITE CHARACTERISTICS'
2454 037707      125      156      145      T9INT:  .ASCIZ  'Unexpected Interrupt Received On WRITE CHARACTERISTICS'
2455 037776      111      156      143      T9TSBA: .ASCIZ  'Incorrect TSBA Address After WRITE CHARACTERISTICS'
2456 040061      103      157      155      T9T9ID: .ASCIZ  'Completion Interrupt'
2457
2458      .EVEN
2459
2460      ;*
2461      ;
2462      ;ROUTINE TO RESTORE COMMAND PACKET TO START-UP (DEFAULT) VALUES
2463      ;
2464      ; -

```


	040760	042511'					.WORD	T10NBA
	040762	011656'					.WORD	PKTSSR
2660	040764		30\$:	ENDSEG				
2661	040764							
	040764							
	040764	104405					TRAP	C\$ESEG
2662	040766			BGNSEG				
	040766	104404						
2663							TRAP	C\$BSEG
2664	040770	005037	002222'	CLR	INTRECV			
2665	040774	012737	025252	MOV	#025252,T10BFR			
2666	041002	012714	100212	MOV	#100212,(R4)			
2667	041006	010465	000000	MOV	R4,TSDB(R5)			
2668	041012	004737	016146'	JSR	PC,CHKTSSR			
2669	041016	103407		BCS	45\$			
2670	041020	010001		MOV	R0,R1			
2674	041022			ERRDF	ERRNO,T10SSR,PKTSSR			
	041022	104455					TRAP	C\$ERDF
	041024	001765					.WORD	1013
	041026	042650'					.WORD	T10SSR
	041030	011656'					.WORD	PKTSSR
2675	041032	005237	002220'	INC	FATFLG			
2676	041036		45\$:	CKLOOP				
	041036	104406						
2677	041040	005737	002222'	TST	INTRECV			
2678	041044	001004		BNE	52\$			
2682	041046			ERRHRD	ERRNO,T10INT,PKTSSR			
	041046	104456					TRAP	C\$ERHRD
	041050	001766					.WORD	1014
	041052	043030'					.WORD	T10INT
	041054	011656'					.WORD	PKTSSR
2683	041056	016501	000002	MOV	TSSR(R5),R1			
2684	041062	012702	000200	MOV	#SSR,R2			
2685	041066	032701	000100	BIT	#0FL,R1			
2686	041072	001402		BEQ	55\$			
2687	041074	052702	000100	BIS	#0FL,R2			
2688	041100	020201		55\$:	CMP	R2,R1		
2689	041102	001404		BEQ	60\$			
2693	041104			ERRHRD	ERRNO,T10NNBA,PKTSSR			
	041104	104456					TRAP	C\$ERHRD
	041106	001767					.WORD	1015
	041110	042573'					.WORD	T10NNBA
	041112	011656'					.WORD	PKTSSR
2694	041114		60\$:					
2695	041114	013701	042332'	MOV	T10BFR,R1			
2696	041120	012702	025252	MOV	#025252,R2			
2697	041124	020102		CMP	R1,R2			
2698	041126	001404		BEQ	70\$			
2702	041130			ERRHRD	ERRNO,T10MBF,EXPREC			
	041130	104456					TRAP	C\$ERHRD
	041132	001770					.WORD	1016
	041134	042414'					.WORD	T10MBF
	041136	015304'					.WORD	EXPREC
2703								
2704	041140		70\$:					
2705	041140	005737	002220'	TST	FATFLG			
2706	041144	001402		BEQ	80\$			


```

2891 ;TEST 10 SUBTEST 4
2892 ;
2893 ;CHECKS THAT THE REGISTER MODIFICATION REFUSED (RMR) BIT IN
2894 ;THE TSSR WILL BE SET IF A WRITE CHARACTERISTICS COMMAND
2895 ;BEING EXECUTED AND ANOTHER "WC" COMMAND IS ATTEMPTED
2896 ;
2897 ;-
2898
2899 042046 BGNSUB ;////////// BEGIN SUBTEST ////////////
    042046 ;
    042046 104402 T10.4: TRAP C$BSUB
2900
2901 042050 004737 043220' JSR PC,T10RT2 ;SET SECOND PACKET UP
2902 042054 004737 043146' JSR PC,T10RST ;SET PACKET TO INITIAL VALUES
2903 042060 SETPRI #PRI00 ;LOWER PRIORITY TO ALLOW INTERRUPTS
    042060 012700 000000 MOV #PRI00,RO
    042064 104441 TRAP C$SPRI
2904 042066 012704 042310' MOV #T10PACKET,R4 ;GET THE ADDRESS OF COMMAND PACKET
2905 042072 012703 042352' MOV #T10PKT,R3 ;GET THE ADDRESS OF 2ND CMD PACKET
2906 042076 012764 000010 000006 MOV #8.,PKBCNT(R4) ;START WITH MINIMUM ALLOWABLE VALUE
2907 042104 012763 000010 000006 MOV #8.,PKBCNT(R3) ;START WITH MINIMUM ALLOWABLE VALUE
2908 042112 5$:
2909 042112 BGNSEG ;>>>>>>>>>> BEGIN SEGMENT >>>>>>>>>>
    042112 104404 TRAP C$BSEG
2910 042114 004737 015604' JSR PC,SOFINIT ;DO SOFT INIT OF CONTROLLER
2911 042120 103405 BCS 10$ ;BR IF SOFT INIT = OK
2915 042122 010001 MOV RO,R1 ;SAVE CONTENTS OF TSSR
2916 042124 ERRDF ERRNO,SF IERR,SF IMSG ;DEVICE FATAL ERROR DURING INIT
    042124 104455 TRAP C$ERDF
    042126 002007 .WORD 1031
    042130 003644' .WORD SFIERR
    042132 011644' .WORD SFIMSG
2917 042134 005037 002220' 10$: CLR FATFLG ;CLEAR FATAL ERROR FLAG
2918 042140 005037 002222' CLR INTRECV ;CLEAR INTERRUPT RECEIVED FLAG
2919 042144 010465 000000 MOV R4,TSDB(R5) ;SET THE PACKET ADDRESS
2920 042150 010365 000000 MOV R3,TSDB(R5) ;SECOND COMMAND PACKET
2921 042154 004737 016060' JSR PC,WAITF ;WAIT FOR SSR TO SET
2922 042160 016501 000002 MOV TSSR(R5),R1 ;GET CONTENTS OF TSSR REGISTER
2923 042164 032701 000200 BIT #SSR,R1 ;CHECK FOR SSR (TSSR) SET
2924 042170 001006 BNE 15$ ;BR, IF SSR SET (GOOD)
2928 042172 ERRDF ERRNO,T10SSR,PKTSSR ;DEVICE FATAL SSR FAILED TO SET
    042172 104455 TRAP C$ERDF
    042174 002010 .WORD 1032
    042176 042650' .WORD T10SSR
    042200 011656' .WORD PKTSSR
2929 042202 005237 002220' 15$: INC FATFLG ;SET FATAL ERROR FLAG
2930 042206 CKLOOP ;LOOP ON ERROR, IF FLAG SET
    042206 104406 TRAP C$CLP1
2931 042210 ESCAPE SEG ;BY-PASS SUBTEST IF FATAL ERROR
    042210 104410 TRAP C$ESCAPE
    042212 000056 .WORD 10000$-
2932 042214 005737 002222' TST INTRECV ;DID AN INTERRUPT OCCUR ?
2933 042220 001004 BNE 22$ ;BRANCH IF YES
2934
2935
2939 042222 ERRHRD ERRNO,T10NINT,PKTSSR
    042222 104456 TRAP C$ERHRD

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042224	002011									
042226	042737'								.WORD	1033
042230	011656'								.WORD	T10NINT
2940	042232	016501	000002	22\$:	MOV	TSSR(R5),R1			.WORD	PKTSSR
2941	042236	012702	110200		MOV	#SSR!RMR!SC,R2				
2942	042242	032701	000100		BIT	#OFL,R1				
2943	042246	001402			BEQ	25\$				
2944	042250	052702	000100		BIS	#OFL,R2				
2945	042254	020201		25\$:	CMP	R2,R1				
2946	042256	001404			BEQ	30\$				
2950	042260				ERRHRD	ERRNO,T10SSR,PKTSSR				
	042260	104456							TRAP	C\$ERHRD
	042262	002012							.WORD	1034
	042264	042650'							.WORD	T10SSR
	042266	011656'							.WORD	PKTSSR
2951	042270			30\$:						
2952	042270				ENDSEG					
	042270									
	042270	104405								
2953	042272				ENDSUB					
	042272									
	042272	104403								
2954	042274				EXIT	TST				
	042274	104432								
	042276	000774								
2955										
2956										
2957										
2958										
2959										
2961	042300									
2963	042310									
2964	042310	100204								
2965	042312	042320'								
2966	042314	000000								
2967	042316	000010								
2968										
2969	042320									
2970	042320	042332'								
2971	042322	000000								
2972	042324	000016								
2973	042326	000000	000000							
2974										
2975	042332									
2976										
2977										
2978										
2979										
2980										
2981	042352									
2982	042352	100204								
2983	042354	042362'								
2984	042356	000000								
2985	042360	000010								
2986										
2987	042362									
2988	042362	042374'								

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2989 042364 000000          .WORD 0
2990 042366 000016          .WORD 14.          ;LENGTH OF MESSAGE BUFFER
2991 042370 000000 000000  .WORD 0,0
2992
2993 042374          T10BUFR: .BLKW 8.          ;MESSAGE BUFFER
2994
2995          ;*
2996          ;LOCAL TEXT MESSAGES FOR TEST
2997          ;-
2998
2999
3000 042414          115      145      163  T10MBF: .ASCIZ 'Message Buffer Modified after MESSAGE BUFFER RELEASE Command'
3001 042511          116      102      101  T10NBA: .ASCIZ 'NBA Not Clear After WRITE CHARACTERISTICS Command'
3002 042573          116      102      101  T10NNBA: .ASCIZ 'NBA Set After MESSAGE BUFFER RELEASE Command'
3003 042650          103      157      156  T10SSR: .ASCIZ 'Contents of TSSR Incorrect After WRITE CHARACTERISTICS'
3004 042737          105      170      160  T10NINT: .ASCIZ 'Expected Interrupt Not Received On WRITE CHARACTERISTICS'
3005 043030          125      156      145  T10INT: .ASCIZ 'Unexpected Interrupt Received On WRITE CHARACTERISTICS'
3006 043117          102      141      163  TST10ID: .ASCIZ 'Basic Packet Protocol'
3007          .EVEN
3008
3009
3010
3011          ;*
3012          ;
3013          ;ROUTINE TO RESTORE COMMAND PACKET TO START-UP (DEFAULT) VALUES
3014          ;
3015          ;-
3016
3017 043146          T10RST:
3018 043146          SAVREG          ;SAVE THE REGISTERS
3019 043152          012701 042310'  MOV      #T10PACKET,R1      ;START OF THE PACKET
3020 043156          012721 100204  MOV      #100204,(R1)+      ;WRITE CHARACTERISTICS WITH ACK, IE
3021 043162          012721 042320'  MOV      #T10DATA,(R1)+    ;ADDRESS OF CHAR DATA BLOCK
3022 043166          005021          CLR      (R1)+              ;EXTENDED ADDRESS
3023 043170          012721 000010  MOV      #8.,(R1)+          ;SIZE OF DATA BLOCK IN BYTES
3024 043174          012721 042332'  MOV      #T10BFR,(R1)+    ;ADDRESS OF MESSAGE BUFFER
3025 043200          005021          CLR      (R1)+              ;
3026 043202          012721 000016  MOV      #14.,(R1)+        ;LENGTH OF MESSAGE BUFFER
3027 043206          005021          CLR      (R1)+              ;
3028 043210          005011          CLR      (R1)               ;
3029 043212          005037 042332'  CLR      T10BFR            ;CLEAR 1ST LOC IN MESSAGE BUFFER
3030 043216          000207          RTS      PC                 ;RETURN
3031          ;*
3032          ;
3033          ;ROUTINE TO RESTORE COMMAND PACKET #2 TO START-UP (DEFAULT) VALUES
3034          ;
3035          ;-
3036
3037 043220          T10RT2:
3038 043220          SAVREG          ;SAVE THE REGISTERS
3039 043224          012701 042352'  MOV      #T10PKT,R1        ;START OF THE PACKET
3040 043230          012721 100204  MOV      #100204,(R1)+    ;WRITE CHARACTERISTICS WITH ACK, IE
3041 043234          012721 042362'  MOV      #T10DTA,(R1)+    ;ADDRESS OF CHAR DATA BLOCK
3042 043240          005021          CLR      (R1)+              ;EXTENDED ADDRESS
3043 043242          012721 000010  MOV      #8.,(R1)+          ;SIZE OF DATA BLOCK IN BYTES
3044 043246          012721 042374'  MOV      #T10BUFR,(R1)+   ;ADDRESS OF MESSAGE BUFFER
3045 043252          005021          CLR      (R1)+              ;

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3046 043254 012721 000016      MOV    #14.,(R1)+      ;LENGTH OF MESSAGE BUFFER
3047 043260 005021              CLR    (R1)+
3048 043262 005011              CLR    (R1)
3049 043264 005037 042374'      CLR    T10BUF         ;CLEAR 1ST LOC IN MESSAGE BUFFER
3050 043270 000207              RTS    PC              ;RETURN
3051 043272                     ENDTST
                                L10075:
                                TRAP    C$ETST
3052 043272 104401
3053
3054      .SBTTL  TEST  11:  NON-TAPE MOTION COMMANDS
3055      ;*
3056      ;
3057      ;THIS TEST VERIFIES PROPER OPERATION OF THE INITIALIZE
3058      ;COMMAND. TWO SUBTESTS ARE USED. THE FIRST VERIFIES THAT
3059      ;THE COMMAND RUNS TO COMPLETION AND STORES A VALID
3060      ;MESSAGE PACKET. THE SECOND VERIFIES THAT NON-ZERO
3061      ;VALUES IN THE COMMAND MODE FIELD CAUSES COMMAND REJECT.
3062      ;
3063      ;
3064      ;-
3065 043274      BGNTST
                                T11::
3070 043274 012700 045302'      MOV    #TST11ID,R0    ;ASCII MESSAGE TO IDENTIFY TEST
3071 043300 004737 016322'      JSR    PC,TSTSETUP    ;DO INITIAL TEST SETUP
3072 043304 012737 000024 002214'  MOV    #20.,LOOPCNT    ;PERFORM 20 ITERATIONS
3073 043312      T11LOOP:
3074 043312      BGNSUB
                                ;//////////////// BEGIN SUBTEST //////////////////
                                T11.1:
                                TRAP    C$BSUB
3075 043312 104402
3076 043314      SETPRI  #PRI00      ;LOWER PRIORITY TO ALLOW INTERRUPTS
                                MOV    #PRI00,R0
                                TRAP    C$SPRI
3077 043322 004737 015604'      JSR    PC,SOFINIT     ;DO SOFT INIT OF CONTROLLER
3078 043326 103405              BCS    3$             ;BR IF SOFT INIT = OK
3082 043330 010001              MOV    R0,R1          ;SAVE CONTENTS OF TSSR
3083 043332 104455              ERDF   ERRNO,SFIERR,SFIMSG ;DEVICE FATAL ERROR DURING INIT
                                TRAP    C$ERDF
                                .WORD   1101
                                .WORD   SFIERR
                                .WORD   SFIMSG
3084 043342      3$:
3085 043342 012704 044530'      MOV    #T11PK2,R4    ;WRITE CHARACTERISTICS PACKET
3086 043346 004737 010472'      JSR    PC,WRTCHR     ;ISSUE WRITE CHARACTERISTICS
3087 043352 103404              BCS    4$             ;BR, IF COMMAND ISSUED OK
3091 043354 104456              ERHRD  ERRNO,WRTMSG,SFIMSG ;WRITE CHARACTERISTICS FAILED
                                TRAP    C$ERHRD
                                .WORD   1102
                                .WORD   WRTMSG
                                .WORD   SFIMSG
3092 043364      4$:
3093 043364 004737 045334'      JSR    PC,T11REST    ;SET UP PACKET FOR COMMAND
3094 043370 012704 044460'      MOV    #T11PACKET,R4 ;GET THE ADDRESS OF COMMAND PACKET
3095 043374      5$:
3096 043374 104404      BGNSEG
                                ;>>>>>>>>>> BEGIN SEGMENT >>>>>>>>>>
                                TRAP    C$BSEG

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3143 043554      60$:
3144
3145             ;*
3146             ;
3147             ;TEST 11, SUBTEST 2
3148             ;
3149             ;CHECK THAT NON-ZERO MODE BITS BEING SET CAUSES
3150             ;INITIALIZE COMMAND TO BE REJECTED
3151             ;
3152             ;
3153             ;-
3154 043554      BGNSUB                      ;////////// BEGIN SUBTEST ///////////
           043554                      T11.2:
           043554 104402                 TRAP    C$BSUB
3155
3156 043556      SETPRI #PRI00             ;LOWER PRIORITY TO ALLOW INTERRUPTS
           043556 012700 000000         MOV     #PRI00,R0
           043562 104441                 TRAP    C$SPRI
3157 043564      BGNSEG                    ;>>>>>>>>>>>> BEGIN SEGMENT >>>>>>>>>>>>
           043564 104404                 TRAP    C$BSEG
3158
3159
3160 043566      JSR      PC,SOFINIT       ;DO SOFT INIT OF CONTROLLER
3161 043572      BCS     3$                ;BR IF SOFT INIT = OK
3165 043574      MOV     R0,R1             ;SAVE CONTENTS OF TSSR
3166 043576      ERRDF   ERRNO,SFIERR,SFIMSG ;DEVICE FATAL ERROR DURING INIT
           043576 104455                 TRAP    C$ERDF
           043600 002123                 .WORD  1107
           043602 003644                 .WORD  SFIERR
           043604 011644                 .WORD  SFIMSG
3167 043606      3$:
3168 043606      MOV     #T11PK2,R4        ;WRITE CHARACTERISTICS PACKET
3169 043612      JSR     PC,WRTCHR         ;ISSUE WRITE CHARACTERISTICS
3170 043616      BCS     4$                ;BR, IF COMMAND ISSUED OK
3174 043620      ERRHRD ERRNO,WRTMSG,SFIMSG ;WRITE CHARACTERISTICS FAILED
           043620 104456                 TRAP    C$ERHRD
           043622 002124                 .WORD  1108
           043624 005050                 .WORD  WRTMSG
           043626 011644                 .WORD  SFIMSG
3175 043630      4$:
3176 043630      JSR     PC,T11REST        ;SET UP PACKET FOR COMMAND
3177 043634      MOV     #T11PACKET,R4    ;GET THE ADDRESS OF COMMAND PACKET
3178 043640      5$:
3179 043640      10$:  CLR      INTRECV      ;CLEAR INTERRUPT RECEIVED FLAG
3180 043644      BIS     #P.MODE,(R4)     ;NON-ZERO COMMAND MODE BITS
3181 043650      MOV     R4,TSDB(R5)      ;SET THE PACKET ADDRESS
3182 043654      JSR     PC,CHKTSSR      ;WAIT FOR SSR TO SET
3183 043660      BCS     15$             ;BR IF CARRY SET (GOOD RETURN)
3184 043662      MOV     R0,R1           ;SAVE CONTENTS OF TSSR
3188 043664      ERRDF   ERRNO,T11SSR,PKTSSR ;DEVICE FATAL SSR FAILED TO SET
           043664 104455                 TRAP    C$ERDF
           043666 002125                 .WORD  1109
           043670 045024                 .WORD  T11SSR
           043672 011656                 .WORD  PKTSSR
3189 043674      15$:  CKLOOP          ;LOOP ON ERROR, IF FLAG SET
           043674 104406                 TRAP    C$CLP1
3190 043676      ESCAPE SEG              ;BY-PASS CHECKS IF FATAL ERROR

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	043676	104410					TRAP	C\$ESCAPE
	043700	000074					.WORD	10000\$-
3191	043702	005737	002222'	TST	INTRECV			
3192	043706	001004		BNE	22\$:DID AN INTERRUPT OCCUR ?		
3196	043710			ERRHRD	ERRNO,T11NINT,PKTSSR	:BRANCH IF YES		
	043710	104456					TRAP	C\$ERHRD
	043712	002126					.WORD	1110
	043714	045154'					.WORD	T11NINT
	043716	011656'					.WORD	PKTSSR
3197	043720	016501	000002	22\$:	MOV TSSR(R5),R1	:GET THE CONTENTS OF TSSR		
3198	043724	012702	100206		MOV @SC!SSR!TSREJ,R2	:EXPECTED CONTENTS OF TSSR		
3199	043730	032701	000100		BIT @OFL,R1	:IS OFF-LINE BIT SET ?		
3200	043734	001402			BEQ 25\$:BRANCH IF NOT OFF-LINE		
3201	043736	052702	000100		BIS @OFL,R2	:SET OFF-LINE IN EXPECTED DATA		
3202	043742	020201		25\$:	CMP R2,R1	:DOES EXPECTED MATCH RECEIVED ?		
3203	043744	001404			BEQ 30\$:OKAY IF MATCH		
3207	043746				ERRHRD ERRNO,T112REJ,PKTSSR	:COMMAND NOT REJECTED		
	043746	104456					TRAP	C\$ERHRD
	043750	002127					.WORD	1111
	043752	044632'					.WORD	T112REJ
	043754	011656'					.WORD	PKTSSR
3208	043756			30\$:				
3209	043756	004737	010724'	35\$:	JSR PC,CKRAM	:CHECK RAM TO MEMORY		
3210	043762	103405			BCS 59\$:RAM OK GO ON		
3214	043764				ERRHRD ERRNO,PKTRAM,RAMERR	:THEY DON'T MATCH		
	043764	104456					TRAP	C\$ERHRD
	043766	002130					.WORD	1112
	043770	004737'					.WORD	PKTRAM
	043772	015320'					.WORD	RAMERR
3215	043774				ENDSEG	:<<<<<<<<<<<<< END SEGMENT <<<<<<<<<<<<<<		
	043774					10000\$:		
	043774	104405					TRAP	C\$ESEG
3216								
3217	043776			59\$:	ENDSUB	:////////// END SUBTEST \\\		
	043776					L10104:		
	043776	104403					TRAP	C\$ESUB
3218								
3219								
3220				:*				
3221				:				
3222				:TEST 11, SUBTEST 3				
3223				:				
3224				:CHECK THAT THE GET STATUS COMMAND IS ACCEPTED				
3225				:				
3226				:				
3227	044000				BGNSUB	:////////// BEGIN SUBTEST \\\		
	044000					T11.3:		
	044000	104402					TRAP	C\$BSUB
3228								
3229	044002				SETPRI @PRI00	:LOWER PRIORITY TO ALLOW INTERRUPTS		
	044002	012700	000000				MOV	@PRI00,R0
	044006	104441					TRAP	C\$SPRI
3230	044010				BGNSEG	:>>>>>>>>>>>>> BEGIN SEGMENT >>>>>>>>>>>>>>		
	044010	104404					TRAP	C\$BSEG
3231	044012	004737	015604'		JSR PC,SOFINIT	:DO SOFT INIT OF CONTROLLER		
3232	044016	103405			BCS 3\$:BR IF SOFT INIT = OK		
3236	044020	010001			MOV R0,R1	:SAVE CONTENTS OF TSSR		

3237	044022			ERRDF	ERRNO,SFIERR,SFIMSG	;DEVICE FATAL ERROR DURING INIT		
	044022	104455					TRAP	C\$ERDF
	044024	002131					.WORD	1113
	044026	003644'					.WORD	SFIERR
	044030	011644'					.WORD	SFIMSG
3238	044032			3\$:				
3239	044032	012704	044530'		MOV	@T11PK2,R4		;WRITE CHARACTERISTICS PACKET
3240	044036	004737	010472'		JSR	PC,WRTCHR		;ISSUE WRITE CHARACTERISTICS
3241	044042	103404			BCS	4\$;BR, IF COMMAND ISSUED OK
3245	044044				ERRHRD	ERRNO,WRTMSG,SFIMSG		;WRITE CHARACTERISTIC FAILED
	044044	104456					TRAP	C\$ERHRD
	044046	002132					.WORD	1114
	044050	005050'					.WORD	WRTMSG
	044052	011644'					.WORD	SFIMSG
3246	044054			4\$:				
3247	044054	004737	045334'		JSR	PC,T11REST		;SET UP PACKET FOR COMMAND
3248	044060	012704	044460'		MOV	@T11PACKET,R4		;GET THE ADDRESS OF COMMAND PACKET
3249	044064			5\$:				
3250	044064	005037	002222'	10\$:	CLR	INTRECV		;CLEAR INTERRUPT RECEIVED FLAG
3251	044070	010465	000000		MOV	R4,TSDB(R5)		;SET THE PACKET ADDRESS
3252	044074	004737	016146'		JSR	PC,CHKTSSR		;WAIT FOR SSR TO SET
3253	044100	103405			BCS	15\$;BR IF CARRY SET (GOOD RETURN)
3254	044102	010001			MOV	R0,R1		;SAVE CONTENTS OF TSSR
3258	044104				ERRDF	ERRNO,T11SR2,PKTSSR		;DEVICE FATAL SSR FAILED TO SET
	044104	104455					TRAP	C\$ERDF
	044106	002133					.WORD	1115
	044110	045100'					.WORD	T11SR2
	044112	011656'					.WORD	PKTSSR
3259	044114			15\$:	CKLOOP			;LOOP ON ERROR, IF FLAG SET
	044114	104406					TRAP	C\$CLP1
3260	044116				ESCAPE	SEG		;BY-PASS SUBTEST IF FATAL ERROR
	044116	104410					TRAP	C\$ESCAPE
	044120	000074					.WORD	10000\$-
3261	044122	005737	002222'		TST	INTRECV		;DID AN INTERRUPT OCCUR ?
3262	044126	001004			BNE	22\$;BRANCH IF YES
3266	044130				ERRHRD	ERRNO,T11NINT,PKTSSR		
	044130	104456					TRAP	C\$ERHRD
	044132	002134					.WORD	1116
	044134	045154'					.WORD	T11NINT
	044136	011656'					.WORD	PKTSSR
3267	044140	016501	000002	22\$:	MOV	TSSR(R5),R1		;GET THE CONTENTS OF TSSR
3268	044144	012702	000200		MOV	@SSR,R2		;EXPECTED CONTENTS OF TSSR
3269	044150	032701	000100		BIT	@OFL,R1		;IS OFF-LINE BIT SET ?
3270	044154	001402			BEQ	25\$;BRANCH IF NOT OFF-LINE
3271	044156	052702	000100		BIS	@OFL,R2		;SET OFF-LINE IN EXPECTED DATA
3272	044162	020201		25\$:	CMP	R2,R1		;DOES EXPECTED MATCH RECEIVED ?
3273	044164	001404			BEQ	30\$;OKAY IF MATCH
3277	044166				ERRHRD	ERRNO,T113REJ,PKTSSR		;COMMAND NOT ACCEPTED
	044166	104456					TRAP	C\$ERHRD
	044170	002135					.WORD	1117
	044172	044713'					.WORD	T113REJ
	044174	011656'					.WORD	PKTSSR
3278	044176			30\$:				
3279	044176	004737	010724'	35\$:	JSR	PC,CKRAM		;CHECK RAM TO MEMORY
3280	044202	103405			BCS	59\$;RAM OK GO ON
3284	044204				ERRHRD	ERRNO,PKTRAM,RAMERR		;THEY DON'T MATCH
	044204	104456					TRAP	C\$ERHRD


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3374 044462 044470' .WORD T11DATA ;ADDRESS OF CHARACTERISTICS BLOCK
3375 044464 000000 .WORD 0
3376 044466 000010 .WORD 8. ;STARTING VALUE OF BLOCK SIZE
3377
3378 044470 T11DATA: ;CHARACTERISTICS DATA BLOCK
3379 044470 044502' .WORD T11BFR ;ADDRESS OF MESSAGE BUFFER
3380 044472 000000 .WORD 0
3381 044474 000016 .WORD 14. ;LENGTH OF MESSAGE BUFFER
3382 044476 000000 000000 .WORD 0.0
3383
3384 044502 T11BFR: .BLKW 8. ;MESSAGE BUFFER
3385
3386
3388 044522 .BLKB 10-<.-TSV2&7>
3390 044530 T11PK2: ;COMMAND PACKET FOR TEST
3391 044530 100204 .WORD 100204 ;WRITE CHAR COMMAND, WITH IE, ACK
3392 044532 044540' .WORD T11DTA ;ADDRESS OF CHARACTERISTICS BLOCK
3393 044534 000000 .WORD 0
3394 044536 000010 .WORD 8. ;STARTING VALUE OF BLOCK SIZE
3395
3396 044540 T11DTA: ;CHARACTERISTICS DATA BLOCK
3397 044540 044552' .WORD T11BF2 ;ADDRESS OF MESSAGE BUFFER
3398 044542 000000 .WORD 0
3399 044544 000016 .WORD 14. ;LENGTH OF MESSAGE BUFFER
3400 044546 000000 000000 .WORD 0.0
3401
3402 044552 T11BF2: .BLKW 8. ;MESSAGE BUFFER
3403
3404
3405
3406
3407 ;*
3408 ;LOCAL TEXT MESSAGES FOR TEST
3409 ;-
3410 044572 111 116 111 T11NBA: .ASCIZ 'INITIALIZE Command Not Accepted'
3411 044632 111 116 111 T112REJ: .ASCIZ 'INITIALIZE Not Rejected With Non-Zero Mode Field'
3412 044713 107 105 124 T113REJ: .ASCIZ 'GET STATUS Not Accepted'
3413 044743 107 105 124 T114REJ: .ASCIZ 'GET STATUS Not Rejected With Non-Zero Mode Field'
3414 045024 103 157 156 T11SSR: .ASCIZ 'Contents of TSSR Incorrect After INITIALIZE'
3415 045100 103 157 156 T11SR2: .ASCIZ 'Contents of TSSR Incorrect After GET STATUS'
3416 045154 105 170 160 T11NINT: .ASCIZ 'Expected Interrupt Not Received On INITIALIZE'
3417 045232 111 156 143 T11TSBA: .ASCIZ 'Incorrect TSBA Address After INITIALIZE'
3418 045302 116 157 156 TST11ID: .ASCIZ 'Non-Tape Motion Commands'
3419 .EVEN
3420
3421
3422 ;*
3423 ;
3424 ;ROUTINE TO RESTORE COMMAND PACKET TO START-UP (DEFAULT) VALUES
3425 ;INITIALIZE COMMAND
3426 ;
3427 ;-
3428
3429 045334 T11REST:
3430 045334 SAVREG ;SAVE THE REGISTERS
3431 045340 012701 044460' MOV #T11PACKET,R1 ;START OF THE PACKET
3432 045344 012721 100213 MOV #100213,(R1)+ ;INITIALIZE WITH ACK, IE

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3433 045350 005021          CLR      (R1)+      ;ADDRESS OF CHAR DATA BLOCK
3434 045352 005021          CLR      (R1)+      ;EXTENDED ADDRESS
3435 045354 005021          CLR      (R1)+      ;SIZE OF DATA BLOCK IN BYTES
3436 045356 005021          CLR      (R1)+      ;ADDRESS OF MESSAGE BUFFER
3437 045360 005021          CLR      (R1)+
3438 045362 005021          CLR      (R1)+      ;LENGTH OF MESSAGE BUFFER
3439 045364 005021          CLR      (R1)+
3440 045366 005011          CLR      (R1)
3441 045370 005037 044502'  CLR      T11BFR      ;CLEAR 1ST LOC IN MESSAGE BUFFER
3442 045374 000207          RTS        PC        ;RETURN
3443
3444
3445
3446
3447
3448
3449
3450 045376          T11RT2:
3451 045376          SAVREG
3452 045402 012701 044460'  MOV      @T11PACKET,R1 ;SAVE THE REGISTERS
3453 045406 012721 100217  MOV      @100217,(R1)+ ;START OF THE PACKET
3454 045412 005021          CLR      (R1)+      ;GET STATUS WITH ACK, IE
3455 045414 005021          CLR      (R1)+      ;ADDRESS OF CHAR DATA BLOCK
3456 045416 005021          CLR      (R1)+      ;EXTENDED ADDRESS
3457 045420 005021          CLR      (R1)+      ;SIZE OF DATA BLOCK IN BYTES
3458 045422 005021          CLR      (R1)+      ;ADDRESS OF MESSAGE BUFFER
3459 045424 005021          CLR      (R1)+      ;LENGTH OF MESSAGE BUFFER
3460 045426 005021          CLR      (R1)+
3461 045430 005011          CLR      (R1)
3462 045432 005037 044502'  CLR      T11BFR      ;CLEAR 1ST LOC IN MESSAGE BUFFER
3463 045436 000207          RTS        PC        ;RETURN
3464 045440          ENDTST
      045440
      045440 104401          L10102: TRAP   C$ETST
3465 045442          ENDMOD

```

```

1          .TITLE   TSV6 - PARAMETER CODING
7
12
18
19 045442   BGNMOD   TSV6
20 045442   TSV6::
21
22          .SBTTL  HARDWARE PARAMETER CODING SECTION
23
24          ;**
25          ; THE HARDWARE PARAMETER CODING SECTION CONTAINS MACROS
26          ; THAT ARE USED BY THE SUPERVISOR TO BUILD P-TABLES.  THE
27          ; MACROS ARE NOT EXECUTED AS MACHINE INSTRUCTIONS BUT ARE
28          ; INTERPRETED BY THE SUPERVISOR AS DATA STRUCTURES.  THE
29          ; MACROS ALLOW THE SUPERVISOR TO ESTABLISH COMMUNICATIONS
30          ; WITH THE OPERATOR.
31          ;--
32 045442   BGNHRD
33 045442   .WORD L10107-L$HARD/2
34 045444   L$HARD::
35 045444   GPRMA   HPM1,0,0,160010,177776,YES      ;GET TSBA/TSDB REGISTER ADDRESS.
36 045444   .WORD   T$CODE
37 045446   .WORD   HPM1
38 045450   .WORD   T$LLOLM
39 045452   .WORD   T$HILIM
40 045454   GPRMA   HPM2,2,0,0,776,YES             ;GET VECTOR ADDRESS.
41 045454   .WORD   T$CODE
42 045456   .WORD   HPM2
43 045460   .WORD   T$LLOLM
44 045462   .WORD   T$HILIM
45          ;GPRMD   HPM3,4,0,340,0,7,YES         ;GET INTERRUPT PRIORITY.
46 045464   ENDHRD
47          .EVEN
48
49          L10107:
50 045464   HPM1:   .ASCIZ  'DEVICE ADDRESS (TSBA/TSDB) '
51 045520   HPM2:   .ASCIZ  'INTERRUPT VECTOR '
52 045544   HPM3:   .ASCIZ  'INTERRUPT PRIORITY '
53          .EVEN
54
55          .SBTTL  SOFTWARE PARAMETER CODING SECTION
56
57          ;**
58          ; THE SOFTWARE PARAMETER CODING SECTION CONTAINS MACROS
59          ; THAT ARE USED BY THE SUPERVISOR TO BUILD P-TABLES.  THE
60          ; MACROS ARE NOT EXECUTED AS MACHINE INSTRUCTIONS BUT ARE
61          ; INTERPRETED BY THE SUPERVISOR AS DATA STRUCTURES.  THE
62          ; MACROS ALLOW THE SUPERVISOR TO ESTABLISH COMMUNICATIONS
63          ; WITH THE OPERATOR.
64          ;--
65 045574   BGNSFT
66 045574   .WORD L10110-L$SOFT/2
67 045576   L$SOFT::
68          ;
69 045576   GPRML   SPM1,0,-1,YES                   ; GET TRANSPORT TEST FLAG.
70 045576   GPRML   SPM4,2,-1,YES                   ; GET ITERATION CONTROL.
71 045576   .WORD   T$CODE
    
```

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045600 045634'
045602 177777
56 : .WORD SPM4
57 : .WORD -1
58 045604 : GPRMD SPM6,4,D,7777,0,7777,YES ; GET LOCAL ERROR LIMIT
: GPRMD SPM7,6,D,7777,0,7777,YES ; GET GLOBAL ERROR LIMIT
: ENDSFT
: .EVEN
045604 L10110:
59
60
61 045604 105 116 101 SPM1: .ASCIZ 'ENABLE TRANSPORT TESTS '
62 045634 111 116 110 SPM4: .ASCIZ 'INHIBIT ITERATIONS '
63 :SPM6: .ASCIZ 'PER TEST ERROR LIMIT '
64 :SPM7: .ASCIZ 'PER UNIT ERROR LIMIT '
65 : .SBTTL PATCH AREA
66
67 :
68 : FINALLY A GENEROUS PATCH AREA.
69 :
70 : AND AN ADJUSTMENT TO ACCOUNT FOR THE "LASTAD BIT7" HACK
71 : DESCRIBED IN "SUPPRG.MEM" (FOR REV C).
72 :
73 :
74 045664 PATCH::
75 :
76 : .BLKW 32.
77 045664 : .BLKW 1.
78 :
79 : .IF NZ, .E377
80 : . = .!377+1
81 :
82 045666 : .ENDC
LASTAD ;SET LAST USED ADDRESS.
: .EVEN
: .WORD 0
: .WORD 0
83 045672 L$LAST::
84 : ENDMOD
85 : .SBTTL HARD CODED P-TABLE
86 :
87 :
88 045672 :++
89 045672 : DIAGNOSTIC IS PRE-PARAMETERIZED PER THIS TABLE
: --
045672 000000 BGNSETUP 1
045674 000003 BGNPTAB
: .WORD 0
: .WORD L10113-./2-1
90 045676 172522 L10111:
: .WORD 172522
91 045700 000224 : .WORD 224
92 045702 000240 : .WORD PRI05
93 045704 : .SBTTL
L10113:
94 045704 : ENDSFT
95 : .EVEN
96 000001 : .END

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T8NVCK	034637R	002	T92DAT	037052R	002	WF.IRE =	000040	XSOILC =	001000	X1.UNC =	000002		
T8PACK	034540R	002	T92DON =	037066R	002	WF.IWF =	000020	XSOLET =	020000	X2.BUF =	000100		
T8SSR	034730R	002	T92REJ	037141R	002	WF.IWR =	000100	XSOMOT =	000200	X2.EXT =	000200		
T8VCK	034602R	002	T93REJ	037240R	002	WF.I3R =	000002	XSONEF =	002000	X2.OPM =	100000		
T9	035036RG	002	T94REJ	037333R	002	WF.I4R =	000001	XSOONL =	000100	X2.RCE =	040000		
T9BFR	037032R	002	T95REJ	037431R	002	WRTCHR	010472RG	002	XSOPED =	000010	X2.REV =	000077	
T9DATA	037020R	002	UAM =	000200 G		WRTERR	005105R	002	XSORLL =	010000	X2.SPA =	035400	
T9INT	037707R	002	UNITN	002200RG	002	WRTMSG	005050R	002	XSORLS =	040000	X2.UNI =	000007	
T9LOOP	035060R	002	UNREC =	000006		WSMBK	021020RG	002	XSOTMK =	100000	X2.WCF =	002000	
T9NBA	037066R	002	USI	004115R	002	XFERAS	015550R	002	XSOVCK =	000020	X3.DCK =	000010	
T9NINT	037616R	002	WAITF	016060RG	002	XNXM	016206R	002	XSOVLE =	004000	X3.MBZ =	000006	
T9PACK	037010R	002	WC.IFA =	000200		XORBFO	007504R	002	XSOWLK =	000004	X3.MDE =	177400	
T9REST	040106R	002	WC.IFE =	000002		XORFOR	007622R	002	XXCOMM	003120RG	002	X3.OPI =	000100
T9SSR	037527R	002	WC.IGO =	000001		XST0 =	000006 G		X\$ALWA =	000000		X3.REV =	000040
T9TSBA	037776R	002	WC.IRE =	000010		XST1 =	000010 G		X\$FALS =	000040		X3.RIB =	000001
T9.1	035060R	002	WC.IRW =	000004		XST2 =	000012 G		X\$OFFS =	000400		X3.SPA =	000200
T9.2	035326R	002	WC.IOT =	000100		XST3 =	000014 G		X\$TRUE =	000020		X3.TRF =	000020
T9.3	035540R	002	WC.I1T =	000040		XST4 =	000016 G		X1.COR =	020000		X4.HSP =	100000
T9.4	035744R	002	WC.ISR =	000020		XSOBOT =	000002		X1.DLT =	100000		X4.MBZ =	017400
T9.5	036132R	002	WF.IED =	000010		XSOEOT =	000001		X1.MBZ =	017375		X4.RCE =	040000
T9.6	036336R	002	WF.IER =	000004		XSOIE =	000040		X1.RBP =	000400		X4.TSM =	020000
T9.7	036570R	002	WF.IHI =	000200		XSOILA =	000400		X1.SPA =	040000		X4.WRC =	000377

. ABS. 000000 000
 000000 001
 ABS 045704 002
 ERRORS DETECTED: 0

VIRTUAL MEMORY USED: 28264 WORDS (111 PAGES)
 DYNAMIC MEMORY: 20614 WORDS (79 PAGES)
 ELAPSED TIME: 00:36:10
 CZTSAA,CZTSAA.SEQ/-SP=SVC/ML,TSV1A,TSV22A,TSV3B,TSV4,TSV5A,TSV6