

Micro Fiche Scan

Name of device(s) tested:

TU80

Test description:

TU80 FRONT END DIAG

MAINDEC Number or Package Identifier (after SEP 1977):

CZTUWA0

Fiche Document Part Number:

AH-T328A-MC

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IDENTIFICATION

PRODUCT ID: AC-T327A-MC  
PRODUCT TITLE: CZTUWAO TUBO FRONT END DIAG  
PRODUCT DATE: 24 - APRIL - 1983  
MAINTAINER: TAPE DIAGNOSTIC ENGINEERING  
AUTHOR: DICE SYSTEMS, INC.

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## ABSTRACT

## 1.0 ABSTRACT

THIS IS A PDP-11/LSI RESIDENT DIAGNOSTIC WHICH CHECKS THE FUNCTIONALITY OF A TUBO MAGTAPE SUBSYSTEM WHILE CONNECTED TO A PDP-11 SYSTEM. THE PROGRAM PROVIDES ERROR MESSAGES WHICH IDENTIFY FAILING FUNCTIONS THAT AID IN THE REPAIR OF THE DEVICE. REFERENCE THE FOLLOWING DIGITAL EQUIPMENT DOCUMENTS:

1. ENGINEERING SPECIFICATION FOR TUBO MAGTAPE CONTROLLER; DOCUMENT NUMBER: YM-C194D-022; REVISION NUMBER 2; DATE: 28-JUL-81.
2. ENGINEERING SPECIFICATION FOR TUBO DIAGNOSTIC PACKAGE; DOCUMENT NUMBER: YM-C194F-00; REVISION NUMBER 0; DATE: 2-SEP-81.
3. ENGINEERING SPECIFICATION FOR TUBO MAGTAPE SUBSYSTEM; DOCUMENT NUMBER: YM-C194S-02; REVISION NUMBER 3; DATE: 10-JUN-81.
4. CIQPMAD XXDP+ PROGRAMMER'S MANUAL; DOCUMENT NUMBER AC-S296A-AC; DATE: 14 JULY 1980.

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## HARDWARE, SOFTWARE REQUIREMENTS AND PREREQUISITES

### 2.0 HARDWARE, SOFTWARE REQUIREMENTS AND PREREQUISITES

#### 2.1 HARDWARE REQUIREMENTS

PDP-11 FAMILY PROCESSOR WITH 32K WORDS OF MEMORY  
TUBO MAGTAPE SUBSYSTEM (DRIVE AND CONTROLLER)  
CAUTION:DIAGNOSTIC REQUIRES 32K WORDS OF MEMORY  
(28K USEABLE I.E. 4K FOR I/O PAGE)

#### 2.2 OPTIONAL HARDWARE:

UP TO 8 TUBO CONTROLLERS PER PDP-11 UP TO 1 DRIVES PER CONTROLLER

#### 2.3 SOFTWARE REQUIREMENTS

PDP-11 DIAGNOSTIC SUPERVISOR (HSAADO.SYS)  
PDP-11 DIAGNOSTIC LOADER/MONITOR (XXDP+)

#### 2.4 PREREQUISITES

FUNCTIONAL PDP-11/LSI FAMILY CENTRAL PROCESSOR AND MEMORY  
FUNCTIONAL CONSOLE TERMINAL  
FUNCTIONAL STANDALONE DIAGNOSTIC SUPERVISOR

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## OPERATING INSTRUCTIONS-OPERATOR COMMANDS

### 3.0 OPERATING INSTRUCTIONS-OPERATOR COMMANDS

#### ----- 3.1 OPEKATOR COMMANDS -----

THE TUBO DIAGNOSTIC IS A PDP-11 DIAGNOSTIC SUPERVISOR COMPATIBLE PROGRAM. ALL LOADING AND RUN TIME INSTRUCTIONS CAN BE REFERENCED IN THE PDP-11 PROGRAMMER'S MANUAL "CIOPMAO XXDP" PROGRAMMER'S MANUAL NUMBER AC-S296A-AC.

BOOT THE DIAGNOSTIC XXDP+ MEDIA (OPERATOR RESPONSES ARE UNDERLINED)

CHMDLBO XXDP+ DL MONITOR 28K  
 BOOTED V/A UNIT 0

ENTER DATE (DD-MMM-YY): 29-JAN-82  
 RESTART ADDRESS: 153726  
 50 HZ? N <CR>

LSI? N Y  
 THIS IS XXDP+ TYPE 'H' OR 'H/L' FOR DETAILS

R CZTUWA

CZTUWABINDRS LOADED  
 DIAG. RUN TIME SERVICES REV D. APR 79  
 CZTUW-A-0  
 \*\*\*\*TUBO LOGIC DIAGNOSTIC\*\*\*\*  
 UNIT IS TUBO  
 DR>  
 DRS>START/FLAG:PNT:HOE

THE ABOVE COMMAND WILL START THE DIAGNOSTIC. THE COMMAND HAS TWO SWITCHES ON WHICH ARE 'PRINT EACH TEST NBR. AS EXECUTED' AND 'HALT ON ERROR'.

#### ----- 3.2 HARDWARE PARAMETERS -----

AFTER INITIAL STARTING OF THE PROGRAM (START COMMAND TO THE DIAGNOSTIC SUPERVISOR), THE PROGRAM WILL ISSUE THE "CHANGE HW?" QUESTION TO ASK IF THE HARDWARE PARAMETERS ARE TO BE CHANGED (BY THE OPERATOR).

ON A 'N' (NO) RESPONSE TO THE "CHANGE HW?" QUESTION, THE DIAGNOSTIC WILL NOT RUN. IT WILL GIVE THE MESSAGE 'NO UNIT'. A 'Y' (YES) IS REQUIRED, AND AT LEAST A '1' IS REQUIRED AT

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THE "#UNITS (D)?" QUESTION.

TSBA/TSDB=172520, VECTOR =224

ON A "Y" (YES) RESPONSE TO THE QUESTION, THE FOLLOWING QUESTIONS WILL THEN BE ASKED TO ALLOW THE OPERATOR TO SELECT THE UNITS TO BE TESTED. A VALUE, IF PRESENT, LOCATED TO THE LEFT OF THE QUESTION MARK IS THE DEFAULT VALUE THAT WILL BE TAKEN ONLY IF A CARRIAGE RETURN IS TYPED AS A RESPONSE. A "(D)" IN A QUESTION INDICATES THAT A DECIMAL NUMBER IS REQUIRED AS A RESPONSE. AN "(O)" INDICATES AN OCTAL NUMBER IS BEING SOLICITED. AN "(L)" THAT A LOGICAL RESPONSE IS TO BE MADE: "Y" FOR YES, "N" FOR NO.

# UNITS (D) ? < ENTER THE NUMBER OF M7454 CONTROLLERS  
 PRESENT TO BE TESTED>

UNIT 0

DEVICE ADDRESS (O) 172520 ? <ENTER THE ADDRESS OF THE  
 TSBA/TSDB REGISTER>

VECTOR (O) 224 ? <ENTER ADDRESS OF INTERRUPT  
 VECTOR>

THE ADDRESS AND VECTOR QUESTIONS WILL BE ASKED FOR EACH OF THE NUMBER OF UNITS (CONTROLLERS) SPECIFIED IN THE "#UNITS ?" QUESTION. LOGICAL UNIT NUMBERS ARE ASSIGNED IN ORDER BEGINNING AT 0. UP TO EIGHT UNITS CAN BE SELECTED FOR TESTING.

-----  
 3.3 SOFTWARE PARAMETERS  
 -----

THE FOLLOWING QUESTIONS ARE ASKED ON A START, RESTART, OR CONTINUE. THEY ALLOW FLEXIBILITY IN THE WAY THE PROGRAM BEHAVES.

CHANGE SW (L) ? < TYPE "Y" TO CAUSE THE FOLLOWING  
 QUESTIONS TO BE ASKED.>

INHIBIT ITERATIONS (L) N ? < TYPE "Y" TO PREVENT MULTIPLE  
 ITERATIONS OF CERTAIN TESTS.  
 THIS CAUSES EACH TEST PASS TO  
 RUN AS QUICKLY AS POSSIBLE.  
 ONLY QUICK-RUNNING LOGIC  
 TESTS USE MULTIPLE ITERATIONS.>

## OPERATING INSTRUCTIONS - SAMPLE PRINTOUTS

## 4.0 OPERATING INSTRUCTIONS - SAMPLE PRINTOUTS

## 4.1     EXAMPLE OF ALL TESTS RUN TOGETHER

TST: 001 INITIALIZE #1  
TST: 002 RAM TEST  
TST: 003 COMMAND REJECT TEST  
TST: 004 WRITE CHARACTERISTICS TEST  
TST: 005 VOLUME CHECK  
TST: 006 COMPLETION INTERRUPT TEST  
TST: 007 BASIC PACKET PROTOCOL TEST  
TST: 008 NON-TAPE MOTION COMMANDS TEST  
TST: 009 DMA MEMORY ADDRESSING TEST  
TST: 010 INITIALIZATION AFTER WRITE CHARACTERISTICS TEST  
TST: 011 BASIC WRITE SUBSYSTEM MEMORY TEST

0 ERRORS

NOTE: PROGRAM NOW STARTS OVER AGAIN AT TEST 1



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OPERATING INSTRUCTIONS - SAMPLE ERROR MESSAGES

5.0 OPERATING INSTRUCTIONS - SAMPLE ERROR MESSAGES

ERROR MESSAGE EXAMPLE 1

TST: 001 FIFO EXERCISER TEST  
 CZTUA HRD ERR 01610 ON UNIT 00 TST 016 SUB 000 PC: 040624  
 FIFO STATUS (IN WORD 9) INCORRECT AFTER WRITE FIFO

TAPE BUS SIGNALS IN WORD #8: - DESIGNATOR <BIT #>  
 PARERR<15> IEOT <12> IFMK <9> IRDY<6> IRWD<2>  
 IRESV2<14> IIDENT<11> IHER <8> IONL<5> IFBY<1>  
 IRESV1<13> ICER <10> ISPEED<7> ILDP<4> IFPT<0>

TAPE BUS SIGNALS IN WORD #9:  
 DATMIS<7> ILW<6> OUTRDY<5> INRDY<4>

MESSAGE BUFFER ADDRESS = 047352

MESSAGE BUFFER CONTENTS:

WORD #0	EXPD: 100020	RECV: 100020	XOR: 000000
WORD #1	EXPD: 000012	RECV: 000012	XOR: 000000
WORD #2	EXPD: 000000	RECV: 000000	XOR: 000000
WORD #3	EXPD: 000010	RECV: 000010	XOR: 000000
WORD #4	EXPD: 000000	RECV: 000000	XOR: 000000
WORD #5	EXPD: 000000	RECV: 000000	XOR: 000000
WORD #6	EXPD: 000000	RECV: 000000	XOR: 000000
WORD #7	EXPD: 000000	RECV: 000000	XOR: 000000
WORD #8	EXPD: 070217	RECV: 070217	XOR: 000000
WORD #9	EXPD: 000074	RECV: 000034	XOR: 000040

ERROR MESSAGE EXAMPLE 2

CZTUWA HRD ERR 00159 ON UNIT 00 TST 001 SUB 005 PC: 026202  
 TSSR NOT CORRECT AFTER SPACE RECORDS COMMAND

TSSR = 100214

TSSR BITS SET: SC,SSR

TERMINATION CLASS CODE = UNRECOVERABLE ERROR

PACKET ADDRESS = 026420

PACKET WORD # = 140010

PACKET WORD # = 000010

PACKET WORD # = 000000

PACKET WORD # = 000024

ERROR MESSAGE EXAMPLE 3

CZTUWA HRD ERR 00121 ON UNIT 00 TST 001 SUB 002 PC: 023306  
 NOT BIT (XST0) NOT SET DURING REWIND (EXTENDED FEATURES MODE)  
 EXPD: 000312 RECV: 000112 XOR: 000200

PROGRAM RUN TIMES

### 6.0 PROGRAM RUN TIMES

THE AVERAGE RUN TIMES OF THE PROGRAM ARE LISTED BELOW. THESE FIGURES ARE TO BE USED AS A GUIDE. THE TIMING WAS DONE ON A PDP-11/23 (LSI) PROCESSOR WITH A LA-34 CONSOLE.

THE PROGRAM RUNS IN TWO MODES: NO ITERATIONS AND DEFAULT MODE. IN THE NO ITERATIONS MODE, EACH TEST IS RUN ONCE, WITH NO ITERATIONS. IN THE DEFAULT MODE EACH TEST IS REPEATED BY THE NUMBER OF TIMES INDICATED BY THE ITERATION COUNT. NO ITERATIONS MODE IS SELECTED BY ANSWERING THE INHIBIT ITERATIONS QUESTION WITH A "Y" (YES).

TEST NUMBER	N/I SECS.	ITER SECS	DEF SECS.
1	1	30	29
2	25	120	95
3	25	475	450
4	20	20	0
5	1	10	9
6	20	20	0
7	2	1	1
8	8	11	3
9	1	1	0
10	1	1	0
11	1	1	0

THE TIMES REQUIRED TO RUN TESTS 1 THROUGH 12 IN ONE COMMAND:

Q.V.        1 MIN 57 SECONDS  
DEFAULT    12 MINS

7.0 TEST DESCRIPTIONS

7.1 TEST 1 - INITIALIZATION TEST #1

\*\*\*\*\*  
\* NOTE: IF THIS TEST DETECTS AN ERROR REPLACE THE TUBO'S \*  
\* CONTROLLER (M7454) \*  
\*\*\*\*\*

THIS TEST VERIFIES THAT THE M7454 MODULE'S DEVICE REGISTERS ARE ACCESSIBLE ON THE BUS (SUBTEST 1) AND THEN CHECKS THAT THE BUILT-IN INITIALIZATION SELF-TEST MICRODIAGNOSTIC DID NOT FIND ANY BASIC PROBLEMS WITH THE MODULE. AREAS OF LOGIC TESTED BY THE SELF-TEST SEQUENCER ARE AS FOLLOWS: ROM AND PIPELINE REGISTER, SEQUENCER, INTERNAL BUSES, 2901 MICROPROCESSOR, RAM AND TRANSPORT STATUS FLOPS. THIS TEST INITIALIZES THE CONTROLLER BY ISSUING THE BUS INIT SIGNAL VIA A RESET INSTRUCTION, OR BY WRITING INTO THE TSSR REGISTER, AND THEN CHECKS THE CONTENTS OF THE TSSR REGISTER. SUCCESSFUL INITIALIZATION IS INDICATED BY SUBSYSTEM READY (SSR) AND NEED BUFFER ADDRESS (NBA) BITS BEING SET (1) AND ALL OTHER BITS (EXCEPT A17, A16, AND OFL, WHICH ARE IGNORE FOR THIS TEST) BEING CLEARED (0). IF THE CONTENTS OF THE TSSR ARE NOT AS EXPECTED, AN ERROR REPORT IS ISSUED LISTING THE EXPECTED DATA, ACTUAL DATA, AND THE DISCREPANCIES. SUBTEST RUN FORCES FORCES SELF-TEST TO RUN BY DOING A BYTE WRITE TO THE TSSR.

7.2 TEST 2 - RAM TEST

\*\*\*\*\*  
\* NOTE: IF THIS TEST DETECTS AN ERROR REPLACE THE TUBO'S \*  
\* CONTROLLER (M7454) \*  
\*\*\*\*\*

THIS TEST VERIFIES THAT ALL LOCATIONS OF THE RAM ON THE M7454 CAN PROPERLY STORE AND READ BACK ALL DATA PATTERNS, AND THAT EACH RAM LOCATION IS UNIQUELY ADDRESSED (IE: THAT ONE AND ONLY ONE LOCATION IS ACCESSED BY ANY PARTICULAR ADDRESS). THESE TESTS ARE PERFORMED BY THREE SUBTESTS DESCRIBED BELOW.

7.2.1 TEST 2, SUBTEST 1:-

THIS SUBTEST VERIFIES EACH LOCATION BY PERFORMING THE FOLLOWING SEQUENCE FOR EACH ADDRESS 0-377 (OCTAL):

1. THE ADDRESS TO BE TESTED IS LOADED INTO THE TSDB+1 (VIA A HI-WRITE BYTE).
2. THE ADDRESSED RAM LOCATION IS READ, THEN WRITTEN INTO THE LOW BYTE OF THE TSBA. THE LOW BYTE OF TSDB.

- 3. THE LOW BYTE OF THE TSBA IS CHECKED TO SEE IF IT CONTAINS THE DATA PATTERN ORIGINALLY WRITTEN; A DISCREPANCY IS REPORTED AS AN ERROR.
- 4. THE ADDRESS OF THE LOCATION BEING TESTED IS AGAIN WRITTEN INTO TSDB+1 (HI-BYTE WRITE), TO CAUSE THE LOCATION UNDER TEST TO AGAIN BE READ INTO THE LOW BYTE OF TSBA. THE LOW BYTE OF TSBA IS AGAIN CHECKED AND DISCREPANCIES REPORTED.

7.2.2 TEST 2, SUBTEST 2:-

THIS SUBTEST USES THE SAME RAM READ/WRITE TECHNIQUES AS SUBTEST 1, EXCEPT THAT MEMORY IS FILLED WITH ZEROS AND A ONES WORD IS 'WALKED' DOWN THROUGH. PRIOR TO THE ALL ONES WRITE TO MEMORY THE MEMORY IS CHECKED TO BE SURE THAT THE ZERO WORD HASN'T 'PICKED' A BIT.

7.2.3 TEST 2, SUBTEST 3:-

THIS SUBTEST IS SIMILAR TO SUBTEST 2, EXCEPT THAT MEMORY IS FIRST SET TO ALL ONES AND A BYE OF ZEROS IS 'WALKED' DOWN THROUGH MEMORY BEGINNING AT LOCATION TOP-2.

7.3 TEST 3: COMMAND REJECT

\*\*\*\*\*  
 \* NOTE: IF THIS TEST DETECTS AN ERROR REPLACE THE TUBO'S \*  
 \* CONTROLLER (M7454) \*  
 \*\*\*\*\*

THIS TEST VERIFIES THAT ALL COMMANDS OTHER THAN WRITE CHARACTERISTICA ARE REJECTED DUE TO THE NEED BUFFER ADDRESS (NBA) BIT BEING SET IN TSSR, AND THAT THE TSBA, AND THE TSSR REGISTERS ARE SET IN THE PROPER STATE AFTER EACH COMMAND IS REJECTED. THIS TEST CHECKS THE MICROPROCESSOR SEQUENCING, BASIC COMMAND DECODING, AND DATA DMA HANDLING. THE TEST CONTAINS TWO SUBTESTS: SUBTEST 1 SEQUENCES THROUGH ALL COMMAND WORDS (OTHER THAN WRITE CHARACTERISTICS) WITH THE INTERRUPT ENABLE BIT (IE) BIT CLEAR AND VERIFIES THAT AN INTERRUPT IS NOT GENERATED BY THE REJECTED COMMAND; SUBTEST TWO PERFORMS SIMILARLY TO SUBTEST 1 BUT SETS THE IE BIT IN EACH COMMAND WORD AND VERIFIES THAT AN INTERRUPT IS GENERATED WHEN THE COMMAND IS REJECTED. THE SUBTEST 1 SETS UP THE INTERRUPT SERVICE ROUTINE TO FLAG UNEXPECTED INTERRUPTS. THE COMMAND WORD IN THE COMMAND BUFFER IS INITIALIZED TO 10000 (OCTAL) AND THE REMAINING THREE WORDS IN THE COMMAND BUFFER ARE SET TO KNOWN UNIQUE PATTERNS. THEN THE FOLLOWING SEQUENCE IS PERFORMED:

- 1. INITIALIZE THE CONTROLLER BY WRITING INTO THE TSSR, PROPER INITIAL CONDITIONS ARE VERIFIED.

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2. TSDB IS WRITTEN WITH ADDRESS OF THE COMMAND BUFFER TO START PROCESSING.
3. THE PROGRAM WAITS FOR SSR TO SET; IF SSR DOES NOT SET, AN ERROR REPORT IS ISSUED AND THE TEST IS ABORTED.
4. THE CONTENTS OF THE TSSR ARE CHECKED. TSSR IS CORRECT IF IT CONTAINS EITHER OCTAL 102206 OR 102306 (BIT 6 DEPENDS ON THE STATE OF THE TAPE TRANSPORT).
5. THE CONTENTS OF TSBA ARE CHECKED. TSBA SHOULD CONTAIN THE INITIAL COMMAND BUFFER ADDRESS (LOADED IN STEP 2) IE: TSBA SHOULD POINT TO THE COMMAND PACKET.
6. USING THE MAINTANENCE MODE WRAP AROUND FUNCTIONS, THE COMMAND IMAGE BLOCK IN THE M7454'S RAM (LOCATIONS 20 - 27 (OCTAL)) ARE CHECKED; THE IMAGE SHOULD CONTAIN A COPY OF THE FOUR COMMAND PACKET WORDS AS SET UP IN CPU MEMORY.
7. THE COMMAND WORD IN THE COMMAND BUFFER IS INCREMENTED TO THE NEXT PATTERN NOT CONTAINING WRITE CHARACTERISTICS; OR IE. THE REMAINING THREE WORDS OF THE COMMAND BUFFER ARE SEQUENCED WITH PSEUDO-RANDOM DATA. IF THE COMMAND WORD HAS NOT REACHED ITS MAXIMUM VALUE (17777+1) THE TEST SEQUENCE IS REPEATED.

SUBTEST 2 IS IDENTICAL TO SUBTEST 1, EXCEPT THAT THE PROGRAM CAUSES THE IE BIT TO BE SET IN EACH COMMAND WORD AND THEN VERIFIES THAT AN INTERRUPT OCCURS.;

#### 7.4 TEST 4 - WRITE CHARACTERISTICS

\*\*\*\*\*  
\* NOTE: IF THIS TEST DETECTS A FAILURE REPLACE THE TUBO'S \*  
\* CONTROLLER (M7454) \*  
\*\*\*\*\*

THIS TEST VERIFIES BASIC OPERATION OF THE WRITE CHARACTERISTICS COMMAND. IT VERIFIES THAT THE COMMAND BLOCK AND CHARACTERISTICS DATA BLOCK ARE FETCHED PROPERLY FROM CPU MEMORY, THE NEED BUFFER ADDRESS (NBA) BIT IN THE TSSR IS HANDLED PROPERLY, AND THAT A PROPER MESSAGE PACKET IS STORED, WHERE APPROPRIATE. THIS TEST DOES NOT CHECK THAT THE VARIOUS FUNCTIONS ENABLED BY CHARACTERISTICS MODE DATA BITS OPERATE PROPERLY; THE FUNCTIONING OF THESE BITS IS VERIFIED IN SUBSEQUENT TESTS. ALL COMMANDS EXECUTED IN THIS TEST HAVE THE INTERRUPT ENABLE (IE) BIT CLEARED TO ZERO, SO NO INTERRUPTS SHOULD BE GENERATED. HOWEVER, THE PROGRAM RUNS AT PROCEESOR PRIORITY ZERO, WITH THE INTERRUPT SERVICE ROUTINE SET UP TO FLAG UNEXPECTED INTERRUPTS. IF AN INTERRUPT

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OCCURS A PROBLEM EXISTS IN EITHER THE LSI-11 BUS INTERFACE SECTION  
OR IN THE ROM OR PIPELINE.

THIS TESTS VARIOUS MICROPROGRAM SEQUENCES, COMMAND DECODING,  
DMA LOGIC, AND BASIC PACKET PROTOCOL HANDLING. THIS IS THE  
FIRST TEST IN WHICH DATA DMA CYCLES (FOR STORING THE MESSAGE  
PACKET) ARE PERFORMED. ANY ERRORS IN THE BODY OF THE TEST  
(IE: ERRORS OTHER THAN INITIALIZATION ERRORS RELATED TO THE  
TRANSPORT BUS) DEFINITELY INDICATE A BAD M7454 MODULE.

7.4.1 TEST 4, SUBTEST 1:-

VERIFIES BASIC STANDARD OPERATION (USING PROPER MESSAGE BUFFER AND LENGTH DATA IN AN INCREMENTING SERIES OF VALUES FOR THE FOURTH CHARACTERISTICS DATA IN THE CHARACTERISTICS DATA BLOCK.). AFTER THE COMMAND IS EXECUTED FOR EACH VALUE OF THE FOURTH CHARACTERISTICS DATA WORD, THE PROGRAM VERIFIES THAT:

1. THE TSSR IS CORRECT, INCLUDING A CHECK THAT THE NBA BIT IS CLEARED AND THAT NORMAL TERMINATION WAS ACCOMPLISHED.;
2. THAT A PROPER MESSAGE PACKET IS STORED.
3. THAT THE COMMAND PACKET CHARACTERISTIC DATA, AND MESSAGE PACKET IMAGE BLOCKS IN M7454 RAM ARE CORRECT.

7.4.2 TEST 4, SUBTEST 2:-

VERIFIES THAT THE COMMAND IS REJECTED AND THAT THE NBA BIT DOES NOT GET CLEARED IF NONZERO BITS ARE SET INTO ANY RESERVED OR UNUSED FIELD WITHIN THE FIRST THREE COMMAND PACKET WORDS.

7.4.3 TEST 4, SUBTEST 3:-

VERIFIES THAT THE COMMAND IS REJECTED AND THAT THE NBA BIT DOES NOT GET CLEARED IF THE MESSAGE BUFFER ADDRESS SPECIFIED IN THE CHARACTERISTICS DATA BLOCK DOES NOT SPECIFY A LEGAL

7.5 TEST 5: VOLUME CHECK

\*\*\*\*\*  
\* NOTE: IF THIS TEST DETECTS AN ERROR REPLACE THE TUBO'S \*  
\* CONTROLLER (M7454) \*  
\*\*\*\*\*

THIS TEST VERIFIES THAT THE VOLUME CHECK (VCK) BIT, A FLAG HELD WITHIN THE M7454 AND APPEARING IN XST0, IS SET BY INITIALIZE AND CLEARED BY EXECUTING A WRITE CHARACTERISTICS COMMAND WITH THE CVC SET. IT IS ALSO VERIFIED THAT A WRITE CHARACTERISTICS WITH THE CVC BIT CLEAR DOES NOT AFFECT THE STATE OF THE VOLUME CHECK BIT. THE ACTUAL FUNCTION OF VOLUME CHECK, THAT OF PREVENTING OR ALLOWING A TAPE MOTIN COMMAND DEPENDING ON WHETHER VOLUME CHECK IS SET OR CLEAR, IS NOT CHECKED BY THIS TEST; THIS FUNCTIONALITY IS CHECKED IN THE INDIVIDUAL TESTS OF TAPE MOTION COMMANDS.

THE TEST PROCEEDS AS FOLLOWS:

1. THE CONTROLLER IS INITIALIZED BY WRITING INTO THE TSSR.
2. A WRITE CHARACTERISTICS COMMAND IS ISSUED (WITH CVC=0)
3. THE PREVIOUS STEP IS REPEATED TO VERIFY THAT VCK DOES NOT CHANGE (REMAINS AT 0).

- 4. A WRITE CHARACTERISTICS COMMAND IS ISSUED WITH CVC=1 AND THE VCK BIT IN XSTO IN THE MESSAGE BUFFER IS EXAMINED; THE VCK BIT SHOULD BE CLEAR (0).
- 5. A WRITE CHARACTERISTICS COMMAND IS ISSUED WITH CVC=0 AND THE VCK BIT IN XSTO IN THE MESSAGE BUFFER IS EXAMINED; THE VCK BIT SHOULD REMAIN CLEAR (0). WHICH SHOULD CAUSE RAM LOCATION 0 TO BE WRITTEN TO ALL 1'S SINCE 2901 REGISTERS 10 AND 11, SPECIFYING THE RAM ADDRESS, SHOULD BE 0. RAM LOCATION IS VERIFIED BY LOW BYTE OF TSBA SHOULD CONTAIN ALL 1'S.
- 4. THE ENTIRE RAM IS SCANNED. LOCATION 0 SHOULD CONTAIN ALL 1'S AND THE REMAINING LOCATIONS, EXCEPT FOR THE MESSAGE BUFFER IMAGE AREA, SHOULD CONTAIN 0. DISCREPANCIES ARE REPORTED. AN ERROR AT THIS POINT IS MOST LIKELY DUE TO A ROM, PIPELINE OR SEQUENCER PROBLEM OR A TIMING PROBLEM.

7.6 TEST 6 - COMPLETION INTERRUPT

\*\*\*\*\*  
 \* NOTE: IF THIS TEST DETECTS AN ERROR REPLACE THE TUBO'S \*  
 \* CONTROLLER (M7454) \*  
 \*\*\*\*\*

THIS TEST VERIFIES THAT AN INTERRUPT IS GENERATED AT THE COMPLETION OF THE WRITE CHARACTERISTICS COMMAND IF THE INTERRUPT ENABLE (IE) BIT IN THE COMMAND HEADER WORD IS SET. THIS TEST CHECKS THE FUNCTIONING OF THE INTERRUPT LOGIC AND BASIC PROCESSING OF THE IE BIT.

THE SEQUENCES OF TEST 7 ARE REPEATED, EXCEPT THAT THE INTERRUPT SERVICE ROUTINE IS SET UP TO EXPECT INTERRUPTS AND EACH WRITE CHARACTERISTICS COMMAND IS ISSUED WITH THE IE BIT SET (1). IT IS VERIFIED, WHERE APPROPRIATE, THAT THE STATUS BIT IN XSTO OF ANY MESSAGE PACKET IS SET AND THAT A COMPLETION INTERRUPT IS GENERATED. FINALLY A SEQUENCE OF TWO COMMANDS ARE ISSUED, THE FIRST WITH IE=1, AND THE SECOND WITH IE=0. IT IS VERIFIED THAT NO INTERRUPT IS GENERATED AFTER THE SECOND COMMAND AND THAT THE IE BIT IN XSTO IS 0.

7.7 TEST 7 - BASIC PACKET PROTOCOL

\*\*\*\*\*  
 \* NOTE: IF THIS TEST DETECTS AN ERROR REPLACE THE TUBO'S \*  
 \* CONTROLLER (M7454) \*  
 \*\*\*\*\*

THIS TEST VERIFIES BASIC OPERATION OF THE MESSAGE BUFFER RELEASE COMMAND, THE FUNCTION OF THE ACK BIT IN THE COMMAND HEADER WORD, AND THE REGISTER MODIFICATION REFUSED (RMR) LOGIC.



7.7.1 TEST 7, SUBTEST 1:-

VERIFIES THAT THE BASIC MESSAGE BUFFER RELEASE COMMAND OPERATES PROPERLY WHEN MESSAGE BUFFER RELEASE INTERRUPTS ARE DISABLED (ERI=0 ON PREVIOUS WRITE CHARACTERISTICS COMMAND). CHECKS THAT TSSR IS UPDATED PROPERLY AND THAT NO INTERRUPT IS GENERATED (EVEN IF THE IE BIT IN THE COMMAND WORD IS SET) AND THAT NO MESSAGE PACKET IS STORED.

7.7.2 TEST 7, SUBTEST 2:-

VERIFIES THAT THE BASIC MESSAGE BUFFER RELEASE COMMAND OPERATES PROPERLY WHEN MESSAGE BUFFER RELEASE INTERRUPTS ARE ENABLED (ERI=1 ON PREVIOUS WRITE CHARACTERISTICS COMMAND). CHECKS THAT TSSR IS UPDATED PROPERLY AND THAT AN INTERRUPT IS GENERATED (IF THE IE BIT IN THE COMMAND WORD IS SET) BUT THAT NO MESSAGE PACKET IS STORED.

7.7.3 TEST 7, SUBTEST 3:-

VERIFIES THAT AFTER THE CPU GIVES UP OWNERSHIP OF A MESSAGE BUFFER (VIA THE MESSAGE BUFFER RELEASE COMMAND), THAT A NEW COMMAND (E.G., WRITE CHARACTERISTICS) IS PROPERLY EXECUTED WHEN ISSUED WITH THE ACK BIT IN THE COMMAND HEADER EITHER SET OR CLEAR.

7.7.4 TEST 7, SUBTEST 4:-

VERIFIES THAT THE REGISTER VERIFICATION REFUSED (RMR) BIT IN TSSR OPERATES PROPERLY WHEN A COMMAND (WRITE CHARACTERISTICS) IS BEING EXECUTED. THE PROGRAM ISSUES THE WRITE CHARACTERISTICS COMMAND (FROM ONE COMMAND BUFFER) THEN IMMEDIATELY WRITES THE ADDRESS OF ANOTHER COMMAND BUFFER (CONTAINING ANOTHER WRITE CHARACTERISTICS COMMAND, BUT WITH THIS ONE SPECIFYING DIFFERENT CHARACTERISTICS DATA). WHEN SSR SETS, THE PROGRAM VERIFIES THAT THAT THE FIRST COMMAND COMPLETED PROPERLY, THAT RMR IS SET, AND THAT THE SECOND COMMAND IS IGNORED.:

7.8 TEST 8 - NON-TAPE MOTION COMMANDS

\*\*\*\*\*  
\* NOTE: IF THIS TEST DETECTS AN ERROR REPLACE THE TUBO'S \*  
\* CONTROLLER (M7454) \*  
\*\*\*\*\*

THIS TEST VERIFIES PROPER OPERATION OF THE GET STATUS AND INITIALIZE COMMANDS. THREE SUBTESTS ARE USED. THE FIRST TWO VERIFY THAT THE RESPECTIVE COMMANDS RU TO COMPLETION AND STORE A VALID MESSAGE PACKET.

TEST 9: COMPLETION INTERRUPT

THIS TEST VERIFIES THAT AN INTERRUPT IS GENERATED AT THE COMPLETION OF THE WRITE CHARACTERISTICS COMMAND IF THE INTERRUPT ENABLE (IE) BIT IN THE COMMAND HEADER WORD IS SET. THIS TEST

CHECKS THE FUNCTIONING OF THE INTERRUPT LOGIC AND BASIC PROCESSING OF THE IE BIT.

THE SEQUENCES OF TEST 7 ARE REPEATED, EXCEPT THAT THE INTERRUPT SERVICE ROUTINE IS SET UP TO EXPECT INTERRUPTS AND EACH WRITE CHARACTERISTICS COMMAND IS ISSUED WITH THE IE BIT SET (1). IT IS VERIFIED, WHERE APPROPRIATE, THAT THE IE STATUS BIT IN XSTO OF ANY MESSAGE PACKET IS SET AND THAT A COMPLETION INTERRUPT IS GENERATED. FINALLY A SEQUENCE OF TWO COMMANDS ARE ISSUED, THE FIRST WITH IE=1 AND THE SECOND WITH IE=0. IT IS VERIFIED THAT NO INTERRUPT IS GENERATED AFTER THE SECOND COMMAND AND THAT THE IE BIT IN XSTO IS 0.

7.9 TEST 9 - MEMORY ADDRESSING TEST

\*\*\*\*\*  
\* NOTE: IF THIS TEST DETECTS AN ERROR REPLACE THE TUBO'S \*  
\* CONTROLLER (M7454) \*  
\*\*\*\*\*

THIS TEST VERIFIES THAT THE CONTROLLER CAN PROPERLY ADDRESS AND ACCESS ALL AVAILABLE CPU MEMORY ( OTHER THAN THAT OCCUPIED BY THE DIAGNOSTIC AND THE DIAGNOSTIC SUPERVISOR CODE) FOR BOTH READING (DATI) AND WRITING (DATO). VERIFIED ARE THE PDP-11 BUS DRIVERS FOR ALL AVAILABLE ADDRESS LINES. UP TO THIS POINT ONLY 16 BITS HAVE BEEN USED FOR DMA TRANSFERS.

7.9.1. TEST 9, SUBTEST 1:-

THIS SUBTEST VERIFIES THE CONTROLLER CAN FETCH A GET STATUS COMMAND FROM ALL AVILABLE MEMORY LOCATIONS. TWO WORD BLOCKS ARE TESTED ONE AT A TIME BY FIRST SETTING ALL AVAILABLE MEMORY TO A BACKGROUND PATTERN OF 125252. A GET STATUS COMMAND IS THEN EXECUTED TO VARIOUS ADDRESSES IN EACH AVAILABLE MEMORY 4K BLOCK. THE VARIOUS ADDRESSES ARE DETERMINED BY FLOATING FIRST A 1 THEN A 0 THROUGH THE ADDRESS BITS.

7.9.2 TEST 9, SUBTEST 2:-

THIS SUBTEST VERIFIES THE CONTROLLER CAN DEPOSIT MESSAGE PACKETS TO ALL AVAILABLE MEMORY LOCATIONS. FIRST ALL AVAILABLE MEMORY IS SET TO A BACKGROUND PATTERN OF 125252. WRITE CHARACTERISTICS COMMANDS ARE THEN EXECUTED WITH MESSAGE BUFFER ADDRESSES SET TO VARIOUS ADDRESSES IN EACH AVAILABLE MEMORY LOCATION. THE VARIOUS ADDRESSES ARE DETERMINED BY FLOATING FIRST A 1 THEN A 0 THROUGH THE ADDRESS BITS.

7.9.3 TEST 9, SUBTEST 3:-

THIS SUBTEST VERIFIES THAT A CONTROLLER CAN FETCH A WRITE CHARACTERISTICS DATA BLOCK FROM ALL AVAILABLE MEMORY LOCATIONS. FIRST ALL AVAILABLE MEMORY IS SET TO A BACKGROUND PATTERN OF 125252. THE WRITE CHARACTERISTICS COMMANDS ARE EXECUTED WITH CHARACTERISTIC DATA BLOCKS AT VARIOUS MEMORY ADDRESSES. THE VARIOUS MEMORY ADDRESSES ARE DETERMINED BY FLOATING FIRST A 1 THEN A 0 THROUGH THE ADDRESS BITS.

7.9.4 TEST 9, SUBTEST 4:-

THIS SUBTEST VERIFIES THE NXM ERROR BIT IN THE TSSR REGISTER IS SET WHEN ATTEMPTING TO FETCH DATA ( A CHARACTERISTIC DATA BLOCK) FROM SELECTED NONEXISTANT LOCATIONS. IF NXM FAILS TO SET IT IS LIKELY THAT AN LSI-11 BUS DRIVER IS FAILING TO ASSERT AN ADDRESS LINE. ADDRESSES TESTED INCLUDE ALL COMBINATIONS OF HIGH ORDER ADDRESS BITS (IE: BITS 16-21).

7.10 TEST 10 - INITIALIZE #3

\*\*\*\*\*  
\* NOTE: IF THIS TEST DETECTS AN ERROR REPLACE THE TUBO'S \*  
\* CONTROLLER (M7454) \*  
\*\*\*\*\*

THIS TEST VERIFIES THAT A HARDWARE INITIALIZE COMMAND INVOKED AFTER A WRITE CHARACTERISTICS COMMAND SETS UP THE COMMAND, MESSAGE AND CHARACTERISTIC IMAGE BLOCK IN THE CONTROLLER RAM CORRECTLY.

7.11 TEST 11 - BASIC WRITE SUBSYSTEM MEMORY COMMAND

\*\*\*\*\*  
\* NOTE: IF THIS TEST DETECTS AN ERROR REPLACE THE TUBO'S \*  
\* CONTROLLER (M7454) \*  
\*\*\*\*\*

THIS TEST VERIFIES THAT THE WRITE SUBSYSTEM MEMORY COMMAND WITH A BSEL0 SELECT CODE OF 0 (NO-OP) EXECUTES CORRECTLY. THE TEST FURTHER VERIFIES MICROPROGRAM COMMAND DECODING AND HANDLING SEQUENCES.

CZTUWAO TUBO FRONT END PRT A MACRO M1200 29-MAR-83 13:24 PAGE 11  
PROGRAM HEADER

```

860      .SBTTL PROGRAM HEADER
866      .MCALL SVC
867 000000 SVC ; INITIALIZE SUPERVISOR MACROS
868      .ENABLE LC
869      .LIST BEX,CND
875 000000 .ENABL AMA,ABS
876 002000 002000 . = 2000
877 002000 BGNMOD TUV2A
      002000 TUV2A::
878
879      :++
880      : THE PROGRAM HEADER IS THE INTERFACE BETWEEN
881      : THE DIAGNOSTIC PROGRAM AND THE SUPERVISOR.
882      :--
883
884
885 002000      POINTER BGNSW,BGNSFT,BGNAU,BGNDU,BGNRPT,BGNSETUP
886 002000      HEADER CZTUW,A,0,655,,0
      LSNAME:: ;DIAGNOSTIC NAME
      002000      .ASCII /C/
      002001      .ASCII /Z/
      002002      .ASCII /T/
      002003      .ASCII /U/
      002004      .ASCII /W/
      002005      .BYTE 0
      002006      .BYTE 0
      002007      .BYTE 0
      LSREV:: ;REVISION LEVEL
      002010      .ASCII /A/
      002011      .ASCII /0/
      LSDEPO:: ;0
      002011      060
      LSUNIT:: ;NUMBER OF UNITS
      002012      .WORD TSPTHV
      002012 000001 ;LONGEST TEST TIME
      002014      .WORD 655.
      002014 001217 ;POINTER TO H.W. QUES.
      LSHPCP:: ;POINTER TO S.W. QUES.
      002016      .WORD LSHARD
      002016 052640 ;PTR. TO DEF. H.W. PTABLE
      LSSPCP:: ;PTR. TO S.W. PTABLE
      002020      .WORD LSSOFT
      002020 053000 ;DIAG. END ADDRESS
      LSHPTP:: ;RESERVED FOR APT STATS
      002022      .WORD LSHW
      002022 002124 ;DIAGNOSTIC TYPE
      002024      .WORD LSSW
      002024 002134 ;APT EXPANSION
      002026      .WORD LSLAST
      002026 053220 ;PTR. TO DISPATCH TABLE
      L$STA:: ;DIAGNOSTIC RUN PRIORITY
      002030      .WORD 0
      002030 000000
      LSCO:: ;
      002032      .WORD 0
      002032 000000
      LSDTYP:: ;
      002034      .WORD 0
      002034 000000
      LSAPT:: ;
      002036      .WORD 0
      002036 000000
      LSDTP:: ;
      002040      .WORD L$DISPATCH
      002040 053166
      L$PRIO:: ;
      002042      .WORD 0
      002042 000000

```

CZTUWAO TUBO FRONT END PRT A  
PROGRAM HEADER

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002044		LSENV1::		:FLAGS DESCRIBE HOW IT WAS SETUP
002044	000000	.WORD	0	
002046		LSEXP1::		:EXPANSION WORD
002046	000000	.WORD	0	
002050		LSMREV::		:SVC REV AND EDIT #
002050	003	.BYTE	CSREVISION	
002051	003	.BYTE	CSREDIT	
002052		LSEF::		:DIAG. EVENT FLAGS
002052	000000	.WORD	0	
002054	000000	.WORD	0	
002056		LSSPC::		
002056	000000	.WORD	0	
002060		LSDEVP::		: POINTER TO DEVICE TYPE LIST
002060	003334	.WORD	LSDVTYP	
002062		LSREPP::		:PTR. TO REPORT CODE
002062	022702	.WORD	LSRPT	
002064		LSEXP4::		
002064	000000	.WORD	0	
002066		LSEXP5::		
002066	000000	.WORD	0	
002070		LSAUT::		:PTR. TO ADD UNIT CODE
002070	022400	.WORD	LSAU	
002072		LSDUT::		:PTR. TO DROP UNIT CODE
002072	022476	.WORD	LSDU	
002074		LSLUN::		:LUN FOR EXERCISERS TO FILL
002074	000000	.WORD	0	
002076		LSDESP::		:POINTER TO DIAG. DESCRIPTION
002076	003342	.WORD	LSDESC	
002100		LSLOAD::		:GENERATE SPECIAL AUTOLOAD EMT
002100	104035	EMT	ESLOAD	
002102		LSETP::		:POINTER TO ERRtbl
002102	000000	.WORD	0	
002104		LSICP::		:PTR. TO INIT CODE
002104	021602	.WORD	LSINIT	
002106		LSCCP::		:PTR. TO CLEAN-UP CODE
002106	022660	.WORD	LSCLEAN	
002110		LSACP::		:PTR. TO AUTO CODE
002110	022604	.WORD	LSAUTO	
002112		LSPRT::		:PTR. TO PROTECT TABLE
002112	021572	.WORD	LSPROT	
002114		LSTEST::		:TEST NUMBER
002114	000000	.WORD	0	
002116		LSDLY::		:DELAY COUNT
002116	000000	.WORD	0	
002120		LSHIME::		:PTR. TO HIGH HEM
002120	000000	.WORD	0	

CZTUWAO TUBO FRONT END PRT A  
DEFAULT HARDWARE P-TABLE

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888  
 889  
 890  
 891  
 892  
 893  
 894 002122  
       002122 000003  
       002124  
       002124  
 895  
 896 002124 172522  
 897 002126 000224  
 898 002130 000240  
 899 002132  
       002132  
 900  
 901  
 902  
 903  
 904  
 905  
 906 002132  
       002132 000004  
       002134  
       002134  
 907  
 908 002134 000000  
 909 002136 000000  
 910  
 911  
 912 002140 000031  
 913 002142 000310  
 914 002144  
       002144  
 915

.SBTTL DEFAULT HARDWARE P-TABLE

```

:++
: THE DEFAULT HARDWARE P-TABLE CONTAINS DEFAULT VALUES OF
: THE TEST-DEVICE PARAMETERS. THE STRUCTURE OF THIS TABLE
: IS IDENTICAL TO THE STRUCTURE OF THE RUN-TIME P-TABLE.
:--

```

```

      .BGNDW  DFPTBL      ;DEFAULT HARD-P-TABLE
      .WORD  L10000-L$HW/2
L$HW::
DFPTBL::
      .WORD  172522      ; 2ND (OF 2) REGISTERS.
      .WORD  224        ; INTERRUPT VECTOR
      .WORD  PRI05      ; INTERRUPT PRIORITY.
      ENDHW

```

L10000:

.SBTTL SOFTWARE P-TABLE

```

:++
: THE SOFTWARE P-TABLE CONTAINS THE VALUES OF THE PROGRAM
: PARAMETERS THAT CAN BE CHANGED BY THE OPERATOR.
:--

```

```

      .BGNSW  SFPTBL
      .WORD  L10001-L$SW/2
L$SW::
SFPTBL::
      TRANSTST:: .WORD  0      ;ENABLE RAM DUMP IF =1
      NOITS::   .WORD  0      ; INHIBIT ITERATION OPTION.
      ; ... 0 = ITERATE.
      ; ...NZ = INHIBIT ITERATE.
      LERRMAX:: .WORD  25.    ; LOCAL (PER TEST) ERROR LIMIT
      GERRMAX:: .WORD  200.   ; GLOBAL (PER UNIT) ERROR LIMIT
      ENDSW

```

L10001:

917  
924  
929  
935  
936  
937  
938  
939  
940  
941  
942  
943  
944  
948 002144

.SBTTL GLOBAL EQUATES SECTION

.SBTTL GLOBAL EQUATES SECTION

:+  
: THE GLOBAL EQUATES SECTION CONTAINS PROGRAM EQUATES THAT  
: ARE USED IN MORE THAN ONE TEST.  
:--

EQUALS ; GET STANDARD EQUATES.

: BIT DEFINITIONS

100000	BIT15== 100000
040000	BIT14== 40000
020000	BIT13== 20000
010000	BIT12== 10000
004000	BIT11== 4000
002000	BIT10== 2000
001000	BIT09== 1000
000400	BIT08== 400
000200	BIT07== 200
000100	BIT06== 100
000040	BIT05== 40
000020	BIT04== 20
000010	BIT03== 10
000004	BIT02== 4
000002	BIT01== 2
000001	BIT00== 1

001000	BIT9== BIT09
000400	BIT8== BIT08
000200	BIT7== BIT07
000100	BIT6== BIT06
000040	BIT5== BIT05
000020	BIT4== BIT04
000010	BIT3== BIT03
000004	BIT2== BIT02
000002	BIT1== BIT01
000001	BIT0== BIT00

: EVENT FLAG DEFINITIONS  
: EF32:EF17 RESERVED FOR SUPERVISOR TO PROGRAM COMMUNICATION

000040	EF.START== 32.	: START COMMAND WAS ISSUED
000037	EF.RESTART== 31.	: RESTART COMMAND WAS ISSUED
000036	EF.CONTINUE== 30.	: CONTINUE COMMAND WAS ISSUED
000035	EF.NEW== 29.	: A NEW PASS HAS BEEN STARTED
000034	EF.PWR== 28.	: A POWER-FAIL/POWER-UP OCCURRED

: PRIORITY LEVEL DEFINITIONS

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 GLOBAL EQUATES SECTION

```

000340 PRI07== 340
000300 PRI06== 300
000240 PRI05== 240
000200 PRI04== 200
000140 PRI03== 140
000100 PRI02== 100
000040 PRI01== 40
000000 PRI00== 0

```

```

: OPERATOR FLAG BITS
:

```

```

000004 EVL== 4
000010 LOT== 10
000020 ADR== 20
000040 IDU== 40
000100 ISR== 100
000200 UAM== 200
000400 BOE== 400
001000 PNT== 1000
002000 PRI== 2000
004000 IXE== 4000
010000 IBE== 10000
020000 IER== 20000
040000 LOE== 40000
100000 HOE== 100000

```

949  
 950 002144

```

KT11 .. :DEFINE MEMORY MANAGEMENT REGISTERS

```

```

.SBTTL MEMORY MANAGEMENT DEFINITIONS

```

```

;*KT11 VECTOR ADDRESS

```

```

000250 MMVEC= 250

```

```

;*KT11 STATUS REGISTER ADDRESSES

```

```

177572 SRO= 177572
177574 SR1= 177574
177576 SR2= 177576
172516 SR3= 172516

```

```

:IF NB

```

```

;*USER 'I' PAGE DESCRIPTOR REGISTERS

```

```

UIPDR0= 177600
UIPDR1= 177602
UIPDR2= 177604
UIPDR3= 177606
UIPDR4= 177610
UIPDR5= 177612
UIPDR6= 177614
UIPDR7= 177616

```

```

:IF NB

```

```

;*USER 'D' PAGE DESCRIPTOR REGISTERS

```

```

UDPDR0= 177620
UDPDR1= 177622
UDPDR2= 177624
UDPDR3= 177626
UDPDR4= 177630
UDPDR5= 177632
UDPDR6= 177634
UDPDR7= 177636

```

```

.ENDC

```

```

;*USER 'I' PAGE ADDRESS REGISTERS

```



CZTUWAO TUBO FRONT END PRT A  
MEMORY MANAGEMENT DEFINITIONS

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```
UIPAR0= 177640
UIPAR1= 177642
UIPAR2= 177644
UIPAR3= 177646
UIPAR4= 177650
UIPAR5= 177652
UIPAR6= 177654
UIPAR7= 177656
  .IF NB
  ;*USER 'D' PAGE ADDRESS REGISTERS
  UDPAR0= 177660
  UDPAR1= 177662
  UDPAR2= 177664
  UDPAR3= 177666
  UDPAR4= 177670
  UDPAR5= 177672
  UDPAR6= 177674
  UDPAR7= 177676
  .ENDC
  .ENDC
  .IF NB
  ;*SUPERVISOR 'I' PAGE DESCRIPTOR REGISTERS
  SIPDR0= 172200
  SIPDR1= 172202
  SIPDR2= 172204
  SIPDR3= 172206
  SIPDR4= 172210
  SIPDR5= 172212
  SIPDR6= 172214
  SIPDR7= 172216
  .IF NB
  ;*SUPERVISOR 'D' PAGE DESCRIPTOR REGISTERS
  SDPDR0= 172220
  SDPDR1= 172222
  SDPDR2= 172224
  SDPDR3= 172226
  SDPDR4= 172230
  SDPDR5= 172232
  SDPDR6= 172234
  SDPDR7= 172236
  .ENDC
  ;*SUPERVISOR 'I' PAGE ADDRESS REGISTERS
  SIPAR0= 172240
  SIPAR1= 172242
  SIPAR2= 172244
  SIPAR3= 172246
  SIPAR4= 172250
  SIPAR5= 172252
  SIPAR6= 172254
  SIPAR7= 172256
  .IF NB
  ;*SUPERVISOR 'D' PAGE ADDRESS REGISTERS
  SDPAR0= 172260
  SDPAR1= 172262
  SDPAR2= 172264
  SDPAR3= 172266
  SDPAR4= 172270
```

CZTUWAO TUBO FRONT END PRT A  
MEMORY MANAGEMENT DEFINITIONS

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```

SDPAR5= 172272
SDPAR6= 172274
SDPAR7= 172276
.ENDC
.ENDC
;*KERNEL 'I' PAGE DESCRIPTOR REGISTERS
172300 KIPDR0= 172300
172302 KIPDR1= 172302
172304 KIPDR2= 172304
172306 KIPDR3= 172306
172310 KIPDR4= 172310
172312 KIPDR5= 172312
172314 KIPDR6= 172314
172316 KIPDR7= 172316
.IF NB
;*KERNEL 'D' PAGE DESCRIPTOR REGISTERS
KDPDR0= 172320
KDPDR1= 172322
KDPDR2= 172324
KDPDR3= 172326
KDPDR4= 172330
KDPDR5= 172332
KDPDR6= 172334
KDPDR7= 172336
.ENDC
;*KERNEL 'I' PAGE ADDRESS REGISTERS
172340 KIPAR0= 172340
172342 KIPAR1= 172342
172344 KIPAR2= 172344
172346 KIPAR3= 172346
172350 KIPAR4= 172350
172352 KIPAR5= 172352
172354 KIPAR6= 172354
172356 KIPAR7= 172356
.IF NB
;*KERNEL 'D' PAGE ADDRESS REGISTERS
KDPAR0= 172360
KDPAR1= 172362
KDPAR2= 172364
KDPAR3= 172366
KDPAR4= 172370
KDPAR5= 172372
KDPAR6= 172374
KDPAR7= 172376
.ENDC

```

CZTUWAO TUBO FRONT END PRT A MACRO M1200 29-MAR-83 13:24 PAGE 14  
 TUBO REGISTER AND PACKET DEFINITIONS

```

955                                     .SBTTL TUBO REGISTER AND PACKET DEFINITIONS
956
957                                     :
958                                     : SOME GENERAL EQUATES.
959                                     :
960
961      000004      ERRVEC==      4      : POINTER TO ERROR VECTOR FOR BUS TIME OUT.
962      000060      TTIVEC==     60      : INTERRUPT VECTOR FOR CONSOLE INPUT
963      177560      TTICSR==    177560   : BUS ADDRESS OF CONSOLE INPUT
964      177562      TTIBFR==    177562   : CONSOLE INPUT DATA BUFFER
965
966                                     :+
967                                     :BIT DEFINITIONS FOR TSSR REGISTER
968                                     :-
969
970      100000      SC=      BIT15      :SPECIAL CONDITION
971      040000      BIE=     BIT14      :BUS INTERFACE ERROR
972      020000      SCE=     BIT13      :SANITY CHECK ERROR
973      010000      RMR=     BIT12      :MODIFICATION REFUSED
974      004000      NXM=     BIT11      :NONEXISTANT MEMORY ERROR
975      002000      NBA=     BIT10      :NEED BUFFER ADDRESS
976      001400      HIADDR= BIT9:BIT8   :EXTENDED ADDRESS BITS
977      000200      SSR=     BIT7       :SUB SYSTEM READY
978      000100      OFL=     BIT6       :OFF LINE BIT
979      000060      FATERR= BIT4:BIT5   :FATAL TERMINATION ERROR CODES
980      000016      TERCLS= BIT3:BIT2:BIT1 :TERMINATION CODES
981
982
983                                     :+
984                                     :BIT DEFINITIONS FOR EXTENDED STATUS REGISTER 0
985                                     :(XST0)
986                                     :
987                                     :-
988
989
990      100000      XSOTMK= BIT15      :TAPE MARK DETECTED
991      040000      XSORLS= BIT14      :RECORD LENGTH SHORT
992      020000      XSOLET= BIT13      :LOGICAL END OF TAPE
993      010000      XSORLL= BIT12      :RECORD LENGTH LONG
994      004000      XSOWLE= BIT11      :WRITE LOCK ERROR
995      002000      XSONEF= BIT10      :NON EXECUTABLE FUNCTION
996      001000      XSOILC= BIT9       :ILLEGAL COMMAND
997      000400      XSOILA= BIT8       :ILLEGAL ADDRESS
998      000200      XSOMOT= BIT7       :TAPE IN MOTION
999      000100      XSOONL= BIT6       :TRANSPORT ON LINE
1000     000040      XSOIE=  BIT5       :INTERRUPT ENABLE
1001     000020      XSOVCK= BIT4       :VOLUME CHECK BIT
1002     000010      XSOPED= BIT3       :PHASE ENCODED DRIVE
1003     000004      XSOWLK= BIT2       :WRITE LOCKED
1004     000002      XSOBOT= BIT1       :BEGINNING OF TAPE
1005     000001      XSOEOT= BIT0       :END OF TAPE
1006
1007
1008                                     :+
1009                                     :BIT DEFINITIONS FOR EXTENDED STATUS REGISTER 1
1010                                     :(XST1)
1011                                     :-

```

TU80 REGISTER AND PACKET DEFINITIONS

```

1012      100000      X1.DLT = BIT15      ;DATA LATE
1013      040000      X1.SPARE= BIT14      ;NOT USED
1014      020000      X1.COR = BIT13      ;CORRECTABLE DATA ERROR
1015      017375      X1.MBZ = BIT12+BIT11+BIT10+BIT9+BIT7+BIT6+BIT5+BIT4+BIT3+BIT2+BIT0 ;ALWAYS 0
1016      000400      X1.RBP = BIT8      ;READ BUS PARITY ERROR
1017      000002      X1.UNC = BIT1      ;UNCORRECTABLE DATA OR HARD ERROR
1018
1019      ;+
1020      ;BIT DEFINITIONS FOR EXTENDED STATUS REGISTER 2
1021      ;(XST2)
1022      ;-
1023      100000      X2.OPM = BIT15      ;OPERATION IN PROGRESS (TAPE MOVING)
1024      040000      X2.RCE = BIT14      ;RAM CHECKSUM ERROR
1025      035400      X2.SPARE= BIT13+BIT12+BIT11+BIT9+BIT8 ;NOT USED BY TU80 (ALWAYS=0)
1026      002000      X2.WCF = BIT10      ;WRITE CLOCK FAILURE (FIFO NOT EMPTIED BY TRANSPORT)
1027      000200      X2.EXTF = BIT7      ;IF WRITE CHAR CMD THEN = EXTENDED FEATURES ENABLED
1028      000100      X2.BUFE = BIT6      ;IF WRITE CHAR CMD THEN = BUFFERING ENABLED
1029      000077      X2.REV = 000077      ;IF WRITE CHAR CMD THEN = MICROCODE REVISION LEVEL
1030      000007      X2.UNIT = BIT2+BIT1+BIT0 ;IF GET STATUS THEN = CURRENTLY SELECTED UNIT NO.
1031
1032      ;+
1033      ;BIT DEFINITIONS FOR EXTENDED STATUS REGISTER 3
1034      ;(XST3)
1035      ;-
1036      177400      X3.MDE = 177400      ;MICRO-DIAGNOSTIC ERROR CODE
1037      000200      X3.SPARE= BIT7      ;NOT USED BY TU80
1038      000100      X3.OPI = BIT6      ;OPERATION INCOMPLETE
1039      000040      X3.REV = BIT5      ;REVERSE
1040      000020      X3.TRF = BIT4      ;TRANSPORT RESPONSE FAILURE
1041      000010      X3.DCK = BIT3      ;DENSITY CHECK
1042      000006      X3.MBZ =BIT2+BIT1 ;NOT USED ALWAYS 0
1043      000001      X3.RIB = BIT0      ;REVERSE INTO BOT
1044
1045      ;+
1046      ;BIT DEFINITIONS FOR EXTENDED STATUS REGISTER 4
1047      ;(XST4)
1048      ;-
1049      100000      X4.HSP = BIT15      ;HIGH SPEED
1050      040000      X4.RCE = BIT14      ;RETRY COUNT EXCEEDED
1051      020000      X4.TSM = BIT13      ;TRANSPORT SPECIAL MCDE
1052      017400      X4.MBZ = BIT12+BIT11+BIT10+BIT9+BIT8 ;NOT USED ALWAYS 0
1053      000377      X4.WRC = 000377      ;WRITE RETRY COUNT FIELD
1054
1055      ;+
1056      ;TSSR TERMINATION CODES (BIT 0-2)
1057      ;-
1058
1059
1060
1061
1062      000006      TSREJ= 3*2      ;COMMAND REJECTED
1063      000006      UNREC= 6      ;UNRECOVERABLE ERROR
1064
1065      ;+
1066      ;DEVICE REGISTER OFFSETS
1067
1068

```

CZTUWAO TUBO FRONT END PRT A MACRO M1200 29-MAR-83 13:24 PAGE 14-2  
TUBO REGISTER AND PACKET DEFINITIONS

```

1069      :-
1070
1071      177776      TSBA== -2
1072      177776      TSBAL== -2
1073      177776      TSDB== -2      ;TSDB/TSBA REGISTER
1074      177776      TSDBL== -2     ;TSDB/TSBA REGISTER
1075      177777      TSBAH== -1
1076      177777      TSDBH== -1     ;TSDB/TSBA REGISTER HIGH BYTE
1077      000000      TSSR== 0       ;TSSR REGISTER
1078      000001      TSSRH== 1      ;TSSR REGISTER HIGH BYTE
1079
1080      :-+
1081      :-+      ; TSDB ADDRESS BIT DEFINITIONS
1082      :-
1083      000003      A1716 = BIT1+BIT0      ;ADDRESS BITS 17;16 ARE IN 1:0
1084
1085      :-+
1086      :-+      ; COMMAND DEFINITIONS
1087      :-
1088      000017      P.GETSTAT      = 17      ;GET STATUS
1089      000013      P.INIT          = 13      ;INITIALIZE
1090      000012      P.CONTROL       = 12      ;CONTROL COMMANDS
1091      000011      P.FORMAT        = 11      ;FORMAT
1092      000010      P.POSITION      = 10      ;POSITION
1093      000006      P.WRTSUB        = 6       ;SUBSYSTEM WRITE
1094      000005      P.WRITE         = 5       ;WRITE
1095      000004      P.WRTCHAR       = 4       ;WRITE CHARACTERISTICS
1096      000001      P.READ          = 1       ;READ
1097
1098      :-+
1099      :-+      ; COMMAND PACKET HEADER WORD BIT DEFINITIONS
1100      :-
1101      100000      P.ACK           = BIT15     ;BUFFER AVAIL FOR CONTROLLER
1102      040000      P.CVC           = BIT14     ;CLEAR VOLUME CHECK
1103      020000      P.OPP           = BIT13     ;REVERSE SEQUENCE OF DATA BITS
1104      010000      P.SWB           = BIT12     ;SWAP BYTES IN MEMORY
1105      007400      P.MODE          = BIT11!BIT10!BIT9!BIT8 ;EXTENDED COMMAND MODE FIELD
1106      000200      P.IE            = BIT7      ;INTERRUPT ENABLE
1107      000140      P.FMT= BIT6!BITS ;PACKET HEADER TYPE (ALWAYS=0)
1108      000037      P.CMD           = 37      ;MAJOR COMMAND FIELD
1109
1110      :-+
1111      :-+      ; CONTROL COMMAND MODE CODES
1112      :-
1112      000000      PC.RELEASE      = 0*256.   ;RELEASE BUFFER
1113      000400      PC.REWIND       = 1*256.   ;REWIND
1114      001000      PC.NOOP         = 2*256.   ;NO-OP
1115      002000      PC.IEREW        = 4*256.   ;REWIND IMMEDIATE INTERRUPT
1116      002400      PC.ERASE        = 5*256.   ;SECURITY ERASE
1117
1118      :-+
1119      :-+      ; CONTROLLER RAM DEFINITIONS
1120      :-
1121      000167      RMCHBEG = 167      ;CHARACTERISTICS IO DATA BEGIN RAM ADDRESS
1122      000200      RMCHEND = 200     ;CHARACTERISTICS IO DATA END RAM ADDRESS
1123      000020      RMPKTBEGB= 20     ;COMMAND PACKET BEGIN RAM ADDRESS
1124      000027      RMPKTEND= 27     ;COMMAND PACKET END RAM ADDRESS
1125      000104      RMMSGBEG= 104    ;MESSAGE BUFFER BEGIN RAM ADDRESS

```

```

1126      000117      RMMSGEND= 117      ;MESSAGE BUFFER END RAM ADDRESS
1127      :+
1128      :
1129      :REGISTER DEFINITIONS IN THE MESSAGE BUFFER
1130      :-
1131      :
1132      :
1133      000006      XST0== 6      ;EXTENDED STATUS REGISTER 0 (WORD 4)
1134      000010      XST1== 8      ;EXTENDED STATUS REGISTER 1 (WORD 5)
1135      000012      XST2== 10     ;EXTENDED STATUS REGISTER 2 (WORD 6)
1136      000014      XST3== 12     ;EXTENDED STATUS REGISTER 3 (WORD 7)
1137      000016      XST4== 14     ;EXTENDED STATUS REGISTER 4 (WORD 8)
1138      :
1139      :
1140      :+
1141      :
1142      :OFFSETS TO WORD LOCATIONS IN PACKET DEFINITIONS
1143      :-
1144      :
1145      :
1146      000002      PKLOW  = 2      ;LOW ORDER CHARACTERISTIC DATA POINTER
1147      000004      PKHI   = 4      ;HIGH ORDER CHARACTERISTIC DATA POINTER
1148      000006      PKBCNT = 6      ;NUMBER OF BYTES IN DATA PACKET
1149      :
1150      000010      EXBCNT=10     ;NUMBER OF BYTES IN EXTENDED DATA PACKET
1151      :
1152      :+
1153      :DATA PACKET OFFSETS FOR WRITE SUBSYSTEM COMMAND
1154      :-
1155      000000      BSELO  = 0      ;BYTE 0
1156      000001      BSEL1  = 1      ;BYTE 1
1157      000002      SEL2   = 2      ;WORD 2
1158      000004      SELDATA = 4     ;WORD 3
1159      :
1160      :+
1161      :BSELO SELECT CODES FOR WRITE SUBSYSTEM COMMAND
1162      :-
1163      000000      PW.NOP      = 0      ;NO-OP
1164      000001      PW.RDRAM    = 1      ;READ RAM
1165      000002      PW.WTRAM    = 2      ;WRITE RAM
1166      000003      PW.RFIFO    = 3      ;READ FIFO
1167      000004      PW.WFIFO    = 4      ;WRITE FIFO
1168      000005      PW.RDSTAT   = 5      ;READ STATUS
1169      000006      PW.WCTL     = 6      ;WRITE TAPE CONTROL
1170      000007      PW.WFRT     = 7      ;WRITE TAPE FORMAT
1171      000010      PW.WMISC    = 10     ;WRITE MISCELLANEOUS
1172      000011      PW.WNPR     = 11     ;WRITE NPR CONTROL
1173      000020      PW.D22      = 20     ;DO MICROTEST 22
1174      000021      PW.D11      = 21     ;DO MICROTEST 11
1175      000022      PW.D13      = 22     ;DO MICROTEST 13
1176      000023      PW.MO1311   = 23     ;DISABLE MICROTEST 11 AND 13
1177      000024      PW.RDEXT    = 24     ;READ EXT. TAPE STATUS (NOT SUPPORTED BY ALL TRANSP)
1178      :
1179      :+
1180      :BSEL1 CODES FOR WRITE TAPE CONTROL
1181      :-
1182      000200      WC.IFAD      = BIT7    ;LEAD - FORMATTER ADDRESS
    
```

CZTUWAO TUBO FRONT END PRT A MACRO M1200 29-MAR-83 13:24 PAGE 14-4  
TUBO REGISTER AND PACKET DEFINITIONS

1183	000100	WC.I0TAD	= BIT6	:ITAD0	- TRANSPORT ADDRESS BIT 0
1184	000040	WC.I1TAD	= BIT5	:ITAD1	- TRANSPORT ADDRESS BIT 1
1185	000020	WC.ISRESV	= BIT4	:IRESV5	- RESERVED #5
1186	000010	WC.IREW	= BIT3	:IREW	- REWIND
1187	000004	WC.IRWU	= BIT2	:IRWU	- REWIND AND UNLOAD
1188	000002	WC.IFEN	= BIT1	:IFEN	- FORMATTER ENABLE
1189	000001	WC.IGO	= BIT0	:GO	
1190					
1191		:+			
1192		:BSEL1 CODES FOR WRITE FORMAT			
1193		-			
1194	000200	WF.IHISP	= BIT7	:IHISP	- HIGH SPEED
1195	000100	WF.IWRT	= BIT6	:IWRT	- WRITE
1196	000040	WF.IREV	= BIT5	:IREV	- REVERSE
1197	000020	WF.IWFM	= BIT4	:IWFM	- WRITE FILE MARK
1198	000010	WF.IEDIT	= BIT3	:IEDIT	- EDIT
1199	000004	WF.IERASE	= BIT2	:IERASE	- ERASE
1200	000002	WF.I3RESV	= BIT1	:IRESV3	- RESERVED #3
1201	000001	WF.I4RESV	= BIT0	:IRESV4	- RESERVED #4
1202					
1203					
1204		:+			
1205		:BSEL1 CODES FOR WRITE MISCELLANEOUS SUBCOMMAND			
1206		-			
1207	000200	MS.EXT	= BIT7	:INVERT SENSE OF EXTENDED FEATURES SWITCH	
1208	000020	MS.RSFIFO	= BIT4	:RESET FIFO AND INPUT PARITY ERROR	
1209	000010	MS.RSTAPE	= BIT3	:RESET TAPE STATUS IN 2 FLIP-FLOPS	
1210	000006	MS.ATTN	= BIT2:BIT1	:ATTENTION TRIGGER FIELD	
1211	000001	MS.RSD	= BIT0	:RESET TIMER A,B THEN DELAY TIMES IN SEL2	
1212		:+			
1213		: MS.ATTN SUBCODES			
1214		-			
1215	000000	MSA.NOP	= 0*2	:NO-OP (NOTHING TRIGGERED)	
1216	000002	MSA.VOL	= 1*2	:SIMULATE ON-LINE/OFF-LINE TRANSITION	
1217	000004	MSA.NRAM	= 2*2	:FORCE NON-FATAL RAM ERROR (FORCES ERRCODE 54)	
1218	000006	MSA.FRAME	= 3*2	:FORCE FATAL RAM ERROR (CAUSES SCE TO SET)	
1219		:+			
1220		: WRITE SUBSYSTEM WRITE NPR BSEL1 BIT DEFINITIONS			
1221		-			
1222	000200	NP.IR	= BIT7	:INTERRUPT REQUEST (0-1 TRANSITION)	
1223	000100	NP.OUT	= BIT6	:TAPE DATA DIRECTION OUT (0= IN)	
1224	000040	NP.LOOP	= BIT5	:ENABLE TRANSPORT LOOPBACK	
1225	000020	NP.WRP	= BIT4	:WRITE CORRECT PARITY (SET=0 TO WRITE WRONG)	
1226		:+			
1227		: READ STATUS MESSAGE BUFFER BIT DEFINITIONS			
1228		-			
1229					
1230	000200	S2.DIM	= BIT7	:WORD #9 BYTE 2 DATA IN MISS	
1231	000100	S2.ILW	= BIT6	: ILW H	
1232	000040	S2.OUTRDY	= BIT5	: OUT RDY H	
1233	000020	S2.INRDY	= BIT4	: IN RDY H	
1234	000010	S2.ATIMR	= BIT3	: TIMER A FLAG H	
1235	000004	S2.BTIMR	= BIT2	: TIMER B FLAG H	
1236	000003	S2.UNDEF	= BIT1:BIT0	: (UNDEFINED)	
1237	100000	S1.PARIN	= BIT15	:WORD #6 BYTE 1 PARIN H	
1238	040000	S1.I2RESV	= BIT14	: IRESV2	
1239	020000	S1.I1RESV	= BIT13	: IRESV1	

CZTUWAO TUBO FRONT END PR1 A MACRO M1200 29-MAR-83 13:24 PAGE 14-5  
 TUBO REGISTER AND PACKET DEFINITIONS

1240	010000	S1.IEOT	= BIT12	:	IEOT L
1241	004000	S1.IIDENT	= BIT11	:	IIDENT H
1242	002000	S1.ICEK	= BIT10	:	ICER H
1243	001000	S1.IFMK	= BIT9	:	IFMK H
1244	000400	S1.IHER	= BIT8	:	IHER H
1245	000200	SO.ISPEED	= BIT7	:	WORD #8 BYTE 0 ISPEED H
1246	000100	SO.IRDY	= BIT6	:	IRDY L
1247	000040	SO.IONL	= BIT5	:	IONL L
1248	000020	SO.ILDP	= BIT4	:	ILDP L
1249	000010	SO.IDBY	= BIT3	:	IDBY L
1250	000004	SO.IRWD	= BIT2	:	IRWD L
1251	000002	SO.IFBY	= BIT1	:	IFBY L
1252	000001	SO.IFPT	= BIT0	:	IFPT L
1253		:		:	
1254		:		:	
1255	177560	TKS	=177560	:	:KEYBOARD STATUS REGISTER
1256	177562	TKB	=177562	:	:KEYBOARD DATA REGISTER
1257	177564	TPS	=177564	:	:CONSOLE PRINTER STATUS REGISTER
1258	177566	TPB	=177566	:	:CONSOLE PRINTER DATA REGISTER
1259	007776	HIMEM	=007776	:	:HIGH MEMORY MASK VALUE
1260		:		:	
1261	174400	CSR	=174400	:	:STATUS AND CONTROL REGISTER
1262	174402	BAR	=174402	:	:DL ADDRESS REGISTER
1263	174404	DAR	=174404	:	:PLATTER ADDRESS
1264	174406	MPR	=174406	:	:MULTIPURPOSE REGISTER
1265		:		:	
1266		:		:	
1267		:		:	
1268	000004	DLGETS	=4	:	:GET STATUS COMMAND
1269	000006	SEEK	=6	:	:SEEK TRACK AND HEAD SELECT
1270	000010	DLRDHD	=10	:	:READ SECTOR HEADER
1271	000014	READ	=14	:	:READ COMMAND
1272	000016	DLRDNH	=16	:	:READ SECTOR NO HEADER CHECK
1273		:		:	
1274	000001	READY	=1	:	:DRIVE READY BIT IN STATUS REG.
1275	000013	DLSR	=13	:	:STATUS AND RESET
1276	177730	DLERR	=177730	:	:MASK FOR COVER OPEN
1277	000006	DLUN	=6	:	:HEADS UNLOADED
1278	000177	DLCYL	=000177	:	:MASK FOR CYLINDER ADDRESS
1279	100200	DLDNER	=100200	:	:DONE SET OR ERROR SET BITS
1280		:		:	
1281	177560	TTICSR	= 177560	:	:KEYBOARD INPUT STATUS
1282	177562	TTIBFR	= 177562	:	:KEYBOARD DATA REGISTER
1283	177564	TTOCSR	= 177564	:	:CONSOLE PRINTER STATUS REGISTER
1284	177566	TTIBFR	= 177566	:	:CONSOLE PRINTER DATA REGISTER
1285					



CZTUWAO TUBO FRONT END PRT A  
SPECIAL MACROS AND OPDEFS.

MACRO M1200 29-MAR-83 13:24 PAGE 15

.SBTTL SPECIAL MACROS AND OPDEFS.

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1287
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1290
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1292
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1295
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1298
1299
1300
1301
1302
1303
1304
1305
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1342
1343

```

```

      .MACRO SAVREG
      JSR   R5,REGSAV
      .ENDM

: +
: MACRO TO FORCE AN ERROR
: -
: MACRO FORCERROR      TAG,NOTSSR
: .NLIST
: .IIF NDF LISTALL, .NLIST
: .LIST
: .IF B NOTSSR
      MOV   TSSR(R5),R1           ;READ TSSR
: .ENDC
      MOV   FORCER,FORCER        ;IS FORCER SET? (LEAVE C BIT ALONE)
      BNE   TAG                  ;BR IF YES
: .NLIST
: .IIF NDF LISTALL, .LIST
: .LIST
: .ENDM

: +
: MACRO TO FORCE AN EXIT TO AVOID SECTION ITERATIONS
: WILL EXIT TO A LABEL IF FORCER IS NEGATIVE
: SO TO FORCE ERRORS AND EXIT ON 1 ERROR SET
: FORCER TO 177777
: TO FORCE ERRORS AND ITERATIONS SET FORCER TO 1.
: -
: MACRO FORCEEXIT      TAG
: .NLIST
: .IIF NDF LISTALL, .NLIST
: .LIST
      MOV   FORCER,FORCER        ;IS FORCER NEGATIVE?
      BMI   TAG                  ;BR IF YES
: .NLIST
: .IIF NDF LISTALL, .LIST
: .LIST
: .ENDM

: +
: MACRO TO INCREMENT ERROR COUNTS
: -
: MACRO NEXT.ERRNO
: .NLIST
: :::IIF NDF LISTALL, .NLIST
: ERRNO=ERRNO+1
: :::IIF NDF LISTALL, .LIST
: .LIST
: .ENDM
: +

```

CZTUWAO TUBO FRONT END PRT A  
SPECIAL MACROS AND OPDEFS.

MACRO M1200 29-MAR-83 13:24 PAGE 15-1

```

1344           ;MACRO TO PERFORM XOR
1345           :-
1346
1347           .MACRO XOR A,B
1348           MOV A,-(SP)
1349           BIC B,(SP)
1350           BIC A,B
1351           BIS (SP)+,B
1352           .ENDM
1353
1354           000000           EN=0           ; INITIALIZE ERROR NUMBER
1355           .SBTTL FORCER - FORCE ERROR FLAG
1356
1357           ;
1358           ; THE FOLLOWING LOCATIONS MAY BE PATCHED BY THE USER
1359           ; TO OBTAIN THE RESULTS DESCRIBED FOR EACH.
1360           ;
1361
1362 002144 000000 FORCER:: 0           ; FORCE TYPE ALL HARD ERRORS (THE ONES CALLED -
1363           ; - BY THE MACRO 'IFERROR'). AN ERROR NEED NOT -
1364           ; - EXIST, JUST ASSUME AND TYPE THE MESSAGE.
1365
1366
1367

```

## .SBTTL GLOBAL DATA SECTION

```

1369
1370
1371      :++
1372      :THE GLOBAL DATA SECTION CONTAINS DATA THAT ARE USED
1373      :IN MORE THAN ONE TEST.
1374      :--
1375
1376      :
1377      :THE FOLLOWING DATA ARE SET FOR EACH UNIT AT INIT TIME.
1378      :SINGLE UNIT DEFAULTS (LISTED) ARE IN THE DEFAULT P-TABLE.
1379      :
1380 002146 000000  EPRTSW::      .WORD 0      ;PRINT SWITCH
1381 002150 000000  UNITN::      .WORD 0      ;UNIT # UNDER TEST.
1382 002152 000000  QVP::       .WORD 0      ;QUICK VERIFY FLAG.
1383 002154 000000  CSRADDR::   .WORD 0      ;ADDRESS OF CSR FOR CURRENT DEVICE
1384 002156 000224  IVEC::      .WORD 224    ;INTERRUPT VECTOR
1385 002160 000200  IPRI::      .WORD PRI04  ;INTERRUPT PRIORITY.
1386 002162 000000  TSTCNT::   .WORD 0      ;NUMBER OF TESTS RUN IN THIS PASS
1387 002164 000000  LOOPCNT::  .WORD 0      ;REMAINING ITERATION COUNT FOR TEST
1388 002166 000000  DEVCNT::   .WORD 0      ;NUMBER OF DEVICE UNDER TEST
1389 002170 000000  FATFLG::   .WORD 0      ;SET IF FATAL ERROR IS DETECTED IN TEST
1390 002172 000000  INTRECV::  .WORD 0      ;SET IF TAPE INTERRUPT WAS RECEIVED
1391 002174 000000  BENBSW::   .WORD 0      ;BUFFER ENABLE SWITCH SW 0=OFF;1=ON
1392 002176 000000  EXPD::     .WORD 0      ;EXPECTED RAM DATA FOR PRAMPKT ROUTINE
1393 002200 000000  RECV::     .WORD 0      ;RECEIVED RAM DATA FOR PRAMPKT ROUTINE
1394 002202 000000  ERRHI::    .WORD 0      ;HIGH ADDRESS MEMORY ERROR
1395 002204 000000  ERRLO::    .WORD 0      ;LOW ADDRESS MEMORY ERROR
1396 002206 000000  RAMDATA::  .BLKW 16.    ;DATA READ FROM RAM PACKET OR MESSAGE BUF AREA
1397 002246 000000  RAMSIZ::   .WORD 0      ;RAM DATA SIZE FOR PRAMPKT ROUTINE
1398 002250 000000  RCVHIADD:: .WORD 0      ;RECEIVED BUFFER HIGH ADDRESS
1399 002252 000000  RCVLOADD:: .WORD 0      ;RECEIVED BUFFER LOW ADDRESS
1400 002254 000000  COUNT::    .WORD 0      ;TEST COUNT PATTERN
1401 002256 000000  DATA::    .WORD 0      ;TEST DATA
1402 002260 000000  TSTFLAG::  .WORD 0      ;TEST FLAG WORD
1403 002262 000000  TSTPTR::   .WORD 0      ;TSTBLK POINTER
1404 002264 000000  PRMNO::    .WORD 0      ;PRINT ROUTINE TEMP
1405 002266 000000  EXPMSG::   .BLKB 100.   ;EXPECTED MESSAGE BUFFER DATA
1406 002432 000000  RECHSG::   .BLKB 100.   ;RECEIVED MESSAGE BUFFER DATA
1407 002576 000000  TMPBFR::   .BLKB 80.    ;TEMPORARY STORAGE FOR PRINT
1408 002716 000000  MESBFA::   .WORD 0      ;STORES ADDRESS OF MESSAGE BUFFER FOR ERR PRT

```

CZTUWAO TUBO FRONT END PRT A  
TSTBLK - TEST DATA TABLE

MACRO M1200 29-MAR-83 13:24 PAGE 17

.SBTTL TSTBLK - TEST DATA TABLE

1410  
1411  
1412  
1413  
1414  
1415  
1416  
1417  
1418  
1419  
1420  
1421  
1422  
1423  
1424  
1425

:+  
: THIS TABLE CONTAINS TEST DATA USED IN SEVERAL TESTS  
: IN SEQUENCE THE DATA IS:  
:  
: ALL ZEROS  
: ALL ONES  
: WALKING ONES  
: WALKING ZEROS  
: ALTERNATING ONES AND ZEROS  
:-

1426 002720  
1427 002720 000000  
1428 002722 177777  
1429 002724 000001  
1430 002726 000002  
1431 002730 000004  
1432 002732 000010  
1433 002734 000020  
1434 002736 000040  
1435 002740 000100  
1436 002742 000200  
1437 002744 000400  
1438 002746 001000  
1439 002750 002000  
1440 002752 004000  
1441 002754 010000  
1442 002756 020000  
1443 002760 040000  
1444 002762 100000  
1445 002764 177776  
1446 002766 177775  
1447 002770 177773  
1448 002772 177767  
1449 002774 177757  
1450 002776 177737  
1451 003000 177677  
1452 003002 177577  
1453 003004 177377  
1454 003006 176777  
1455 003010 175777  
1456 003012 173777  
1457 003014 167777  
1458 003016 157777  
1459 003020 137777  
1460 003022 077777  
1461 003024 125252  
1462 003026 052525  
1463 003030

TSTBLK::

.WORD 0  
.WORD 177777  
.WORD BIT0  
.WORD BIT1  
.WORD BIT2  
.WORD BIT3  
.WORD BIT4  
.WORD BIT5  
.WORD BIT6  
.WORD BIT7  
.WORD BIT8  
.WORD BIT9  
.WORD BIT10  
.WORD BIT11  
.WORD BIT12  
.WORD BIT13  
.WORD BIT14  
.WORD BIT15  
.WORD ^CBIT0  
.WORD ^CBIT1  
.WORD ^CBIT2  
.WORD ^CBIT3  
.WORD ^CBIT4  
.WORD ^CBIT5  
.WORD ^CBIT6  
.WORD ^CBIT7  
.WORD ^CBIT8  
.WORD ^CBIT9  
.WORD ^CBIT10  
.WORD ^CBIT11  
.WORD ^CBIT12  
.WORD ^CBIT13  
.WORD ^CBIT14  
.WORD ^CBIT15  
.WORD 125252  
.WORD 052525

: ALL ZEROS  
: ALL ONES  
: DATA FOR WALKING ONES

: DATA FOR WALKING ZEROS

: ALTERNATING ONES, ZEROS  
: ALTERNATING ONES, ZERO OPPOSITE FROM ABOVE

TBLEND==.

```

1465          .SBTTL GLOBAL ENVIRONMENT STORAGE
1466
1467          ; STORAGE FOR DEVICE REGISTERS
1468
1469 003030 000000 100000 000000 DUMMY: 0,100000,0,U          ; DUMMY DEVICE REGISTERS...
1470 003040 000000 000000 000000      0,0,0,0,0,0,0,0      ; ...FOR MULTI-UNIT CHECKOUT.
1471
1472
1473
1474 003060 000000          DUFLG:          .WORD 0          ; 'DROPPED UNIT' FLAG.
1475                                ; INHIBITS CODE IN 'CLEAN-UP'.
1476 003062 000000          NDEV:          .WORD 0          ; FLAG TO SAY NO DEVICE.
1477
1478 003064 000000          TEMP1:         .WORD 0          ; SOME TEMP LOCATIONS.
1479 003066 000000          TEMP2:         .WORD 0
1480 003070 000000          XXCOMM:        .WORD C          ; XXDP+ COMM BLOCK POINTER.
1481 003072 000000          FREE:         .WORD 0          ; 1ST FREE MEMORY ADDRESS...
1482 003074 000000          FRESIZ:        .WORD 0          ; ...AND SIZE (IN WORDS).
1483 003076 000000          FREEHI:        .WORD 0          ; LAST WORD IN FREE SPACE
1484 003100 000000          KTFLG:         .WORD 0          ; KT11, MEM AVAIL FLAG -
1485                                ; - .WORD 0 = <24K OR NO KT -
1486                                ; - NZ = >24K AND KT.
1487 003102 000000          KTENABLE:       .WORD 0          ; SET BY TEST ROUTINES TO FLAG >28K UNDER TEST
1488 003104 002000          PST32W:        .WORD 2000       ; 32W BLOCK ADDRESS FOR 32K START
1489 003106 000000          SIFLAG:        .WORD 0
1490 003110 000000          BADDAT:        .WORD 0          ; ACTUAL DATA
1491 003112 000000          GDDAT:         .WORD 0          ; EXPECTED DATA
1492 003114 000000          LOOPFL:        .WORD 0
1493 003116          CTAB:                .WORD 0          ; CONFIGURATION TABLES.
1494 003116 000000          CTABM:         .WORD 0          ; CONFIG WORK.
1495 003120          .WORD 0
1496 003122          .WORD 0
1497 003124          .WORD 0
1498 003126 177777          .WORD -1          ; END OF MEM TABLE.
1499 003130
1500          CTABE:          ; ERROR STATISTICS TABLE (1 WORD PER UNIT), 64 UNITS MAX:
1501          ;
1502          ;          0          =          UNIT NOT TESTED
1503          ;          100000      =          UNIT ONLINE, NO ERRORS
1504          ;          10XXXX      =          UNIT ONLINE, ENCOUNTERED XXXX ERRORS
1505          ;          160000      =          UNIT DROPPED, NON-EXISTENT DEVICE REGISTER
1506          ;          160001      =          UNIT DROPPED, NOT IDLE AT START
1507          ;          14XXXX      =          UNIT DROPPED, ENCOUNTERED XXXX ERRORS
1508          ;
1509 003130          ERTABL:          .BLKW 64.
1510 003330 000000          ERTABE:          .WORD 0
1511
1512 003332 000000          SKIPT:          .WORD 0          ; 1=SKIP SUBTEST 0=NO SKIP OF SUBTEST

```

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003334  
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124 125 070  
  
003342  
003342  
003342  
  
103 132 124  
  
003400 003440 003443 003447  
003420 003501 003505 003511  
123 103 000  
102 111 105  
123 103 105  
122 115 122  
116 130 115  
116 102 101  
102 111 124  
102 111 124  
123 123 122  
117 106 114  
102 111 124  
102 111 124  
102 111 124  
102 111 124  
102 111 124  
102 111 124  
102 111 124  
102 111 124  
124 123 123  
124 123 123  
040 040 116  
045 101 040  
045 101 040  
045 101 040  
045 101 040  
045 116 045

```

.SBTTL GLOBAL TEXT MESSAGES
:++
: THE GLOBAL TEXT SECTION CONTAINS FORMAT STATEMENTS,
: MESSAGES, AND ASCII INFORMATION THAT ARE USED IN
: MORE THAN ONE TEST.
:--

:++
: NAMES OF DEVICES SUPPORTED
:--

      DEVTYP <TUBO>
LSDVTYP::
      .ASCIZ /TUBO/
      .EVEN

:++
: TEST DESCRIPTION
:
      DESCRIPT <CZTUWAO TUBO FRONT END PRT A>
LSDESC::
      .ASCIZ /CZTUWAO TUBO FRONT END PRT A/
      .EVEN

:++
: BIT TO ASCII CONVERSION FOR TSSR REGISTER
:--

TSSRBIT::      .WORD 1$,2$,3$,4$,5$,6$,7$,8$
                .WORD 9$,10$,11$,12$,13$,14$,15$,16$
1$:      .ASCIZ 'SC'
2$:      .ASCIZ 'BIE'
3$:      .ASCIZ 'SCE'
4$:      .ASCIZ 'RMR'
5$:      .ASCIZ 'NXM'
6$:      .ASCIZ 'NBA'
7$:      .ASCIZ 'BIT9'
8$:      .ASCIZ 'BIT8'
9$:      .ASCIZ 'SSR'
10$:     .ASCIZ 'OFL'
11$:     .ASCIZ 'BIT5'
12$:     .ASCIZ 'BIT4'
13$:     .ASCIZ 'BIT3'
14$:     .ASCIZ 'BIT2'
15$:     .ASCIZ 'BIT1'
16$:     .ASCIZ 'BIT0'
        .EVEN
SFIERR: .ASCIZ 'TSSR ERROR AFTER SOFT INIT'
SFHERR: .ASCIZ 'TSSR ERROR AFTER BUS RESET'
NXR:    .ASCIZ / NON-EXISTANT DEVICE REGISTER/
NXRX:   .ASCIZ /% ADDRESS: %06/
TSSX:   .ASCII /% TSBA,TSSR EXP'D: %06%,%06%/
        .ASCIZ /% TSBA,TSSR REC'D: %06%,%06%/
FUSI:   .ASCII /%N%/
    
```

CZTUWAO TUBO FRONT END PRT A MACRO M1200 29-MAR-83 13:24 PAGE 19-1  
GLOBAL TEXT MESSAGES

1565	004021	040	040	125	USI:	.ASCIZ	/ UNEXPECTED INTERRUPT/
1566	004050	040	040	111	NSI:	.ASCIZ	/ INTERRUPT EXPECTED, NOT RECEIVED/
1567	004113	045	116	045	FNOINTR:	.ASCII	/XNZA/
1568	004117	040	040	116	NOINTR:	.ASCIZ	/ NO INTERRUPT WAS GENERATED/
1569	004154	040	040	111	IFault:	.ASCIZ	/ INTERRUPT FAULT/
1570	004176	045	101	040	INTX:	.ASCIZ	/XA CPU PC: X06XA TSBA: X06/
1571	004233	040	040	042	NOINIT:	.ASCIZ	/ 'BUS-INIT' DIDN'T INITIALIZE CONTROLLER/
1572	004305	040	040	042	NSINIT:	.ASCIZ	/ 'SOFT-INIT' DIDN'T INITIALIZE THE DPU/
1573	004355	040	040	042	BRINIT:	.ASCIZ	/ 'BUS-RESET' DIDN'T INITIALIZE THE DPU/
1574							
1575	004425	000			NUL:	.ASCIZ	//
1576	004426	045	116	000	NULCR:	.ASCIZ	/XN/
1577	004431	045	101	040	EXPGOT:	.ASCIZ	/XA EXP'D: X06XA, REC'D: X06/
1578	004465	045	116	045	EXPGT2:	.ASCIZ	/XNZA EXP'D: X06XA, X06XNZA REC'D: X0XA, X06/
1579	004541	045	101	040	DJAD12:	.ASCIZ	/XA REG(W) WRITTEN TO: X06XA REG(R) READ: EXP'D: X06XA, REC'D: X06/
1580	004643	122	101	115	PKTRAM::	.ASCIZ	'RAM Contents Do Not Match Packet Sent'
1581	004711	040	040	103	SCME:	.ASCIZ	/ CONFIG DOESN'T MATCH MFG. MASTER/
1582	004754	127	122	111	WRTMSG:	.ASCIZ	'WRITE CHARACTERISTICS Failed'
1583	005011	124	123	123	WRTERR:	.ASCIZ	'TSSR Incorrect After WRITE Command, More Bits Set Than SSR'
1584	005104	124	123	123	RDERR:	.ASCIZ	'TSSR Incorrect After READ Command, More Bits Set Than SSR'
1585						.EVEN	
1586							
1587							
1588							

CZTUWAO TUBO FRONT END PRT A  
GLOBAL ERROR REPORT SECTION

MACRO M1200 29-MAR-83 13:24 PAGE 20

.SBTTL GLOBAL ERROR REPORT SECTION

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1597

```

:++
: THE GLOBAL ERROR REPORT SECTION CONTAINS THE PRINTB AND PRINTX
: CALLS THAT ARE USED IN MORE THAN ONE TEST.
: ASCII TEXT STRINGS ARE FOUND IN THE GLOBAL TEXT SECTION.
:--
    
```

1598 005176  
005176  
1599 005176  
005176 013746 003062  
005202 012746 003675  
005206 012746 000002  
005212 010600  
005214 104415  
005216 062706 000006  
1600 005222 004737 005230  
1601 005226  
005226  
005226 104423

```

BGNMSG NXRERR ;NON-EXISTANT DEVICE REGISTER.
NXRERR: ;NODEV = NEXM ADDRESS.
PRINTX #NXRX,NODEV
MOV NODEV,-(SP)
MOV #NXRX,-(SP)
MOV #2,-(SP)
MOV SP,R0
TRAP C$PNTX
ADD #6,SP ; PRINT EXTENSION IF REQUIRED.
JSR PC,EXTEND
ENDMSG
L10002: TRAP C$MSG
    
```

1602  
1603  
1604  
1605  
1606  
1607

```

:
: THIS ROUTINE APPENDS A UNIQUE EXTENSION (IF REQUIRED)
: TO ANY OF THE ABOVE ERROR SIGNATURES.
:
    
```

1608 005230 005727  
1609 005232 000000  
1610 005234 001402  
1611 005236 004777 177770  
1612 005242  
005242 012746 004426  
005246 012746 000001  
005252 010600  
005254 104415  
005256 062706 000004  
1613 005262 000207

```

EXTEND: TST (PC)+
EXTA: 0 ; 0 = NO EXTENSION.
BEQ 1$
JSR PC,@EXTA ; APPEND EXTENSION TEXT.
1$: PRINTX #NULCR ; PRINT A BLANK LINE
MOV #NULCR,-(SP)
MOV #1,-(SP)
MOV SP,R0
TRAP C$PNTX
ADD #4,SP
RTS PC
    
```



.SBTTL PRITSSR - PRINT TSSR CONTENTS

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 1634 005264  
 1635 005264  
 1636 005270 010104  
 1637 005272  
     005272 010446  
     005274 012746 005731  
     005300 012746 000002  
     005304 010600  
     005306 104414  
     005310 062706 000006  
 1638 005314 010400  
 1639 005316 004737 016534  
 1640 005322 103410  
 1641 005324  
     005324 012746 006151  
     005330 012746 000001  
     005334 010600  
     005336 104415  
     005340 062706 000004  
 1642 005344 010403  
 1643 005346 042703 001476  
 1644 005352 001434  
 1645 005354 012702 002576  
 1646 005360 012701 003400  
 1647 005364 005703  
 1648 005366 001413  
 1649 005370 000241  
 1650 005372 006103  
 1651 005374 103006  
 1652 005376 011100  
 1653 005400 112022  
 1654 005402 001376  
 1655 005404 112762 000054 177777  
 1656 005412 005721  
 1657 005414 000763  
 1658 005416 105042  
 1659 005420  
     005420 012746 002576  
     005424 012746 006122

:+  
 :ROUTINE TO DISPLAY THE CONTENTS, AND BIT DEFINITIONS, OF  
 :THE TSSR REGISTER. THIS ROUTINE IS NORMALLY CALLED ONLY  
 :BY A MESSAGE PRINTING ROUTINE

:INPUTS:

        R1        CONTENTS OF TSSR

:SUBORDINATE ROUTINES:

        CHKAMB    CHECK FOR AMBIGUOUS CONTENTS

PRITSSR:

```

    SAVREG                ;SAVE GENERAL REGISTERS
    MOV R1,R4             ;SAVE THE TSSR CONTENTS
    PRINTB #TSSRFOR,R4   ;PRINT THE CONTENTS OF TSSR
    MOV R4,-(SP)
    MOV #TSSRFOR,-(SP)
    MOV #2,-(SP)
    MOV SP,R0
    TRAP C$PNTB
    ADD #6,SP
    MOV R4,R0             ;GET TSSR BACK FOR CHKAMB
    JSR PC,CHKAMB        ;ARE CONTENTS AMBIGUOUS ?
    BCS 5$               ;BRANCH IF NOT
    PRINTX #AMBTSSR      ;SHOW CONTENTS ARE AMBIGUOUS
    MOV #AMBTSSR,-(SP)
    MOV #1,-(SP)
    MOV SP,R0
    TRAP C$PNTX
    ADD #4,SP
5$:  MOV R4,R3             ;CONTENTS OF TSSR
    BIC #HIADDR!FATERR!TERCLS,R3 ;CLEAR ALL MULTIPLE BIT FIELDS
    BEQ 20$              ;NO BITS ARE SET
    MOV #TMPBFR,R2       ;TEMPORARY ASCII BUFFER
    MOV #TSSRBIT,R1      ;ASCII EQUIVALENT OF BITS
10$: TST R3              ;REMAINING BITS TO CONVERT
    BEQ 15$              ;BRANCH WHEN ALL ARE DONE
    CLC                  ;CLEAR CARRY FOR SHIFT
    ROL R3               ;SHIFT NEXT BIT TO CARRY
    BCC 13$              ;BRANCH IF BIT NOT SET
    MOV (R1),R0          ;POINTER TO BIT DEFINITION
11$: MOVB (R0)+,(R2)+    ;MOVE ASCII TO BUFFER
    BNE 11$              ;MOVE ALL BITS
    MOVB #' ,-(R2)       ;INSERT A COMMA TO TERMINATE
13$: TST (R1)+          ;POINT TO NEXT DESCRIPTION
    BR 10$               ;GET THE REMAINING BITS
15$: CLRB -(R2)         ;TERMINATE THE LINE
    PRINTX #TSSDEF,#TMPBFR ;PRINT THE BIT DEFINITIONS
    MOV #TMPBFR,-(SP)
    MOV #TSSDEF,-(SP)
    
```

CZTUWAO TUBO FRONT END PRT A  
PRITSSR - PRINT TSSR CONTENTS

MACRO M1200 29-MAR-83 13:24 PAGE 22-1

005430	012746	000002		MOV	#2,-(SP)			
005434	010600			MOV	SP,R0			
005436	104415			TRAP	CSPNTX			
005440	062706	000006		ADD	#5,SP			
1660								
1661	005444	010403	20S:	MOV	R4,R3	:GET THE TSSR CONTENTS		
1662	005446	042703		BIC	#^CTERCLS,R3	:CLEAR ALL BUT TERMINATION		
1663	005452	016303		MOV	TCOCOD(R3),R3	:GET THE TERMINATION CODE MEANING		
1664	005456			PRINTX	#TCOASC,R3	:PRINT THE TERMINATION CODE		
	005456	010346		MOV	R3,-(SP)			
	005460	012746		MOV	#TCOASC,-(SP)			
	005464	012746		MOV	#2,-(SP)			
	005470	010600		MOV	SP,R0			
	005472	104415		TRAP	CSPNTX			
	005474	062706		ADD	#6,SP			
1665	005500	010403		MOV	R4,R3	:TSSR CONTENTS AGAIN		
1666	005502	042703		BIC	#^CFATERR,R3	:CLEAR ALL BUT FATAL TERMINATION		
1667	005506	001421		BEQ	25S	:DON'T PRINT IF ZERO		
1668	005510	006203		ASR	R3			
1669	005512	006203		ASR	R3			
1670	005514	006203		ASR	R3	:ALINE TERMINATION CODE FOR INDEX		
1671	005516	016303		MOV	TSFCOD(R3),R3	:GET THE FATAL TERMINATION CODE		
1672	005522			PRINTX	#TFCASC,R3	:PRINT THE FATAL TERMINATION CODE		
	005522	010346		MOV	R3,-(SP)			
	005524	012746		MOV	#TFCASC,-(SP)			
	005530	012746		MOV	#2,-(SP)			
	005534	010600		MOV	SP,R0			
	005536	104415		TRAP	CSPNTX			
	005540	062706		ADD	#6,SP			
1673	005544	012737	002170	MOV	#25,FATFLG	:DROP UNIT AFTER THIS ERROR		
1674	005552	010403		MOV	R4,R3	:GET TSSR CONTENTS		
1675	005554	042703		BIC	#^CHIADDR,R3	:CLEAR ALL BUT EXTENDED ADDRESS		
1676	005560	001411		BEQ	30S	:DON'T PRINT IF ZERO		
1677	005562			PRINTX	#TEXASC,R3	:PRINT THE EXTENDED ADDRESS BITS		
	005562	010346		MOV	R3,-(SP)			
	005564	012746		MOV	#TEXASC,-(SP)			
	005570	012746		MOV	#2,-(SP)			
	005574	010600		MOV	SP,R0			
	005576	104415		TRAP	CSPNTX			
	005600	062706		ADD	#6,SP			
1678	005604	022704	100210	30S:	CMF	#100210,R4	:CHECK FOR MEDIA ERROR	
1679	005610	001003		BNE	31S		:BR, IF PROBABLY NOT TAPE ERROR	
1680	005612	012737	005672	002146	MOV	#EPRT3,EPRTSW	: 'PROBABLY MEDIA RELETED ERROR - BAD TAPE'	
1681	005620	005737	002146	31S:	TST	EPRTSW	:CHECK FOR THE SWITCH EMPTY	
1682	005624	001003		BNE	310S		:BR, IF SWITCH IS NOT EMPTY	
1683	005626	012737	005672	002146	MOV	#EPRT1,EPRTSW	:SET SWITCH TO DEFAULT	
1684	005634	013737	002146	005644	310S:	MOV	EPRTSW,32S+2	:PUT REAL SWITCHABLE MESSAGE IN PLACE
1685	005642			32S:	PRINTB	#EPRT1	:PRINT THE ERPOP MESSAGE	
	005642	012746	005672		MOV	#EPRT1,-(SP)		
	005646	012746	000001		MOV	#1,-(SP)		
	005652	010600			MOV	SP,R0		
	005654	104414			TRAP	CSPNTB		
	005656	062706	000004		ADD	#4,SP		
1686	005662	012737	005672	002146	MOV	#EPRT1,EPRTSW	:RESET TO NORMAL ERROR POINTER	
1687	005670	000207			RTS	PC	:RETURN TO CALLER	
1688								
1689	005672			EPRT2:				

CZTUWAO TUBO FRONT END PRT A  
PRITSSR - PRINT TSSR CONTENTS

MACRO M1200 29-MAR-83 13:24 PAGE 22-2

1690	005672				EPRT3:		
1691	005672	045	116	045	EPRT1:	.ASCIZ	'XNZA *****REPLACE M7454*****XS'
1692	005731	045	116	045	TSSRFOR:	.ASCIZ	'XNZA TSSR = X06'
1693	005751	045	116	045	TEXASC:	.ASCIZ	'XNZA Extended Address Bits = X06'
1694	006012	045	116	045	TCOASC:	.ASCIZ	'XNZA Termination Class Code = XT'
1695	006053	045	116	045	TFCASC:	.ASCIZ	'XNZA Fatal Termination Class Code = XT'
1696	006122	045	116	045	TSSDEF:	.ASCIZ	'XNZA TSSR Bits Set: XT'
1697	006151	045	116	045	AMBTSSR:	.ASCIZ	'XNZA TSSR Contents Are Ambiguous'
1698						.EVEN	
1699	006212	006232	006255	006303	TCOCOD:	.WORD	1S,2S,3S,4S,5S,6S,7S,8S
1700	006232	116	157	162	1S:	.ASCIZ	'Normal Termination'
1701	006255	124	145	162	2S:	.ASCIZ	'Termination Condition'
1702	006303	124	141	160	3S:	.ASCIZ	'Tape Status Alert'
1703	006325	106	165	156	4S:	.ASCIZ	'Function Reject'
1704	006345	122	145	143	5S:	.ASCIZ	'Recoverable Error - Tape Position One Record Down'
1705	006427	122	145	143	6S:	.ASCIZ	'Recoverable Error - Tape Was Not Moved'
1706	006476	125	156	162	7S:	.ASCIZ	'Unrecoverable Error'
1707	006522	106	141	164	8S:	.ASCIZ	'Fatal Controller Error'
1708						.EVEN	
1709							
1710	006552	006562	006616	006627	TSFCOD:	.WORD	1S,2S,3S,4S
1711	006562	111	156	164	1S:	.ASCIZ	'Internal Diagnostic Failure'
1712	006616	122	145	163	2S:	.ASCIZ	'Reserved'
1713	006627	102	165	163	3S:	.ASCIZ	'Bus Interface or Sanity Check Error'
1714	006673	122	145	163	4S:	.ASCIZ	'Reserved'
1715						.EVEN	

CZTUWAO TUBO FRONT END PRT A MACRO M1200 29-MAR-83 13:24 PAGE 23  
 PRIPKT - PRINT THE ADDRESS/CONTENTS OF COMMAND PACKET

```

1717 .SBTTL PRIPKT - PRINT THE ADDRESS/CONTENTS OF COMMAND PACKET
1718
1719 :THIS ROUTINE PRINTS THE ADDRESS AND CONTENTS OF A COMMAND PACKET.
1720 :THIS ROUTINE IS NORMALLY ONLY CALLED FROM A PRINT ROUTINE.
1721 :INPUT:
1722 :      R0      NUMBER OF WORDS IN PACKET
1723 :      R3      HIGH ORDER COMMAND PACKET ADDRESS
1724 :      R4      ADDRESS OF COMMAND PACKET
1725 :      NOTE:   R3 IS IGNORED IF THE KTENABLE FLAG IS CLEAR.
1726 :-
1727 PRIPKT::
1728         SAVREG          ;SAVE THE REGISTERS
1729         MOV R0,R5       ;SAVE NO. OF WRD'S IN PACKET
1730         TST KTENABLE   ;ABOVE 28K UNDER TEST?
1731         BNE 10$        ;BR IF YES
1732         CLR R3         ;SET HIGH ORDER ADDRESS TO 0
1733 10$:    MOV R3,R1      ;COPY HIGH ORDER ADDRESS
1734         MOV R4,R0      ;GET LOWER ADDRESS
1735         ROL R0         ;SHIFT BIT 15 INTO C BIT
1736         ROL R1         ;AND INTO HIGH ORDER.
1737         PRINTB #PKTADD,R1,R4 ;PRINT PACKET ADDRESS
1738         MOV R4,-(SP)
1739         MOV R1,-(SP)
1740         MOV #PKTADD,-(SP)
1741         MOV #3,-(SP)
1742         MOV SP,R0
1743         TRAP CSPNTB
1744         ADD #10,SP
1745 15$:    MOV R3,R0      ;GET HIGH ORDER ADDRESS
1746         BEQ 20$       ;BR IF NOT ABOVE 28K.
1747         MOV R4,R1      ;GET LOW ORDER ADDRESS
1748         JSR PC,SETMAP  ;SETUP PAR6 MAPPING FOR 18 BIT ADDRESS
1749         MOV R0,R4      ;GET RETURNED PAR6 ADDRESS BIAS
1750 20$:    CLR R1         ;SAVE WORD NUMBER
1751 25$:    MOV (R4)+,R2    ;GET PACKET CONTENTS
1752         PRINTB #PKTFRM,R1,R2 ;PRINT THE DATA
1753         MOV R2,-(SP)
1754         MOV R1,-(SP)
1755         MOV #PKTFRM,-(SP)
1756         MOV #3,-(SP)
1757         MOV SP,R0
1758         TRAP CSPNTB
1759         ADD #10,SP
1760         IN R1          ;NEXT WORD NUMBER
1761         CMP R1,R5      ;DONE ALL PACKET WORDS?
1762         BLT 25$       ;LOOP TILL ALL DONE
1763         PRINTB #PKTNEW ;JUST A COUPLE NEW LINES
1764         MOV #PKTNEW,-(SP)
1765         MOV #1,-(SP)
1766         MOV SP,R0
1767         TRAP CSPNTB
1768         ADD #4,SP
1769         RTS PC        ;RETURN
1770 045 PKTFRM: .ASCIZ 'XNZX Packet Word #XD1XA = Z06'
1771 045 PKTADD: .ASCIZ 'XNZX Packet Address = Z01Z05'
1772 045 PKTNEW: .ASCIZ 'XNZXZXA '
1773         .EVEN

```

CZUWAO TUBO FRONT END PRT A MACRO M1200 29-MAR-83 13:24 PAGE 24  
 PRIBXOR - PRINT EXPD, RECV AND XOR BYTE

.SBTTL PRIBXOR - PRINT EXPD, RECV AND XOR BYTE

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```

: *
: PRINT EXPECTED DATA, RECEIVED DATA, AND XOR OF THE DATA BYTE
: THIS ROUTINE IS NORMALLY CALLED ONLY FOR PRINT ROUTINES.
: INPUTS:
:       R1      RECEIVED DATA
:       R2      EXPECTED DATA
: OUTPUT:
:       R0      XOR OF EXPECTED/RECEIVED DATA
: -
    
```

```

PRIBXOR::
    SAVREG                ;SAVE THE REGISTERS
    MOV R2,R3              ;EXPECTED DATA
    XOR R1,R3              ;FORM THE EXCLUSIVE OR
    MOV #^C<377>,R0       ;BYTE MASK
    BIC R0,R1              ;SAVE LOW BYTE RECV
    BIC R0,R2              ;SAVE LOW BYTE EXPD
    BIC R0,R3              ;SAVE LOW BYTE XOR
    PRINTB #XORBFOR,R2,R1,R3 ;PRINT THE MESSAGE
    MOV R3,-(SP)
    MOV R1,-(SP)
    MOV R2,-(SP)
    MOV #XORBFOR,-(SP)
    MOV #4,-(SP)
    MOV SP,R0
    TRAP CSPNTB
    ADD #12,SP
    MOV R3,R0              ;R0 HAS XOR ON RETURN
    RTS                    ;RETURN TO CALLER

    .ASCIZ 'X%X EXPD: X03XA RECV: X03XA XOR: X03'
    .EVEN
    
```

```

007156
007156
007162 010203
007164
007174 012700 177400
007200 040001
007202 040002
007204 040003
007206
007206 010346
007210 010146
007212 010246
007214 012746 007240
007220 012746 000004
007224 010600
007226 104414
007230 062706 000012
007234 010300
007236 000207
    
```

```

045 116 045 XORBFOR: .ASCIZ 'X%X EXPD: X03XA RECV: X03XA XOR: X03'
.EVEN
    
```

CZTUWAO TUBO FRONT END PRT A MACRO M1200 29-MAR-83 13:24 PAGE 25  
 PRI XOR - PRINT EXPD, RECV AND XOR

1791  
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 1793  
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 1801  
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 1808  
 1809 007306  
 1810 007306  
 1811 007312 010203  
 1812 007314  
 1813 007324  
       007324 010346  
       007326 010146  
       007330 010246  
       007332 012746 007356  
       007336 012746 000004  
       007342 010600  
       007344 104414  
       007346 062706 000012  
 1814 007352 010300  
 1815 007354 000207  
 1816  
 1817 007356 045 116 045  
 1818

.SBTTL PRI XOR - PRINT EXPD, RECV AND XOR

```

:PRINT EXPECTED DATA, RECEIVED DATA, AND XOR OF THE TWO
:THIS ROUTINE IS NORMALLY CALLED ONLY FOR PRINT ROUTINES.
    
```

:INPUTS:

```

R1 RECEIVED DATA
R2 EXPECTED DATA
    
```

:OUTPUT:

```

R0 XOR OF EXPECTED/RECEIVED DATA
    
```

PRI XOR::

```

SAVREG ;SAVE THE REGISTERS
MOV R2,R3 ;EXPECTED DATA
XOR R1,R3 ;FORM THE EXCLUSIVE OR
PRINTB #XORFOR,R2,R1,R3 ;PRINT THE MESSAGE
MOV R3,-(SP)
MOV R1,-(SP)
MOV R2,-(SP)
MOV #XORFOR,-(SP)
MOV #4,-(SP)
MOV SP,R0
TRAP CSPNTB
ADD #12,SP
MOV R3,R0 ;R0 HAS XOR ON RETURN
RTS PC ;RETURN TO CALLER
    
```

```

XORFOR: .ASCIZ 'X%X EXPD: %06XA RECV: %06XA XOR: %06'
.EVEN
    
```

CZTUWAO TUBO FRONT END PRT A MACRO M1200 29-MAR-83 13:24 PAGE 26  
PRIEQU - PRINT BIT NUMBERS AS ASCII EQUIVALENT

1820  
1821  
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1867  
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1869  
1870

007424  
007424  
007430 000207  
  
  
  
  
  
  
  
  
  
  
  
007432  
007432  
007436 010446  
007440 012746 007462  
007444 012746 000002  
007450 010600  
007452 104414  
007454 062706 000006  
007460 000207

.SBTTL PRIEQU - PRINT BIT NUMBERS AS ASCII EQUIVALENT

:+  
:ROUTINE TO CONVERT BIT VALUES TO ASCII AND PRINT THE STRING  
:THIS ROUTINE IS NORMALLY CALLED FROM A PRINT ROUTINE

:INPUTS:

R0 OCTAL VALUE TO CONVERT  
R1 TABLE OF POINTERS TO ASCII EQUIVALENT

:-  
PRIEQU:

SAVREG ;SAVE THE REGISTERS  
RTS PC ;RETURN TO CALLER

.SBTTL PRIRAM - PRINT RAM ADDRESS

:+  
:PRINT CONTROLLER RAM ADDRESS.  
:THIS ROUTINE IS NORMALLY CALLED ONLY FROM PRINT ROUTINES.

:INPUTS:

R4 RAM ADDRESS

:-  
PRIRAM:

SAVREG ;SAVE R1-R5 UNTIL NEXT RETURN  
PRINTB #RAMFOR,R4 ;PRINT RAM ADDRESS IN ERROR  
MOV R4,-(SP)  
MOV #RAMFOR,-(SP)  
MOV #2,-(SP)  
MOV SP,R0  
TRAP CSPNTB  
ADD #6,SP  
RTS PC ;RETURN

RAMFOR: .ASCIZ 'XNZA CONTROLLER RAM ADDRESS = X06'  
.EVEN

.SBTTL PRIADD - PRINT MEMORY ERROR ADDRESS

:+  
:PRINT MEMORY ADDRESS  
:THIS ROUTINE IS NORMALLY CALLED ONLY FROM PRINT ROUTINES.

:IMPLICIT INPUTS

ERRHI - HIGH ORDER ADDRESS  
ERRLO - LOW ORDER ADDRESS

CZTUWAO TUBO FRONT END PRT A MACRO M1200 29-MAR-83 13:24 PAGE 26-1  
PRIADD - PRINT MEMORY ERROR ADDRESS

```

1871
1872
1873 007524
1874 007524
1875 007530 013700 002202
1876 007534 013701 002204
1877 007540 010102
1878 007542 006101
1879 007544 006100
1880 007546
      007546 010246
      007550 010046
      007552 012746 007574
      007556 012746 000003
      007562 010600
      007564 104414
      007566 062706 000010
1881 007572 000207
      RTS PC ;RETURN

```

```

1882
1883 007574 045 116 045 PRIA0: .ASCIZ 'XNZA MEMORY ERROR ADDRESS = X01X05'
1884 .EVEN

```

```

1885
1886
1887 .SBTTL PRITADD - PRINT MEMORY TEST ADDRESS

```

```

1888
1889
1890 :PRINT MEMORY ADDRESS
1891 :THIS ROUTINE IS NORMALLY CALLED ONLY FROM PRINT ROUTINES.
1892
1893 : IMPLICIT INPUTS
1894
1895 ERRHI - HIGH ORDER ADDRESS
1896 ERRLO - LOW ORDER ADDRESS
1897
1898

```

```

1899 007640
1900 007640
1901 007644 013700 002202
1902 007650 013701 002204
1903 007654 010102
1904 007656 006101
1905 007660 006100
1906 007662
      007662 010246
      007664 010046
      007666 012746 007710
      007672 012746 000003
      007676 010600
      007700 104414
      007702 062706 000010
1907 007706 000207
      RTS PC ;RETURN

```

```

1908 007710 045 116 045 PRIT0: .ASCIZ 'XNZA MEMORY TEST ADDRESS = X01X05'
1909 .EVEN

```



CZTUWAO TUBO FRONT END PRT A MACRO M1200 29-MAR-83 13:24 PAGE 27  
SPACE - SPACE RECORDS (FORWARD AND REVERSE) COMMAND

.SBTTL SPACE - SPACE RECORDS (FORWARD AND REVERSE) COMMAND

1911  
1912  
1913  
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1915  
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1945

ROUTINE TO ISSUE A SPACE RECORDS  
COMMAND (FORWARD OR REVERSE)  
INPUT:  
R3 NUMBER OF RECORDS TO BE SPACED OVER  
BIT15 CONTROLS DIRECTION  
BIT15 = 0 IS FORWARD  
BIT15 = 1 IS REVERSE  
R5 FIRST DEVICE UNIBUS ADDRESS  
REQUIRES A WRITE CHARACTERISTICS DONE PREVIOUSLY  
OUTPUT:  
CARRY SET - SPACE RECORDS COMMAND OK  
CLR - SPACE RECORDS FAILED  
R0 THE CONTENTS OF R4 IS MOVED TO R0  
IMPLICIT OUTPUT.  
TAPE HAS BEEN MOVED  
SIDE EFFECTS:  
-

1946 007752  
1947 007752  
1948 007756 012737 000764 010150  
1949 007764 012737 140010 010140  
1950 007772 005703  
1951 007774 100403  
1952 007776 010337 010142  
1953 010002 000407  
1954 010004 042703 100000  
1955 010010 010337 010142  
1956 010014 052737 000400 010140  
1957 010022 012704 010140  
1958 010026 010465 177776  
1959 010032 004737 016740  
1960 010036 103420  
1961 010040  
010040 012727 000250  
010044 000000  
010046 013727 002116  
010052 000000  
010054 005567 177772  
010060 001375

SPACE::  
SAVREG ;SAVE THE GENERAL REGISTERS  
MOV #500.,SDELAY ;SET UP DELAY  
MOV #140010,80\$ ;SET UP COMMAND, SPACE FORWARD  
TST R3 ;CHECK FOR DIRECTION  
BMI 5\$ ;BR, IF REVERSE INDICATED  
MOV R3,90\$ ;LOAD UP NUMBER OF RECORDS TO SPACE  
BR 10\$ ;GO DO COMMAND  
5\$: BIC #BIT15,R3 ;CLEAR DIRECTION BIT  
MOV R3,90\$ ;LOAD UP NUMBER OF RECORDS TO SPACE  
BIS #BIT8,80\$ ;SET REVERSE BIT IN COMMAND PACKET  
10\$: MOV #80\$,R4 ;SET UP R4 WITH PACKET ADDRESS  
MOV R4,TSDB(R5) ;SEND OUT COMMAND  
15\$: JSR PC,WAITF ;WAIT FOR SSR  
BCS 20\$ ;BR, IF SSR IS SET AND OK  
DELAY 250 ;DELAY ABOUT .25 SECONDS  
MOV #250,(PC)+  
.WORD 0  
MOV LSDLY,(PC)+  
.WORD 0  
DEC -6(PC)  
BNE .-4

CZTUWAO TUBO FRONT END PRT A MACRO M1200 29-MAR-83 13:24 PAGE 27-1  
SPACE - SPACE RECORDS (FORWARD AND REVERSE) COMMAND

```

010062 005367 177756          DEC    -22(PC)
010066 001367          BNE    .-20
1962 010070 005337 010150    DEC    SDELAY      ;BUMP DELAY COUNTER DOWN
1963 010074 001356          BNE    158         ;BR, IF MORE DELAY
1964 010076 000411          BR     608         ;BR IF TROUBLE CARRY = CLEAR
1965 010100 016501 000000    20$:  MOV    TSSR(R5),R1 ;READ TSSR
1966 010104 012702 000200    MOV    #SSR,R2     ;SET UP EXPECTED
1967 010110 020201          25$:  CMP    R2,R1   ;ARE THEY OK
1968 010112 001401          BEQ   408         ;BR, IF EQUAL = OK
1969 010114 000402          BR     608         ;TROUBLE EXIT
1970 010116 000261          40$:  SEC                 ;SET CARRY NO TROUBLE
1971 010120 000401          BR     708         ;EXIT
1972 010122 000241          60$:  CLC                 ;CARRY CLEAR = ERROR
1973 010124          70$:
1974 010124 010400          MOV    R4,R0      ;PASS PACKET ADDRESS
1975 010126 000207          RTS    PC         ;RETURN
1976
1977          ;PACKET FOR SPACE COMMAND
1978          ;
1980 010130          .BLKB 10-<.-TUV2A&7>
1982          ;
1983          ;COMMAND WORD
1984 010140 000000    80$:  .WORD
1985          ;NUMBER OF RECORDS TO BE SPACED OVER WORD
1986 010142 000000    90$:  .WORD
1987 010144 000000          .WORD
1988 010146 000000          .WORD
1989 010150 000000    SDELAY: .WORD 0      ;DELAY COUNTER
1990          .EVEN

```

CZTUWAO TU80 FRONT END PRT A MACRO M1200 29-MAR-83 13:24 PAGE 28  
WRTCHR - WRITE CHARACTERISTICS COMMAND

.SBTTL WRTCHR - WRITE CHARACTERISTICS COMMAND

1992  
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2021  
2022  
2023 010152  
2024 010152  
2025 010156 005037 002174  
2026 010162 010465 177776  
2027 010166 004737 017054  
2028 010172 103401  
2029 010174 000423  
2030 010176 016501 000000  
2031 010202 012702 000200  
2032 010206 032701 000100  
2033 010212 001402  
2034 010214 052702 000100  
2035 010220 020201  
2036 010222 001401  
2037 010224 000407  
2038 010226 062704 000010  
2039 010232 011403  
2040 010234 010337 002716  
2041 010240 000261  
2042 010242 000401  
2043 010244 000241  
2044 010246 016500 000000  
2045 010252 000207  
2046  
2047

:+  
:ROUTINE TO ISSUE A WRITE CHARACTERISTICS  
:COMMAND SO THAT OTHER COMMANDS WILL BE ACCEPTED  
:INPUT:  
:R4 ADDRESS OF PACKET FROM TEST  
:R5 FIRST DEVICE UNIBUS ADDRESS  
:REQUIRES A CALL TO SOFINIT BE DONE PREVIOUSLY  
:OUTPUT:  
:R0 TSSR CONTENTS  
:CARRY SET - WRITE CHARACTERISTICS COMMAND OK  
:CLR - WRITE CHARACTERISTICS FAILED  
:IMPLICIT OUTPUT:  
:MESSAGE BUFFER AND OTHER BUFFERS ALL SET UP  
:SOFTWARE SWITCHES SET AS FOLLOWS:  
:BENBSW = BUFFER ENABLE SWITCH ON OR OFF  
:SIDE EFFECTS:  
:-

WRTCHR::  
:SAVREG  
:CLR BENBSW :CLEAR BUFFER ENABLE SWITCH  
10\$: MOV R4,TSDB(R5) :SEND OUT COMMAND  
:JSR PC,CHKTSSR :WAIT FOR SSR  
:BCS 20\$ :BR, IF SSR IS SET AND OK  
:BR 60\$ :BR IF TROUBLE CARRY = CLEAR  
20\$: MOV TSSR(R5),R1 :READ TSSR  
:MOV #SSR,R2 :SET UP EXPECTED  
:BIT #OFL,R1 :WAS OFF LINE SET IN TSSR  
:BEQ 25\$ :BR, IF NO OFL SET  
:BIS #OFL,R2 :MAKE THEM LOOK ALIKE  
25\$: CMP R2,R1 :ARE THEY OK  
:BEQ 40\$ :BR, IF EQUAL = OK  
:BR 60\$ :TROUBLE EXIT  
40\$: ADD #8,R4 :POINT TO WRT CHARA DATA PACKET  
:MOV (R4),R3 :GET ADDRESS OF MESSAGE BUFFER  
:MOV R3,MESBFA :STORE FOR PRINT ROUTINES  
:SEC :SET CARRY NO TROUBLE  
:BR 70\$ :EXIT  
60\$: CLC :CARRY CLEAR = ERROR  
70\$: MOV TSSR(R5),R0 :RETURN TSSR CONTENTS  
:RTS PC :RETURN

CZTUWAO TUBO FRONT END PRT A MACHO M1200 29-MAR-83 13:24 PAGE 29  
 REWIND - POSITION TAPE (REWIND) COMMAND

.SBTTL REWIND - POSITION TAPE (REWIND) COMMAND

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2075  
2076  
2077 010254  
2078 010254  
2079 010260 012704 010350  
2080 010264 010465 177776  
2081 010270 012703 000550  
2082 010274 004737 016740  
2083 010300 103417  
2084 010302  
010302 012727 000372  
010306 000000  
010310 013727 002116  
010314 000000  
010316 005367 177772  
010322 001375  
010324 005367 177756  
010330 001367  
2085 010332 005303  
2086 010334 001357  
2087 010336 000241  
2088 010340 010400  
2089 010342 000207  
2090  
2091  
2093 010344  
2095 010350  
2096 010350 102010  
2097 010352 000000

```

+
: THIS ROUTINE WILL REWIND THE SELECTED TAPE.
:
: CAUTION: THE ROUTINE DOES NOT WAIT FOR BOT
:           TO ARRIVE. ALSO THE CALLER MUST CHECK FOR
:           SSR TO SET IN THE TSSR
:
: CALLING SEQUENCE:
:
: DO A SOFT INIT
: DO A WRITE CHARACTERISTICS
: JSR     PC,REWIND
:
: INPUT:
:
: R5      FIRST DEVICE UNIBUS ADDRESS
:
: OUTPUT
:
: R0      THE CONTENTS OF R4 IS PASSED TO R0
:
: -
REWIND::
: SAVREG                                :SAVE R1-R5 UNTIL NEXT RETURN
: MOV     #RWPACK,R4                    :GET PACKET ADDRESS
: MOV     R4,TSDB(R5)                   :SEND PACKET ADDRESS TO EXECUTE
: MOV     #360.,R3                       :ENOUGH TIME FOR 2400' REEL TO REWIND
10$: : JSR     PC,WAITF                     :WAIT FOR SSR TO SET
: BCS     20$                            :LEAVE WHEN SSR IS SET
: DELAY   250.                           :WAIT FOR .25 SECONDS
: MOV     #250.,(PC)+
: .WORD   0
: MOV     LSDLY,(PC)+
: .WORD   0
: DEC     -6(PC)
: BNE     -4
: DEC     -22(PC)
: BNE     -20
: DEC     R3                              :BUMP COUNTER DOWN
: BNE     10$                             :KEEP GOING
: CLC
: CLC                                     :CLEAR CARRY TO SET ERROR
20$: : MOV     R4,R0                        :PASS THE PACKET ADDRESS
: RTS     PC                              :RETURN
:
: .BLKB   10-<.-TUV2A&7>
RWPACK: : .WORD   102010                    :POSTION COMMAND (REWIND)
: .WORD   0                               :NOT USED
    
```

CZTUWAO TUBO FRONT END PRT A MACRO M1200 29-MAR-83 13:24 PAGE 30  
 CKRAM - COMPARE RAM TO I/O PACKET

.SBTTL CKRAM - COMPARE RAM TO I/O PACKET

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2100  
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2110  
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2118  
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2148  
2149  
2150

010354  
010354  
010360 012701 002206  
010364 012702 000020  
010370 005003  
010372 004737 017054  
010376 004737 017054  
010402 110265 177777  
010406 004737 017054  
010412 116511 177776  
010416 122124  
010420 001401  
010422 005203  
010424 005202  
010426 020227 000027  
010432 003761  
010434 005703  
010436 001402  
010440 000241  
010442 000401  
010444 000261  
010446 012737 000010 002246  
010454 000207

```

:ROUTINE TO READ THE FIRST 8 BYTES FROM RAM
:MEMORY AND COMPARE THIS DATA TO A COMMAND PACKET.
:INPUT:
:
:   R4      ADDRESS OF THE COMMAND PACKET
:   R5      FIRST DEVICE UNIBUS ADDRESS
:OUTPUT:
:
:   CARRY   SET - RAM MATCHES PACKET
:           CLR - RAM DOES NOT MATCH PACKET
:IMPLICIT OUTPUT:
:
:   THE TABLE RAMDATA IS FILLED WITH THE
:   DATA HELD IN RAM.
:   RAMSIZ IS SET TO 8. FOR PRAMPKT ROUTINE
:SIDE EFFECTS:
:
:-
    
```

```

CKRAM::
    SAVREG          ;SAVE THE GENERAL REGISTERS
    MOV             #RAMDATA,R1 ;ADDRESS TO SAVE THE RAM DATA
    MOV             #RMPKTBEGR,R2 ;BYTE ADDRESS OF FIRST RAM DATA
    CLR            R3          ;CLEAR THE ERROR FLAG
    JSR            PC,CHKTSSR   ;WAIT FOR SSR
    JSR            PC,CHKTSSR   ;WAIT FOR SSR TO SET
    MOV            R2,TSDBH(R5) ;SELECT NEXT RAM ADDRESS
    JSR            PC,CHKTSSR   ;WAIT FOR SSR TO SET
    MOV            TSBAL(R5),(R1) ;READ THE RAM DATA
    CMP            (R1)+,(R4)+  ;COMPARE TO EXPECTED
    BEQ            20$         ;BRANCH IF OK
    INC            R3          ;SET ERROR FLAG
    INC            R2          ;ADDRESS OF NEXT RAM LOCATION
    CMP            R2,#RMPKTEND ;REACHED END YET ?
    BLE            10$         ;BRANCH TILL ALL READ
    TST            R3          ;WAS AN ERROR FOUND ?
    BEQ            30$         ;BRANCH IF NOT
    CLC            ;CLEAR CARRY TO SHOW ERROR
    BR            50$         ;AND EXIT
    SEC            ;SHOW GOOD COMPARE
    MOV            #8.,RAMSIZ  ;SETUP RAMSIZ FOR PRAMPKT ROUTINE
    RTS            PC         ;RETURN
    
```

```

2152
2153
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2160
2162
2164
2165
2167
2170
2171
2172 010456
2173 010456
2174 010462 013705 010642
2175 010466 012701 002206
2176 010472 013702 010640
2177 010476 013703 002246
2178 010502 004737 017054
2179 010506 110265 177777
2180 010512 004737 017054
2181 010516 116521 177776
2182 010522 062702 000001
2183 010526 077313
2184 010530 013704 002246
2185 010534 013702 010640
2186 010540 060204
2187 010542 162704 000001
2188 010546
      010546 010446
      010550 010246
      010552 012746 010644
      010556 012746 000003
      010562 010600
      010564 104415
      010566 062706 000010
2189 010572 012701 002206
2190 010576 013703 002246
2191 010602 005004
2192 010604 112104
2193 010606 042704 177400
2194 010612
      010612 010446
      010614 012746 010715
      010620 012746 000002
      010624 010600
      010626 104415
      010630 062706 000006
2195 010634 077316
2196 010636 000207
2198 010640 000000
2199 010642 000000
2200 010644 045 116
2201 010715 045 101
2202
  
```

```

.SBTTL RAMER - READ AND DISPLAY SELECTED RAM
:+
:ROUTINE TO READ THE SELECTED RAM LOCATIONS
:INPUT:
:      R5      FIRST DEVICE UNIBUS ADDRESS
:      CONSOLE WILL ALSO BE PRINTED TO
:IMPLICIT OUTPUT:
:      THE TABLE RAMDATA IS FILLED WITH THE
:      DATA HELD IN RAM.
:SIDE EFFECTS:
:-
RAMER::
      SAVREG      ;SAVE THE GENERAL REGISTERS
      MOV      RAMR5H,R5      ;RESET R5 TO FIRST DEVICE REGISTER
      MOV      #RAMDATA,R1    ;ADDRESS TO SAVE THE RAM DATA
      MOV      RAMHLD,R2      ;BYTE ADDRESS OF THE FIRST RAM DATA
      MOV      RAMSIZ,R3      ;SET THE SIZE OF THE READ UP
10$:  JSR      PC,CHKTSSR      ;WAIT FOR THE SSR TO SET
      MOVB     R2,TSDBH(R5)    ;SELECT NEXT RAM ADDRESS
      JSR      PC,CHKTSSR      ;WAIT FOR SSR TO SET
      MOVB     TSBAL(R5),(R1)+ ;READ THE RAM DATA
20$:  ADD      #1,R2          ;ADDRESS OF THE NEXT RAM LOCATION
      SOB     R3,10$         ;NUMBER OF LOCATIONS COUNTER
      MOV     RAMSIZ,R4      ;GET THE RAM SIZE
      MOV     RAMHLD,R2      ;GET THE STARTING RAM ADDRESS
      ADD     R2,R4          ;CALCULATE THE END ADDRESS
      SUB     #1,R4          ;CORRECT VALUE OF PRINTOUT
      PRINTX  #RAMIOP,R2,R4   ;RAM ADDRESS = 10 - 17, ETC.
      MOV     R4,-(SP)
      MOV     R2,-(SP)
      MOV     #RAMIOP,-(SP)
      MOV     #3,-(SP)
      MOV     SP,R0
      TRAP   CSPNTX
      ADD     #10,SP
      MOV     #RAMDATA,R1    ;ADDRESS OF WHERE RAM DATA IS
      MOV     RAMSIZ,R3      ;THE SIZE OF THE RAM FIELD READ
30$:  CLR     R4              ;NO EXTRA DATA LEFT OVER
      MOVB   (R1)+,R4        ;PICK UP BYTE OF RAM DATA
      BIC    #177400,R4      ;GET RID OF SIGN EXTEND
      PPINTX #RAMPD,R4      ;'010 211 111 222 377 000 123 134 ETC.'
      MOV     R4,-(SP)
      MOV     #RAMPD,-(SP)
      MOV     #2,-(SP)
      MOV     SP,R0
      TRAP   CSPNTX
      ADD     #6,SP
      SOB     R3,30$        ;LOOP UNTIL ALL PRINTED
50$:  RTS     PC              ;RETURN
      RAMHLD: .WORD 0        ;RAM ADDR HOLDER 1ST ADDRESS
      RAMR5H: .WORD 0        ;HOLDS R5 FOR LATER
045  RAMIOP: .ASCIZ 'XN% Ram Address (Octal) = X03%A - X03%N'
040  RAMPD:  .ASCIZ '%A X03%A '
      .EVEN
  
```

CZTUWAO TUBO FRONT END PRT A MACRO M1200 29-MAR-83 13:24 PAGE 32  
 CKRAM2 - COMPARE RAM TO I/O CHARACTERISTICS DATA

```

2204 .SBTTL CKRAM2 - COMPARE RAM TO I/O CHARACTERISTICS DATA
2205 :+
2206 :ROUTINE TO READ THE FIRST 8 OR 10 BYTES FROM RAM
2207 :MEMORY AND COMPARE THIS DATA TO A CHARACTERISTICS DATA BLOCK.
2208 :INPUT:
2209 :
2210 :
2211 :
2212 :       R4      ADDRESS OF THE CHARACTERISTICS DATA
2213 :       R5      FIRST DEVICE UNIBUS ADDRESS
2214 :
2215 :OUTPUT:
2216 :
2217 :       CARRY   SET - RAM MATCHES PACKET
2218 :              CLR - RAM DOES NOT MATCH PACKET
2219 :
2220 :IMPLICIT OUTPUT:
2221 :
2222 :       THE TABLE RAMDATA IS FILLED WITH THE
2223 :       DATA HELD IN RAM.
2224 :       RAMSIZ IS SET TO 8. OR 10. FOR PRAMPKT ROUTINE
2225 :
2226 :SIDE EFFECTS:
2227 :
2228 :
2229 :-
2230
2231 CKRAM2::
2232 SAVREG ;SAVE THE GENERAL REGISTERS
2233 MOV #RAMDATA,R1 ;ADDRESS TO SAVE THE RAM DATA
2234 MOV #RMCHBEG,R2 ;BYTE ADDRESS OF FIRST RAM DATA
2235 CLR R3 ;CLEAR THE ERROR FLAG
2236 JSR PC,CHKTSSR ;WAIT FOR SSR
2237 JSR PC,CHKTSSR ;WAIT FOR SSR TO SET
2238 MOVB R2,TSDBH(R5) ;SELECT NEXT RAM ADDRESS
2239 JSR PC,CHKTSSR ;WAIT FOR SSR TO SET
2240 MOVB TSBAL(R5),(R1) ;READ THE RAM DATA
2241 CMPB (R1)+,(R4)+ ;COMPARE TO EXPECTED
2242 BEQ 20$ ;BRANCH IF OK
2243 INC R3 ;SET ERROR FLAG
2244 INC R2 ;ADDRESS OF NEXT RAM LOCATION
2245 MOV #8.,RAMSIZ ;ASSUME NORMAL NOT SET
2246 CMP R2,#RMCHEND-2 ;REACHED END YET ?
2247 BLE 10$ ;BRANCH TILL ALL READ
2248 TST R3 ;WAS AN ERROR FOUND ?
2249 BEQ 30$ ;BRANCH IF NOT
2250 CLC ;CLEAR CARRY TO SHOW ERROR
2251 BR 50$ ;AND EXIT
2252 SEC ;SHOW GOOD COMPARE
2253 RTS PC ;RETURN
2254

```

CZTUWAG TU80 FRONT END PRT A MACRO M1200 29-MAR-83 13:24 PAGE 33  
CKMSG - COMPARE WRITE CHAR. MESSAGE BUFFERS

.SBTTL CKMSG - COMPARE WRITE CHAR. MESSAGE BUFFERS

2256  
2257  
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2280  
2281 011032  
2282 011032  
2283 011036 010037 002250  
2284 011042 010137 002252  
2285 011046 005737 003102  
2286 011052 001403  
2287 011054 004737 020106  
2288 011060 010001  
2289 011062 005004  
2290 011064 005003  
2291 011066 010205  
2292 011070 011264 002266  
2293 011074 011164 002432  
2294 011100 022221  
2295 011102 001401  
2296 011104 005203  
2297 011106 062704 000002  
2298 011112 020427 000014  
2299 011116 003764  
2300 011120 032765 000200 000012  
2301 011126 001403  
2302 011130 020427 000016  
2303 011134 003755  
2304 011136 005703  
2305 011140 001402  
2306 011142 000241  
2307 011144 000401  
2308 011146 000261  
2309 011150 000207  
2310

```

:ROUTINE TO COMPARE A WRITE CHARACTERISTICS EXPD AND RECV
:BUFFER. THE EXPECTED AND RECEIVED BUFFERS ARE STORED FOR
:ERROR PRINT ROUTINES.
:INPUT:
      R0      RECV MESSAGE BUFFER HIGH ORDER ADDRESS
      R1      RECV MESSAGE BUFFER LOW ORDER ADDRESS
      R2      EXPD MESSAGE BUFFER ADDRESS
:OUTPUT:
      CARRY   SET - MESSAGE BUFFERS MATCH
      CLR    -MESSAGE BUFFERS DON'T MATCH
:IMPLICIT OUTPUT:
      EXPMSG  BUFFER IS SET TO EXPD DATA
      RECMSG  BUFFER IS SET TO RECV DATA
      RCVHIADD SET TO HIGH ORDER ADDRESS OF RECV
      RCVLOADD SET TO LOW ORDER ADDRESS OF RECV
-
CKMSG::
      SAVREG          :SAVE R1-R5 UNTIL NEXT RETURN
      MOV      PO,RCVHIADD :SAVE RECV HIGH ADDRESS
      MOV      R1,RCVLOAD  :SAVE RECV LOW ADDRESS
      TST      KTENABLE    :TESTING ABOVE 28K?
      BEQ      10$         :BR IF NO
      JSR      PC,SETMAP   :RETURN ADDRESS BIASED TO PAR6 IN R0
      MOV      R0,R1      :GET RETURNED ADDRESS BIASED TO PAR6
10$:  CLR      R4         :WORD IN BUFFER
      CLR      R3         :CLEAR ERROR SEEN FLAG
      MOV      R2,R5      :GET EXPD BUFFER ADDRESS
15$:  MOV      (R2),EXPMSG(R4) :SAVE EXPD FOR ERROR REPORT
      MOV      (R1),RECMSG(R4) :SAVE RECV FOR ERROR REPORT
      CMP      (R2)+,(R1)+  :EXPD EQUAL RECV?
      BEQ      25$         :BR IF YES
      INC      R3         :SET ERROR SEEN FLAG
25$:  ADD      #2,R4      :POINT TO NEXT WORD ADDRESS
      CMP      R4,#14     :DONE FIRST 7 WORDS?
      BLE     15$         :BR IF NO
      BIT      #X2.EXTF,XST2(R5) :IS EXTENDED FEATURES SET IN EXPD?
      BEQ      50$         :BR IF NO
      CMP      R4,#16     :DONE EXTENDED FEATURES WORD?
      BLE     15$         :BR IF NO
50$:  TST      R3         :ANY ERRORS SEEN?
      BEQ      55$         :BR IF NO
      CLC          :SET FAILURE
      BR      60$
55$:  SEC          :SET SUCCESS
60$:  RTS      PC       :RETURN

```



CZTUWAO TUBO FRONT END PRT A MACRO M1200 29-MAR-83 13:24 PAGE 34  
CKMSG2 - COMPARE EXPD RECV MESSAGE BUFFERS

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2336  
2337  
2338  
2339 011152  
2340 011152  
2341 011156 020327 000144  
2342 011162 003412  
2343 011164 012703 000144  
2344 011170  
011170 012746 011304  
011174 012746 000001  
011200 010600  
011202 104417  
011204 062706 000004  
2345 011210 010037 002250  
2346 011214 010137 002252  
2347 011220 005737 003102  
2348 011224 001403  
2349 011226 004737 020106  
2350 011232 010001  
2351 011234 005004  
2352 011236 005005  
2353 011240 111264 002266  
2354 011244 111164 002432  
2355 011250 122221  
2356 011252 001401  
2357 011254 005205  
2358 011256 062704 000001  
2359 011262 020403  
2360 011264 002001  
2361 011266 000764  
2362 011270 005705  
2363 011272 001402

.SBTTL CKMSG2 - COMPARE EXPD RECV MESSAGE BUFFERS

ROUTINE TO COMPARE AN EXPECTED AND RECEIVED MESSAGE  
BUFFER. THE EXPECTED AND RECEIVED BUFFERS ARE STORED FOR  
ERROR PRINT ROUTINES.

INPUT:

R0 RECV MESSAGE BUFFER HIGH ORDER ADDRESS  
R1 RECV MESSAGE BUFFER LOW ORDER ADDRESS  
R2 EXPD MESSAGE BUFFER ADDRESS  
R3 NUMBER OF BYTES TO COMPARE

OUTPUT:

CARRY SET - MESSAGE BUFFERS MATCH  
CLR - MESSAGE BUFFERS DON'T MATCH

IMPLICIT OUTPUT:

EXPMSG BUFFER IS SET TO EXPD DATA  
RECMG BUFFER IS SET TO RECV DATA  
RCVHIADD SET TO HIGH ORDER ADDRESS OF RECV  
RCVLOADD SET TO LOW ORDER ADDRESS OF RECV

CKMSG2::

SAVREG ;SAVE R1-R5 UNTIL NEXT RETURN  
CMP R3,#RECMG-EXPMSG;@D IS COUNT ABOVE MAX ALLOWED?  
BLE 5\$ ;@D BR IF NO  
MOV #RECMG-EXPMSG,R3;@D  
PRINTF #DEBUGMSG ;@D  
MOV #DEBUGMSG,-(SP)  
MOV J1,-(SP)  
MOV SP,R0  
TRAP CSPNTF  
ADD #4,SP  
5\$: MOV R0,RCVHIADD ;SAVE RECV HIGH ADDRESS  
MOV R1,RCVLOAD ;SAVE RECV LOW ADDRESS  
TST KENABLE ;TESTING ABOVE 28K?  
BEQ 10\$ ;BR IF NO  
JSR PC,SETMAP ;RETURN ADDRESS BIASED TO PAR6 IN R0  
MOV R0,R1 ;GET RETURNED ADDRESS BIASED TO PAR6  
10\$: CLR P4 ;WORD IN BUFFER  
CLR R5 ;CLEAR ERROR SEEN FLAG  
15\$: MOVB (R2),EXPMSG(R4) ;SAVE EXPD FOR ERROR REPORT  
MOVB (R1),RECMG(R4) ;SAVE RECV FOR ERROR REPORT  
CMPB (R2)+,(R1)+ ;EXPD EQUAL RECV?  
BEQ 25\$ ;BR IF YES  
INC R5 ;SET ERROR SEEN FLAG  
25\$: ADD #1,R4 ;POINT TO NEXT BYTE  
CMP R4,R3 ;DONE ALL BYTES?  
BGE 50\$ ;BR IF YES  
BR 15\$ ;DO NEXT BYTE  
50\$: TST R5 ;ANY ERRORS SEEN?  
BEQ 55\$ ;BR IF NO

CZTUWAO TUBO FRONT END PRT A MACRO M1200 29-MAR-83 13:24 PAGE 34-1  
CKMSG2 - COMPARE EXPD RECV MESSAGE BUFFERS

```

2364 011274 000241          CLC          ;SET FAILURE
2365 011276 000401          BR          608          ;
2366 011300 000261          558: SEC          ;SET SUCCESS
2367 011302 000207          608: RTS          Pr          ;RETURN
2368
2369 011304 120 122 117 DEBUGMSG: .ASCIZ 'PROGRAM INTERNAL ERROR -CKMSG2 MESSAGE BUFFER EXCEEDED-';000
2370 011374 045 116 045 FERCM: .ASCII /XNZA ***/
2371 011405 040 040 124 ERCM: .ASCIZ / TSSR ERROR CODE REC'D = /
2372 011440 056 056 056 SIMSG: .ASCIZ /.... AFTER DOING SOFT INIT/
2373 011473 124 105 123 TINERR: .ASCIZ /TEST: .../
2374 .EVEN

```

CZTUWAO TURBO FRONT END PRT A MACRO F 200 29-MAR-83 13:24 PAGE 35  
 CKMSG2 - COMPARE EXPD RECV MESSAGE BUFFERS

2376  
 2377  
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011506  
 011506  
 011506 004737 005264  
 011512 004737 017772  
 011516  
 011516  
 011516 104423  
 011520  
 011520  
 011520 004737 005264  
 011524 012700 000004  
 011530 004737 006704  
 011534 013700 002716  
 011540 005001  
 011542 004737 013702  
 011546  
 011546  
 011546 104423

```

:PRINT ROUTINE TO FAIAL SOFT INIT ERRORS
:INPUT:
:      R1      CONTENTS OF TSSR AT ERROR
:SIDE EFFECTS:
:      EXECUTES DROP UNIT TO CEASE TESTING
:-
      BGNMSG  SFMSG
SFMSG:: JSR    PC,PRITSSR    ;PRINT CONTENTS OF TSSR REGISTER
        JSR    PC,CKDROP    ;DROP UNIT, IF ALLOWED
        ENDMSG
L10003: TRAP    CSMSG

:PRINT ROUTINE TO PRINT THE CONTENTS OF
:TSSR AND A COMMAND PACKET OTHER THAN GET STATUS COMMAND PACKET.
:INPUTS:
:      R1      TSSR CONTENTS
:      R4      ADDRESS OF COMMAND PACKET
:-
      BGNMSG  PKTSSR
PKTSSR:: JSR    PC,PRITSSR    ;PRINT THE CONTENTS OF TSSR REGISTER
        MOV    #4,R0        ;NO. OF WORDS IN PACKET
        JSR    PC,PRIPKT    ;PRINT THE CONTENTS OF COMMAND PACKET
        MOV    MESBFA,R0    ;ADDRESS OF MESSAGE BUFFER
        CLR    R1           ;ASSUME NO HIGH MEMORY
        JSR    PC,PRMESS    ;PRINT THE MESSAGE BUFFER ALSO
        ENDMSG
L10004: TRAP    CSMSG

:PRINT ROUTINE TO PRINT THE CONTENTS OF
:TSSR AND A GET STATUS COMMAND PACKET.
:INPUTS:
:      R1      TSSR CONTENTS
:      R4      ADDRESS OF COMMAND PACKET
    
```

CZTUWAO TUBO FRONT END PRT A MACRO M1200 29-MAR-83 13:24 PAGE 35-1  
 CKMSG2 - COMPARE EXPD RECV MESSAGE BUFFERS

```

2427
2428 011550          BGNMSG  PKTGETS
      011550          PKTGETS::
2429 011550 004737 005264      JSR      PC,PRITSSR      ;PRINT THE CONTENTS OF TSSR REGISTER
2430 011554 012700 000002      MOV      #2,R0          ;NO. OF WORDS IN GET STATUS PACKET
2431 011560 004737 006704      JSR      PC,PRIPKT      ;PRINT THE CONTENTS OF COMMAND PACKET
2432 011564          ENDMSG
      011564          L10005:
      011564 104423      TRAP      CSMSG

2433
2434
2435
2436          ;+
2437          ;PRINT TSSR ERRORS FOR INITIALIZATION TESTS
2438          ;
2439          ;INPUTS:
2440          ;
2441          ;      R1      TSSR CONTENTS
2442          ;      R4      ADDRESS OF COMMAND PACKET
2443          ;-
2444 011566          BGNMSG  SFFMSG
      011566          SFFMSG::
2445 011566 004737 005264      JSR      PC,PRITSSR      ;PRINT CONTENTS OF TSSR REGISTER
2446 011572          ENDMSG
      011572          L10006:
      011572 104423      TRAP      CSMSG

2447
2448
2449          .SBTTL  PKTMES - PRINT TSSR AND MESSAGE BUFFER
2450          ;+
2451          ;PRINT ROUTINE TO PRINT THE CONTENTS OF TSSR AND MESSAGE
2452          ;BUFFER FOR ERROR REPORTS
2453          ;
2454          ;INPUTS:
2455          ;
2456          ;      R1      CONTENTS OF TSSR
2457          ;      R2      LOW ORDER MESSAGE BUFFER
2458          ;      R3      HIGH ORDER MESSAGE BUFFER ADDRESS
2459          ;      NOTE: R3 IS IGNORED IF KTENABLE FLAG IS CLEAR
2460          ;-
2461          ;
2462 011574          BGNMSG  PKTMES
      011574          PKTMES::
2463 011574 004737 005264      JSR      PC,PRITSSR      ;PRINT CONTENTS OF TSSR
2464 011600 010200          MOV      R2,R0          ;LOW ORDER ADDRESS
2465 011602 010301          MOV      R3,R1          ;HIGH ORDER ADDRESS
2466 011604 004737 013702      JSR      PC,PRMESS      ;PRINT THE MESSAGE BUFFER
2467 011610          ENDMSG
      011610          L10007:
      011610 104423      TRAP      CSMSG

2468
    
```

CZTUWAO TUBO FRONT END PRT A MACRO M1200 29-MAR-83 13:24 PAGE 36  
ADDSSR - PRINT TEST ADDRESS AND TSSR

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2478
2479
2480
2481
2482 011612
      011612
2483 011612 004737 007640
2484 011616 016501 000000
2485 011622 004737 005264
2486 011626
      011626
      011626 104423
2487
2488
2489
2490
2491
2492
2493
2494
2495
2496
2497
2498
2499
2500
2501 011630
      011630
2502 011630 012700 000007
2503 011634 004737 015242
2504 011640
      011640
      011640 104423
2505
2506

```

```

      .SBTTL  ADDSSR - PRINT TEST ADDRESS AND TSSR
      :+
      :PRINT ROUTINE TO PRINT THE CONTENTS OF
      :TSSR AND A MEMORY TEST ADDRESS
      :INPUTS:
      :
      :      RS      FIRST DEVICE UNIBUS ADDRESS
      :      ERRHI   HIGH ORDER MEMORY TEST ADDRESS
      :      ERRLO   LOW ORDER MEMORY TEST ADDRESS
      :-
      BGNMSG  ADDSSR
ADDSSR::
      JSR     PC,PRITADD      ;PRINT MEMORY TEST ADDRESS
      MOV     TSSR(R5),R1    ;GET CURRENT TSSR
      JSR     PC,PRITSSR     ;PRINT THE CONTENTS OF TSSR REGISTER
      ENDMSG
L10010:
      TRAP   CSMSG

      .SBTTL  MSGEXP - PRINT WRITE CHAR. EXPD-RCV MESSAGE BUFFERS
      :+
      :PRINT ROUTINE TO PRINT WRITE CHARACTERISTIC MESSAGE BUFFER
      :IMPLICIT INPUTS:
      :
      :      EXPMSG  - EXPECTED MESSAGE BUFFER
      :      RECMSG  - RECEIVED MESSAGE BUFFER
      :      RCVHIADD- RECEIVED MESSAGE BUFFER HIGH ORDER ADDRESS
      :      RCVLOADD- RECEIVED MESSAGE BUFFER LOW ORDER ADDRESS
      :-
      BGNMSG  MSGEXP
MSGEXP::
      MOV     #7,R0          ;ASSUME NO EXT FEATURES
      JSR     PC,PRMSGEXP   ;PRINT EXPD/RCV MESSAGE BUFFERS
      ENDMSG
L10011:
      TRAP   CSMSG

```

CZIUMAO TUBO FRONT END PRT A MACRO M1200 29-MAR-83 13:24 PAGE 37  
FIFEXP - PRINT FIFO EXP/RECV DATA

2508  
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2527  
2528  
2529

011642  
011642  
011642 010146 011714  
011644 012746 011714  
011650 012746 000002  
011654 010600  
011656 104415  
011660 062706 000006  
011664  
011664 012746 011763  
011670 012746 000001  
011674 010600  
011676 104415  
011700 062706 000004  
011704 010100  
011706 004737 015612  
011712  
011712  
011712 104423  
011714 045 116  
011763 045 116

```
.SBTTL FIFEXP - PRINT FIFO EXP/RECV DATA
*
:PRINT ROUTINE TO PRINT FIFO EXP/RECV DATA
:      R1      - BYTE COUNT
:IMPLICIT INPUTS:
:      EXPMSG - EXPECTED MESSAGE BUFFER (CONTAINS FIFO DATA ONLY)
:      RECMSG - RECEIVED MESSAGE BUFFER (CONTAINS FIFO DATA ONLY)
:--
:      BGNMSG FIFEXP
FIFEXP::
:PRINTX #FIF1MSG,R1      :PRINT BYTES TRANSFERRED
:MOV R1,-(SP)
:MOV #FIF1MSG,-(SP)
:MOV #2,-(SP)
:MOV SP,R0
:TRAP C$PNTX
:ADD #6,SP
:PRINTX #FIF2MSG      :PRINT HEADER MSG
:MOV #FIF2MSG,-(SP)
:MOV #1,-(SP)
:MOV SP,R0
:TRAP C$PNTX
:ADD #4,SP
:MOV R1,R0      :GET BYTE COUNT
:JSR PC,PRBYTEXP :PRINT FIFO BYTES IN ERROR
:ENDMSG
L10012:
:TRAP C$MSG
:ASCIZ 'XNZA NUMBER OF BYTES TRANSFERRED = X02'
:ASCIZ 'XNZA FIFO DATA BYTES IN ERROR:'
.EVEN
```

CZTUWAO TUBO FRONT END PRT A MACRO M1200 29-MAR-83 13:24 PAGE 38  
MSGSTAT - PRINT STATUS HEADER AND MESSAGE BUFFERS

2531  
2532  
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2539  
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2541  
2542  
2543  
2544 012022  
012022  
2545 012022 012701 012064  
2546 012026 012100  
2547 012030 001410  
2548 012032  
012032 010046  
012034 012746 000001  
012040 010600  
012042 104415  
012044 062706 000004  
2549 012050 000766  
2550 012052 012700 000012  
2551 012056 004737 015242  
2552 012062  
012062  
012062 104423  
2553  
2554 012064 012102 012144 012235  
2555 012102 045 116 045  
2556 012144 045 116 045  
2557 012235 045 116 045  
2558 012326 045 116 045  
2559 012417 045 116 045  
2560 012461 045 116 045  
2561  
2562  
2563  
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2565  
2566  
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2575  
2576  
2577 012536  
012536  
2578 012536 012701 012600

```

.SBTIL MSGSTAT - PRINT STATUS HEADER AND MESSAGE BUFFERS
:
:PRINT ROUTINE TO PRINT MESSAGE BUFFER EXPD/RCV
:
:IMPLICIT INPUTS:
:
:   EXPMSG - EXPECTED MESSAGE BUFFER
:   RECMSG - RECEIVED MESSAGE BUFFER
:   RCVHIADD- RECEIVED MESSAGE BUFFER HIGH ORDER ADDRESS
:   RCVLOADD- RECEIVED MESSAGE BUFFER LOW ORDER ADDRESS
:
:
:   BGNMSG MSGSTAT
MSGSTAT::
10$:  MOV     #STATCOD,R1      ;ASCII ADDRESS TABLE
      MOV     (R1)+,R0        ;DONE ALL MSG LINES?
      BEQ     20$             ;BR IF YES
      PRINTX R0              ;PRINT STATUS BIT NAMES
      MOV     R0,-(SP)
      MOV     #1,-(SP)
      MOV     SP,R0
      TRAP   C$PNTX
      ADD     #4,SP
      BR     10$              ;DO ANOTHER MSG LINE
20$:  MOV     #10.,R0         ;NUMBER OF WORDS IN A READ STATUS BUFFER
      JSR    PC,PRMSGEXP     ;PRINT EXPD/RCV MESSAGE BUFFERS
      ENDMG
L10013: TRAP   C$MSG

STATCOD: .WORD 1$,2$,3$,4$,5$,6$,0
1$: .ASCIZ 'XNZA Tape Bus Signals in Word #8:'
2$: .ASCIZ 'XNZA PARERR<15> IEOT <12> IFMK <9> IRDY<6> IRWD<2>'
3$: .ASCIZ 'XNZA IRESV2<14> IIDENT<11> IHER <8> IONL<5> IFBY<1>'
4$: .ASCIZ 'XNZA IRESV1<13> ICER <10> ISPEED<7> ILDP<4> IFPT<0>'
5$: .ASCIZ 'XNZA Tape Bus Signals in Word #9:'
6$: .ASCIZ 'XNZA DATMIS<7> ILW<6> OUTRDY<5> INRDY<4>'
      .EVEN

.SBTTL MSGLOOP - PRINT LOOPBACK HEADER AND MESSAGE BUFFERS
:
:PRINT ROUTINE TO PRINT MESSAGE BUFFER EXPD/RCV
:
:IMPLICIT INPUTS:
:
:   EXPMSG - EXPECTED MESSAGE BUFFER
:   RECMSG - RECEIVED MESSAGE BUFFER
:   RCVHIADD- RECEIVED MESSAGE BUFFER HIGH ORDER ADDRESS
:   RCVLOADD- RECEIVED MESSAGE BUFFER LOW ORDER ADDRESS
:
:
:   BGNMSG MSGLOOP
MSGLOOP::
MOV     #LOOPCOD,R1      ;ASCII ADDRESS TABLE

```

CZTUWAO TUBO FRONT END PRT A MACRO M1200 29-MAR-83 13:24 PAGE 38-1  
MSGLOOP - PRINT LOOPBACK HEADER AND MESSAGE BUFFERS

```

2579 012542 012100          10$: MOV      (R1)+,R0          ;DONE ALL MSG LINES?
2580 012544 001410          BEQ      20$              ;BR IF YES
2581 012546          PRINTX  R0              ;PRINT STATUS BIT NAMES
      012546 010046          MOV      R0,-(SP)
      012550 012746 000001    MOV      #1,-(SP)
      012554 010600          MOV      SP,R0
      012556 104415          TRAP     C$PNTX
      012560 062706 000004    ADD      #4,SP
2582 012564 000766          BR       10$              ;DO ANOTHER MSG LINE
2583 012566 012700 000012    20$: MOV      #10.,R0          ;NUMBER OF WORDS IN A READ STATUS BUFFER
2584 012572 004737 015242    JSR      PC,PRMSGEXP      ;PRINT EXPD/RECV MESSAGE BUFFERS
2585 012576          ENDMSG
      012576          L10014:
      012576 104423          TRAP     C$MSG
2586
2587 012600 012620 012673 012772 LOOPCOD: .WORD 1$,2$,3$,4$,5$,6$,7$,0
2588 012620          045 116 045 18: .ASCIZ 'XNZA Tape Bus Loopback Signals in Word #8:'
2589 012673          045 116 045 28: .ASCIZ 'XNZA PARERR<15> IRESV2<14> IRESV1<13>'
2590 012772          045 116 045 38: .ASCIZ 'XNZA IHISP=>IEOT<12> IWRT=>IIDENT<11> IREV =>ICER <10>'
2591 013071          045 116 045 48: .ASCIZ 'XNZA IWFM =>IFMK<09> IEDIT=>IHER <08> IFAD =>ISPEED<07>'
2592 013170          045 116 045 58: .ASCIZ 'XNZA ITADO=>IRDY<06> ITAD1=>IONL <05> IERASE=>ILDPA <04>'
2593 013267          045 116 045 68: .ASCIZ 'XNZA IREW =>IDBY<03> IRWU =>IRWD <02> IFEN =>IFBY <01>'
2594 013366          045 116 045 78: .ASCIZ 'XNZA IGO =>IFPT<00>'
2595
2596
      .EVEN

```



CZTUWAO TUBJ FRONT END PRT A MACRO M1200 29-MAR-83 13:24 PAGE 39  
 MSGSUB - PRINT WRITE SUBSYSTEM MESSAGE BUFFER

2598  
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 2600  
 2601  
 2602  
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 2605  
 2606  
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 2609  
 2610  
 2611 013414  
 013414  
 2612 013414 012700 000012  
 2613 013420 004737 015242  
 2614 013424  
 013424  
 013424 104423  
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 2632 013426  
 013426  
 2633 013426 004737 007524  
 2634 013432 013701 002176  
 2635 013436 013702 002200  
 2636 013442 004737 007306  
 2637 013446  
 013446  
 013446 104423  
 2638

```
.SBTTL MSGSUB - PRINT WRITE SUBSYSTEM MESSAGE BUFFER
:
:PRINT ROUTINE TO PRINT MESSAGE BUFFER EXPD/RCV
:
:IMPLICIT INPUTS:
:
:   EXPMSG - EXPECTED MESSAGE BUFFER
:   RECMSG - RECEIVED MESSAGE BUFFER
:   RCVHIADD- RECEIVED MESSAGE BUFFER HIGH ORDER ADDRESS
:   RCVLOADD- RECEIVED MESSAGE BUFFER LOW ORDER ADDRESS
:-
:   BGNMSG MSGSUB
MSGSUB::
:   MOV     #10,R0           ;SIZE OF WRITE SUBSYSTEM BUFFER
:   JSR     PC,PRMSGEXP     ;PRINT EXPD/RCV MESSAGE BUFFERS
:   ENDMSG
L10015:
:   TRAP    C$MSG
```

```
.SBTTL MEMADD - PRINT MEMORY ADDRESS DATA ERROR
:
:PRINT ROUTINE TO PRINT MEMORY ADDRESS DATA COMPARE ERROR
:
:IMPLICIT INPUTS:
:
:   ERRHI - MEMORY ERROR HIGH ORDER ADDRESS
:   ERRLO - MEMORY ERROR LOW ORDER ADDRESS
:   EXP   - EXPECTED DATA
:   RECV  - RECEIVED DATA
:-
:   BGNMSG MEMADD
MEMADD::
:   JSR     PC,PRIADD       ;PRINT MEMORY ADDRESS IN ERROR
:   MOV     EXPD,R1         ;GET EXPD DATA
:   MOV     RECV,R2        ;GET RECEIVED DATA
:   JSR     PC,PRIOR       ;PRINT EXPD/RCV
:   ENDMSG
L10016:
:   TRAP    C$MSG
```

CZTUWAO TUBO FRONT END PRT A MACRO M1200 29-MAR-83 13:24 PAGE 40  
 PRAMPKT - PRINT RAM AND PACKET DATA

2640  
2641  
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2661 013450  
2662 013450  
2663 013454 012701 002206  
2664 013460 005002  
2665 013462 122124  
2666 013464 001000  
2667 013466 116105 177777  
2668 013472 116403 177777  
2669 013476  
2670 013506 042703 177400  
2671 013512 116137 177777 002200  
2672 013520 116437 177777 002176  
2673 013526  
013526 010346  
013530 013746 002176  
013534 013746 002200  
013540 010246  
013542 012746 013616  
013546 012746 000005  
013552 010600  
013554 104414  
013556 062706 000014  
2674 013562 005202  
2675 013564 005737 002246  
2676 013570 001404  
2677 013572 020237 002246  
2678 013576 003731  
2679 013600 000403  
2680 013602 020227 000010  
2681 013606 002725  
2682 013610 005037 002246  
2683 013614 000207  
2684  
2685 013616 045 116 045 RAMASC: .ASCIZ 'XNXA BYTE: XD2XA RAM: X03XA Packet: X03XA XOR:X03'  
2686

```

.SBTTL PRAMPKT - PRINT RAM AND PACKET DATA
:
:PRINT ROUTINE TO DISPLAY RAM/PACKET DATA
:WHEN THE RAM DATA DOES NOT MATCH.
:
:INPUTS:
:
:      R4      POINTER TO COMMAND PACKET
:
:IMPLICIT INPUTS:
:
:      RAMDATA  DATA AS READ FROM THE RAM
:      RAMSIZ   NUMBER OF BYTES IN PACKET
:              IF RAMSIZ=0 THEN DEFAULT TO 8.
:
:IMPLICIT OUTPUTS:
:
:      RAMSIZ  SET TO 0
:
PRAMPKT:
:      SAVREG          :SAVE R1-R5 UNTIL NEXT RETURN
:      MOV      #RAMDATA,R1 :DATA FROM THE RAM
:      CLR      R2          :INIT BYTE NUMBER
5$:  :      CMPB     (R1)+,(R4)+ :COMPARE EXPECTED, RECEIVED
:      BNE     7$          :BR IF NO MATCH
7$:  :      MOVB    -1(R1),R5   :GET RECV RAM DATA
:      MOVB    -1(R4),R3   :GET EXPD PACKET DATA
:      XOR     R5,R3       :XOR EXPD/RECV
:      BIC     #177400,R3  :LOW BYTE ONLY
:      MOVB    -1(R1),RECV :GET RECEIVED RAM DATA
:      MOVB    -1(R4),EXPD :GET EXPECTED RAM DATA
:      PRINTB  #RAMASC,R2,RECV,EXPD,R3
:      MOV     R3,-(SP)
:      MOV     EXPD,-(SP)
:      MOV     RECV,-(SP)
:      MOV     R2,-(SP)
:      MOV     #RAMASC,-(SP)
:      MOV     #5,-(SP)
:      MOV     SP,R0
:      TRAP   C$PNTB
10$: :      ADD     #14,SP
:      INC     R2          :UPDATE BYTE COUNT
:      TST    RAMSIZ      :DEFAULT TO 8.?
:      BEQ    15$        :BR IF YES
:      CMP    R2,RAMSIZ  :DONE ALL BYTES?
:      BLE    5$         :BR IF NO
:      BR    25$
15$: :      CMP    R2,#8.    :DONE DEFAULT NUMBER OF BYTES?
20$: :      BLT    5$         :BR IF NO
25$: :      CLR    RAMSIZ   :SET DEFAULT RAMSIZ
:      RTS    PC        :RETURN

```

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2726  
2727

013702  
013702  
013706 010537 010642  
013712 010005  
013714 005737 003102  
013720 001001  
013722 005001  
013724 010103  
013726 006100  
013730 006101  
013732  
013732 010546  
013734 010146  
013736 012746 014533  
013742 012746 000003  
013746 010600  
013750 104415  
013752 062706 000010  
013756 022715 177777  
013762 001010  
013764  
013764 012746 014453  
013770 012746 000001  
013774 010600  
013776 104415  
014000 062706 000004  
2719 014004  
014004 012746 014600  
014010 012746 000001  
014014 010600  
014016 104415  
014020 062706 000004  
2720 014024 005004  
2721 014026 010501  
2722 014030 010300  
2723 014032 001403  
2724 014034 004737 020106  
2725 014040 010005  
2726 014042  
2727 014042

.SBTTL PRMESS - PRINT CONTENTS OF MESSAGE BUFFER  
:  
: THIS ROUTINE PRINTS THE CONTENTS OF  
: THE 7 WORD MESSAGE BUFFER RETURNED BY THE  
: TUBO.  
: INPUT:  
: R0 LOW ORDER ADDRESS OF MESSAGE BUFFER  
: R1 HIGH ORDER ADDRESS OF MESSAGE BUFFER  
: NOTE: R1 IS IGNORED IF KTENABLE FLAG IS CLEAR  
: THIS ROUTINE IS NORMALLY CALLED FROM A PRINT ROUTINE  
:-

PRMESS:  
SAVREG ;SAVE THE REGISTERS  
MOV R5,RAMR5H ;SAVE DEVICE REGISTER POINTER  
MOV R0,R5 ;SAVE LOW ORDER ADDRESS  
TST KTENABLE ;ADDRESS ABOVE 28K?  
BNE 10\$ ;BR IF YES  
CLR R1 ;SET HIGH ORDER ADDRESS TO 0  
10\$: MOV R1,R3 ;SAVE HIGH ORDER ADDRESS  
ROL R0 ;SHIFT BIT15 TO C BIT  
ROL R1 ;SHIFT TO HIGH ORDER FOR PRINTOUT  
PRINTX #PROASC,R1,R5 ;PRINT MESSAGE BUFFER ADDRESS  
MOV R5,-(SP)  
MOV R1,-(SP)  
MOV #PROASC,-(SP)  
MOV #3,-(SP)  
MOV SP,R0  
TRAP CSPNTX  
ADD #10,SP  
CMP #177777,(R5) ;MESSAGE BUFFER FULL OF ONES  
BNE 15\$ ;BR IF BUFFER IS PROBABLY OKAY  
PRINTX #MESBFN ;'MESSAGE BUFFER PROBABLY NOT VALID'  
MOV #MESBFN,-(SP)  
MOV #1,-(SP)  
MOV SP,R0  
TRAP CSPNTX  
ADD #4,SP  
15\$: PRINTX #PRIAS ;PRINT HEADER FOR CONTENTS  
MOV #PRIASC,-(SP)  
MOV #1,-(SP)  
MOV SP,R0  
TRAP CSPNTX  
ADD #4,SP  
CLR R4 ;NUMBER OF THE NEXT WORD  
MOV R5,R1 ;COPY LOW ORDER ADDRESS  
MOV R3,R0 ;COPY HIGH ORDER ADDRESS  
BEQ 20\$ ;BR IF NOT ABOVE 28K  
JSR PC,SETMAP ;SETUP PAR ADDRESS IN R0  
MOV R0,R5 ;GET PAR FORMAT ADDRESS ABOVE 28K  
20\$: PRINTX #MESHEA,(R5)+ ;PRINT 'MESSAGE BUFFER HEADER ='

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 PRMESS - PRINT CONTENTS OF MESSAGE BUFFER

	014042	012546		MOV	(R5)+,-(SP)	
	014044	012746	014636	MOV	#MESHEA,-(SP)	
	014050	012746	000002	MOV	#2,-(SP)	
	014054	010600		MOV	SP,RO	
	014056	104415		TRAP	CSPNTX	
	014060	062706	000006	ADD	#6,SP	
2728	014064			PRINTX	#DATAFL,(R5)+ ;PRINT 'DATA FIELD LENGTH	='
	014064	012546		MOV	(R5)+,-(SP)	
	014066	012746	014703	MOV	#DATAFL,-(SP)	
	014072	012746	000002	MOV	#2,-(SP)	
	014076	010600		MOV	SP,RO	
	014100	104415		TRAP	CSPNTX	
	014102	062706	000006	ADD	#6,SP	
2729	014106			PRINTX	#RBPCRA,(R5)+ ;PRINT 'RESIDUAL BYTE COUNTER ='	
	014106	012546		MOV	(R5)+,-(SP)	
	014110	012746	014750	MOV	#RBPCRA,-(SP)	
	014114	012746	000002	MOV	#2,-(SP)	
	014120	010600		MOV	SP,RO	
	014122	104415		TRAP	CSPNTX	
	014124	062706	000006	ADD	#6,SP	
2730	014130			PRINTX	#XSOCN,(R5)+ ;PRINT 'XSTATO CONTENTS	='
	014130	012546		MOV	(R5)+,-(SP)	
	014132	012746	015015	MOV	#XSOCN,-(SP)	
	014136	012746	000002	MOV	#2,-(SP)	
	014142	010600		MOV	SP,RO	
	014144	104415		TRAP	CSPNTX	
	014146	062706	000006	ADD	#6,SP	
2731	014152			PRINTX	#XS1CN,(R5)+ ;PRINT 'XSTAT1 CONTENTS	='
	014152	012546		MOV	(R5)+,-(SP)	
	014154	012746	015062	MOV	#XS1CN,-(SP)	
	014160	012746	000002	MOV	#2,-(SP)	
	014164	010600		MOV	SP,RO	
	014166	104415		TRAP	CSPNTX	
	014170	062706	000006	ADD	#6,SP	
2732	014174			PRINTX	#XS2CN,(R5)+ ;PRINT 'XSTAT2 CONTENTS	='
	014174	012546		MOV	(R5)+,-(SP)	
	014176	012746	015127	MOV	#XS2CN,-(SP)	
	014202	012746	000002	MOV	#2,-(SP)	
	014206	010600		MOV	SP,RO	
	014210	104415		TRAP	CSPNTX	
	014212	062706	000006	ADD	#6,SP	
2733	014216			PRINTX	#XS3CN,(R5)+ ;PRINT 'XSTAT3 CONTENTS	='
	014216	012546		MOV	(R5)+,-(SP)	
	014220	012746	015174	MOV	#XS3CN,-(SP)	
	014224	012746	000002	MOV	#2,-(SP)	
	014230	010600		MOV	SP,RO	
	014232	104415		TRAP	CSPNTX	
	014234	062706	000006	ADD	#6,SP	
2734	014240	022737	000001	002134	CMP	#1,TRANSTST ;CHECK SOFTWARE P-TABLE
2735	014246	001402		BEQ	40\$ ;DO DUMP	
2736	014250	000137	014360	JMP	50\$ ;DON'T DO THE DUMP	
2737	014254			40\$: PRINTX	#RAMFHR	
	014254	012746	014362	MOV	#RAMFHR,-(SP)	
	014260	012746	000001	MOV	#1,-(SP)	
	014264	010600		MOV	SP,RO	
	014266	104415		TRAP	CSPNTX	
	014270	062706	000006	ADD	#4,SP	

CZTUWAO TUBO FRONT END PRT A MACRO M1200 29-MAR-83 13:24 PAGE 41-2  
 PRMESS - PRINT CONTENTS OF MESSAGE BUFFER

2738	014274	012737	000010	002246	MOV	#8.,RAMSIZ	;RAM FIELD IS 8 BYTES LONG
2739	014302	012737	000020	010640	MOV	#20,RAMHLD	;FIELD STARTS AT 20 OCTAL (10 HEX)
2740	014310	004737	010456		JSR	PC,RAMER	;READ AND PRINT THEM
2741	014314	012737	000040	010640	MOV	#40,RAMHLD	;FIELD STARTS AT 40 OCTAL (20 HEX)
2742	014322	004737	010456		JSR	PC,RAMER	;READ AND PRINT THEM
2743	014326	012737	000060	010640	MOV	#60,RAMHLD	;FIELD STARTS AT 60 OCTAL (30 HEX)
2744	014334	004737	010456		JSR	PC,RAMER	;READ AND PRINT THEM
2745	014340	012737	000020	002246	MOV	#16.,RAMSIZ	;RAM FIELD IS SIXTEEN BYTES LONG
2746	014346	012737	000100	010640	MOV	#100,RAMHLD	;FIELD STARTS AT 100 OCTAL (40 HEX)
2747	014354	004737	010456		JSR	PC,RAMER	;READ AND PRINT THEM
2748	014360	000207			PC		;RETURN
2749	014362	045	116	045	SOS:		
2750	014453	045	116	045	RAMFHR:	.ASCIZ	'ZXZA ***** SPECIAL M7454 RAM MEMORY DUMP *****'
2751	014533	045	116	045	MESBFN:	.ASCIZ	'ZXZA MESSAGE BUFFER CONTENTS PROBABLY NOT VALID'
2752	014600	045	116	045	PROASC:	.ASCIZ	'ZXZA Message Buffer Address = X01X05'
2753					PRIASC:	.ASCIZ	'ZXZA Message Buffer Contents:'
2754	014636	045	116	045	MESHEA:	.ASCIZ	'ZXZA Message Buffer Header = X06'
2755	014703	045	116	045	DATAFL:	.ASCIZ	'ZXZA Data Field Length = X06'
2756	014750	045	116	045	RBPCRA:	.ASCIZ	'ZXZA Residual Byte Counter = X06'
2757	015015	045	116	045	XSOCON:	.ASCIZ	'ZXZA XSTAT0 Contents = X06'
2758	015062	045	116	045	XS1CON:	.ASCIZ	'ZXZA XSTAT1 Contents = X06'
2759	015127	045	116	045	XS2CON:	.ASCIZ	'ZXZA XSTAT2 Contents = X06'
2760	015174	045	116	045	XS3CON:	.ASCIZ	'ZXZA XSTAT3 Contents = X06'
2761						.EVEN	

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PRMSGEXP - PRINT EXPD/RCV MESSAGE BUFFERS

```

2763          .SBTTL PRMSGEXP - PRINT EXPD/RCV MESSAGE BUFFERS
2764
2765          :+
2766          :ROUTINE TO PRINT EXPECTED AND RECEIVED MESSAGE BUFFERS
2767          RO      - NUMBER OF WORDS IN BUFFER
2768          :IMPLICIT INPUTS:
2769          EXPMSG  - EXPECTED MESSAGE BUFFER
2770          RECMG  - RECEIVED MESSAGE BUFFER
2771          RCVHIADD- RECEIVED MESSAGE BUFFER HIGH ORDER ADDRESS
2772          RCVLOADD- RECEIVED MESSAGE BUFFER LOW ORDER ADDRESS
2773
2773 015242     PRMSGEXP::
2774 015242     SAVREG                                ;SAVE R1-R5 UNTIL NEXT RETURN
2775 015246     010005     MOV      RO,R5                ;SAVE NUMBER OF WORDS
2776 015250     013700     002252     MOV      RCVLOADD,RO    ;GET RECV LOW ADDRESS
2777 015254     010004     MOV      RO,R4                ;COPY LOW ADDRESS
2778 015256     013701     002250     MOV      RCVHIADD,R1    ;GET RECV HIGH ADDRESS
2779 015262     006100     ROL      RO                    ;SHIFT BIT15 TO C BIT
2780 015264     006101     ROL      R1                    ;SHIFT TO HIGH ORDER FOR PRINTOUT
2781 015266     PRINTX   #PRMSGO,R1,R4                ;PRINT MESSAGE BUFFER ADDRESS
2782          015266     010446     MOV      R4,-(SP)
2783          015270     010146     MOV      R1,-(SP)
2784          015272     012746     015422     MOV      #PRMSGO,-(SP)
2785          015276     012746     000003     MOV      #3,-(SP)
2786          015302     010600     MOV      SP,RO
2787          015304     104415     TRAP    CSPNTX
2788          015306     062706     000010     ADD      #10,SP
2789          015312     PRINTX   #PRMSG1                ;PRINT HEADER FOR CONTENTS
2790          015312     012746     015467     MOV      #PRMSG1,-(SP)
2791          015316     012746     000001     MOV      #1,-(SP)
2792          015322     010600     MOV      SP,RO
2793          015324     104415     TRAP    CSPNTX
2794          015326     062706     000004     ADD      #4,SP
2795          015332     005004     CLR      R4                                ;NUMBER OF THE CURRENT WORD
2796          015334     012701     002266     MOV      #EXPMSG,R1                ;GET EXPD BUFFER ADDRESS
2797          015340     012702     002432     MOV      #RCMSG,R2                ;GET RECV BUFFER ADDRESS
2798          015344     011100     20$:   MOV      (R1),RO                ;GET EXPD
2799          015346     011203     MOV      (R2),R3                ;GET RECV
2800          015350     XOR      RO,R3                ;XOR EXPD/RCV
2801          015360     PRINTX   #PRMSG2,R4,(R1)+,(R2)+,R3
2802          015360     010346     MOV      R3,-(SP)
2803          015362     012246     MOV      (R2)+,-(SP)
2804          015364     012146     MOV      (R1)+,-(SP)
2805          015366     010446     MOV      R4,-(SP)
2806          015370     012746     015525     MOV      #PRMSG2,-(SP)
2807          015374     012746     000005     MOV      #5,-(SP)
2808          015400     010600     MOV      SP,RO
2809          015402     104415     TRAP    CSPNTX
2810          015404     062706     000014     ADD      #14,SP
2811          015410     005204     INC      R4                                ;NUMBER OF THE NEXT
2812          015412     020405     CMP     R4,R5                ;DONE ALL YET?
2813          015414     002001     BGE    50$                    ;BR IF YES
2814          015416     000752     BR     20$                    ;DO ANOTHER
2815          015420     000207     BR     50$                    ;RETURN
2816          015422     045      116     045 PRMSG0: .ASCIZ 'XNZA Message Buffer Address = X01X05'
2817          015467     045      116     045 PRMSG1: .ASCIZ 'XNZA Message Buffer Contents:'
2818          015525     045      116     045 PRMSG2: .ASCIZ 'XNZA WORD #XD2XA EXPD: X06XA RECV: X06XA XOR: X06'
2819          .EVEN

```

CZTUWAO TUBO FRONT END PRT A MACRO M1200 29-MAR-83 10:24 PAGE 43  
PRBYTEXP - PRINT ERROR BYTES IN EXP/REC MESSAGE BUFFER

```

2800
2801
2802
2803
2804
2805
2806
2807
2808
2809
2810
2811
2812
2813 015612
2814 015612
2815 015616 010005
2816 015620 005037 002264
2817 015624 005004
2818 015626 012701 002266
2819 015632 012702 002432
2820 015636 111100
2821 015640 042700 177400
2822 015644 110037 016160
2823 015650 111203
2824 015652 042703 177400
2825 015656 110337 016162
2826 015662
2827 015672 122122
2828 015674 001431
2829 015676 005237 002264
2830 015702 023727 002264 000010
2831 015710 101023
2832 015712
      015712 010346
      015714 013746 016162
      015720 013746 016160
      015724 010446
      015726 012746 016026
      015732 012746 000005
      015736 010600
      015740 104415
      015742 062706 000014
2833 015746
2834 015756 000404
2835 015760
2836 015760
2837 015770
2838 015770 005204
2839 015772 020405
2840 015774 002001
2841 015776 000717
2842 016000
      016000 013746 002264
      016004 012746 016113
      016010 012746 000002
      016014 010600
      016016 104415

```

```

.SBTTL PRBYTEXP - PRINT ERROR BYTES IN EXP/REC MESSAGE BUFFER
+
:ROUTINE TO PRINT ERROR BYTES IN MESSAGE BUFFERS
:ONLY THE FIRST 8 ERRORS ENCOUNTERED ARE PRINTED DUE TO SCREEN SPACE
:
:RO - NUMBER OF BYTES IN BUFFER
:
:IMPLICIT INPUTS:
:
:EXPMSG - EXPECTED MESSAGE BUFFER
:RECMG - RECEIVED MESSAGE BUFFER
-
PRBYTEXP::
  SAVREG ;SAVE R1-R5 UNTIL NEXT RETURN
  MOV RO,R5 ;SAVE NUMBER OF BYTES
  CLR PRNO ;INIT ERROR COUNT
  CLR R4 ;NUMBER OF THE CURRENT BYTE
  MOV #EXPMSG,R1 ;GET EXPD BUFFER ADDRESS
  MOV #RECMG,R2 ;GET RECV BUFFER ADDRESS
20$: MOVB (R1),RO ;GET EXPD BYTE
     BIC #C<377>,RO ;CLEAR UPPER BYTE
     MOVB RO,PRBEXP ;SAVE FOR ERROR REPORT
     MOVB (R2),R3 ;GET RECV BYTE
     BIC #C<377>,R3 ;CLEAR UPPER BYTE
     MOVB R3,PRBREC ;FOR ERROR REPORT
     XOR RO,R3 ;XOR EXPD/RECV
     CPB (R1)+,(R2)+ ;EXPD = RECV?
     BEQ 30$ ;BR IF YES
     INC PRNO ;UPDATE ERROR COUNT
     CMP PRNO,#8. ;PRINTED 8?
     BHI 30$ ;BR IF YES
27$: PRINTX #PRBMSG,R4,PRBLXP,PRBREC,R3
     MOV R3,-(SP)
     MOV PRBREC,-(SP)
     MOV PRBEXP,-(SP)
     MOV R4,-(SP)
     MOV #PRBMSG,-(SP)
     MOV #5,-(SP)
     MOV SP,RO
     TRAP CSPNTX
     ADD #14,SP
     FORCEXIT 50$ 50$ ;200
     BR 35$ 35$ ;200
30$:
35$: FORCERROR 27$,NOTSSR ;200
     ;200
     INC R4 ;NUMBER OF THE NEXT
     CMP R4,R5 ;DONE ALL YET?
     BGE 50$ ;BR IF YES
     BR 20$ ;DO ANOTHER
50$: PRINTX #PRBTOT,PRNO ;PRINT TOTAL ERROR COUNT
     MOV PRNO,-(SP)
     MOV #PRBTOT,-(SP)
     MOV #2,-(SP)
     MOV SP,RO
     TRAP CSPNTX

```

CZTUWAO TUBO FRONT END PRT A MACRO M1200 29-MAR-83 13:24 PAGE 43-1  
PRBYTEXP - PRINT ERROR BYTES IN EXP/REC MESSAGE BUFFER

```

2843 016020 062706 000006          ADD    #6,SP
2844 016024 000207          RTS     PC           ;RETURN
2845 016026      045      116      045 PRBMSG: .ASCIZ 'XNZA BYTE #XD2XA EXPD: X03XA RECV. X03XA XOR: X03'
2846 016113      045      116      045 PRBTOT: .ASCIZ 'XNZA NUMBER OF BYTES IN ERROR = XD2'
2847                                .EVEN
2848 016160 000000          PPBEXP: .WORD 0      ;EXPD
2849 016162 000000          PRBREC: .WORD 0      ;RECV
2850
2851          :+
2852          :PRINT ROUTINE TO DISPLAY EXPD/RECV DATA
2853          :INPUTS:
2854          :
2855          :
2856          :
2857          :
2858          :
2859          :
2860          :
2861          :
2862 016164          BGNMSG EXPREC
2863 016164          EXPREC:: JSR    PC,PRIXOR      ;PRINT THE DATA
2864 016170          ENDMSG
2865 016170 104423          L10017: TRAP    CSMSG
2866

```



CZTUWAO TUBO FRONT END PRT A MACRO M1200 29-MAR-83 13:24 PAGE 44  
EXPBREC - PRINT EXPD/RECV BYTE DATA

2868  
2869  
2870  
2871  
2872  
2873  
2874  
2875  
2876  
2877  
2878  
2879  
2880  
2881 016172  
016172  
2882 016172 004737 007156  
2883 016176  
016176  
016176 104423

```
.SBTTL EXPBREC - PRINT EXPD/RECV BYTE DATA
:
:PRINT ROUTINE TO DISPLAY BYTE EXPD/RECV DATA
:
:INPUTS:
:
:      R1      RECEIVED DATA BYTE
:      R2      EXPECTED DATA BYTE
:
:
:
:      BGNMSG  EXPBREC
EXPBREC::
:      JSR     PC,PRIBXOR      ;PRINT THE DATA
:      ENDMSG
L10020:
:      TRAP    CMSG
```

2884  
2885  
2886  
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2889  
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2897  
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2900  
2901  
2902  
2903  
2904  
2905  
2906  
2907  
2908 016200  
016200  
2909 016200 004737 013450  
2910 016204  
016204  
016204 104423

```
.SBTTL RAMERR - PRINT RAM AND PACKET DATA
:
:PRINT ROUTINE TO DISPLAY RAM/PACKET DATA
:
:INPUTS:
:
:      R4      POINTER TO COMMAND PACKET
:
:IMPLICIT INPUTS:
:
:      RAMDATA  DATA AS READ FROM THE RAM
:      RAMSIZ   NUMBER OF BYTES IN PACKET
:              IF RAMSIZ=0 THEN DEFAULT TO 8.
:
:IMPLICIT OUTPUTS:
:
:      RAMSIZ  SET TO 0
:
:
:      BGNMSG  RAMERR
RAMERR::
:      JSR     PC,PRAMPKT      ;PRINT RAM/PACKET DATA
:      ENDMSG
L10021:
:      TRAP    CMSG
```

2911  
2912  
2913  
2914  
2915  
2916  
2917  
2918

```
.SBTTL RAMTADD - PRINT TEST ADDRESS, RAM AND PACKET DATA
:
:PRINT ROUTINE TO DISPLAY RAM/PACKET DATA
:
:INPUTS:
```

CZTUWAO TUBO FRONT END PRT A MACRO M1200 29-MAR-83 13:24 PAGE 44-1  
 RAMTADD - PRINT TEST ADDRESS, RAM AND PACKET DATA

```

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2920
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2931
2932
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2934
2935 016206
      016206
2936 016206 004737 007640
2937 016212 004737 013450
2938 016216
      016216
      016216 104423
2939
2940
2941
2942
2943
2944
2945
2946
2947
2948
2949
2950
2951
2952
2953 016220
      016220
2954 016220 042701 177400
2955 016224 042702 177400
2956 016230 004737 007432
2957 016234 004737 007306
2958 016240
      016240
      016240 104423
2959
2960
2961
2962
2963
2964
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2966
2967
2968
2969
    
```

```

      R4      POINTER TO COMMAND PACKET
:IMPLICIT INPUTS:
      RAMDATA  DATA AS READ FROM THE RAM
      RAMSIZ   NUMBER OF BYTES IN PACKET
              IF RAMSIZ=0 THEN DEFAULT TO 8.
      ERRHI    HIGH ORDER TEST ADDRESS
      ERRLO    LOW ORDER TEST ADDRESS
:IMPLICIT OUTPUTS:
      RAMSIZ   SET TO 0
:-
      BGNMSG   RAMTADD
RAMTADD::
      JSR     PC,PRITADD      ;PRINT TEST ADDRESS
      JSR     PC,PRAMPKT     ;PRINT RAM/PACKET DATA
      ENDMSG
L10022:
      TRAP    CSMSG
    
```

```

      .SBTTL   RAMEXP - PRINT RAM EXPD/RECV DATA
:↑
:PRINT ROUTINE TO DISPLAY EXPD/RECV DATA
:INPUTS:
      R1      RECEIVED DATA
      R2      EXPECTED DATA
      R4      CONTROLLER RAM ADDRESS
:-
      BGNMSG   RAMEXP
RAMEXP::
      BIC     #'C<377>,R1    ;SAVE EXPD RAM DATA BYTE
      BIC     #'C<377>,R2    ;SAVE EXPD RAM DATA BYTE
      JSR     PC,PRIRAM     ;PRINT THE RAM ADDRESS
      JSR     PC,PRIXOR     ;PRINT THE DATA
      ENDMSG
L10023:
      TRAP    CSMSG
    
```

```

      .SBTTL   TIMEXP - PRINT TIMER A,B AND EXP/REC
:↑
:PRINT ROUTINE TO DISPLAY EXPD/RECV DATA
:AND TIMER A,B HEADER MESSAGE
:INPUTS:
      R1      RECEIVED DATA
      R2      EXPECTED DATA
    
```

CZTUWAO TUBO FRONT END PRT A MACRO H1200 29-MAR-83 13:24 PAGE 44-2  
TIMEXP - PRINT TIMER A,B AND EXP/REC

```

2970      :-
2971
2972 016242      BGNMSG  TIMEXP
016242
2973 016242      TIMEXP:: PRINTX  #TIMSGO      :PRINT HEADER
016242 012746 016270      MOV      #TIMSGO,-(SP)
016246 012746 000001      MOV      #1,-(SP)
016252 010600      MOV      SP,R0
016254 104415      TRAP    C$PNTX
016256 062706 000004      ADD      #4,SP
2974 016262 004737 007306      JSR      PC,PRIXOR      :PRINT THE DATA
2975 016266      ENDMSG
016266
016266 104423      L10024: TRAP    C$MSG
2976
2977
2978 016270      045      116      045  TIMSGO: .ASCIZ  'XNXA TIMER A STATUS IS IN BIT 3XNXA TIMER B STATUS IS IN BIT 2'
2979      .EVEN
2980
2981      :-
2982      :PRINT ROUTINE FOR TSSR ERRORS ON DATA TRANSFERS
2983      :INPUTS:
2984      :
2985      :
2986      :
2987      :
2988      :
2989      :
2990      :
2991      :
2992 016370      BGNMSG  BADSSR
016370
2993 016370 010246      BADSSR:: MOV      R2,-(SP)      :SAVE DATA TRANSFERRED
2994 016372 042702 177400      BIC      #177400,R2      :GET JUST ONE BYTE
2995 016376      PRINTB  #XFERASC,R2
016376 010246      MOV      R2,-(SP)
016400 012746 016430      MOV      #XFERASC,-(SP)
016404 012746 000002      MOV      #2,-(SP)
016410 010600      MOV      SP,R0
016412 104414      TRAP    C$PNTB
016414 062706 000006      ADD      #6,SP
2996 016420 012602      MOV      (SP)+,R2      :RESTORE R2
2997 016422 004737 005264      JSR      PC,PRITSSR      :DECODE TSSR CONTENTS
2998 016426      ENDMSG
016426
016426 104423      L10025: TRAP    C$MSG
2999 016430      045      116      045  XFERASC: .ASCIZ  'XNXA Data Transferred = X03'
3000

```

CZTUWA0 TU80 FRONT END PRT A MACRO M1200 29-MAR-83 13:24 PAGE 45  
 SOFINIT - SOFT INITIALIZE OF CONTROLLER

.SBTTL SOFINIT - SOFT INITIALIZE OF CONTROLLER

3002  
 3003  
 3004  
 3005  
 3006  
 3007  
 3008  
 3009  
 3010  
 3011  
 3012  
 3013  
 3014  
 3015  
 3016  
 3017  
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 3019  
 3020  
 3021  
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 3023  
 3024  
 3025  
 3026  
 3027  
 3028  
 3029

```

:ROUTINE TO DO A SOFT INITIALIZE OF THE CONTROLLER
:BY WRITING INTO THE TSSR REGISTER. AFTER THE INIT,
:THE TSSR REGISTER IS TESTED FOR ERRORS. ANY ERRORS
:DETECTED SHOULD BE TREATED AS DEVICE FATAL ERRORS.
    
```

```

:INPUTS:
      R5      ADDRESS OF FIRST REGISTER
    
```

```

:OUTPUTS:
      R0      CONTENTS OF TSSR, IF ERROR
      CARRY   SET IF INIT WAS OKAY
             CLEAR IF FATAL ERROR
    
```

```

:CALLING SEQUENCE:
      MOV     #ADDRESS,R5
      JSR    PC,SOFINIT
      BCS    CONTINUE
      ERRDF                      :REPORT FATAL ERROR
    
```

```

SOFINIT::
      SAVREG                      : SAVE THE REGISTERS
      MOV     #0,TSSR(R5)         : DO THE INIT.
      JSR    PC,WAITF            : WAIT FOR SSR
      MOV     TSSR(R5),R0        :GET THE TSSR REGISTER
      MOV     R0,R4              :TSSR CONTENTS
      BIC     #^C<HIADDR!OFL>,R4
      BIS     #SSR!NBA,R4        :R4 HAS EXPECTED CONTENTS
      CMP     R4,R0              :ONLY EXPECTED BITS SET ?
      BEQ     $$                  :BRANCH IF OKAY
      CLC                                :CLEAR THE CARRY FOR ERROR
      BR     10$                  :GO TO EXIT
      SEC                                :SET THE CARRY BIT
      5$:    RTS                    :RETURN TO CALLER
      10$:
    
```

```

3030 016464
3031 016464
3032 016470 012765 000000 000000
3033 016476 004737 016740
3034 016502 016500 000000
3035 016506 010004
3036 016510 042704 176277
3037 016514 052704 002200
3038 016520 020400
3039 016522 001402
3040 016524 000241
3041 016526 000401
3042 016530 000261
3043 016532 000207
    
```

CZTUWAO TUBO FRONT END PRT A MACRO M1200 29-MAR-83 13:24 PAGE 46  
 CHKAMB - CHECK TSSR FOR AMBIGUITY

```

3045          .SBTTL  CHKAMB - CHECK TSSR FOR AMBIGUITY
3046
3047
3048
3049          : THIS ROUTINE TESTS THE CONTENTS OF THE TSSR REGISTER
3050          : FOR AMBIGUITY
3051
3052          : INPUT:
3053
3054          :         R0      CONTENTS OF TSSR
3055
3056          : OUTPUT:
3057
3058          :         R0      CONTENTS OF TSSR
3059
3060          :         CARRY   SET - NO AMBIGUITY
3061          :         CLR    - AMBIGUOUS CONTENTS
3062
3063          : -
3064
3065          CHKAMB:
3066          SAVREG          ;SAVE THE GENERAL REGISTERS
3067          MOV             R0,R4          ;CONTENTS OF TSSR
3068          BIT             #SC,R0        ;IS BIT 15 SET ?
3069          BNE             5$           ;BRANCH IF YES
3070          BIT             #^C<NBA!OFL!SSR!HIADDR>,R0 ;ANY OTHER BITS SET ?
3071          BNE             40$         ;MUST BE AN ERROR
3072          BR              45$         ;RETURN WITH SUCCESS
3073          5$: BIT         #SSR,R0      ;IS READY BIT SET ?
3074          BNE             10$         ;BRANCH IF READY BIT IS SET.
3075          BIT             #BIT5,R0    ;IS FATAL ERROR BIT SET ?
3076          BEQ             40$         ;ERROR IF NOT
3077          BIC             #^CTERCLS,R4 ;CLEAR ALL BUT TERMINATION CODE
3078          CMP             R4,#16      ;ALL THREE BITS MUST BE SET
3079          BNE             40$         ;ERROR IF NOT SET
3080          BR              45$         ;OK IF ALL ARE SET
3081          10$: BIT        #BIT5,R0    ;IS FATAL ERROR BIT SET ?
3082          BEQ             45$         ;ERROR IF BIT IS SET WITH SSR
3083          BIT             #BIT2!BIT1,R0 ;IS THIS A FUNCTION REJECT
3084          BNE             45$         ;BR, IF TSSR IS OK
3085          40$: CLC              ;AMBIGUOUS CONTENTS
3086          BR              50$
3087          45$: SEC              ;SHOW SUCCESS - NO AMBIGUITY
3088          50$: RTS             PC      ;RETURN TO CALLER
3089

```

CZTUWAO TUBO FRONT END PRT A MACRO M1200 29-MAR-83 13:24 PAGE 47  
 ENAINT,DSBINT - ENABLE/DISABLE INTERRUPTS

```

3091          .SBTTL ENAINT,DSBINT - ENABLE/DISABLE INTERRUPTS
3092
3093          :
3094          : DEFAULT DISPLAY INTERRUPT HANDLERS.
3095          : IF DISPLAY TIME-OUT, REPORT DEV FATAL, AND ABORT PASS.
3096          : OTHERWISE, SAVE DPU REGISTERS AND DISMISS.
3097          :
3098          : BIT DEFINITIONS FOR "INTMASK" AND "INTFLAG" BYTES:
3099          :
3100          :          IOKCKIN=BIT7          : DON'T CHECK FOR BAD INTERRUPTS -- TEST WILL.
3101          :          IOKSTP=BIT0          : EXPECT "STOP" INTERRUPT.
3102          :
3103          : INTERRUPT MASK -- SAYS EXPECTING INTERRUPTS
3104          INTMASK: .BYTE 0
3105          : INTERRUPT FLAG -- SAYS WE GOT ONE (IF POSITIVE)
3106          INTFLAG: .BYTE 0
3107          :
3108          : SAVED INTERRUPT VECTOR:
3109          INTVEC: .WORD 0
3110          : SAVE CPU PC
3111          INTCPC: .WORD 0
3112          :
3113          : SUBROUTINE TO ENABLE INTERRUPTS:
3114          ENAINT: MOV     RO,-(SP)          : SAVE R0
3115                   MOV     IVEC,RO        : GET POINTER TO VECTORS
3116                   MOV     #INTR,(RO)+    : SET UP INTERRUPT VECTOR
3117                   MOV     #PRI07,(RO)+
3118                   MOV     (SP)+,RO      : RESTORE R0
3119                   MOV     (SP),-(SP)
3120                   MOV     #0,2(SP)      : SET CPU TO LEVEL 0
3121                   RTI
3122          :
3123          : SUBROUTINE TO DISABLE INTERRUPTS (RAISE PRIORITY TO LEVEL 7)
3124          DSBINT: MOV     (SP),-(SP)
3125                   MOV     #PRI07,2(SP)
3126                   RTI
3127

```

CZTUWAC TUBO FRONT END PRT A  
INTR - INTERRUPT HANDLERS

MACRO M1200 29-MAR-83 13:24 PAGE 48

```

3129                                     .SBTTL INTR - INTERRUPT HANDLERS
3130
3131 016706                               BGNSRV INTR          ;DEFINE INTERRUPT ENTRY
      016706                               INTR::
3132 016706 012737 000001 002172         MOV      #1,INTRECV      ;SET FLAG TO SHOW INTERRUPT RECEIVED
3133 016714 105037 016635                 CLRB    INTFLAG        ;CLEAR FLAG TO SAY WE GOT INTERRUPT
3134 016720 132737 000001 016634         BITB    #IOKSTP,INTMASK ;EXPECTING STOP INTERRUPT?
3135 016726 001003                        BNE     1$             ;BR IF YES
3136 016730 152737 000001 016635         BISB    #IOKSTP,INTFLAG ;NO. SET THE ERROR FLAG.
3137
3138                                     ;SAVE REGISTERS, MSG BUFFER, ETC.
3139 016736                               1$:
3140 016736                               ENDSRV
      016736                               L10026:
      016736 000002                       RTI
3141
3142

```

```

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3145
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3153
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3158 016740
      016740 104422
3159 016742 012746 010000
3160 016746
      016746 012727 000001
      016752 000000
      016754 013727 002116
      016760 000000
      016762 005367 177772
      016766 001375
      016770 005367 177756
      016774 001367
3161 016776 016500 000000
3162 017002 105700
3163
3164 017004 100420
3165 017006
      017006 012727 000001
      017012 000000
      017014 013727 002116
      017020 000000
      017022 005367 177772
      017026 001375
      017030 005367 177756
      017034 001367
3166 017036 005316
3167 017040 001356
3168 017042 000241
3169 017044 000401
3170 017046 000261
3171 017050 005326
3172 017052 000207
    
```

```

.SBTTL WAITF - WAIT FOR SUBSYSTEM READY
:
: SUBROUTINE TO WAIT FOR THE SUBSYSTEM READY FLAG
:
: INPUTS:
:
: R5 ADDRESS OF FIRST DEVICE REGISTER
:
: OUTPUTS:
:
: R0 CONTENTS OF LAST TSSR READ
: CARRY SET - READY BIT SET
: CLR - TIMEOUT WAITING FOR READY
:
WAITF:: BREAK ; DO A SUPVSR BREAK FIRST.
TRAP CSBRK
MOV #10000,-(SP) ;BIG MSEC TIMER
DELAY 1 ;DELAY 100US
MOV #1,(PC)+
.WORD 0
MOV LSDLY,(PC)+
.WORD 0
DEC -6(PC)
BNE -.4
DEC -22(PC)
BNE -.20
2$: MOV TSSR(R5),R0 ;READ THE TSSR REGISTER
TSTB R0 ;TEST FOR READY BIT SET
BMI 3$ ; EXIT ON STOP FLAG.
DELAY 1 ; WAIT 100 USEC
MOV #1,(PC)+
.WORD 0
MOV LSDLY,(PC)+
.WORD 0
DEC -6(PC)
BNE -.4
DEC -22(PC)
BNE -.20
DEC (SP) ;REDUCE DELAY COUNT
BNE 2$ ;RETRY UNTIL TIMER EXPIRES
CLC ; C = 0, CONTROLLER STILL RUNNING...
BR 4$ ;...OR HUNG-UP AFTER 300 MSEC.
3$: SEC ; C = 1, CONTROLLER IS STOPPED.
4$: DEC (SP)+ ;RESTORE STACK WITHOUT CHANGING CARRY BIT
RTS PC
    
```



CZTUWAO TUBO FRONT END PRT A MACRO M1200 29-MAR-83 13:24 PAGE 50  
CHKTSSR - CHECK TSSR FOR READY

.SBTTL CHKTSSR - CHECK TSSR FOR READY

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3189  
3190  
3191  
3192

:+  
: THIS ROUTINE WAITS FOR READY IN THE TSSR  
: AND TESTS FOR AMBIGUOUS BIT SETTINGS IN TSSR.  
: INPUT:  
: R5 ADDRESS OF CSR REGISTERS  
: OUTPUT:  
: R0 CONTENTS OF TSSR  
: CARRY SET - OKAY  
: CLR - NOT READY AMBIGUOUS, OR SC SET  
:-

3193 017054  
3194 017054 004737 016740  
3195 017060 103014  
3196 017062 004737 016534  
3197 017066 103006  
3198 017070 032700 100000  
3199 017074 001405  
3200 017076 032700 074000  
3201 017102 001402  
3202 017104 000241  
3203 017106 000401  
3204 017110 000261  
3205 017112 000207

CHKTSSR:  
JSR PC, WAITF ;WAIT FOR READY  
BCC 20\$ ;BRANCH IF TIME OUT  
JSR PC, CHKAMB ;TSSR AMBIGUOUS?  
BCC 10\$ ;BR IF YES  
BIT #SC, R0 ;SPECIAL CONDITION SET?  
BEQ 15\$ ;BR IF NO  
BIT #<SCE!BIE!RMR!NXM>, R0 ;ANY ERROR BITS SET?  
BEQ 15\$ ;BR IF NO  
10\$: CLC ;SET FAILURE  
BR 20\$ ;  
15\$: SEC ;SET SUCCESS  
20\$: RTS PC ;RETURN TO CALLER

CZTUWAO TUBO FRONT END PRT A MACRO M1200 29-MAR-83 13:24 PAGE 51  
 XNXM - CHECK FOR NONEXISTENT MEMORY

```

3207          .SBTTL XNXM - CHECK FOR NONEXISTENT MEMORY
3208
3209          :+
3210          : ROUTINE TO TEST FOR A NEXM IN THE RANGE (R1) THRU (R2).
3211          : ON RETURN, IF 'C' = 1, (R1) = NEXM ADDRESS.
3212          : 'C' = 0, ALL ADDRESSES OK.
3213          :
3214          : CALL:  MOV ADR1,R1
3215          :         MOV ADR2,R2
3216          :         JSR PC,NXM
3217          :         RETURN
3218          :         :TEST 'C' AND PROCEED.
3219          XNXM:  MOV     #2$,@#4           ; SET BUSERR VECTOR.
3220          :         MOV     #PRI04,@#6
3221          :         CLR     R3             ; FLAG.
3222          1$:    TST     (R1)           ; TEST THE ADDRESS(ES).
3223          :         :IF ANY TRAP, CONTINUE AT 2$.
3224          :         :OTHERWISE, CONTINUE HERE.
3225          :         :BR IF FINISHED (NO NEXM'S).
3226          :         :SET NEXT ADDRESS...
3227          :         :...AND CONTINUE.
3228          :         :
3229          2$:    COM     R3             ; GOT ONE, SET FLAG...
3230          :         MOV     #3$, (SP)
3231          :         RTI
3232          :         :...AND DISMISS INTERRUPT...
3233          3$:    CLRVEC #4           ; ...AND GIVE BACK THE VECTOR.
3234          :         MOV     #4,R0
3235          :         TRAP   C$CVEC
3236          :         TST     R3           ; DID WE CATCH ONE ??
3237          :         BEQ     .+4         ; NO, 'C' = 0, SKIP NEXT.
3238          :         SEC     PC         ; YES, 'C' = 1, (R1) = NEXM ADDR.
3239          :         RTS
3240
3241          .SBTTL TSTLOOP - CHECK ITERATION COUNT
3242          :+
3243          : SUBROUTINE TO EXECUTE TEST ITERATIONS.
3244          : EXIT WITH 'C' SET IF LOOPS ALLOWED AND LOOP COUNT NON-ZERO.
3245          : LOOP COUNTER IS SET BY 'BEGIN.TEST' MACRO.
3246          :
3247          : CALL:  LOOPTO ARG
3248          :
3249          TSTLOOP:
3250          :         TST     NOITS       ; ITERATIONS INHIBITED?
3251          :         BNE     1$          ; YES.
3252          :         TST     QVP        ; NO.
3253          :         BMI     1$          ; LOOPS DISALLOWED IN QUICK PASS.
3254          :         DEC     LOOPCNT     ; BUMP LOOP COUNTER.
3255          :         BNE     2$
3256          1$:    CLC
3257          :         BR     3$
3258          :         :LOOP DISABLED, OR DONE.
3259          2$:    SEC
3260          :         :LOOP ENABLED.
3261          3$:    RTS     PC

```

CZTUWAO TUBO FRONT END PRT A MACRO M1200 29-MAR-83 13:24 PAGE 52  
TSTLOOP - CHECK ITERATION COUNT

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017226  
017226  
017230  
017234  
017240  
017244  
017250  
017254  
017256  
017262  
017264  
017266  
017274  
017274  
017276  
017300  
017302  
017304  
017306  
017314  
017314  
017316  
017320  
017322  
017324  
017332  
017332  
017336  
017340

```
.SBTTL TSTSETUP - PRINT TEST NAME AND INIT ERROR COUNTS
:
: PRINT THE NUMBER AND NAME OF EACH TEST AS WE GO ALONG.
: INCREMENT 'TESTK' TO INDICATE THE NUMBER OF TESTS
: IN THE CURRENT RUN SEQUENCE.
: CLEAR THE ERROR COUNTER AND SIGNATURE EXTENSION FLAGS.
:
: INPUT:
:
: RO POINTER TO TEST ID ASCIZ STRING
:
: OUTPUT:
:
: R5 ADDRESS OF FIRST DEVICE REGISTER
:
: IMPLICIT OUTPUTS:
:
: TSTCNT UPDATED TO COUNT TESTS PERFORMED SINCE START OR RESTART
:
: SIDE EFFECTS:
:
: INTERRUPT LEVEL IS RASIED TO LEVEL OF
: THE DEVICE UNDER TEST
:
: -
```

```
TSTSETUP::
MOV RO,-(SP) ;SAVE THE TEST ID MESSAGE
CLR SIFLAG ; CLEAR 'SOFT INIT' FLAG
CLR ERRK ; CLEAR LOCAL ERROR COUNTER.
CLR EXTA ; CLEAR ERROR EXTENSION FLAG.
CLRB INTMASK ; CLEAR INTERRUPT MASK (CHECK ERROR)
MOV UNITN,RO ; GET THE UNIT NUMBER,
ASL RO ; ... AND MAKE IT A WORD OFFSET.
TST MODEV ; DID STARTUP FIND THE DEVICE?
BEQ 4$ ; BR IF YES
BPL 3$ ; BR IF NOT IDLE
BIS #16000,ERTABL(RO) ; FLAG ERROR IN THE ERNOR TABLE
ERRDF 1,NXR,NXRERR ; NO DEVICE HERE -- PRINT IT
TRAP CSERDF
.WORD 1
.WORD NXR
.WORD NXRERR
BR 2$
BIS #160001,ERTABL(RO) ; FLAG ERROR IN THE ERROR TABLE
ERRDF 2,NOINIT ; DEVICE NOT IDLE
TRAP CSERDF
.WORD 2
.WORD NOINIT
.WORD 0
MOV #-1,DUFLG ; DROP THE UNIT
DODU UNITN
MOV UNITN,RO
TRAP CSDODU
DOCLM ; ABORT THE PASS
```

CZTUWAO TUBO FRONT END PRT A MACRO M1200 29-MAR-83 13:24 PAGE 52-1  
 TSTSETUP - PRINT TEST NAME AND INIT ERROR COUNTS

3307	017340	104444			TRAP	CSDCLN			
3308	017342	000423			BR	58			
3309	017344			48:	RFLAGS	R0			: GET THE OPERATOR FLAGS.
	017344	104421			TRAP	CSR+LA			
3310	017346	032700	001000		BIT	#PNT,R0			: PRINT THE TEST NUMBERS?
3311	017352	001412			BEQ	18			: BR IF NO
3312	017354	011600			MOV	(SP),R0			:GET THE ID MESSAGE
3313	017356				PRINTF	#TNAM,R0			:DISPLAY THE TEST ID
	017356	010046			MOV	R0,-(SP)			
	017360	012746	017422		MOV	#TNAM,-(SP)			
	017364	012746	000002		MOV	#2,-(SP)			
	017370	010600			MOV	SP,R0			
	017372	104417			TRAP	CSPNTF			
	017374	062706	000006		ADD	#6,SP			
3314	017400	005237	002162	18:	INC	TSTCNT			: BUMP TEST COUNTER.
3315	017404				SETPRI	IPRI			:PRIORITY THAT OF DEVICE
	017404	013700	002160		MOV	IPRI,R0			
	017410	104441			TRAP	CSSPRI			
3316	017412	005726		58:	TST	(SP)+			:FIX UP THE STACK
3317	017414	013705	002154		MOV	CSRADDR,R5			: ADDRESS OF TSV REGISTERS ON UNIBUS
3318	017420	000207			RTS	PC			
3319	017422	045	123	045	TNAM:	.ASCIZ	'XSXTXA Test'		
3320						.EVEN			

CZ7JWA0 TUBO FRONT END PRT A MACRO M1200 29-MAR-83 13:24 PAGE 53  
 TSTEND - PRINT ERRORS RECEIVED

```

3322
3323
3324
3325
3326
3327 017436
      017436 104421
3328 017440 030027 020000
3329 017444 001412
3330 017446
      017446 013746 017474
      017452 012746 017476
      017456 012746 000002
      017462 010600
      017464 104417
      017466 062706 000006
3331 017472 000207
3332
3333 017474 000000
3334 017476 045 101 040
3335 017515 105 122 122
3336
3337
3338
3339
3340
3341
3342 017562 005237 017474
3343 017566 010046
3344 017570 013700 002150
3345 017574 006300
3346 017576 062700 003130
3347 017602 005210
3348 017604 032710 007777
3349 017610 001001
3350 017612 005310
3351 017614 012600
3352 017616 000207
3353
3354 017620 010046
3355 017622 013700 002150
3356 017626 006300
3357 017630 016000 003130
3358 017634 042700 170000
3359 017640 020037 002142
3360 017644 103004
3361 017646 023737 017474 002140
3362 017654 103417
3363 017656
      017656 104421
3364 017660 032700 000040
3365 017664 001013
3366 017666 012737 177777 003060
3367 017674
      017674 104455
      017676 000004
      017700 017515
    
```

```

.SBTTL TSTEND - PRINT ERRORS RECEIVED
:
: AT END OF EACH TEST, PRINT THE NUMBER OF ERRORS RECEIVED
: IF NORMAL ERROR REPORTING IS DISABLED (FLA:IER).
:
TSTEND: RFLAGS RO
        TRAP CSRFLA
        BIT RO,#IER
        BEQ 18 ; BR IF 'IER' NOT SET.
        PRINTF #ESUM,ERRK ; PRINT ERROR COUNT.
        MOV ERRK,-(SP)
        MOV #ESUM,-(SP)
        MOV #2,-(SP)
        MOV SP,RO
        TRAP CSPNTF
        ADD #6,SP
18: RTS PC

ERRK: 0 ; LOCAL ERROR COUNT.
ESUM: .ASCIZ /%A %D%A ERRORS/
EMAXDU: .ASCIZ /ERROR LIMIT REACHED -- DROPPING UNIT/
        .EVEN

.SBTTL INCERK - INCREMENT LOCAL ERROR COUNT
:
: ROUTINES TO INCREMENT LOCAL ERROR COUNT AND CHECK FOR LIMIT:
:
INCERK: INC ERRK ; INCREMENT LOCAL ERROR COUNT
        MOV RO,-(SP) ; SAVE RO
        MOV UNITN,RO ; GET UNIT NUMBER,
        ASL RO ; ... AND MAKE IT A WORD OFFSET.
        ADD #ERTABL,RO ; RO GETS ADDRESS OF ERROR TABLE ENTRY.
        INC (RO) ; INCREMENT THE DEVICE ERROR COUNT
        BIT #7777,(RO) ; DID WE OVERFLOW THE FIELD?
        BNE 18 ; BR IF NO.
        DEC (RO) ; YES -- BACK IT UP TO 7777.
18: MOV (SP)+,RO ; RESTORE RO
        RTS PC ; RETURN TO CALLER.

CKEMAX: MOV RO,-(SP) ; SAVE RO
        MOV UNITN,RO ; GET UNIT NUMBER
        ASL RO ; ... AND MAKE IT A WORD OFFSET
        MOV ERTABL(RO),RO ; GET ERROR TABLE ENTRY
        BIC #17000,RO ; EXTRACT ERROR COUNT FIELD
        CMP RO,GERRMAX ; IS GLOBAL LIMIT EXCEEDED FOR THIS UNIT?
        BHIS 18 ; BR IF YES
        CMP ERRK,LERRMAX ; IS LOCAL LIMIT EXCEEDED FOR THIS TEST?
        BLO 28 ; BR IF NO
18: RFLAGS RO ; GET OPERATOR FLAGS
        TRAP CSRFLA
        BIT #IDU,RO ; IS DROPPING INHIBITED?
        BNE 28 ; BR IF YES.
        MOV #-1,DUFLG ; NO -- DROP THE UNIT
        ERDF 4,EMAXDU
        TRAP CBERDF
        .WORD 4
        .WORD EMAXDU
    
```



CZTUWAO TUBO FRONT END PRT A MACRO M1200 29-MAR-83 13:24 PAGE 54  
CKDROP - CHECK IF UNIT SHOULD BE DROPPED

```

3396 .SBTTL CKDROP - CHECK IF UNIT SHOULD BE DROPPED
3397
3398 :+
3399 : CHECK IF UNIT SHOULD BE DROPPED
3400 CKDROP: MOV RO,-(SP)
3401 FORCERROR 1S,NOTSSR
3402 RFLAGS RO
3403 TRAP CSRFLA
3404 BIT #IDU,RO
3405 BNE 1S
3406 MOV (SP),RO
3407 MOV #-1,DUFLG
3408 DODU UNITN
3409 MOV UNITN,RO
3410 TRAP CSDODU
3411
3412
3413
3414
3415 :ABORT THE PASS
3416
3417
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```

```

:ABORT THE PASS
1S:
PC

```

```

3415 .SBTTL CONFIG - DETERMINE CONFIGURATION OF SYSTEM
3416
3417 : SUBROUTINE - DETERMINE CONFIGURATION OF TUBO SYSTEM.
3418
3419 CONFIG:
3420 JSR PC,SOFINIT
3421 RTS PC
3422
3423 : SUBROUTINE - ENABLE MEM MGT.
3424
3425
3426 KTON: TST KTF LG ; GOT KT?
3427 BEQ 1S ; NO.
3428 MOV #1,SRO ; YES. ENABLE KT11.
3429 RTS PC
3430
3431 : SUBROUTINE - DISABLE MEM MGT.
3432
3433
3434 KTOFF: TST KTF LG ; GOT KT11?
3435 BEQ 1S ; NO.
3436 NOP
3437 NOP
3438 MOV #0,SRO ; DISABLE KT.
3439 RTS PC

```

CZTUWAO TUBO FRONT END PRT A  
SETMAP - SETUP PAR6 MAPPING

MACRO M1200 29-MAR-83 13:24 PAGE 55

.SBTTL SETMAP - SETUP PAR6 MAPPING

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020106			
020106			
020112	005737	003100	
020116	001433		
020120	010102		
	000006		
020152	042701	000177	
020156	020137	003100	
020162	103011		
020164	010137	172354	
020170	042702	160000	
020174	062702	140000	
020200	010200		
020202	000261		
020204	000401		
020206	000241		
020210	000207		

```

:~
:~
:~ THIS ROUTINE SETS UP KERNEL PAR6 TP HANDLE
:~ AN 18 BIT ADDRESS. THE OFFSET INTO THE PAGE
:~ IS RETURNED BIASED TO PAR6.
:~
:~ INPUTS:
:~
:~          R0      HIGH ORDER ADDRESS BITS
:~          R1      LOW ORDER ADDRESS BITS
:~
:~ OUTPUTS:
:~
:~          R0      OFFSET INTO BLOCK WITH PAR6 BIAS (I.E. THE ADDRESS)
:~          CARRY   SET IF SUCCESS
:~                  CLR IF ERROR
:~
:~ SETMAP:
:~          SAVREG          ;SAVE R1-R4 UNTIL NEXT RETURN
:~          TST             KTF LG          ;SYSTEM HAVE ABOVE 28K?
:~          BEQ             10$           ;BR IF NO
:~          MOV             R1,R2        ;SAVE LOW ORDER BITS
:~          .REPT           6
:~          ASR             R0           ;CONVERT WORD ADDRESS TO 32W BLOCKS
:~          ROR             R1           ;MAKE IT DOUBLE PRECISION
:~          .ENDR
:~          BIC             #177,R1      ;ALINE FOR LOWER 4K BOUNDARY
:~          CMP             R1,KTF LG    ;HIGHER THAN EXISTING MEMORY?
:~          BHIS            10$         ;BR IF YES
:~          MOV             R1,#KIPAR6   ;SETUP MAPPING REGISTER PAR6
:~          BIC             #160000,R2   ;SETUP DISPLACEMENT IN PAGE
:~          ADD             #140000,R2   ;ADD IN PAR6 BIAS
:~          MOV             R2,R0       ;RETURN IN R0
:~          SEC             ;SET SUCCESS
:~          BR              15$         ;
:~          10$:           CLC           ;SET FAILURE
:~          15$:           RTS          PC ;RETURN

```



CZTUWAO TUBO FRONT END PRT A MACRO M1200 29-MAR-83 13:24 PAGE 56  
 FILLMEM - FILL MEMORY WITH BACKGROUND PATTERN

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020212  
 020212  
 020216 004737 020064  
 020222 010003  
 020224 013701 003072  
 020230 013702 003074  
 020234 010321  
 020236 005302  
 020240 003375  
 020242 005737 003100  
 020246 001452  
 020250 004737 020046  
 020254 005000  
 020256 013701 003104  
 000006  
 020326 004737 020106  
 020332 010320  
 020334 020027 160000  
 020340 103774  
 020342 162700 020000  
 020346 062737 000200 172354  
 020354 023737 172354 003100  
 020362 001402  
 020364 000137 020332  
 020370 004737 020064  
 020374 000207

```

.SBTTL FILLMEM - FILL MEMORY WITH BACKGROUND PATTERN
↑
FILL MEMORY WITH A BACKGROUND PATTERN
:
INPUTS:
    RO = BACKGROUND PATTERN
    FREE = FIRST LOCATION AVAILABLE TO DIAGNOSTIC
    KTFLG = SET TO HIGHEST MEMORY LOCATION IF > 28K.
:
OUTPUTS:
    NONE
:
FILLMEM:
    SAVREG                ;SAVE R1-R5 UNTIL NEXT RETURN
    JSR PC,KTOFF          ;DISABLE KT.
    MOV RO,R3             ;COPY TEST PATTERN
    MOV FREE,R1           ;GET FIRST FREE LOCATION
    MOV FRESIZ,R2        ;SIZE OF FREE SPACE BELOW 28K.
10$: MOV R3,(R1)+        ;STORE A BACKGROUND WORD
    DEC R2               ;DONE ALL MEMORY IN FREE SPACE?
    BGT 10$              ;BR IF NO
    TST KTFLG           ; GOT KT?
    BEQ 55$             ; NO. GET OUT.
    JSR PC,KTON         ; YES. ENABLE KT.
    CLR RO              ;HIGH ORDER ADDRESS START
    MOV PST32W,R1       ;GET >28K START ADDRESS (IN 32W BLOCKS)
    .REPT 6
    CLC                 ;CLEAR C BIT
    ROL R1              ;CONVERT BLOCKS TO WORDS
    ROL R0              ;MAKE IT DOUBLE PRECISION
    .ENDR
30$: JSR PC,SETMAP      ;SETUP PAR6 MAPPING REGISTER
    MOV R3,(R0)+        ;STORE TEST PATTERN IN >28K ADDRESS
    CMP RO,#160000      ;END OF PAR6 MAPPING AREA?
    BLO 30$             ;BR IF NO
    SUB #20000,R0       ;BACKUP INTO PAR6 MAPPING BEGIN
    ADD #200,#KIPAR6    ;POINT TO NEXT 4K BLOCK >28K.
    CMP #KIPAR6,KTFLG  ;END OF MEMORY?
    BEQ 50$            ;BR IF YES
    JMP 30$            ;KEEP GOING ON ETC.
50$: JSR PC,KTOFF      ;DISABLE KT.
55$: RTS PC
    
```

CZTUWAO TUBO FRONT END PRT A MACRO M1200 29-MAR-83 13:24 PAGE 57  
 CMPMEM - COMPARE MEMORY TO BACKGROUND PATTERN

```

3530          .SBTTL  CMPMEM - COMPARE MEMORY TO BACKGROUND PATTERN
3531          :
3532          : COMPARE MEMORY WITH A BACKGROUND PATTERN
3533          :
3534          : INPUTS:
3535          :
3536          :     RO = BACKGROUND PATTERN
3537          :     FREE = FIRST LOCATION AVAILABLE TO DIAGNOSTIC
3538          :     KTFLG = SET TO HIGHEST MEMORY LOCATION IF > 28K.
3539          :
3540          : OUTPUTS:
3541          :
3542          :     CARRY - SET IF NO ERROR
3543          :     CARRY - CLR IF ERROR
3544          :
3545          : IMPLICIT OUTPUTS:
3546          :
3547          :     ERRHI - ERROR HIGH ADDRESS
3548          :     ERRLO - ERROR LOW ADDRESS
3549          :     EXPD  - EXPECTED DATA
3550          :     RECV  - RECEIVED DATA
3551          :
3552          : CMPMEM:
3553          :     SAVREG          :SAVE R1-R5 UNTIL NEXT RETURN
3554          :     MOV RO,R3        :COPY TEST PATTERN
3555          :     JSR PC,KTOFF     :DISABLE KT.
3556          :     MOV FREE,R1      :GET FIRST FREE LOCATION
3557          :     MOV FRESIZ,R2    :SIZE OF FREE SPACE BELOW 28K.
3558          :     CMP R3,(R1)     :FREE SPACE LOCATION EQUAL TO EXPD?
3559          :     BEQ 15$         :BR IF YES
3560          :     MOV R1,ERRLO    :SAVE ADDRESS IN ERROR
3561          :     CLR ERRHI      :NO HIGH ADDRESS
3562          :     MOV R3,EXPD     :SAVE EXPD FOR ERROR REPORT
3563          :     MOV (R1),RECV   :SAVE RECV FOR ERROR REPORT
3564          :     BR 50$         :
3565          :     15$: TST (R1)+  :POINT TO NEXT ADDRESS
3566          :     DEC R2         :DONE ALL MEMORY IN FREE SPACE?
3567          :     BGT 10$       :BR IF NO
3568          :     TST KTFLG     : GOT KT?
3569          :     BEQ 55$      : NO. GET OUT.
3570          :     JSR PC,KTON   : YES. ENABLE KT.
3571          :     CLR RO       :HIGH ORDER ADDRESS START
3572          :     MOV PST32W,R1 :GET >28K START ADDRESS (IN 32W BLOCKS)
3573          :     .REPT 6
3574          :     ROL R1       :CONVERT BLOCKS TO WORDS
3575          :     ROL RO       :MAKE IT DOUBLE PRECISION
3576          :     .ENDR
3577          :     BIC #177,R1  :ALINE 4K BOUNDARY
3578          :     MOV RO,-(SP) :SAVE HIGH ORDER
3579          :     MOV R1,-(SP) :SAVE LOW ORDER
3580          :     JSR PC,SETMAP :SETUP PAR6 MAPPING REGISTER
3581          :     MOV RO,R4     :COPY ADDRESS BIASED TO PAR6
3582          :     MOV (SP)+,R1  :RESTORE LOW ORDER IN NON PAR6 FORMAT
3583          :     MOV (SP)+,RO  :RESTORE HIGH ORDER IN NON PAR6 FORMAT
3584          :     30$: CMP R3,(R4) :ABOVE 28K LOCATION EQUAL EXPD?
3585          :     BEQ 32$      :BR IF YES
3586          :     MOV RO,ERRHI :SAVE HIGH ORDER IN ERROR

```

CZTUWAO TUBO FRONT END PRT A MACRO M1200 29-MAR-83 13:24 PAGE 57-1  
 CMPMEM - COMPARE MEMORY TO BACKGROUND PATTERN

3587	020556	010137	002204		MOV	R1,ERRLO	:SAVE LOW ORDER IN ERROR
3588	020562	010337	002176		MOV	R3,EXPD	:SAVE EXPD FOR ERROR REPORT
3589	020566	011437	002200		MOV	(R4),RECV	:SAVE RECV FOR ERROR REPORT
3590	020572	000421			BR	50\$	:
3591	020574	062701	000002	32\$:	ADD	#2,K1	:UPDATE NON PAR6 ADDRESS
3592	020600	005500			ADC	R0	:MAKE IT DOUBLE PRECISION ADD
3593	020602	062704	000002		ADD	#2,R4	:UPDATE PAR FORMAT ADDRESS
3594	020606	020427	160000		CMP	R4,#160000	:END OF PAR6 MAPPING AREA?
3595	020612	103755			BLO	30\$	:BR IF NO
3596	020614	162704	020000		SUB	#20000,R4	:BACKUP INTO PAR6 MAPPING BEGIN
3597	020620	062737	000200	172354	ADD	#200,@#KIPAR6	:POINT TO NEXT 4K BLOCK >28K.
3598	020626	023737	172354	003100	CMP	@#KIPAR6,KTFLG	:END OF MEMORY?
3599	020634	101744			BLOS	30\$	:BR IF NO
3600	020636	004737	020064	50\$:	JSR	PC,KTOFF	:TURN OFF MEMORY MAPPING
3601	020642	000241			CLC		:SET FAILURE
3602	020644	000403			BR	60\$	:
3603	020646	004737	020064	55\$:	JSR	PC,KTOFF	:TURN OFF MEMORY MAPPING
3604	020652	000261			SEC		:SET SUCCESS
3605	020654	000207		60\$:	RTS	PC	
3606							

CZTUWAO TUBO FRONT END PRT A  
REGSAV - SAVE R1-R5 ON STACK

MACRO M1200 29-MAR-83 13:24 PAGE 58

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3628 020656  
3629 020656  
3630 020656 104422  
3631 020660 010446  
3632 020662 010346  
3633 020664 010246  
3634 020666 010146  
3635 020670 010546  
3636 020672 016605 000012  
3637 020676 004736  
3638 020700 012601  
3639 020702 012602  
3640 020704 012603  
3641 020706 012604  
3642 020710 012605  
3643 020712 104422  
3644 020714 000207

```
.SBTTL REGSAV - SAVE R1-R5 ON STACK
:
:ROUTINE TO
:SAVE R1 THROUGH R5 ON THE STACK
:
:CALLING SEQUENCE:
:
:       JSR     R5,REGSAV
:
:THIS IS A COOROUTINE WHICH TRANSFER CONTROL BACK TO
:THE CALLING ROUTINE. AT THE END OF THE CALLING ROUTINE,
:THE RTS PC RETURNS CONTROL TO THIS ROUTINE TO RESTORE
:REGISTERS.
:
:THIS ROUTINE SHOULD ONLY BE CALLED FROM ROUTINES WHICH ARE
:CALLED VIA A JSR PC INSTRUCTION
:
:-
```

```
REGSAV:
BREAK                               ;LOOK FOR CNTL C
TRAP                                C$BRK
MOV      R4,-(SP)
MOV      R3,-(SP)
MOV      R2,-(SP)
MOV      R1,-(SP)
MOV      R5,-(SP)
MOV      10.(SP),R5
JSR      PC,@(SP)+
MOV      (SP)+,R1
MOV      (SP)+,R2
MOV      (SP)+,R3
MOV      (SP)+,R4
MOV      (SP)+,R5
BREAK                               ;LOOK FOR CNTL C
TRAP                                C$BRK
RTS      PC
```

```

3646 .SBTTL GETPAT - GET 8 BIT PATTERN FROM OPERATOR
3647
3648
3649 :ROUTINE TO REQUEST AN 8 BIT DATA PATTERN FROM THE OPERATOR
3650
3651 :INPUTS:
3652
3653 :NONE.
3654
3655 :OUTPUTS:
3656
3657 :RO OCTAL NUMBER FROM THE OPERATOR
3658
3659 :CALLING SEQUENCE:
3660
3661 :JSR PC,GETPAT
3662
3663 :-
3664
3665 020716 GETPAT::
3666 020716 1S: SAVREG ;SAVE THE GENERAL REGISTERS
3667 020722 GMANID DATASC,PATDAT,0,377,0,377,NO
020722 104443 TRAP CSGMAN
020724 000406 BR 10000$
020726 020752 .WORD PATDAT
020730 000022 .WORD TSCODE
020732 020754 .WORD DATASC
020734 000377 .WORD 377
020736 000000 .WORD TSLOLIM
020740 000377 .WORD TSHILIM
020742
10000$:
3668 020742 BNCOMPLETE 1$ ;RETRY IF ERROR
020742 103367 BCC 1$
3669 020744 013700 020752 MOV PATDAT,RO ;DATA PATTERN FROM OPERATOR
3670 020750 000207 RTS PC ;RETURN TO CALLER
3671
3672
3673 :+
3674 :LOCAL DATA AREA
3675 :-
3676 020752 000000 PATDAT: .WORD 0 ;TEMPORARY STORAGE FOR DATA
3677 020754 105 116 124 DATASC: .ASCIZ 'ENTER DATA PATTERN'
3678 .EVEN

```

CZTUWAO TUBO FRONT END PRT A MACRO M1200 29-MAR-83 13:24 PAGE 60  
GETSEL - ISSUE MENU AND GET OPERATOR RESPONSE

```

3680
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3690 021000
3691 021000
3692 021004 010002
3693 021006 010203
3694 021010 005713
3695 021012 001412
3696 021014
      021014 012346
      021016 012746 021164
      021022 012746 000002
      021026 010600
      021030 104417
      021032 062706 000006
3697 021036 000764
3698 021040
      021040 104443
      021042 000406
      021044 021220
      021046 000042
      021050 021171
      021052 177777
      021054 000000
      021056 177777
      021060
3699 021060
      021060 103352
3700 021062 013700 021220
3701 021066 020001
3702 021070 101411
3703 021072
      021072 012746 021116
      021076 012746 000001
      021102 010600
      021104 104417
      021106 062706 000004
3704 021112 000735
3705 021114 000207
3706 021116 045 116 045
3707 021164 045 116 045
3708 021171 105 156 164
3709
3710 021220 000000

```

```

.SBTL GETSEL - ISSUE MENU AND GET OPERATOR RESPONSE
:
:ROUTINE TO ISSUE A MENU AND GET
:THE OPERATOR'S RESPONSE.
:INPUTS:
:   R0 ADDRESS OF ASCIZ STRING OF MENU
:   R1 MAXIMUM ALLOWABLE OPERATOR RESPONSE
:OUTPUTS:
:   R0 NUMBER OF THE OPERATOR'S SELECTION
:-
GETSEL::
  SAVREG                                ;SAVE GENERAL REGIS ERS
  MOV R0,R2                              ;SAVE THE MENU ADDRESS
1$: MOV R2,R3                              ;START OF MENU STRING
2$: TST (R3)                              ;END OF ASCII ?
   BEQ 3$                                  ;BRANCH IF ALL LINES DISPLAYED
   PRINTF #SELASC,(R3)+                  ;DISPLAY THE MENU
   MOV (R3)+,-(SP)
   MOV #SELASC,-(SP)
   MOV #2,-(SP)
   MOV SP,R0
   TRAP CSPNTF
   ADD #6,SP
   BR 2$
3$: GMANID MENASC,MENRES,D,-1,0,-1,NO
   TRAP CSGMAN
   BR 10001$
   .WORD MENRES
   .WORD TSCODE
   .WORD MENASC
   .WORD -1
   .WORD TSLOLIM
   .WORD TSHI:IM
10001$: BNCOMPLETE 1$ ;RETRY IF ERROR
        BCC 1$
        MOV MENRES,R0 ;GET THE OPERATOR'S REPLY
        CMP R0,R1 ;COMPARE TO MAXIMUM ALLOWED
        BLOS 5$ ;BRANCH IF OK
        PRINTF #MENERR ;DISPLAY ERROR MESSAGE
        MOV #MENERR,-(SP)
        MOV #1,-(SP)
        MOV SP,R0
        TRAP CSPNTF
        ADD #4,SP
        BR 1$ ;RETRY
5$: RTS PC ;RETURN TO CALLER
MENERR: .ASCIZ 'ZXZA *** Menu Selection Too Large ***'
SELASC: .ASCIZ 'ZXZT'
MENASC: .ASCIZ 'Enter Menu Selection: '
        .EVEN
MENRES: .WORD 0

```

CZTUWAO TUBO FRONT ENC PRT A MACRO M1200 29-MAR-83 13:24 PAGE 61  
CHKMAN - CHECK MANUAL INTERVENTION LEGALITY

3712  
3713  
3714  
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021222  
021222  
021226 104450  
021230 103411  
021232 012746 021256  
021236 012746 000001  
021242 010600  
021244 104417  
021246 062706 000004  
021252 000241  
021254 000207  
021256 045 116 045

```

.SBTTL  CHKMAN - CHECK MANUAL INTERVENTION LEGALITY
:
:ROUTINE TO TEST FOR MANUAL INTERVENTION LEGALITY.
:INPUT:
:      NONE.
:OUTPUT:
:      CARRY  0      MANUAL INTERVENTION NOT ALLOWED
:           1      MANUAL INTERVENTION IS OK
:SIDE EFFECTS:
:      A MESSAGE IS DISPLAYED WARNING THAT TEST IS
:      NOT EXECUTED IF MANUAL INTERVENTION IS NOT
:      ALLOWED.
:
CHKMAN::
  SAVREG          ;SAVE THE REGISTERS
  MANUAL          ;SEE IF MANUAL INTERVENTION OK
  TRAP  CSMANI
  BCOMPLETE 1$   ;BRANCH IF ALLOWED
  BCS 1$
  PRINTF #NOMAN ;PRINT THE WARNING MESSAGE
  MOV #NOMAN,-(SP)
  MOV #1,-(SP)
  MOV SP,R0
  TRAP C$PNTF
  ADD #4,SP
  CLC          ;CLEAR CARRY FOR ERROR
  RTS  PC     ;RETURN
1$:
NOMAN: .ASCIZ 'XNZA *** Manual Intervention not Allowed - Test Aborted ***'
      .even

```

CZTUWAO TUBO FRONT END PRT A MACRO M1200 29-MAR-83 13:24 PAGE 62  
 ENVIRN - SETUP FREE DIAGNOSTIC SPACE

```

3745          .SBTTL  ENVIRN - SETUP FREE DIAGNOSTIC SPACE
3746          :
3747          : SUBROUTINE TO SET-UP VARIOUS ENVIRONMENTAL PARAMETERS.
3748          :
3749          ENVIRN: MEMORY  R0
                   TRAP    CSMEM
3750          021352 104431      003072      MOV    R0,FREE      ; GET 1ST FREE ADDRESS...
3751          021354 010037      003072      ADD    #2,FREE
3752          021360 062737      003074      MOV    (R0),FRESIZ ; ...AND WORD COUNT.
3753          021366 011037      003074      SUB    #4,FRESIZ
3754          021372 162737      003074      MOV    #4,FRESIZ
3755          021400 013702      002012      MOV    LSUNIT,R2   ; GET NUMBER OF UNITS
3756          021404 162737      000007      SUB    #7,FRESIZ   ; TAKE AWAY 7 WORDS PER UNIT
3757          021412 005302
3758          021414 001373
3759          021416 013700      003072      DEC    R2
3760          021422 063700      003074      BNE   10$
3761          021426 162700      000002      MOV    FREE,R0     ;GET FIRST FREE ADDRESS
3762          021432 010037      003076      ADD    FRESIZ,R0   ;POINT TO LAST FREE ADDRESS
3763          021436 000207
                   SUB    #2,R0
                   MOV    R0,FREEH1 ;BACKUP 1 WORD
                   RTS   PC        ;STORE LAST FREE ADDRESS
                   ;RETURN

```



CZTUWAJ TUBO FRONT END PRT A MACRO M1200 29-MAR-83 13:24 PAGE 63  
KTINIT - SETUP KT11 MEMORY MANAGEMENT REGISTERS

```

3765                                     .SBTTL KTINIT - SETUP KT11 MEMORY MANAGEMENT REGISTERS
3766
3767
3768                                     :
3769                                     : ROUTINE TO INIT KT-11
3770                                     :
3771                                     : -
3772                                     KTINIT
3773 021440 005037 003100 CLR KTFLG ; INIT >28K MEMORY FLAG
3774 021444 005037 003102 CLR KTENABLE ; INIT TEST >28K FLAG
3775 021450 023727 002120 001577 CMP LSHIME,#1577 ; GOT ENOUGH MEMORY (>28K)?
3776 021456 101444 9S ; NO.
3777 021460 013700 000004 MOV @ERRVEC,RO ; SAVE OLD ERR VEC PTR.
3778 021464 012737 021556 000004 MOV #2S,@ERRVEC ; SET ERR VEC PTR.
3779 021472 005737 177572 TST @SRO ; GOT KT11?
3780 021476 000240 NOP ; (TRAP IF NO).
3781 021500 013737 002120 003100 MOV LSHIME,KTFLG ; YES. SET KT FLAG.
3782 021506 042737 000177 003100 BIC #177,KTFLG
3783 021514 010037 000004 MOV RO,@ERRVEC ; RESTORE OLD ERR VEC PTR.
3784 021520 005000 CLR RO ; RO = AR DATA.
3785 021522 012701 172340 MOV #KIPAR,R1 ; R1 = KI REGS PTR.
3786 021526 012761 077406 177740 1S: MOV #77406,-40(R1) ; SET DESCRIPTOR REG.
3787 021534 010021 MOV RO,(R1)+ ; SET KIPAR REG.
3788 021536 062700 000200 ADD #200,RO ; BUMP AR DATA BY "4K".
3789 021542 020027 002000 CMP RO,#2000 ; AT "I/O"?
3790 021546 001367 1S ; NO.
3791 021550 012741 177600 MOV #177600,-(R1) ; YES. SET KTPAR7 FOR I/O.
3792 021554 000405 BR 9S
3793
3794 021556 012716 021564 2S: MOV #6S,(SP) ; SET UP RETURN
3795 021562 000002 RTI ; RTI TO NEXT LOCATION
3796
3797 021564 010037 000004 6S: MOV RO,@ERRVEC ; RESTORE OLD ERR VEC PTR.
3798
3799 021570 000207 9S: RTS PC
3800 : .IIF DF ONEFILE, .PAGE
3801
3802
3803
3804
3805
3806
3807
3808
3809
3810
3811
3812
3813
3814
3815
3816
3817
3818 021572 .SBTTL PROTECTION TABLE
3819 021572 BGNPROT
3819 021572 177777 177777 177777 LSPROT:: .WORD -1, -1, -1, -1 ;NO DEVICE PROTECTION REQUIRED.
3820 021602 ENDPROT
3821

```

CZ UWA0 TUBO FRONT END PRT A MACRO M1200 29-MAR-83 13:24 PAGE 64  
INITIALIZE SECTION

```

3823                                     .SBTTL INITIALIZE SECTION
3824
3825                                     :++
3826                                     :THE INITIALIZE SECTION CONTAINS THE CODING THAT IS PERFORMED
3827                                     :AT THE BEGINNING OF EACH PASS.
3828
3829                                     :IF "START" OR "RESTART", SET QUICK-PASS FLAG AND BUS-INIT.
3830                                     :IF "CONTINUE", NOTHING IS REQUIRED.
3831
3832                                     :--
3833                                     :+
3834                                     :INSERT TEMPORARY JUMP TO ODT
3835                                     :--
3836 021602                                BGNINIT
                                021602
3837 021602                                LSINIT::
3838 021602 012737 005672 002146          40$:
3839 021610 005037 003106
3840 021614 005037 003102
3841 021620 005037 002246
3842 021624
                                021624 012700 000036
                                021630 104447
3843 021632
                                021632 103023
3844 021634 023737 002150 002012
3845 021642 103073
3846 021644 005737 003060
3847 021650 100475
3848 021652 013701 002150
3849 021656 006301
3850 021660 005761 003130
3851 021664 001521
3852 021666 032761 040000 003130
3853 021674 001063
3854 021676
                                021676 104432
                                021700 000430
3855 021702                                1$:
                                021702 012700 000035
                                021706 104447
3856 021710
                                021710 103055
3857 021712
                                021712 012700 000040
                                021716 104447
3858 021720
                                021720 103404
3859 021722
                                021722 012700 000037
                                021726 104447
3860 021730
                                021730 103034
3861 021732                                2$:
3862 021732
                                021732 104433
3863 021734 005037 002162

```

:++  
:THE INITIALIZE SECTION CONTAINS THE CODING THAT IS PERFORMED  
:AT THE BEGINNING OF EACH PASS.

:IF "START" OR "RESTART", SET QUICK-PASS FLAG AND BUS-INIT.  
:IF "CONTINUE", NOTHING IS REQUIRED.

:--  
:+  
:INSERT TEMPORARY JUMP TO ODT  
:--

BGNINIT

LSINIT::

40\$:

MOV #EPR1,EPR1SW ;SET UP PRIMARY MESSAGE FOR REPLACEMENT  
CLR SIFLAG ;CLEAR "SOFT INIT" FLAG  
CLR KTENABLE ;CLEAR TEST ABOVE 28K FLAG  
CLR RAMSIZ ;CLEAR RAM SIZE FOR RAMERR ROUTINE

READF #EF.CONTINUE  
MOV #EF.CONTINUE,RO  
TRAP CSREFG

BNCOMPLETE 1\$

BCC 1\$  
CMP UNITN,LSUNIT ;UNIT IN RANGE?  
BHIS 4\$ ;BR IF NO.  
TST DUFLG ;DROPPED UNIT?  
BMI NXTU ;BR IF YES

MOV UNITN,R1

ASL R1  
TST ERTABL(R1)

BEQ SETU  
BIT #BIT14,ERTABL(R1) ;DROPPED?

BNE NXTU

EXIT INIT ;DO NOTHING IF "CONTINUE".

TRAP CSEXIT

.WORD L10030--

1\$:

READF #EF.NEW

MOV #EF.NEW,RO

TRAP CSREFG

BNCOMPLETE NXTU ;TAKE NEXT UNIT IF NOT NEW PASS.

BCC NXTU

READF #EF.START

MOV #EF.START,RO

TRAP CSREFG

BCOMPLETE 2\$

BCS 2\$

READF #EF.RESTART

MOV #EF.RESTART,RO

TRAP CSREFG

BNCOMPLETE 31\$

BCC 31\$

2\$:

BRESET

TRAP CSRESET

CLR TSTCNT

;1ST PASS, BUS-INIT...

;BUS RESET.

;NUMBER OF TESTS RUN IN PASS

CZTUWAO TU80 FRONT END PRT A MACRO M1200 29-MAR-83 13:24 PAGE 64-1  
INITIALIZE SECTION

3864	021740	005037	002170		CLR	FATFLG		:RESET FLAG TO ZERO 'FATAL ERRORS'
3865	021744	000406			BR	198		:BR, IF THE FLAG IS NOT SET
3866								: (NO DEBUGGER ETC.)
3867	021746	012746	000340		MOV	#340,-(SP)		
3868	021752	012746	021766		MOV	#208,-(SP)		:RETURN TO DEBUGGER
3869	021756	000137	046040		JMP	O.ODT		:ENTER THE DEBUGGER
3870	021762	005037	003332		CLR	SKIPT		:CLEAR THE SUBTEST 'SKIPPER'
3871	021766							
3872	021766	012737	177777	002152	MOV	#-1,QVP		:...QUICK VERIFY...
3873	021774	004737	021352		JSR	PC,ENVIRN		:SET ENVIRONMENT.
3874	022000	004737	021440		JSR	PC,KTINIT		:INITIALIZE KT MEMORY MANAGEMENT
3875	022004	012700	003130		MOV	#ERTABL,RO		
3876	022010	005020			CLR	(RO)+		:CLEAR THE ERROR TABLE
3877	022012	020027	003330		CMP	RO,#ERTABE		
3878	022016	103774			BLO	308		
3879	022020	000404			BR	48		
3880	022022	005037	002152		CLR	QVP		
3881	022026	000137	022076		JMP	PASRPT		:GO REPORT THE STATUS
3882								
3883	022032							
3884	022032	012737	177777	002150	NEWPAS: MOV	#-1,UNITN		:INIT UNIT NUMBER...
3885	022040	005037	002166		CLR	DEVcnt		:CLEAR COUNT OF DEVICES RUNNING
3886	022044				NXTU: BREAK			
3887	022046	005237	002150		TRAP	CSBRK		
3888	022052	023737	002150	002012	INC	UNITN		:...AND SET NEXT UNIT NUMBER.
3889	022060	103423			CMP	UNITN,LSUNIT		
3890	022062	012737	177777	003060	BLO	SETU		
3891	022070	000401			MOV	#-1,DUFLG		
3892	022072				BR	118		
3893	022074	000240			DOCLN			:ABORT, NO MORE UNITS.
3894	022076				TRAP	CSDCLN		
3895	022076	023727	002012	000001	NOP			
3896	022104	101752			118: PASRPT: CMP	LSUNIT,#1		:HOW MANY UNITS SELECTED?
3897	022106	005737	002166		BLOS	NEWPAS		:BR IF ONLY 1
3898	022112	001747			TST	DEVcnt		:ARE ANY STILL RUNNING?
3899	022114				BEG	NEWPAS		:BR IF NO
3900	022116	032700	000100		RFLAGS	RO		
3901	022122	001343			TRAP	CSRFLA		
3902					BIT	#ISR,RO		:SHOULD WE PRINT STATISTICS
3903	022124				BNE	NEWPAS		:BR IF NO
3904	022126	000741						
3905	022130				DORPT			
3906					TRAP	CSDRPT		
3907	022130				BR	NEWPAS		
3908	022136							
3909	022140	005037	003060		108: SETU: GPHARD	UNITN,RO		:GET UNIT N P-TABLE POINTER.
3910	022144	005237	002166		MOV	UNITN,RO		
3911	022150	012001			TRAP	CSGPHRD		
3912	022152	010137	002154		BNCOMPLETE	NXTU		:BR IF UNIT NOT AVAILABLE.
3913					BCC	NXTU		
					CLR	DUFLG		:CLEAR 'DROPPED' FLAG.
					INC	DEVcnt		
					MOV	(RO)+,R1		:GET 1ST REGISTER ADDRESS.
					MOV	R1,CSRADDR		:ADDRESS OF REGISTERS OF UNIT UNDER TEST

CZTUWAO TUBO FRONT END PRT A MACRO M1200 29-MAR-83 13:24 PAGE 64-2  
INITIALIZE SECTION

```

3914 022156 012001      MOV      (R0)+,R1      ;GET VECTOR ADDRESS.
3915 022160 011002      MOV      (R0),R2      ;GET INTERRUPT PRIORITY
3916 022162 010237 002160  MOV      R2,IPRI      ;SET INTERRUPT PRIORITY.
3917 022166 010137 002156  MOV      R1,IVEC      ;SET INTERRUPT VECTOR POINTER...
3918 022172 012721 016706  MOV      #INIR,(R1)+  ;...VECTOR...
3919 022176 010221      MOV      R2,(R1)+    ;...AND PRIORITY.
3920
3921 022200      1$:
3922      :      TST      QVP      ;1ST PASS ??
3923      :      BEQ      5$      ;NO, SKIP THE PASS 1 STUFF.
3924
3925      :
3926      :      ;1ST PASS, CHECK THAT DEVICE ADDRESSES ARE VALID, AND
3927      :      ;THAT THE DISPLAY STATUS IS PROPERLY INITIALIZED.
3928      :
3929 022200 013701 002150      MOV      UNITN,R1
3930 022204 006301      ASL      R1
3931 022206 052761 100000 003130  BIS      #BIT15,ERTABL(R1) ;SAY DEVICE RUNNING
3932 022214 005037 005232      CLR      EXTA      ;CLEAR ERROR EXTENSION FLAG.
3933 022220 023727 002012 000001  CMP      L$UNIT,#1  ;ARE WE TESTING MULTIPLE UNITS?
3934 022226 101416      BLOS     10$      ;BR IF NO.
3935 022230      RFLAGS     RO      ;YES -- GET OPERATOR FLAGS.
3936 022232 104421      TRAP     CSRFLA
3937 022236 032700 001000  BIT      #PNT,RO      ;SHOULD WE PRINT UNIT #?
3938 022240 001412      BEQ      10$      ;BR IF NOT.
3939 022244 013746 002150      PRINTF  #PUNIT,UNITN ;PRINT THE UNIT #
3940 022244 012746 022332      MOV      UNITN,-(SP)
3941 022250 012746 000002      MOV      #PUNIT,-(SP)
3942 022254 010600      MOV      #2,-(SP)
3943 022256 104417      MOV      SP,RO
3944 022260 062706 000006      TRAP     CSPNTF
3945 022264      ADD      #6,SP
3946 022264 005037 003062 10$:
3947 022270 013701 002154      CLR      NODEV
3948 022274 010102      MOV      CSRADDR,R1 ;ADDRESS OF FIRST REGISTER
3949 022276 062702 000000      MOV      R1,R2      ;START OF REGISTERS
3950 022302 004737 017114      ADD      #TSSR,R2   ;ADDRESS OF TSSR REGISTER
3951 022306 103005      JSR      PC,XNXM    ;TEST BOTH CONTROLLER REGISTERS...
3952 022310 010137 003062      BCC     2$          ;...AND BR IF ALL OK.
3953 022314 012737 177777 003060  MOV      R1,NODEV   ;FLAG DEVICE AS NON-EXISTENT
3954 022322      MOV      #-1,DUFLG ;DROP THIS UNIT.
3955
3956      2$:
3957      :      ;FINALLY, SET CPU PRIORITY AND WE'RE DONE.
3958      :
3959      5$:
3960 022322      SETPRI  #PRI00      ;ENABLE INTERRUPTS.
3961 022322 012700 000000      MOV      #PRI00,RO
3962 022326 104441      TRAP     CSSPRI
3963 022330      ENDINIT
3964 022330 104411  L10030:
3965 022332      TRAP     CSINIT
3966 022332      045      116      045  PUNIT: .ASCIZ  /%N%N%***** TESTING UNIT %D2%A *****/
3967      .EVEN

```

CZTUWAO TUBO FRONT END PRT A  
ADD AND DROP UNITS SECTIONS

MACRO M1200 29-MAR-83 13:24 PAGE 65

.SBTTL ADD AND DROP UNITS SECTIONS

3958  
3959  
3960  
3961  
3962  
3963  
3964

;++  
: THE ADD-UNIT SECTION CONTAINS THE CODING THAT CAUSES A DEVICE  
: TO BE (A) ADDED TO THE TEST LIST FOR THE FIRST TIME,  
: OR (B) RE-INSERTED IF IT HAD BEEN PREVIOUSLY DROPPED.  
:--

3965 022400  
022400  
3966 022400 010001  
3967 022402 006301  
3968 022404 052761 100000 003130  
3969 022412 042761 040000 003130  
3970 022420  
022420 010046  
022422 012746 022446  
022426 012746 000002  
022432 010600  
022434 104417  
022436 062706 000006

LSAU:: BGNAU  
MOV RO,R1 ; GET UNIT TO BE ADDED (RO)  
ASL R1 ; MAKE IT A WORD INDEX  
BIS #100000,ERTABL(R1) ; SET THE 'ACTIVE' BIT  
BIC #40000,ERTABL(R1) ; CLEAR THE 'DROPPED' BIT  
PRINTF #1\$,RO  
MOV RO,-(SP)  
MOV #1,-(SP)  
MOV #2,-(SP)  
MOV SP,RO  
TRAP CSPNTF  
ADD #6,SP  
EXIT AU  
.WORD JSJMP  
.WORD L10031-2-  
18: .ASCIZ /XNZA UNIT XDZA ADDED/  
.EVEN

3971 022442  
022442 000167  
022444 000026  
3972 022446 045 116 045 18:

3973  
3974  
3975 022474  
022474  
022474 104452

ENDAU ; UNUSED.  
L10031: TRAP CSAU

3976  
3977  
3978  
3979  
3980  
3981  
3982  
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3984  
3985  
3986

;++  
: THE DROP-UNIT SECTION CONTAINS THE CODING THAT CAUSES A DEVICE  
: TO BE REMOVED FROM THE TEST LIST.  
:  
: SUPVSR DOES THE 'DROPPING'. THIS IS JUST TO TELL THE MAN.  
: 'DROPPED' UNITS ARE RE-SELECTED ON OPERATOR 'STA' OR 'ADD'  
: COMMAND, OTHERWISE REMAIN INACTIVE. THE 'DISPLAY' COMMAND  
: WILL PRINT ALL DROPPED UNITS, AND THE P-TABLES OF THOSE  
: WHICH ARE STILL ACTIVE.  
: UPON ENTRY, RO CONTAINS THE UNIT TO BE DROPPED.

3987 022476  
022476  
3988 022476 012737 177777 003060  
3989 022504 010001  
3990 022506 006301  
3991 022510 052761 140000 003130  
3992 022516 000240 000240 000240  
3993 022524  
022524 010046  
022526 012746 022552  
022532 012746 000002  
022536 010600  
022540 104417  
022542 062706 000006

BGNDU  
LSDU:: MOV #-1,DUFLG  
MOV RO,R1  
ASL R1  
BIS #140000,ERTABL(R1) ; SAY DROPPED  
240,240,240 ; ??????????  
PRINTF #1\$,RO  
MOV RO,-(SP)  
MOV #1,-(SP)  
MOV #2,-(SP)  
MOV SP,RO  
TRAP CSPNTF  
ADD #6,SP  
EXIT DU  
.WORD JSJMP  
.WORD L10032-2-

3994 022546  
022546 000167  
022550 000030

CZTUWAO TUBO FRONT END PRT A  
ADD AND DROP UNITS SECTIONS

MACRO M1200 29-MAR-83 13:24 PAGE 65-1

3995	022552	045	116	045	18:	.ASCIZ /XNZA UNIT XDZA DROPPED/ .EVEN ENDDU	
3996							
3997	022602				L10032:	TRAP	CSDU
	022602	104453					
	022602						
3998					:++		
3999					: AUTO-DROP CODE SECTION.		
4000					:--		
4001	022604					BGNAUTO	
	022604				LSAUTO::		
4002	022604	012703	000550		10\$:	MOV #360.,R3	:ENOUGH TIME FOR 2400' REEL TO REWIND
4003	022610	004737	016740			JSR PC,WAIF	:WAIT FOR SSR TO SET
4004	022614	103420				BCS 20\$	:LEAVE WHEN SSR IS SET
4005	022616					DELAY 250.	:WAIT FOR .25 SECONDS
	022616	012727	000372			MOV #250.,(PC)+	
	022622	000000				.WORD 0	
	022624	013727	002116			MOV LSDLY,(PC)+	
	022630	000000				.WORD 0	
	022632	005367	177772			DEC -6(PC)	
	022636	001375				BNE .-4	
	022640	005367	177756			DEC -22(PC)	
	022644	001367				BNE .-20	
4006	022646	005303				DEC R3	:BUMP COUNTER DOWN
4007	022650	001357				BNE 10\$	:KEEP GOING
4008	022652	004737	017772			JSR PC,CKDROP	:TRY AND DROP UNIT
4009	022656				20\$:		
4010	022656					ENDAUTO	: UNUSED.
	022656				L10033:		
	022656	104461				TRAP	C\$AUTO

CZTUWAO TUBO FRONT END PRT A MACRO M1200 29-MAR-83 13:24 PAGE 66  
CLEAN-UP AND REPORT CODING SECTIONS

.SBTTL CLEAN-UP AND REPORT CODING SECTIONS

4012  
4013  
4014  
4015  
4016  
4017  
4018

;++  
: THE CLEANUP CODING SECTION CONTAINS THE CODING THAT IS  
: EXECUTED AT THE END OF EACH PASS (OR SUB-PASS).  
: USE TO RETURN DEVICE UNDER TEST TO A NEUTRAL STATE.  
:--

4019 022660  
022660  
4020 022660 005737 003060  
4021 022664 100405  
4022  
4023  
4024 022666 012765 000000 000000  
4025 022674 004737 016740  
4026 022700  
4027 022700  
022700  
022700 104412

BGNCLN  
LSCLEAN::  
TST DUFLG ;'DROPPED' FLAG IS SET ON...  
BMI 1\$ ;...AND GROSS CONTROLLER FAULT...  
;...DON'T TRY TO XCT CLEANUP CODE.  
MOV #0,TSSR(R5) ;DO SOFT INIT  
JSR PC,WAITF  
1\$:  
2\$: ENDCLN  
L10034:  
TRAP CSCLEAN

4028  
4029  
4030  
4031

;++  
: THE REPORT CODING SECTION CONTAINS THE  
: 'PRINTS' CALLS THAT GENERATE STATISTICAL REPORTS.  
:--

4032 022702  
022702  
4033 022702  
022702 012746 023144  
022706 012746 000001  
022712 010600  
022714 104416  
022716 062706 000004  
4034 022722 010246  
4035 022724 010346  
4036 022726 010446  
4037 022730 012704 003130  
4038 022734 005003  
4039 022736 011402  
4040 022740 001467  
4041 022742 100066  
4042 022744 032702 040000  
4043 022750 001015  
4044 022752 042702 170000  
4045 022756  
022756 010246  
022760 010346  
022762 012746 023201  
022766 012746 000003  
022772 010600  
022774 104416  
022776 062706 000010  
4046 023002 000446  
4047 023004 020227 160000  
4048 023010 001012  
4049 023012  
023012 010346  
023014 012746 023251  
023020 012746 000002

BGNRPT  
LSRPT::  
PRINTS #DEVSUM  
MOV #DEVSUM,-(SP)  
MOV #1,-(SP)  
MOV SP,R0  
TRAP CSPNTS  
ADD #4,SP  
MOV R2,-(SP)  
MOV R3,-(SP)  
MOV R4,-(SP)  
MOV #ERTABL,R4 ; GET START OF ERROR TABLE.  
CLR R3 ; CLEAR UNIT NUMBER  
1\$: MOV (R4),R2 ; GET ERROR TABLE ENTRY & TEST IT.  
BEQ 4\$ ; ZERO IF UNIT NOT RUN  
BPL 4\$  
BIT #BIT14,R2 ; WAS UNIT DROPPED?  
BNE 2\$ ; BR IF YES  
BIC #^C7777,R2 ; GET ERROR COUNT FIELD  
PRINTS #DEVOML,R3,R2 ; PRINT  
MOV R2,-(SP)  
MOV R3,-(SP)  
MOV #DEVOML,-(SP)  
MOV #3,-(SP)  
MOV SP,R0  
TRAP CSPNTS  
ADD #10,SP  
BR 4\$  
2\$: CMP R2,#160000 ; WAS UNIT NON-EXISTENT?  
BNE 3\$ ; BR IF NO  
PRINTS #DEVNXR,R3  
MOV R3,-(SP)  
MOV #DEVNXR,-(SP)  
MOV #2,-(SP)

CZTUWAO TUBO FRONT END PRT A MACRO M1200 29-MAR-83 13:24 PAGE 66-1  
CLEAN-UP AND REPORT CODING SECTIONS

```

023024 010600      MOV      SP,R0
023026 104416      TRAP     CSPNTS
023030 062706 000006  ADD     #6,SP
4050 023034 000431  BR      4C
4051 023036 020227 160001  3C:    CMP     R2,#160001      ; WAS UNIT NOT READY AT STARTUP?
4052 023042 001012      BNE     308             ; BR IF NO.
4053 023044      PRINTS  #DEVNRD,R3
023044 010346      MOV     R3,-(SP)
023046 012746 023333  MOV     #DEVNRD,-(SP)
023052 012746 000002  MOV     #2,-(SP)
023056 010600      MOV     SP,R0
023060 104416      TRAP     CSPNTS
023062 062706 000006  ADD     #6,SP
4054 023066 000414  BR      48
4055 023070 042702 170000  308:   BIC     #^C7777,R2
4056 023074      PRINTS  #DEVDRD,R3,R2
023074 010246      MOV     R2,-(SP)
023076 010346      MOV     R3,-(SP)
023100 012746 023414  MOV     #DEVDRD,-(SP)
023104 012746 000003  MOV     #3,-(SP)
023110 010600      MOV     SP,R0
023112 104416      TRAP     CSPNTS
023114 062706 000010  ADD     #10,SP
4057 023120 062704 000002  48:    ADD     #2,R4
4058 023124 005203      INC     R3
4059 023126 020427 003330  CMP     R4,#ERTABE
4060 023132 103701      BLO     18
4061 023134 012604      MOV     (SP)+,R4
4062 023136 012603      MOV     (SP)+,R3
4063 023140 012602      MOV     (SP)+,R2
4064 023142      ENDRPT      ; UNUSED.
023142      L10035:    TRAP     CSRPT
023142 104425
4065
4066
4067 023144      045      116      045  DEVSUM: .ASCIZ /%X%ADEVICE STATUS SUMMARY:%N/
4068 023201      045      101      040  DEVONL: .ASCIZ /%A UNIT %D3%A ONLINE, ERRORS = %D%N/
4069 023251      045      101      040  DEVNXR: .ASCIZ /%A UNIT %D3%A DROPPED, NON-EXISTENT REGISTER%N/
4070 023333      045      101      040  DEVNRD: .ASCIZ /%A UNIT %D3%A DROPPED, NOT READY AT STARTUP%N/
4071 023414      045      101      040  DEVDRD: .ASCIZ /%A UNIT %D3%A DROPPED, ERRORS = %D%N/
4072      .EVEN
4075
4082
4088
4096

```



.SBTTL TEST 1: BUS RESET TEST

4098  
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.....

THIS TEST VERIFIES THAT THE DV132 MODULE'S DEVICE REGISTERS ARE ACCESSIBLE ON THE BUS (SUBTEST 1) AND THEN CHECKS THAT THE BUILT-IN INITIALIZATION SELF-TEST MICRODIAGNOSTIC DID NOT FIND ANY BASIC PROBLEMS IN THE MODULE. AREAS OF LOGIC TESTED BY THE SELF-TEST SEQUENCE ARE AS FOLLOWS: ROM AND PIPELINE REGISTER, SEQUENCER, INTERNAL BUSES, 2901 MICROPROCESSOR, AND, RAM. THIS TEST INITIALIZES THE CONTROLLER BY ISSUING THE BUS INIT SIGNAL VIA A RESET INSTRUCTION, OR BY WRITING INTO THE TSSR REGISTER, WAITS A PERIOD OF TIME (TO ALLOW THE CONTROLLER'S INITIALIZATION MICRODIAGNOSTIC SEQUENCE TO BE COMPLETED), AND THEN CHECKS THE CONTENTS OF THE TSSR REGISTER. SUCCESSFUL INITIALIZATION IS INDICATED BY SUBSYSTEM READY (SSR) AND NFEED BUFFER ADDRESS (NBA) BITS BEING SET (1) AND ALL OTHER BITS (EXCEPT A17 AND A16 AND OFL, WHICH ARE IGNORED FOR THIS TEST) BEING CLEAR (0). IF THE CONTENTS OF TSSR ARE NOT AS EXPECTED, AN ERROR REPORT IS ISSUED LISTING THE EXPECTED DATA, ACTUAL DATA, AND THE DISCREPANCIES. THE ERROR REPORT ANALYZES THE TSSR CONTENTS AND DISCERNs AND REPORTS ONE OF THREE POSSIBILITIES:

1. TSSR CONTENTS ARE AMBIGUOUS (ANY OF BITS 11-14 ARE SET, OR STATES OF SSR AND SC BITS DO NOT CORRESPOND TO THE APPARENT ERROR CODE IN BITS 0-5): INDICATES THAT THE TSSR CONTENT CANNOT BE TRUSTED. INDICATES A CATASTROPHIC CONTROLLER MALFUNCTION. THIS IS A FATAL ERROR (EXECUTION IS ABORTED). FIELD ACTION WOULD BE TO REPLACE THE DV132. IF THE DV132 ITSELF IS BEING DEBUGGED, THE PROGRAM SHOULD BE RESTARTED WITH LOOP ON ERROR ENABLED IN ORDER TO PROBE FOR THE PROBLEM.
2. SSR = 0, SC = 0 AND THE ERROR CODE IN BITS 0-5 IS IN THE RANGE 17-13: THIS IS A FATAL ERROR. THE ERROR CODE IS DECODED AND THE APPROPRIATE DESCRIPTION GIVEN. INDICATES THAT A SERIOUS PROBLEM EXISTS.

```

023464          BGNTST
023464          T1::
4138 023464 005037 002170          CLR    FATFLG          ;CLEAR FATAL ERROR FLAG
4139 023470 012737 005672 002146  MOV    #EPR1,EPR1SW    ;SET UP ERROR MESSAGE SWITCH
4140 023476 005037 003100          CLR    KFLG           ;HOLD OFF KT11
4145 023502 012700 023700          MOV    #TST1ID,RO     ;ASCII MESSAGE TO IDENTIFY TEST
4146 023506 004737 017226          JSR    PC,TSTSETUP    ;DO INITIAL TEST SETUP
4147 023512 012737 000005 002164  MOV    #5.,LOOPCNT    ;PERFORM 5 ITERATIONS
4148 023520          T1LOOP:
4149 023520 005003          CLR    R3            ;USE R3 AS FATAL ERROR FLAG
4150
4151 023522          BGNSUB          ;////////// BEGIN SUBTEST //////////
      023522          T1.1:
      023522 104402          TRAP   CSBSUB
4152
4153 023524          BRESET          ;ISSUE A BUS RESET
      023524 104433          TRAP   CSRESET
4154 023526 004737 016740          JSR    PC,WAITF      ;WAIT FOR READY

```

4155 023532 016501 000000  
4156 023536 010102  
4157 023540 042702 176277  
4158 023544 052702 002200  
4159 023550 020102  
4160 023552 001405  
4164 023554  
023554 104455  
023556 000145  
023560 003603  
023562 011566  
4165 023564 005203  
4166 023566  
4167 023566  
023566  
023566 104403  
4168

MOV TSSR(R5),R1  
MOV R1,R2  
BIC #^C<HIADDR!OFL>,R2  
BIS #SSR!NBA,R2  
CMP R1,R2  
BEQ 10\$  
ERRDF ERRNO,SFHERR,SFFMSG

:GET THE CONTENTS OF TSSR  
:CONTENTS OF TSSR  
:THESE BITS MAY BE SET  
:READY AND NEW DATA SHOULD BE SET  
:COMPARE EXPECTED TO RECEIVED  
:BRANCH IF COMPARE  
:REPORT A FATAL ERROR

TRAP C\$ERDF  
.WORD 101  
.WORD SFHERR  
.WORD SFFMSG

10\$: INC R3  
ENDSUB

:SET THE FATAL ERROR FLAG  
:////////// END SUBTEST ///////////  
L10037:  
TRAP C\$ESUB

CZTUWAO TUBO FRONT END PRT A MACRO M1200 29-MAR-83 13:24 PAGE 68  
TEST 1: BUS RESET TEST

```

4170 023570 005703          TST      R3          ;DID WE HAVE FATAL ERROR ?
4171 023572 001402          BEQ      20$         ;BRANCH IF NOT
4172 023574 004737 017772          JSR      PC,CKDROP ;GO DROP THIS UNIT, IF ALLOWED
4173 023600 005003          CLR      R3          ;RESET FATAL ERROR FLAG
4174
4175
4176 023602          BGNSUB          ;//////////////// BEGIN SUBTEST //////////////////
      023602          T1.2:          TRAP      CSBSUB
      023602 104402
4177
4178 023604 005065 000000          CLR      TSSR(R5)   ;WRITE TO ISSUE A SOFT RESET
4179 023610 004737 016740          JSR      PC,WAITF   ;WAIT FOR READY TO SET
4180 023614 016501 000000          MOV      TSSR(R5),R1 ;GET REGISTER TSSR DATA
4181 023620 010102          MOV      R1,R2     ;CONTENTS OF TSSR
4182 023622 042702 176277          BIC      #^C<HIADDR!OFL>,R2 ;THESE BITS MAY BE SET
4183 023626 052702 002200          BIS      #SSR!NBA,R2 ;READY AND NEW DATA SHOULD BE SET
4184 023632 020102          CMP      R1,R2     ;COMPARE EXPECTED TO RECEIVED
4185 023634 001405          BEQ      10$         ;BRANCH IF COMPARE
4189 023636          ERRDF   ERRNO,SFIERR,SFFMSG ;REPORT A FATAL ERROR
      023636 104455          TRAP      CSERDF
      023640 000146          .WORD    102
      023642 003550          .WORD    SFIERR
      023644 011566          .WORD    SFFMSG
4190 023646 005203          INC      R3          ;SET THE ERROR FLAG
4191 023650          10$:
4192 023650          ENDSUB          ;//////////////// END SUBTEST //////////////////
      023650          L10040:          TRAP      CSESUB
      023650 104403
4193
4194
4195 023652 005703          TST      R3          ;FATAL ERROR DETECTED ?
4196 023654 001402          BEQ      20$         ;BRANCH IF NOT
4197 023656 004737 017772          JSR      PC,CKDROP ;SEE IF TIME TO DROP UNIT
4198 023662 004737 017174          JSR      PC,TSTLOOP ;SHOULD WE DO ITERATIONS ?
4199 023666 103002          BCC     40$         ;BRANCH IF NOT
4200 023670 000137 023520          JMP      T1LOOP     ;LOOP UNTIL COUNT EXPIRED
4201 023674          40$:          EXIT      TST       ;ALL DONE THIS TEST
      023674 104432          TRAP      CSEXIT
      023676 000022          .WORD    L10036-.
4202
4203          ;+
4204          ;LOCAL TEXT MESSAGES FOR TEST
4205          ;-
4206
4207 023700          111      156      151  TST11D: .ASCIZ 'Initialization'
4208          .EVEN
4209 023720          ENDTST
      023720          L10036:          TRAP      CSETST
      023720 104401
4210
4211

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4261 023722  
023722  
4262 023722 005037 002170  
4263 023726 012737 005672 002146  
4264 023734 005037 003100  
4265  
4266 023740  
023740  
023740 104402  
4267

.SBTTL TEST 2: RAM TEST

THIS TEST VERIFIES THAT ALL LOCATIONS OF THE RAM ON THE DV132 CAN PROPERLY STORE AND READ BACK ALL DATA PATTERNS, AND THAT EACH RAM LOCATION IS UNIQUELY ADDRESSED (I.E., THAT ONE AND ONLY ONE LOCATION IS ACCESSED BY ANY PARTICULAR ADDRESS). THESE TESTS ARE PERFORMED BY THREE SUBTESTS, DESCRIBED BELOW. A BYPRODUCT OF THESE TESTS IS A VERIFICATION OF TWO REGISTERS IN THE 2901 AND THE CAPABILITY OF THE 2901 TO CORRECTLY PERFORM AN ADD.

TEST 2, SUBTEST 1: -

THIS SUBTEST VERIFIES EACH RAM LOCATION BY FIRST PLACING THE DV132 INTO MAINTENANCE MODE BY WRITING INTO THE LOW BYTE OF TSDB AND THEN PERFORMING THE FOLLOWING SEQUENCE FOR EACH ADDRESS 0-7777 (OCTAL):

1. THE ADDRESS TO BE TESTED IS LOADED INTO THE TSDB (VIA A WORD WRITE).
2. THE ADDRESSED RAM LOCATION IS WRITTEN, THEN READ INTO THE LOW BYTE OF TSBA, BY WRITING A DATA BYTE INTO THE LOW BYTE OF TSDB.
3. THE LOW BYTE OF TSBA IS CHECKED TO SEE IF IT CONTAINS THE DATA PATTERN ORIGINALLY WRITTEN; A DISCREPANCY IS REPORTED AS AN ERROR.
4. THE ADDRESS OF THE LOCATION BEING TESTED IS AGAIN WRITTEN INTO TSDB (WORD WRITE), TO CAUSE THE LOCATION UNDER TEST TO AGAIN BE READ INTO THE LOW BYTE OF TSBA. THE LOW BYTE OF TSBA IS AGAIN CHECKED AND DISCREPANCIES REPORTED.
5. THE HIGH BYTE OF TSBA IS CHECKED; IT SHOULD CONTAIN THE SUM OF THE HIGH AND LOW BYTES LAST WRITTEN INTO TSDB AS A WORD. A DISCREPANCY IS REPORTED AS A 2901 PROBLEM.
6. THE CONTENT OF TSSR IS CHECKED; SETTING OF THE SC BIT IS IGNORED. OTHER DISCREPANCIES IN TSSR ARE REPORTED.

BGNTST

CLR FATFLG  
MOV #EPRT1,EPRTSW  
CLR KTFLG

T2::  
:CLEAR FATAL ERROR FLAG  
:SET UP ERROR MESSAGE SWITCH  
:HOLD OFF KT11  
://////////////// BEGIN SUBTEST //////////////////  
T2.1:  
TRAP CSBSUB

BGNSUB

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 TEST 2: RAM TEST

```

4272 023742 012700 024666          MOV    #TST2ID,R0          ;ASCII MESSAGE TO IDENTIFY TEST
4273 023746 004737 017226          JSR    PC,TSTSETUP        ;DO INITIAL TEST SETUP
4274 023752 012737 000002 002164  MOV    #2,LOOPCNT        ;PERFORM 2 ITERATIONS
4275 023760          T2LOOP:
4276 023760 004737 016464          JSR    PC,SDFINIT        ;DO INITIALIZE ON CONTROLLER
4277 023764 103405          BCS    20$              ;BR IF INIT WAS OK
4281 023766 010001          MOV    R0,R1            ;CONTENTS OF TSSR REGISTER
4282 023770          ERRDF  ERRNO,SFIERR,SFIMSG ;FATAL ERROR TSSR WAS NOT OK
                                TRAP    CSERDF
                                .WORD   201
                                .WORD   SFIERR
                                .WORD   SFIMSG
                                023770 104455
                                023772 000311
                                023774 003550
                                023776 011506
4283 024000 012704 000002 20$:  MOV    #2,R4            ;SET RAM ADDRESS AT TWO
4284 024004          25$:
4285
4286 024004 110402          MOVB   R4,R2            ;EXPECTED DATA FROM WRAP-AROUND
4287 024006 110465 177777          MOVB   R4,TSDBH(R5)     ;LOAD ADDRESS INTO TSDB
4288 024012 110265 177776          MOVB   R2,TSDBL(R5)     ;LOADS DATA INTO RAM LOCATION
4289 024016 116501 177776          MOVB   TSBAL(R5),R1     ;READS WRAP DATA
4290 024022 120102          CMPB   R1,R2            ;DOES WRITTEN(WRAP) = READ
4291 024024 001404          BEQ    30$              ;BR IF OK, THEY ARE EQUAL
4295 024026          ERRHRD ERRNO,TSBAM2,EXPREC ;DATA NOT WRAPPED CORRECTLY
                                TRAP    CSERHRD
                                .WORD   202
                                .WORD   TSBAM2
                                .WORD   EXPREC
                                024026 104456
                                024030 000312
                                024032 024524
                                024034 016164
4296 024036          30$:
4297
4298 024036 005204          INC    R4                ;NEXT ADDRESS
4299 024040 020427 000400          CMP    R4,#400          ;END OF RAM MEMORY CHECK
4300 024044 001357          BNE    25$              ;LOOP TILL ALL RAM WRITTEN
4301 024046 005002          CLR    R2                ;CLEAR OUT R2 HIGH BITS
4302 024050 005304          DEC    R4                ;SET BACK TO 377
4303 024052 110402          40$:  MOVB   R4,R2            ;GET DATA PATTERN BACK IN SHAPE
4304 024054 110465 177777          MOVB   R4,TSDBH(R5)     ;LOAD UP RAM ADDRESS POINTER
4305 024060 116501 177776          MOVB   TSBAL(R5),R1     ;READ RAM CONTENTS BACK
4306 024064 120102          CMPB   R1,R2            ;CHECK WITH DATA WRITTEN
4307 024066 001404          BEQ    45$              ;BR IF OK, DATA IN = DATA OUT
4311 024070          ERRHRD ERRNO,TSBAM2,EXPREC ;WRITTEN DATA NOT = TO READ
                                TRAP    CSERHRD
                                .WORD   203
                                .WORD   TSBAM2
                                .WORD   EXPREC
                                024070 104456
                                024072 000313
                                024074 024524
                                024076 016164
4312 024100          45$:  CKLOOP          ;SCOPE LOOP
                                TRAP    CSCLP1
                                024100 104406
4313 024102 005304          DEC    R4                ;DROP DATA COUNTER (PATTERN)
4314 024104 022704 000002          CMP    #2,R4            ;AT LOC TWO YET
4315 024110 001360          BNE    40$              ;BR, IF NOT AT TWO YET
4316
4317 024112          ENDSUB          ;////////// END SUBTEST ////////////
                                L10042:
                                024112 104403          TRAP    CSESUB
4318

```

CZTUWAO TUBO FRONT END PRT A MACRO M1200 29-MAR-83 13:24 PAGE 71  
TEST 2: RAM TEST

```

4320
4321 024114          BGNSUB          ://////////////// BEGIN SUBTEST //////////////////
      024114          T2.2:          TRAP      CSBSUB
      024114 104402
4322      :          TEST 2, SUBTEST 2
4323      :
4324      :
4325      :          THIS SUBTEST WRITES RAM WITH ALL ZEROS
4326      :          THEN WALKS AN ALL ONES WORD DOWN THROUGH MEMORY
4327      :
4328 024116 004737 016464 JSR      PC,SOFINIT          ;DO INITIALIZE ON CONTROLLER
4329 024122 103405 BCS      20$                ;BR IF INIT WAS OK
4333 024124 010001 MOV      R0,R1              ;CONTENTS OF TSSR REGISTER
4334 024126          ERRDF   ERRNO,SFIERR,SFIMSG ;FATAL ERROR TSSR WAS NOT OK
      024126 104455          TRAP      CSERDF
      024130 000314          .WORD    20$
      024132 003550          .WORD    SFIERR
      024134 011506          .WORD    SFIMSG
4335 024136 005002 20$: CLR      R2          ;TEST DATA = 0
4336 024140 012704 000002 MOV      #2,R4          ;STARTING RAM ADDRESS = 2
4337 024144          25$:
4338
4339 024144 110465 177777 MOVB     R4,TSDBH(R5)      ;LOAD ADDRESS INTO TSDB
4340 024150 110265 177776 MOVB     R2,TSDBL(R5)      ;LOADS DATA INTO RAM LOCATION
4341 024154 116501 177776 MOVB     TSBAL(R5),R1     ;READS WRAP DATA
4342 024160 120102 CMPB     R1,R2            ;DOES WRITTEN(WRAP) = READ ?
4343 024162 001404 BEQ      30$                ;BR IF OK, THEY ARE EQUAL
4347 024164          ERRHRD  ERRNO,TSBAM2,EXPREC ;DATA NOT WRAPPED CORRECTLY
      024164 104456          TRAP      CSERHRD
      024166 000315          .WORD    20$
      024170 024524          .WORD    TSBAM2
      024172 016164          .WORD    EXPREC
4348 024174          30$:
4349
4350 024174 005204          INC      R4          ;NEXT ADDRESS
4351 024176 020427 000400 CMP      R4,#400          ;END OF RAM MEMORY CHECK
4352 024202 001360 BNE      25$                ;BR, MORE RAM TO GO
4353
4354 024204 005304 35$: JFC      R4          ;SET BACK TO 377
4355 024206 005002 CLR      R2          ;SET TO ALL ZEROS
4356 024210          40$:
4357 024210 110465 177777 MOVB     R4,TSDBH(R5)      ;LOAD UP THE ADDRESS FOR RAM
4358 024214 116501 177776 MOVB     TSBAL(R5),R1     ;READ THE RAM CONTENTS BACK
4359 024220 005002 CLR      R2          ;LOOKING FOR 000000 (EXPECTED)
4360 024222 120102 CMPB     R1,R2            ;BOTH SHOULD BE 00000000 BINARY
4361 024224 001404 BEQ      43$                ;BR, IF DATA IS GOOD
4365 024226          ERRHRD  ERRNO,TSBAM3,EXPREC ;CHARACTERISTICS DATA NOT CORRECT
      024226 104456          TRAP      CSERHRD
      024230 000316          .WORD    20$
      024232 024606          .WORD    TSBAM3
      024234 016164          .WORD    EXPREC
4366 024236 012702 000377 43$: MOV      #000377,R2          ;SET ALL ONES WORD
4367 024242 110465 177777 MOVB     R4,TSDBH(R5)      ;LOAD UP RAM ADDRESS POINTER
4368 024246 110265 177776 MOVB     R2,TSDBL(R5)      ;WRITE DATA INTO RAM
4369 024252 116501 177776 MOVB     TSBAL(R5),R1     ;READ RAM CONTENTS BACK
4370 024256 120102 CMPB     R1,R2            ;CHECK WITH DATA WRITTEN
4371 024260 001404 BEQ      45$                ;BR IF OK, DATA IN = DATA OUT

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CZTUWAO TUBO FRONT END PRT A MACRO M1200 29-MAR-83 13:24 PAGE 72  
TEST 2: RAM TEST

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4384
4385 024306          BGNSUB          ;;;;;;;;;;;;; BEGIN SUBTEST ;;;;;;;;;;;;;
         024306          ;                T2.3:
         024306 104402          ;                TRAP      CSBSUB
4386
4387      :          TEST 2, SUBTEST 3
4388      :
4389      :          THIS SUBTEST WRITES RAM WITH ALL ONES
4390      :          THEN WALKS A ZERO WORD DOWN THROUGH MEMORY
4391      :
4392 024310 004737 016464      JSR      PC,SOFINIT      ;DO INITIALIZE ON CONTROLLER
4393 024314 103405          BCS      20$          ;BR IF INIT WAS OK
4397 024316 010001          MOV      R0,R1          ;CONTENTS OF TSSR REGISTER
4398 024320          ERRDF   ERRNO,SFIEPR,SFIMSG ;FATAL ERROR TSSR WAS NOT OK
         024320 104455          ;                TRAP      CSERDF
         024322 000320          ;                .WORD    20$
         024324 003550          ;                .WORD    SFIERR
         024326 011506          ;                .WORD    SFIMSG
4399 024330 012702 177777      20$:    MOV      #177777,R2      ;SET DATA AT ALL ONES
4400 024334 012704 000002          MOV      #2,R4          ;SET RAM ADDRESS AT TWO
4401 024340          25$:
4402 024340          BGNSEG          ;>>>>>>>>>>>>>>> BEGIN SEGMENT >>>>>>>>>>>>>>>
         024340 104404          ;                TRAP      CSBSEG
4403 024342 110465 177777      MOVB   R4,TSDBH(R5)      ;LOAD ADDRESS INTO TSDB
4404 024346 110265 177776      MOVB   R2,TSDBL(R5)      ;LOADS DATA INTO RAM LOCATION
4405 024352 116501 177776      MOVB   TSBAL(R5),R1     ;READS WRAP DATA
4406 024356 120102          CMPB   R1,R2          ;DUES WRITTEN(WRAP) = READ ?
4407 024360 001404          BEQ    30$          ;BR IF OK, THEY ARE EQUAL
4411 024362          ERRHRD  ERRNO,TSBAM2,EXPREC ;DATA NOT WRAPPED CORRECTLY
         024362 104456          ;                TRAP      CSERHRD
         024364 000321          ;                .WORD    209
         024366 024524          ;                .WORD    TSBAM2
         024370 016164          ;                .WORD    EXPREC
4412 024372          30$:
4413 024372          ENDSEG        ;<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<< END SEGMENT <<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<
         024372          ;                10000$:
         024372 104405          ;                TRAP      CSESEG
4414
4415 024374 005204          INC     R4            ;NEXT ADDRESS
4416 024376 020427 000400      CMP    R4,#400        ;END OF RAM MEMORY CHECK
4417 024402 001356          BNE    25$          ;BR, MORE RAM TO GO
4418
4419 024404 005304          35$:    DEC     R4            ;SET BACK TO 377
4420 024406          40$:
4421 024406 012702 000377      MOV    #000377,R2     ;SET UP EXPECTED DATA REGISTER
4422 024412 005001          CLR    R1            ;CLEAN OUT REGISTER
4423 024414 110465 177777      MOVB   R4,TSDBH(R5)   ;SELECT ADDRESS IN RAM
4424 024420 116501 177776      MOVB   TSBAL(R5),R1   ;PICK UP RAM CONTENTS
4425 024424 120102          CMPB   R1,R2          ;IS MEMORY STILL ALL ONES
4426 024426 001404          BEQ    43$          ;BR, IF OK (ALL ONES)
4430 024430          ERRHRD  ERRNO,TSBAM3,EXPREC ;MEMORY CHANGED AFTER ALL ONES WRITE
         024430 104456          ;                TRAP      CSERHRD
         024432 000322          ;                .WORD    210
         024434 024606          ;                .WORD    TSBAM3
         024436 016164          ;                .WORD    EXPREC
4431 024440 005002          43$:    CLR    R2            ;SET UP NEW EXPECTED
4432 024442 110465 177777      MOVB   R4,TSDBH(R5)   ;LOAD UP RAM ADDRESS POINTER

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TEST 2: RAM TEST

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4433 024446 110265 177776      MOVB  R2,TSDBL(R5)      ;WRITE DATA INTO RAM
4434 024452 116501 177776      MOVB  TSBAL(R5),R1     ;READ RAM CONTENTS BACK
4435 024456 120102             CMPB  R1,R2            ;CHECK WITH DATA WRITTEN
4436 024460 001404             BEQ   458              ;BR IF OK, DATA IN = DATA OUT
4440 024462             ERRHRD ERRNO,TSBAM2,EXPREC ;WRITTEN DATA NOT = TO READ
      024462 104456             TRAP  CSEHRD
      024464 000323             .WORD 211
      024466 024524             .WORD TSBAM2
      024470 016164             .WORD EXPREC
4441 024472             458:  CKLOOP          ;SCOPE LOOP
      024472 104406             TRAP  CSCLP1
4442 024474 005304             DEC   R4               ;DROP RAM ADDRESS POINTER
4443 024476 022704 000002      CMP   #2,R4           ;CHECK LOC TWO
4444 024502 001341             BNE  408              ;BR, IF NOT AT LOC 2 YET
4445
4446 024504             ENDSUB              ;////////////////// END SUBTEST ////////////////////
      024504             L10044:
      024504 104403             TRAP  CSESUB
4447
4448 024506 004737 017174      JSR   PC,TSTLOOP      ;DO WE NEED TO ITERATE TEST ?
4449 024512 103002             BCC  638              ;BRANCH IF NOT
4450 024514 000137 023760      JMP   T2LOOP          ;EXECUTE AGAIN
4451 024520             638:  EXIT  TST        ;ALL DONE THIS TEST
      024520 104432             TRAP  CSEXIT
      024522 000150             .WORD L10041-.
4452
4453             ;+
4454             ;LOCAL TEXT MESSAGFS FOR TEST
4455             ;-
4456 024524 040 127 162 TSBAM2: .ASCIZ ' Write to TSDB Not Equal to Read of TSBA Low Byte'
4457 024606 127 162 151 TSBAM3: .ASCIZ 'Write To RAM Location Modified Another Location'
4458 024666 122 141 155 TST2ID: .ASCIZ 'Ram'
4459             .EVEN
4460 024672             ENDTST
      024672             L10041:
      024672 104401             TRAP  CSETST

```

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 TEST 3: COMMAND REJECT

.SBTTL TEST 3: COMMAND REJECT

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THIS TEST VERIFIES THAT ALL COMMANDS OTHER THAN WRITE CHARACTERISTICS ARE REJECTED DUE TO THE NEED BUFFER ADDRESS (NBA) BIT BEING SET IN TSSR, AND THAT THE TSBA AND TSSR REGISTERS ARE LEFT IN THE PROPER STATE AFTER EACH COMMAND IS REJECTED. THIS TEST CHECKS MICROPROCESSOR SEQUENCING, BASIC COMMAND DECODING AND DATA DMA HANDLING. THIS TEST CONTAINS TWO SUBTESTS: SUBTEST 1 SEQUENCES THROUGH ALL COMMAND WORDS (OTHER THAN WRITE CHARACTERISTICS) WITH THE INTERRUPT ENABLE (IE) BIT CLEAR AND VERIFIES THAT AN INTERRUPT IS NOT GENERATED BY THE REJECTED COMMAND; SUBTEST 2 PERFORMS SIMILARLY TO SUBTEST 1 BUT SETS THE IE BIT IN EACH COMMAND WORD AND VERIFIES THAT AN INTERRUPT IS GENERATED WHEN THE COMMAND IS REJECTED. SUBTEST 1 SETS UP THE INTERRUPT SERVICE ROUTINE TO FLAG UNEXPECTED INTERRUPTS. THE COMMAND WORD IN THE COMMAND BUFFER IS INITIALIZED TO 10000 (OCTAL) AND THE REMAINING THREE WORDS IN THE COMMAND BUFFER ARE SET TO KNOWN UNIQUE PATTERNS. THEN THE FOLLOWING SEQUENCE IS PERFORMED:

1. INITIALIZE THE CONTROLLER BY WRITING INTO THE TSSR; PROPER INITIAL CONDITIONS ARE VERIFIED.
2. TSDB IS WRITTEN WITH ADDRESS OF THE COMMAND BUFFER TO START PROCESSING.
3. THE PROGRAM WAITS FOR SSR TO SET; IF SSR DOES NOT SET, AN ERROR REPORT IS ISSUED AND THE TEST IS ABORTED.
4. THE CONTENTS OF TSSR ARE CHECKED. TSSR IS CORRECT IF IT CONTAINS EITHER OCTAL 102206 OR 102306 (BIT 6 DEPENDS UPON THE STATE OF THE TAPE TRANSPORT).
5. THE CONTENTS OF TSBA ARE CHECKED. TSBA SHOULD CONTAIN THE INITIAL COMMAND BUFFER ADDRESS (LOADED IN STEP 2) PLUS 10 (OCTAL); I.E., TSBA SHOULD POINT TO THE WORD JUST AFTER THE COMMAND PACKET (NOTE THAT 4 COMMAND PACKET WORDS ARE ALWAYS FETCHED).
6. USING THE MAINTENANCE MODE WRAPAROUND FUNCTIONS, THE COMMAND IMAGE BLOCK IN THE DV132'S RAM (LOCATIONS 201-210 (OCTAL)) ARE CHECKED; THE IMAGE SHOULD CONTAIN A COPY OF THE FOUR COMMAND PACKET WORDS AS SET UP IN CPU MEMORY.
7. THE COMMAND WORD IN THE COMMAND BUFFER IS INCREMENTED TO THE NEXT PATTERN NOT CONTAINING WRITE CHARACTERISTICS OR IE. THE REMAINING THREE WORD OF THE COMMAND BUFFER ARE SEQUENCED WITH PSEUDO-RANDOM DATA. IF THE COMMAND WORD HAS NOT REACHED ITS MAXIMUM VALUE (177777+1), THE TEST SEQUENCE IS REPEATED.

SUBTEST 2 IS IDENTICAL TO SUBTEST 1, EXCEPT THAT THE PROGRAM









4710 025616 052525

.WORD 052525

4711

4712

4713

4714

4715

4716

:+  
:LOCAL TEXT MESSAGES FOR TEST  
:-

4717 025620

103

157

155

T3NBA: .ASCIZ

'Command Not Rejected'

4718 025645

103

157

156

T3SSR: .ASCIZ

'Contents of TSSR Incorrect After Write Packet'

4719 025723

125

156

145

T3INT: .ASCIZ

'Unexpected Interrupt Received On Write Packet'

4720 026001

105

170

160

T3NINT: .ASCIZ

'Expected Interrupt Not Received On Write Packet'

4721 026061

111

156

143

T3TSBA: .ASCIZ

'Incorrect TSBA Address After Packet Write'

4722 026133

103

157

155

TST3ID: .ASCIZ

'Command Reject'

4723

.EVEN

4724 026152

ENDTST

026152

L10045:

TRAP

CSETST

026152 104401

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.SBTTL TEST 4: WRITE CHARACTERISTICS

THIS TEST VERIFIES BASIC OPERATION OF THE WRITE CHARACTERISTICS COMMAND. IT VERIFIES THAT THE COMMAND BLOCK AND CHARACTERISTICS DATA BLOCK ARE FETCHED PROPERLY FROM CPU MEMORY, THE NEED BUFFER ADDRESS (NBA) BIT IN TSSR IS HANDLED PROPERLY, AND THAT A PROPER MESSAGE PACKET IS STORED, WHERE APPROPRIATE. THIS TEST DOES NOT CHECK THAT THE VARIOUS FUNCTIONS ENABLED BY CHARACTERISTIC MODE DATA BITS OPERATE PROPERLY; THE FUNCTIONING OF THESE BITS IS VERIFIED IN SUBSEQUENT TESTS. ALL COMMANDS EXECUTED IN THIS TEST HAVE THE INTERRUPT ENABLE (IE) BIT CLEARED TO ZERO, SO NO INTERRUPTS SHOULD BE GENERATED. HOWEVER, THE PROGRAM RUNS AT PROCESSOR PRIORITY 0, WITH THE INTERRUPT SERVICE ROUTINE SET UP TO FLAG UNEXPECTED INTERRUPTS. IF AN INTERRUPT OCCURS, A PROBLEM EXISTS IN EITHER THE LSI-11 BUS INTERFACE SECTION OR IN THE ROM OR PIPELINE.

THIS TEST CHECKS VARIOUS MICROPROGRAM SEQUENCES, COMMAND DECODING, DMA LOGIC, AND BASIC PACKET PROTOCOL HANDLING. THIS IS THE FIRST TEST IN WHICH DATA DMA CYCLES (FOR STORING THE MESSAGE PACKET) ARE PERFORMED. ANY ERRORS IN THE BODY OF THE TEST (I.E. ERRORS OTHER THAN INITIALIZATION ERRORS RELATED TO THE TRANSPORT BUS) DEFINITELY INDICATE A BAD M7454 MODULE.

4754 026154  
026154  
4755 026154 005037 002170  
4756 026160 012737 005672 002146  
4757 026166 005037 003100  
4762 026172 012700 030107  
4763 026176 004737 017226  
4764 026202 012737 000002 002164  
4765 026210  
4766 026210  
026210  
026210 104402  
4767 026212 004737 030136  
4768  
4769 026216  
026216 012700 000000  
026222 104441  
4770 026224 012703 002732  
4771 026230 012704 027240  
4772 026234 012764 000010 000006  
4773 026242  
4774 026242  
026242 104404  
4775  
4776 026244 004737 016464  
4777 026250 103405  
4781 026252 010001  
4782 026254  
026254 104455

.....  
T4LOOP:

BGNSTST  
BGNSUB  
SS:

```

BGNSTST
                                T4::
CLR     FATFLG                ;CLEAR FATAL ERROR FLAG
MOV     #EPR1,EPR1SW          ;SET UP ERROR MESSAGE SWITCH
CLF     KTFLG                  ;HOLD OFF KT11
MOV     #TST4ID,R0            ;ASCII MESSAGE TO IDENTIFY TEST
JSR     PC,TSTSETUP           ;DO INITIAL TEST SETUP
MOV     #2.,LOOPCNT           ;PERFORM 2 ITERATIONS

                                //////////// BEGIN SUBTEST ////////////
                                T4.1:
JSR     PC,T4REST             ;SET PACKET TO START-UP VALUES
SETPRI  #PRI00                ;LOWER PRIORITY TO ALLOW INTERRUPTS
MOV     #PRI00,R0             MOV     #PRI00,R0
TRAP    C$SPRI                TRAP    C$SPRI

MOV     #TSTBLK+10.,R3        ;START OF TEST DATA
MOV     #T4PACKET,R4          ;GET THE ADDRESS OF COMMAND PACKET
MOV     #8.,PKBCNT(R4)        ;START WITH MINIMUM ALLOWABLE VALUE

                                >>>>>>>>>>>> BEGIN SEGMENT >>>>>>>>>>>>
TRAP    C$BSEG                TRAP    C$BSEG

JSR     PC,SOFINIT            ;DO SOFT INIT OF CONTROLLER
BCS     10$                   ;BR IF SOFT INIT = OK
MOV     R0,R1                  ;SAVE CONTENTS OF TSSR
ERRDF   ERRNO,SFIERR,SFIMSG   ;DEVICE FATAL ERROR DURING INIT
TRAP    C$SERDF                TRAP    C$SERDF
    
```



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TEST 4: WRITE CHARACTERISTICS

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	026256	000621						.WORD	401
	026260	003550						.WORD	SFIERR
	026262	011506						.WORD	SFIMSG
4783	026264	005037	002170	10\$:	CLR	FATFLG			:CLEAR FATAL ERROR FLAG
4784	026270	005037	002172		CLR	INTNECV			:CLEAR INTERRUPT RECEIVED FLAG
4785	026274	010465	177776		MOV	R4,TSDB(R5)			:SET THE PACKET ADDRESS
4786	026300	004737	017054		JSR	PC,CHKTSSR			:WAIT FOR SSR TO SET
4787	026304	103407			BCS	15\$			:BR IF CARRY SET (GOOD RETURN)
4788	026306	010001			MOV	R0,R1			:SAVE CONTENTS OF TSSR
4792	026310				ERRDF	ERRNO,T4SSR,PKTSSR			:DEVICE FATAL SSR FAILED TO SET
	026310	104455						TRAP	C\$ERDF
	026312	000622						.WORD	402
	026314	027646						.WORD	T4SSR
	026316	011520						.WORD	PKTSSR
4793	026320	004737	017720		JSR	PC,FATCHK			:INC AND CHECK FOR MORE THAN 25 ERRORS
4794	026324			15\$:	CKLOOP				:LOOP ON ERROR, IF FLAG SET
	026324	104406						TRAP	C\$CLP1
4795	026326				ESCAPE	SEG			:BY-PASS SUBTEST IF FATAL ERROR
	026326	104410						TRAP	C\$ESCAPE
	026330	000126						.WORD	10000\$-
4796	026332	005737	002172		TST	INTRECV			:DID AN INTERRUPT OCCUR ?
4797	026336	001404			BEQ	22\$			:BRANCH IF NOT
4801	026340				ERRHRD	ERRNO,T4INT,PKTSSR			
	026340	104456						TRAP	C\$ERHRD
	026342	000623						.WORD	403
	026344	027735						.WORD	T4INT
	026346	011520						.WORD	PKTSSR
4802	026350	016501	000000	22\$:	MOV	TSSR(R5),R1			:GET THE CONTENTS OF TSSR
4803	026354	012702	000200		MOV	#SSR,R2			:EXPECTED CONTENTS OF TSSR
4804	026360	032701	000100		BIT	#OFL,R1			:IS OFF-LINE BIT SET ?
4805	026364	001407			BEQ	25\$			:BRANCH IF NOT OFF-LINE
4806	026366	052702	000100		BIS	#OFL,R2			:SET OFF-LINE IN EXPECTED DATA
4807	026372	020201		25\$:	CMP	R2,R1			:DOES EXPECTED MATCH RECEIVED ?
4808	026374	001404			BEQ	30\$			:OKAY IF MATCH
4812	026376				ERRHRD	ERRNO,T4NBA,PKTSSR			:NBA NOT ZERO
	026376	104456						TRAP	C\$ERHRD
	026400	000624						.WORD	404
	026402	027376						.WORD	T4NBA
	026404	011520						.WORD	PKTSSR
4813	026406			30\$:	CKLOOP				:LOOP ON ERROR ?
	026406	104406						TRAP	C\$CLP1
4814	026410	004737	017054		JSR	PC,CHKTSSR			:WAIT FOR READY, NON-AMBIGUOUS
4815	026414	016501	177776		MOV	TSBA(R5),R1			:GET TSBA REGISTER CONTENTS
4816	026420	012702	027240		MOV	#T4PACKET,R2			:START OF THE BUFFER
4817	026424	020102			CMP	R1,R2			:COMPARE EXPECTED TO RECEIVED
4818	026426	001404			BEQ	35\$			:ERROR IF NOT EQUAL
4822	026430				ERRHRD	ERRNO,T4TSBA,EXPREC			:PRINT THE ERROR & EXPD/RCV
	026430	104456						TRAP	C\$ERHRD
	026432	000625						.WORD	405
	026434	030024						.WORD	T4TSBA
	026436	016164						.WORD	EXPREC
4823									
4824									
4825	026440	004737	010354	35\$:	JSR	PC,CKRAM			:SEE IF DATA IN RAM IS CORRECT
4826	026444	103404			BCS	40\$			:BRANCH IF PACKET IN RAM IS CORRECT
4830	026446				ERRHRD	ERRNO,PKTRAM,RAMERR			:REPORT THE RAM ERROR(S)
	026446	104456						TRAP	C\$ERHRD







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 TEST 4: WRITE CHARACTERISTICS

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4931
4932
4933 026764          BGNSUB          ;//////////////// BEGIN SUBTEST //////////////////
                                026764          T4.3:
                                026764 104402          TRAP      CSBSUB
4934
4935 026766          SETPRI   #PRI00          ;LOWER PRIORITY TO ALLOW INTERRUPTS
                                026766 012700 000000          MOV        #PRI00,R0
                                026772 104441          TRAP      CSSPRI
4936 026774          012703 027304          ;START OF TEST DATA FOR SUBTEST
4937 027000          012704 027240          ;GET THE ADDRESS OF COMMAND PACKET
4938 027004          004737 030136          ;RESTORE PACKET TO STARTING VALUES
4939
4940
4941 027010          004737 016464          JSR        PC,SOFINIT          ;DO SOFT INIT OF CONTROLLER
4942 027014          103405          BCS        10$                ;BR IF SOFT INIT = OK
4946 027016          010001          MOV        R0,R1              ;SAVE CONTENTS OF TSSR
4947 027020          027020 104455          ERRDF     ERRNO,SFIERR,SFIMSG ;DEVICE FATAL ERROR DURING INIT
                                027022 000634          TRAP      CSERDF
                                027024 003550          .WORD     412
                                027026 011506          .WORD     SFIERR
                                027030 005037 002172          .WORD     SFIMSG
4948 027030          052737 000001 027250 10$: CLR        INTRECV          ;CLEAR INTERRUPT RECEIVED FLAG
4949 027034          010465 177776          BIS        #1,T4DATA          ;MAKE ADDRESS ODD
4950 027042          004737 016740          MOV        R4,TSDB(R5)        ;SET THE PACKET ADDRESS
4951 027046          103405          JSR        PC,WAITF           ;WAIT FOR SSR TO SET
4952 027052          010001          BCS        15$                ;BR IF CARRY SET (GOOD RETURN)
4953 027054          027056 104455          MOV        R0,R1              ;SAVE CONTENTS OF TSSR
4957 027056          000635          ERRDF     ERRNO,T4SSR,PKTSSR ;DEVICE FATAL SSR FAILED TO SET
                                027060 027646          TRAP      CSERDF
                                027062 011520          .WORD     413
                                027064 011520          .WORD     T4SSR
                                027066 104406          .WORD     PKTSSR
4958 027066          027066 104406          15$: CKLOOP          ;LOOP ON ERROR, IF FLAG SET
                                027070          ESCAPE  SUB          TRAP      CSCLP1
                                027072 000116          ;BY-PASS SUBTEST IF FATAL ERROR
                                027074 005737 002172          TRAP      C$ESCAPE
                                027100 001404          .WORD     L10053-
4960 027074          005737 002172          TST        INTRECV          ;DID AN INTERRUPT OCCUR ?
4961 027100          001404          BEQ        22$                ;BRANCH IF NOT
4965 027102          027102 104456          ERRHRD    ERRNO,T4INT,PKTSSR
                                027104 000636          TRAP      CSERHRD
                                027106 027735          .WORD     414
                                027110 011520          .WORD     T4INT
                                027112 016501 000000          .WORD     PKTSSR
4966 027112          012702 102206          22$: MOV        TSSR(R5),R1        ;GET THE CONTENTS OF TSSR
4967 027116          032701 000100          MOV        #SC!SSR!TSREJ!NBA,R2 ;EXPECTED CONTENTS OF TSSR
4968 027122          001402          BIT        #OFL,R1           ;IS OFF-LINE BIT SET ?
4969 027126          001402          BEQ        25$                ;BRANCH IF NOT OFF-LINE
    
```

CZYUWAO TUBO FRONT END PRT A  
TEST 4: WRITE CHARACTERISTICS

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```

4970 027130 052702 000100
4971 027134 020201
4972 027136 001414
4973 027140 010100
4974 027142
4975 027152 020027 002000
4976 027156 001404
4980 027160
    027160 104456
    027162 000637
    027164 027550
    027166 011520
4981 027170
    027170 104406
4982 027172 032701 002000
4983 027176 001004
4987 027200
    027200 104456
    027202 000640
    027204 027320
    027206 011520
4988
4989 027210
    027210
    027210 104403
4990
4991 027212 005737 002170
4992 027216 001402
4993 027220 004737 017772
4994 027224
4995 027224
    027224 104432
    027226 000756
4996
4997
4998
4999
5000
5002 027230
5004 027240
5005 027240 100004
5006 027242 027250
5007 027244 000000
5008 027246 000010
5009
5010 027250
5011 027250 027264
5012 027252 000000
5013 027254 000016
5014 027256 000000
5015 027260
5016
5017 027260 000000 000000
5018 027264
5019
5020
5021

```

```

25$:  BIS    #OFL,R2
      CMP    R2,R1
      BEQ    30$
      MOV    R1,R0
      XOR    R2,R0
      CMP    R0,#NBA
      BEQ    30$
      ERRHRD ERRNO,T44REJ,PKTSSR
      :SET OFF-LINE IN EXPECTED DATA
      :DOES EXPECTED MATCH RECEIVED ?
      :OKAY IF MATCH
      :DATA FROM TSSR
      :FIND BITS IN ERROR
      :IS NBA ONLY BIT IN ERRGR ?
      :DON'T PRINT ERROR IF NBA ONLY BAD BIT
      :COMMAND NOT REJECTED
      TRAP   C$ERHRD
      .WORD  415
      .WORD  T44REJ
      .WORD  PKTSSR

30$:  CKLOOP
      :LOOP ON ERROR ?
      TRAP   C$CLP1

      BIT    #NBA,R1
      BNE    35$
      ERRHRD ERRNO,T42NBA,PKTSSR
      :IS NBA BIT SET ?
      :OKAY IF NBA SET
      :NBA NOT SET
      TRAP   C$ERHRD
      .WORD  416
      .WORD  T42NBA
      .WORD  PKTSSR

35$:  ENDSUB
      :////////////////// END SUBTEST ////////////////////
      L10053:
      TRAP   C$ESUB

60$:  TST    FATFLG
      BEQ    60$
      JSR    PC,CKDROP
      :ANY FATAL ERRORS ?
      :BRANCH IF NOT
      :TRY TO DROP THE UNIT

      EXIT   TST
      :ALL DONE THIS TEST
      TRAP   C$EXIT
      .WORD  L10050-.

: +
: LOCAL STORAGE FOR THIS TEST
: -

T4PACKET: .BLKB 10-<.-TUV2AB7>
          .WORD 100004
          .WORD T4DATA
          .WORD 0
          .WORD 8.
          :COMMAND PACKET FOR TEST
          :WRITE CHARACTERISTICS COMMAND, WITH ACK
          :ADDRESS OF CHARACTERISTICS BLOCK

T4DATA:  .WORD T4BFR
          .WORD 0
          .WORD 14.
          .WORD 0
          :CHARACTERISTICS DATA BLOCK
          :ADDRESS OF MESSAGE BUFFER

T4SP:
          :LENGTH OF MESSAGE BUFFER

T4BFR:   .WORD 0,0
          .BLKB 8.
          :SPACE
          :MESSAGE BUFFER

: +
:

```

CZTUWAO TUBO FRONT END PRT A  
TEST 4: WRITE CHARACTERISTICS

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```

5022
5023
5024
5025
5026
5027
5028
5029
5030
5031 027304
5032 027304 000000 037140
5033 027310 000002 000001
5034 027314 000004 100100
5035      027320
5036
5037
5038
5039
5040
5041
5042 027320 116 102 101
5043 027376 127 122 111
5044 027451 127 122 111
5045 027550 127 122 111
5046 027646 103 157 156
5047 027735 125 156 145
5048 030024 111 156 143
5049 030107 127 162 151
5050
5051

```

:TEST DATA FOR SUBTEST TWO

:DATA HAS FORMAT:

```

:      1ST WORD      OFFSET TO TEST WORD IN PACKET
:      2ND WORD      BITS TO SET FOR TEST
:--

```

T42DATA:

```

.WORD 0,BIT5!BIT6!BIT9!BIT10!BIT11!BIT12!BIT13
.WORD 2,BIT0
.WORD 4,BIT6!BIT15

```

T42DONE=.

:+  
:LOCAL TEXT MESSAGES FOR TEST

:--

```

101 T42NBA: .ASCIZ 'NBA Not Set On Rejected WRITE CHARACTERISTICS'
111 T4NBA: .ASCIZ 'WRITE CHARACTERISTICS Command Not Accepted'
111 T42REJ: .ASCIZ 'WRITE CHARACTERISTICS Not Rejected With Non-Zero Unused Fields'
111 T44REJ: .ASCIZ 'WRITE CHARACTERISTICS Not Rejected With Invalid Block Address'
156 T4SSR: .ASCIZ 'Contents of TSSR Incorrect After WRITE CHARACTERISTICS'
145 T4INT: .ASCIZ 'Unexpected Interrupt Received On WRITE CHARACTERISTICS'
143 T4TSBA: .ASCIZ 'Incorrect TSBA Address After WRITE CHARACTERISTICS'
151 TST4ID: .ASCIZ 'Write Characteristics'
.EVEN

```

CZTUWA0 TUBO FRONT END PRT A  
 TEST 4: WRITE CHARACTERISTICS

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5053  
 5054  
 5055  
 5056  
 5057  
 5058  
 5059  
 5060 030136  
 5061 030136  
 5062 030142 012701 027240  
 5063 030146 012721 100004  
 5064 030152 012721 027250  
 5065 030156 005021  
 5066 030160 012721 000010  
 5067 030164 012721 027264  
 5068 030170 005021  
 5069 030172 012721 000020  
 5070 030176 005021  
 5071 030200 005011  
 5072 030202 000207  
 5073 030204  
 030204  
 030204 104401  
 5074

:+  
 :  
 :  
 :  
 :-

:ROUTINE TO RESTORE COMMAND PACKET TO START-UP (DEFAULT) VALUES

T4REST:

```

    SAVREG          ;SAVE THE REGISTERS
    MOV #T4PACKET,R1 ;START OF THE PACKET
    MOV #1C0004,(R1)+ ;WRITE CHARACTERISTICS WITH ACK
    MOV #T4DATA,(R1)+ ;ADDRESS OF CHAR DATA BLOCK
    CLR (R1)+        ;EXTENDED ADDRESS
    MOV #8,(R1)+     ;SIZE OF DATA BLOCK IN BYTES
    MOV #T4BFR,(R1)+ ;ADDRESS OF MESSAGE BUFFER
    CLR (R1)+
    MOV #16,(R1)+   ;LENGTH OF MESSAGE BUFFER
    CLR (R1)+
    CLR (R1)
    RTS PC          ;RETURN
    ENDTST
    
```

L10050: TRAP CSETST



.SBTTL TEST 5: VOLUME CHECK

5076  
5077  
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5081  
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5090  
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5094  
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5100  
5101  
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5106  
5107  
5108  
5109

THIS TEST VERIFIES THAT THE VOLUME CHECK (VCK) BIT, A FLAG HELD WITHIN THE M7454 AND APPEARING IN XSTO, IS SET BY INITIALIZE AND CLEARED BY EXECUTING A WRITE CHARACTERISTICS COMMAND WITH THE CVC BIT SET. IT IS ALSO VERIFIED THAT A WRITE CHARACTERISTICS COMMAND WITH THE CVC BIT CLEAR DOES NOT AFFECT THE STATE OF THE VOLUME CHECK BIT. THE ACTUAL FUNCTION OF VOLUME CHECK, THAT OF PREVENTING OR ALLOWING A TAPE MOTION COMMAND DEPENDING UPON WHETHER VOLUME CHECK IS SET OR CLEAR, IS NOT CHECKED BY THIS TEST; THIS FUNCTIONALITY IS CHECKED IN THE INDIVIDUAL TESTS OF TAPE MOTION COMMANDS.

THE TEST PROCEEDS AS FOLLOWS:

1. THE CONTROLLER IS INITIALIZED BY WRITING INTO THE TSSR.
2. A WRITE CHARACTERISTICS COMMAND IS ISSUED (WITH CVC=0) AND XSTO IN THE RETURNED MESSAGE BUFFER IS EXAMINED; THE VCK BIT SHOULD BE CLEAR (0).
3. THE PREVIOUS STEP IS REPEATED TO VERIFY THAT VCK DOES NOT CHANGE (REMAINS AT 0).
4. A WRITE CHARACTERISTICS COMMAND IS ISSUED WITH CVC=1 AND THE VCK BIT IN XSTO IN THE MESSAGE BUFFER IS EXAMINED; THE VCK BIT SHOULD BE CLEAR (0).
5. A WRITE CHARACTERISTICS COMMAND IS ISSUED WITH CVC=0 AND THE VCK BIT IN XSTO IN THE MESSAGE BUFFER IS EXAMINED; THE VCK BIT SHOULD REMAIN CLEAR (0).

5110  
5111  
5112  
5113  
5118  
5119  
5120  
5121  
5122  
5123  
5124  
5125  
5126  
5127  
5131  
5132  
5133  
5134

```

030206
030206 005037 J02170
030212 012737 005672 002146
030220 005037 003100
030224 012700 031377
030230 004737 017226
030234 012737 000002 002164
030242
030242 012704 030710
030246 012702 030732
030252 012762 052525 000006
030260 004737 016464
030264 103405
030266 010001
030270
030270 104455
030272 000765
030274 003550
030276 011506
030300 042714 040000
030304 010465 177776

```

BGNTST

```

CLR FATFLG ;CLEAR FATAL ERROR FLAG
MOV #EPRT1,EPRTSW ;SET UP ERROR MESSAGE SWITCH
CLR KTFLG ;HOLD OFF KT11
MOV #TST5ID,R0 ;ASCII MESSAGE TO IDENTIFY TEST
JSR PC,TSTSETUP ;DO INITIAL TEST SETUP
MOV #2,LOOPCNT ;PERFORM 2 ITERATIONS

T5LOOP:
MOV #T5PACKET,R4 ;PACKET FOR WRITE CHARACTERISTICS
MOV #T5BFR,R2 ;ADDRESS OF THE MESSAGE BUFFER
MOV #052525,XSTO(R2) ;SET XSTATO TO KNOWN VALUE
JSR PC,SOFINIT ;DO SOFT INIT OF CONTROLLER
BCS 10$ ;BR IF SOFT INIT = OK
MOV R0,R1 ;SAVE CONTENTS OF TSSR
ERRDF ERRNO,SFIERR,SFIMSG ;DEVICE FATAL ERROR DURING INIT

TRAP CSERDF
.WORD 501
.WORD SFIERR
.WORD SFIMSG

10$: BIC #BIT14,(R4) ;CLEAR THE CVC BIT
MOV R4,TSDB(R5) ;SET THE PACKET ADDRESS FOR WRITE CHAR

```

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 TEST 5: VOLUME CHECK

5135	030310	004737	017054			JSR	PC,CHKTSSR		:WAIT FOR SSR TO SET		
5136	030314	103405				BCS	15\$		:BR IF CARRY SET (GOOD RETURN)		
5137	030316	010001				MOV	RO,R1		:SAVE CONTENTS OF TSSR		
5141	030320					ERRDF	ERRNO,T5SSR,PKTSSR		:DEVICE FATAL SSR FAILED TO SET		
	030320	104455								TRAP	C\$ERDF
	030322	000766								.WORD	502
	030324	031211								.WORD	T5SSR
	030326	011520								.WORD	PKTSSR
5142	030330			15\$:		CKLOOP			:LOOP ON ERROR, IF FLAG SET		
	030330	104406								TRAP	C\$CLP1
5143	030332					ESCAPE	TST		:EXIT IF FATAL ERROR		
	030332	104410								TRAP	C\$ESCAPE
	030334	001060								.WORD	L10054-
5144	030336	016203	000006			MOV	XSTO(R2),R3		:STORE STATUS FOR A WHILE		
5145	030342	020327	052525			CMP	R3,#052525		:CHECK FOR XSTATO OVER WRITTEN (GOOD!)		
5146	030346	001006				BNE	20\$		:BR, IF XSTATO HAS BEEN UPDATED		
5147	030350	016501	000000			MOV	TSSR(R5),R1		:PICK UP TSSR FOR ERROR PRINTOUT		
5151	030354					ERRHRD	ERRNO,T5NMSG,PKTSSR		: 'NO MESSAGE PACKET RETURNED'		
	030354	104456								TRAP	C\$ERHRD
	030356	000767								.WORD	503
	030360	031300								.WORD	T5NMSG
	030362	011520								.WORD	PKTSSR
5152	030364	032762	000020	000006	20\$:	BIT	#XSOVCK,XSTO(R2)		:IS VOLUME CHECK CLEAR IN XSTO ?		
5153	030372	001006				BNE	23\$		:OKAY IF VOLUME CHECK IS CLEARED		
5157	030374	016501	000000			MOV	TSSR(R5),R1		:CONTENTS OF TSSR FOR ERROR REPORT		
5158	030400					ERRHRD	ERRNO,T5VCK2,PKTMES		:VOLUME CHECK NOT SET		
	030400	104456								TRAP	C\$ERHRD
	030402	000770								.WORD	504
	030404	031045								.WORD	T5VCK2
	030406	011574								.WORD	PKTMES
5159	030410				23\$:	CKLOOP			:LOOP ON ERROR ?		
	030410	104406								TRAP	C\$CLP1
5160	030412	010465	177776			MOV	R4,TSDB(R5)		:SET THE PACKET ADDRESS FOR WRITE CHAR		
5161	030416	004737	017054			JSR	PC,CHKTSSR		:WAIT FOR SSR TO SET		
5162	030422	103405				BCS	25\$		:BR IF CARRY SET (GOOD RETURN)		
5163	030424	010001				MOV	RO,R1		:SAVE CONTENTS OF TSSR		
5167	030426					ERRDF	ERRNO,T5SSR,PKTSSR		:DEVICE FATAL SSR FAILED TO SET		
	030426	104455								TRAP	C\$ERDF
	030430	000771								.WORD	505
	030432	031211								.WORD	T5SSR
	030434	011520								.WORD	PKTSSR
5168	030436				25\$:	CKLOOP			:LOOP ON ERROR, IF FLAG SET		
	030436	104406								TRAP	C\$CLP1
5169	030440					ESCAPE	TST		:EXIT IF FATAL ERROR		
	030440	104410								TRAP	C\$ESCAPE
	030442	000752								.WORD	L10054-
5170	030444	026203	000006			CMP	XSTO(R2),R3		:THE XSTO SHOULD NOT HAVE CHANGED		
5171	030450	001406				BEQ	27\$		:OKAY IF VOLUME CHECK IS SET		
5175	030452	016501	000000			MOV	TSSR(R5),R1		:CONTENTS OF TSSR FOR ERROR REPORT		
5176	030456					ERRHRD	ERRNO,T5NVCK,PKTMES		:VOLUME CHECK NOT SET		
	030456	104456								TRAP	C\$ERHRD
	030460	000772								.WORD	506
	030462	031121								.WORD	T5NVCK
	030464	011574								.WORD	PKTMES
5177	030466				27\$:	CKLOOP			:LOOP ON ERROR ?		
	030466	104406								TRAP	C\$CLP1
5178	030470	032762	000020	000006	30\$:	BIT	#XSOVCK,XSTO(R2)		:IS VOLUME CHECK SET IN XSTO ?		

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 TEST 5: VOLUME CHECK

```

5179 030476 001006          BNE      33$      ;OKAY IF VOLUME CHECK IS SET
5183 030500 016501 000000  MOV      TSSR(R5),R1  ;CONTENTS OF TSSR FOR ERROR REPORT
5184 030504          ERRHRD  ERRNO,T5VCK2,PKTMES ;VOLUME CHECK NOT SET
          030504 104456          TRAP      CSERHRD
          030506 000773          .WORD    507
          030510 031045          .WORD    T5VCK2
          030512 011574          .WORD    PKTMES
5185 030514          33$:   CKLOOP  ;LOOP ON ERROR ?
          030514 104406          TRAP      C$CLP1
5186 030516 052714 040000  BIS      #BIT14,(R4)  ;SET THE CVC BIT
5187 030522 010465 177776  MOV      R4,TSDB(R5)  ;SET THE PACKET ADDRESS FOR WRITE CHAR
5188 030526 004737 017054  JSR      PC,CHKTSSR   ;WAIT FOR SSR TO SET
5189 030532 103405          BCS      35$      ;BR IF CARRY SET (GOOD RETURN)
5190 030534 010001          MOV      R0,R1      ;SAVE CONTENTS OF TSSR
5194 030536          ERRDF  ERRNO,T5SSR,PKTSSR ;DEVICE FATAL SSR FAILED TO SET
          030536 104455          TRAP      CSERDF
          030540 000774          .WORD    508
          030542 031211          .WORD    T5SSR
          030544 011520          .WORD    PKTSSR
5195 030546          35$:   CKLOOP  ;LOOP ON ERROR, IF FLAG SET
          030546 104406          TRAP      C$CLP1
5196 030550          ESCAPE  TST      ;EXIT IF FATAL ERROR
          030550 104410          TRAP      C$ESCAPE
          030552 000642          .WORD    L10054-.
5197 030554 032762 000020 000006  BIT      #XSOVCK,XSTO(R2) ;IS VOLUME CHECK CLEAR IN XSTO ?
5198 030562 001406          BEQ      40$      ;OKAY IF VOLUME CHECK IS CLEARED
5202 030564 016501 000000  MOV      TSSR(R5),R1  ;CONTENTS OF TSSR FOR ERROR REPORT
5203 030570          ERRHRD  ERRNO,T5VCK,PKTMES ;VOLUME CHECK NOT CLEARED
          030570 104456          TRAP      CSERHRD
          030572 000775          .WORD    509
          030574 030752          .WORD    T5VCK
          030576 011574          .WORD    PKTMES
5204 030600          40$:   CKLOOP  ;LOOP ON ERROR ?
          030600 104406          TRAP      C$CLP1
5205 030602 042714 040000  BIC      #BIT14,(R4)  ;CLEAR THE CVC BIT
5206 030606 010465 177776  MOV      R4,TSDB(R5)  ;SET THE PACKET ADDRESS FOR WRITE CHAR
5207 030612 004737 017054  JSR      PC,CHKTSSR   ;WAIT FOR SSR TO SET
5208 030616 103405          BCS      45$      ;BR IF CARRY SET (GOOD RETURN)
5209 030620 010001          MOV      R0,R1      ;SAVE CONTENTS OF TSSR
5213 030622          ERRDF  ERRNO,T5SSR,PKTSSR ;DEVICE FATAL SSR FAILED TO SET
          030622 104455          TRAP      CSERDF
          030624 000776          .WORD    510
          030626 031211          .WORD    T5SSR
          030630 011520          .WORD    PKTSSR
5214 030632          45$:   CKLOOP  ;LOOP ON ERROR, IF FLAG SET
          030632 104406          TRAP      C$CLP1
5215 030634          ESCAPE  TST      ;EXIT IF FATAL ERROR
          030634 104410          TRAP      C$ESCAPE
          030636 000556          .WORD    L10054-.
5216 030640 032762 000020 000006  BIT      #XSOVCK,XSTO(R2) ;IS VOLUME CHECK CLEAR IN XSTO ?
5217 030646 001406          BEQ      50$      ;OKAY IF VOLUME CHECK IS CLEARED
5221 030650 016501 000000  MOV      TSSR(R5),R1  ;CONTENTS OF TSSR FOR ERROR REPORT
5222 030654          ERRHRD  ERRNO,T5NVCK,PKTMES ;VOLUME CHECK NOT CLEARED
          030654 104456          TRAP      CSERHRD
          030656 000777          .WORD    511
          030660 031121          .WORD    T5NVCK
          030662 011574          .WORD    PKTMES
    
```

```

5223 030664          50$:  CKLOOP          ;LOOP ON ERROR ?
      030664 104406          TRAP      C$CLP1
5224 030666 004737 017174 60$:  JSR      PC,TSTLOOP      ;SHOULD WE DO ITERATIONS ?
5225 030672 103002          BCC      6?S          ;BRANCH IF NOT
5226 030674 000137 030242 JMP      T5LUJP        ;LOOP UNTIL COUNT EXPIRED
5227 030700          62$:  EXIT      TST          ;ALL DONE THIS TEST
      030700 104432          TRAP      C$EXIT
      030702 000512          .WORD    L10054-.

5228
5229
5230          ;+
          ;LOCAL STORAGE FOR THIS TEST
5231          ;-
5232
5234 030704          TSPACKET: .BLKB 10-<.-TUV2A&7>
5236 030710          ;COMMAND PACKET FOR TEST
5237 030710 100004          .WORD 100004          ;WRITE CHARACTERISTICS COMMAND
5238 030712 030720          .WORD T5DATA          ;ADDRESS OF CHARACTERISTICS BLOCK
5239 030714 000000          .WORD 0
5240 030716 000010          .WORD 10          ;STARTING VALUE OF COUNTER
5241
5242 030720          T5DATA:          ;CHARACTERISTICS DATA BLOCK
5243 030720 030732          .WORD T5BFR          ;ADDRESS OF MESSAGE BUFFER
5244 030722 000000          .WORD 0
5245 030724 000020          .WORD 16.          ;LENGTH OF MESSAGE BUFFER
5246 030726 000000 000000 .WORD 0.0
5247
5248 030732          T5BFR: .BLKW 8.          ;MESSAGE BUFFER
5249
5250
5251          ;+
          ;LOCAL TEXT MESSAGES FOR TEST
5252          ;-
5253
5254
5255 030752          126 103 113 T5VCK: .ASCIZ 'VCK Bit NOT Cleared After WRITE CHARACTERISTICS With CVC=1'
5256 031045          126 103 113 T5VCK2: .ASCIZ 'VCK Bit NOT Set After INITIALIZE With CVC=0'
5257 031121          126 103 113 T5NVCK: .ASCIZ 'VCK Bit Modified After WRITE CHARACTERISTICS With CVC=0'
5258 031211          103 157 156 T5SSR: .ASCIZ 'Contents of TSSR Incorrect After Write Characteristics'
5259 031300          116 157 040 T5MSG: .ASCIZ 'No Message Packet Returned To Host After WRITE CHARACTERISTICS'
5260 031377          126 157 154 T5T5ID: .ASCIZ 'Volume Check'
5261          .EVEN
5262 031414          ENDTST
      031414
      031414 104401          L10054:          TRAP      C$ETST
    
```



CZTUWAO TUBO FRONT END PRT A  
TEST 6: COMPLETION INTERRUPT

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```

5321 031552               ERRDF   ERRNO,T6SSR,PKTSSR      :DEVICE FATAL SSR FAILED TO SET
      031552 104455                               TRAP    C$ERDF
      031554 001132                               .WORD  602
      031556 033037                               .WORD  T6SSR
      031560 011520                               .WORD  PKTSSR
5322 031562 004737 017720       JSR    PC,FATCHK      :INC AND CHECK FOR MORE THAN 25
5323 031566               15$:   CKLOOP                :LOOP ON ERROR, IF FLAG SET
      031566 104406                               TRAP    C$CLP1
5324 031570               ESCAPE  SEG                  :BY-PASS SUBTEST IF FATAL ERROR
      031570 104410                               TRAP    C$ESCAPE
      031572 000056                               .WORD  10000$.
5325 031574 005737 002172       TST    INTRECV       :DID AN INTERRUPT OCCUR ?
5326 031600 001004       BNE    22$
5330 031602               ERRHRD  ERRNO,T6NINT,PKTSSR
      031602 104456                               TRAP    C$ERHRD
      031604 001133                               .WORD  603
      031606 033126                               .WORD  T6NINT
      031610 011520                               .WORD  PKTSSR
5331 031612 016501 000000       22$:   MOV    TSSR(R5),R1    :GET THE CONTENTS OF TSSR
5332 031616 012702 000200       MOV    #SSR,R2      :EXPECTED CONTENTS OF TSSR
5333 031622 032701 000100       BIT    #OFL,R1      :IS OFF-LINE BIT SET ?
5334 031626 001402               BEQ    25$           :BRANCH IF NOT OFF-LINE
5335 031630 052702 000100       BIS    #OFL,R2      :SET OFF-LINE IN EXPECTED DATA
5336 031634 020201 000100       25$:   CMP    R2,R1      :DOES EXPECTED MATCH RECEIVED ?
5337 031636 001404       BEQ    30$           :OKAY IF MATCH
5341 031640               ERRHRD  ERRNO,T6NBA,PKTSSR    :NBA NOT ZERO
      031640 104456                               TRAP    C$ERHRD
      031642 001134                               .WORD  604
      031644 032376                               .WORD  T6NBA
      031646 011520                               .WORD  PKTSSR
5342 031650               30$:   ENDSEG                  :<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<
5343 031650               ENDSEG                  :<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<
      031650 104405                               10000$: TRAP    C$ESEG
5344 031652 012364 000006       MOV    (R3)+,PKBCNT(R4) :SET THE TEST WORD
5345 031656 020327 003030       CMP    R3,#TBLEND    :HAS ALL DATA BEEN TESTED ?
5346 031662 103002               BHIS   55$           :BRANCH IF ALL DATA DONE
5347 031664 000137 031504       JMP    55$           :BRANCH TILL BACK TO ZERO
5349 031670               55$:   ENDSUB                  :////////////////// END SUBTEST ////////////////////
5350 031670               L10056: TRAP    C$ESUB
      031670 104403
5351 031672 005737 002170       TST    FATFLG       :ANY FATAL ERRORS ?
5352 031676 001402       BEQ    70$           :BRANCH IF NOT
5353 031700 004737 017772       JSR    PC,CKDROP    :TRY TO DROP THE UNIT
5354 031704
5355 031704

```







CZTUWAO TUBO FRONT END PRT A  
TEST 6: COMPLETION INTERRUPT

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```

5423          ;+
5424          ;TEST 6, SUBTEST 3
5425          ;
5426          ;SUBTEST TO VERIFY THAT A WRITE CHARACTERISTICS COMMAND IS
5427          ;REJECTED IF AN ILLEGAL DATA BLOCK ADDRESS IS ISSUED.
5428          ;-
5429 032116          BGNSUB          ;////////// BEGIN SUBTEST //////////
          032116          T6.3:
          032116 104402          TRAP      CSBSUB
5430 032120          SETPRI #PRI00          ;LOWER PRIORITY TO ALLOW INTERRUPTS
          032120 012700 000000          MOV      #PRI00,R0
          032124 104441          TRAP      CSSPRI
5431 032126 012703 032362          ;START OF TEST DATA FOR SUBTEST
5432 032132 012704 032320          5S:  MOV      #T6PACKET,R4          ;GET THE ADDRESS OF COMMAND PACKET
5433 032136 004737 033416          JSR      PC,T6REST          ;RESTORE PACKET TO STARTING VALUES
5434 032142 004737 016464          JSR      PC,SOFINIT          ;DO SOFT INIT OF CONTROLLER
5435 032146 103405          BCS      10S          ;BR IF SOFT INIT = OK
5439 032150 010001          MOV      R0,R1          ;SAVE CONTENTS OF TSSR
5440 032152          ERRDF  ERRNO,SFIERR,SFIMSG          ;DEVICE FATAL ERROR DURING INIT
          032152 104455          TRAP      CSERDF
          032154 001141          .WORD   609
          032156 003550          .WORD   SFIERR
          032160 011506          .WORD   SFIMSG
5441 032162 005037 002172          10S: CLR      INTRECV          ;CLEAR INTERRUPT RECEIVED FLAG
5442 032166 052737 000001 032330  BIS      #1,T6DATA          ;MAKE ADDRESS ODD
5443 032174 010465 177776          MOV      R4,TSDB(R5)          ;SET THE PACKET ADDRESS
5444 032200 004737 016740          JSR      PC,WAITF          ;WAIT FOR SSR TO SET
5445 032204 103405          BCS      15S          ;BR IF CARRY SET (GOOD RETURN)
5446 032206 010001          MOV      R0,R1          ;SAVE CONTENTS OF TSSR
5450 032210          ERRDF  ERRNO,T6SSR,PKTSSR          ;DEVICE FATAL SSR FAILED TO SET
          032210 104455          TRAP      CSERDF
          032212 001142          .WORD   610
          032214 033037          .WORD   T6SSR
          032216 011520          .WORD   PKTSSR
5451 032220          15S:  CKLOOP          ;LOOP ON ERROR, IF FLAG SET
          032220 104406          TRAP      CSCLP1
5452 032222          ESCAPE  SUB          ;BY-PASS SUBTEST IF FATAL ERROR
          032222 104410          TRAP      CSESCAPE
          032224 000056          .WORD   L10060-.
5453 032226 005737 002172          TST      INTRECV          ;DID AN INTERRUPT OCCUR ?
5454 032232 001004          BNE      22S          ;BRANCH IF YES
5458 032234          ERRHRD  ERRNO,T6NINT,PKTSSR
          032234 104456          TRAP      CSERHRD
          032236 001143          .WORD   611
          032240 033126          .WORD   T6NINT
          032242 011520          .WORD   PKTSSR
5459 032244 016501 000000          22S:  MOV      TSSR(R5),R1          ;GET THE CONTENTS OF TSSR
5460 032250 012702 102206          MOV      #SC!SSR!TSREJ!NBA,R2          ;EXPECTED CONTENTS OF TSSR
5461 032254 032701 000100          BIT      #OFL,R1          ;IS OFF-LINE BIT SET ?
5462 032260 001402          BEQ      25S          ;BRANCH IF NOT OFF-LINE
5463 032262 052702 000100          BIS      #OFL,R2          ;SET OFF-LINE IN EXPECTED DATA
5464 032266 020201          25S:  CMP      R2,R1          ;DOES EXPECTED MATCH RECEIVED ?
5465 032270 001404          BEQ      30S          ;OKAY IF MATCH
5469 032272          ERRHRD  ERRNO,T64REJ,PKTSSR          ;COMMAND NOT REJECTED
          032272 104456          TRAP      CSERHRD
          032274 001144          .WORD   612
          032276 032643          .WORD   T64REJ

```

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TEST 6: COMPLETION INTERRUPT

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```

5470 032300 011520                                .WORD  PKTSSR
5471 032302 30$:                                ;////////// END SUBTEST ////////////
032302                                           L10060:
5472 032302 104403                                TRAP   C$ESUB
032304                                           ;ALL DONE THIS TEST
032304 104432                                TRAP   C$EXIT
032306 001162                                .WORD  L10055-.

5473                                           ;+
5474                                           ;LOCAL STORAGE FOR THIS TEST
5475                                           ;-
5477 032310                                .BLKB  10-<.-TUV2AB7>
5479 032320                                T6PACKET: ;COMMAND PACKET FOR TEST
5480 032320 100204                                .WORD  100204 ;WRITE CHAR COMMAND, WITH IE, ACK
5481 032322 032330                                .WORD  T6DATA ;ADDRESS OF CHARACTERISTICS BLOCK
5482 032324 000000                                .WORD  0
5483 032326 000010                                .WORD  8. ;STARTING VALUE OF BLOCK SIZE
5484 032330                                T6DATA: ;CHARACTERISTICS DATA BLOCK
5485 032330 032342                                .WORD  T6BFR ;ADDRESS OF MESSAGE BUFFER
5486 032332 000000                                .WORD  0
5487 032334 000016                                .WORD  14. ;LENGTH OF MESSAGE BUFFER
5488 032336 000000 000000                                .WORD  0,0
5489 032342                                T6BFR: .BLKW 8. ;MESSAGE BUFFER

5490                                           ;+
5491                                           ;TEST DATA FOR SUBTEST TWO
5492                                           ;DATA HAS FORMAT:
5493                                           ; 1ST WORD      OFFSET TO TEST WORD IN PACKET
5494                                           ; 2ND WORD      BITS TO SET FOR TEST
5495                                           ;-
5496 032362                                T62DATA:
5497 032362 000000 036140                                .WORD  0,BIT5!BIT6!BIT6!BIT10!BIT11!BIT12!BIT13
5498 032366 000002 000001                                .WORD  2,BIT0
5499 032372 000004 100100                                .WORD  4,BIT6!BIT15
5500                                           T62DONE=.
5501                                           ;+
5502                                           ;LOCAL TEXT MESSAGES FOR TEST
5503                                           ;-
5504 032376 127 122 111 T6NBA: .ASCIZ 'WRITE CHARACTERISTICS Command Not Accepted'
5505 032451 127 122 111 T62REJ: .ASCIZ 'WRITE CHARACTERISTICS Not Rejected With Non-Zero Unused Fields'
5506 032550 127 122 111 T63REJ: .ASCIZ 'WRITE CHARACTERISTICS Not Rejected With Invalid Data Count'
5507 032643 127 122 111 T64REJ: .ASCIZ 'WRITE CHARACTERISTICS Not Rejected With Invalid Block Address'
5508 032741 127 122 111 T65REJ: .ASCIZ 'WRITE CHARACTERISTICS Not Rejected With Invalid Buffer Length'
5509 033037 103 157 156 T6SSR: .ASCIZ 'Contents of TSSR Incorrect After WRITE CHARACTERISTICS'
5510 033126 105 170 160 T6NINT: .ASCIZ 'Expected Interrupt Not Received On WRITE CHARACTERISTICS'
5511 033217 125 156 145 T6INT: .ASCIZ 'Unexpected Interrupt Received On WRITE CHARACTERISTICS'
5512 033306 111 156 143 T6TSBA: .ASCIZ 'Incorrect TSBA Address After WRITE CHARACTERISTICS'
5513 033371 103 157 155 T6TID: .ASCIZ 'Completion Interrupt'
5514                                           .EVEN

```

CZUWAO TUBO FRONT END PRT A  
TEST 6: COMPLETION INTERRUPT

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```

5516
5517
5518
5519
5520
5521
5522 033416
5523 033416
5524 033422 012701 032320
5525 033426 012721 100204
5526 033432 012721 032330
5527 033436 005021
5528 033440 012721 000010
5529 033444 012721 032342
5530 033450 005021
5531 033452 012721 000016
5532 033456 005021
5533 033460 005011
5534 033462 005037 032342
5535 033466 000207
5536 033470
      033470
      033470 104401

```

```

: +
:
:
: -

```

:ROUTINE TO RESTORE COMMAND PACKET TO START-UP (DEFAULT) VALUES

T6REST:

```

SAVREG          ;SAVE THE REGISTERS
MOV #T6PACKET,R1 ;START OF THE PACKET
MOV #100204,(R1)+ ;WRITE CHARACTERISTICS WITH ACK, IE
MOV #T6DATA,(R1)+ ;ADDRESS OF CHAR DATA BLOCK
CLR (R1)+        ;EXTENDED ADDRESS
MOV #8,(R1)+     ;SIZE OF DATA BLOCK IN BYTES
MOV #T6BFR,(R1)+ ;ADDRESS OF MESSAGE BUFFER
CLR (R1)+
MOV #14,(R1)+   ;LENGTH OF MESSAGE BUFFER
CLR (R1)+
CLR (R1)
CLR T6BFR       ;CLEAR 1ST LOC IN MESSAGE BUFFER
RTS PC          ;RETURN
ENDTST

```

```

L10055:
      TRAP      CSETST

```







CZTUWAO TUBO FRONT END PRT A  
TEST 7: BASIC PACKET PROTOCOL

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```

5664
5665
5666
5667
5668
5669
5670
5671
5672
5673 034112      BGNSUB                    ;//////////////// BEGIN SUBTEST //////////////////
      034112                                T7.2:
      034112 104402                                TRAP C$BSUB
5674
5675 034114 004737 036556      JSR    PC,T7RST        ;SET PACKET TO INITIAL VALUES
5676 034120                                SETPRI #PRI00         ;LOWER PRIORITY TO ALLOW INTERRUPTS
      034120 012700 000000                                MOV    #PRI00,R0
      034124 104441                                TRAP  C$SPRI
5677 034126 012704 035630      MOV    #T7PACKET,R4    ;GET THE ADDRESS OF COMMAND PACKET
5678 034132 012764 000010 000006  MOV    #8.,PKBCNT(R4) ;START WITH MINIMUM ALLOWABLE VALUE
5679 034140
5680 034140                                BGNSEG                    ;>>>>>>>>>> BEGIN SEGMENT >>>>>>>>>>
      034140 104404                                TRAP  C$BSEG
5681
5682 034142 004737 016464      JSR    PC,SOFINIT      ;DO SOFT INIT OF CONTROLLER
5683 034146 103405      BCS    10$            ;BR IF SOFT INIT = OK
5687 034150 010001      MOV    R0,R1          ;SAVE CONTENTS OF TSSR
5688 034152      ERRDF  ERRNO,SFIERR,SFIMSG ;DEVICE FATAL ERROR DURING INIT
      034152 104455                                TRAP  C$ERDF
      034154 001305                                .WORD 709
      034156 003550                                .WORD SFIERR
      034160 011506                                .WORD SFIMSG
5689 034162 005037 002170      CLR    FATFLG         ;CLEAR FATAL ERROR FLAG
5690 034166 005037 002172      CLR    INTRECV        ;CLEAR INTERRUPT RECEIVED FLAG
5691 034172 012737 000020 035646  MOV    #000020,T7DATA+6 ;SET ERI IN CHARACTERISTICS DATA
5692 034200 010465 177776      MOV    R4,T5DB(R5)    ;SET THE PACKET ADDRESS
5693 034204 004737 017054      JSR    PC,CHKTSSR     ;WAIT FOR SSR TO SET
5694 034210 103407      BCS    15$            ;BR IF CARRY SET (GOOD RETURN)
5695 034212 010001      MOV    R0,R1          ;SAVE CONTENTS OF TSSR
5699 034214      ERRDF  ERRNO,T7SSR,PKTSSR ;DEVICE FATAL SSR FAILED TO SET
      034214 104455                                TRAP  C$ERDF
      034216 001306                                .WORD 710
      034220 036260                                .WORD T7SSR
      034222 011520                                .WORD PKTSSR
5700 034224 004737 017720      JSR    PC,FATCHK     ;INC AND CHECK FOR MORE THAN 25 ERRORS
5701 034230                                CKLOOP                    ;LOOP ON ERROR, IF FLAG SET
      034230 104406                                TRAP  C$CLP1
5702 034232      ESCAPE SEG            ;BY-PASS SUBTEST IF FATAL ERROR
      034232 104410                                TRAP  C$ESCAPE
      034234 000056                                .WORD 10000$-
5703 034236 005737 002172      TST    INTRECV        ;DID AN INTERRUPT OCCUR ?
5704 034242 001004      BNE    22$            ;BRANCH IF YES
5708 034244      ERRHRD  ERRNO,T7NINT,PKTSSR ;
      034244 104456                                TRAP  C$ERHRD
      034246 001307                                .WORD 711
      034250 036347                                .WORD T7NINT
      034252 011520                                .WORD PKTSSR
5709 034254 016501 000000      22$: MOV    TSSR(R5),R1 ;GET THE CONTENTS OF TSSR

```







CZTUWAO TUBO FRONT END PRT A  
TEST 7: BASIC PACKET PROTOCOL

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```

5772  ;+
5773  ;TEST 7, SUBTEST 3
5774  ;
5775  ;CHECKS THAT THE CPU GIVES UP OWNERSHIP OF THE MESSAGE BUFFER
5776  ;AFTER THE MESSAGE BUFFER RELEASE, AND THAT FOLLOWING COMMANDS
5777  ;WORK CORRECTLY
5778  ;
5779  ;
5780  034504             BGNSUB                ;///////////////// BEGIN SUBTEST ///////////////////
      034504             TRAP                 T7.3:              TRAP   C$BSUB
      034504 104402
5781
5782  034506 004737 036556    JSR          PC,T7RST           ;SET PACKET TO INITIAL VALUES
5783  034512             SETPRI          #PRI00       ;LOWER PRIORITY TO ALLOW INTERRUPTS
      034512 012700 000000             MOV          #PRI00,R0
      034516 104441             TRAP                 C$SPRI
5784  034520 012704 035630    MOV          #T7PACKET,R4        ;GET THE ADDRESS OF COMMAND PACKET
5785  034524 012764 000010 000006    MOV          #8.,PKBCNT(R4)     ;START WITH MINIMUM ALLOWABLE VALUE
5786  034532
5787  034532             BGNSEG                ;>>>>>>>>>>>> BEGIN SEGMENT >>>>>>>>>>>>
      034532 104404             TRAP                 C$BSEG
5788
5789  034534 004737 016464    JSR          PC,SOFINIT        ;DO SOFT INIT OF CONTROLLER
5790  034540 103405             BCS          10$              ;BR IF SOFT INIT = OK
5794  034542 010001             MOV          R0,R1            ;SAVE CONTENTS OF TSSR
5795  034544             ERRDF          ERRNO,SFIERR,SFIMSG ;DEVICE FATAL ERROR DURING INIT
      034544 104455             TRAP                 C$SERDF
      034546 001315             .WORD        717
      034550 003550             .WORD        SFIERR
      034552 011506             .WORD        SFIMSG
5796  034554 005037 002170 10$:    CLR          FATFLG           ;CLEAR FATAL ERROR FLAG
5797  034560 005037 002172    CLR          INTRECV         ;CLEAR INTERRUPT RECEIVED FLAG
5798  034564 010465 177776    MOV          R4,TSDB(R5)     ;SET THE PACKET ADDRESS
5799  034570 004737 017054    JSR          PC,CHKTSSR      ;WAIT FOR SSR TO SET
5800  034574 103407             BCS          15$              ;BR IF CARRY SET (GOOD RETURN)
5801  034576 010001             MOV          R0,R1            ;SAVE CONTENTS OF TSSR
5805  034600             ERRDF          ERRNO,T7SSR,PKTSSR ;DEVICE FATAL SSR FAILED TO SET
      034600 104455             TRAP                 C$SERDF
      034602 001316             .WORD        718
      034604 036260             .WORD        T7SSR
      034606 011520             .WORD        PKTSSR
5806  034610 004737 017720 15$:    JSR          PC,FATCHK       ;INC AND CHECK FOR MORE THAN 25 ERRORS
5807  034614             CKLOOP
      034614 104406             TRAP                 C$CLP1
5808  034616             ESCAPE  SEG                ;BY-PASS SUBTEST IF FATAL ERROR
      034616 104410             TRAP                 C$ESCAPE
      034620 000056             .WORD        10000$.
5809  034622 005737 002172    TST          INTRECV         ;DID AN INTERRUPT OCCUR ?
5810  034626 001004             BNE          22$              ;BRANCH IF YES
5814  034630             ERRHRD          ERRNO,T7NINT,PKTSSR
      034630 104456             TRAP                 C$SERHRD
      034632 001317             .WORD        719
      034634 036347             .WORD        T7NINT
      034636 011520             .WORD        PKTSSR
5815  034640 016501 000000 22$:    MOV          TSSR(R5),R1     ;GET THE CONTENTS OF TSSR
5816  034644 012702 000200    MOV          #SSR,R2        ;EXPECTED CONTENTS OF TSSR
5817  034650 032701 000100    BIT          #OFL,R1         ;IS OFF-LINE BIT SET ?
```











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 TEST 7: BASIC PACKET PROTOCOL

```

6016
6017
6018
6019
6021 035626
6023 035630
6024 035630 100204
6025 035632 035640
6026 035634 000000
6027 035636 000010
6028
6029 035640
6030 035640 035652
6031 035642 000000
6032 035644 000016
6033 035646 000000 000000
6034
6035 035652
6036
6037
6038
6039
6040
6041 035672
6042 035672 100204
6043 035674 035702
6044 035676 000000
6045 035700 000010
6046
6047 035702
6048 035702 035714
6049 035704 000000
6050 035706 000016
6051 035710 000000 000000
6052
6053 035714
6054
6055
6056
6057
6058
6059
6060 035734 115 145 163
6061 036031 116 102 101
6062 036113 116 102 101
6063
6064 036170 103 157 156
6065 036260 103 157 156
6066 036347 105 170 160
6067 036440 125 156 145
6068 036527 102 141 163
6069
6070
    ;+
    ;LOCAL STORAGE FOR THIS TEST
    ;-
    T7PACKET: .BLKB 10-<.-TUV2AB7>
    .WORD 100204
    .WORD T7DATA
    .WORD 0
    .WORD 8.
    T7DATA:
    .WORD T7BFR
    .WORD 0
    .WORD 14.
    .WORD 0.0
    T7BFR: .BLKW 8.
    ;+
    ;TEST DATA FOR SUBTEST FOUR
    ;-
    T7PKT:
    .WORD 100204
    .WORD T7DTA
    .WORD 0
    .WORD 8.
    T7DTA:
    .WORD T7BUFR
    .WORD 0
    .WORD 14.
    .WORD 0.0
    T7BUFR: .BLKW 8.
    ;+
    ;LOCAL TEXT MESSAGES FOR TEST
    ;-
    T7MBF: .ASCIZ 'Message Buffer Modified after MESSAGE BUFFER RELEASE Command'
    T7NBA: .ASCIZ 'NBA Not Clear After WRITE CHARACTERISTICS Command'
    T7NNBA: .ASCIZ 'NBA Set After MESSAGE BUFFER RELEASE Command'
    T7SSRM: .ASCIZ 'Contents Of TSSR Incorrect After Message Buffer Release'
    T7SSR: .ASCIZ 'Contents of TSSR Incorrect After WRITE CHARACTERISTICS'
    T7NINT: .ASCIZ 'Expected Interrupt Not Received On WRITE CHARACTERISTICS'
    T7INT: .ASCIZ 'Unexpected Interrupt Received On WRITE CHARACTERISTICS'
    TST7ID: .ASCIZ 'Basic Packet Protocol'
    .EVEN
    
```



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TEST 7: BASIC PACKET PROTOCOL

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6072  
6073  
6074  
6075  
6076  
6077  
6078

```

: +
:
: ROUTINE TO RESTORE COMMAND PACKET TO START-UP (DEFAULT) VALUES
:
: -

```

6079 036556  
6080 036556  
6081 036562 012701 035630  
6082 036566 012721 100204  
6083 036572 012721 035640  
6084 036576 005021  
6085 036600 012721 000010  
6086 036604 012721 035652  
6087 036610 005021  
6088 036612 012721 000016  
6089 036616 005021  
6090 036620 005011  
6091 036622 005037 035652  
6092 036626 000207

```

T7RST:
    SAVREG          ;SAVE THE REGISTERS
    MOV             #T7PACKET,R1 ;START OF THE PACKET
    MOV             #100204,(R1)+ ;WRITE CHARACTERISTICS WITH ACK, IE
    MOV             #T7DATA,(R1)+ ;ADDRESS OF CHAR DATA BLOCK
    CLR             (R1)+         ;EXTENDED ADDRESS
    MOV             #8,(R1)+      ;SIZE OF DATA BLOCK IN BYTES
    MOV             #T7BFR,(R1)+  ;ADDRESS OF MESSAGE BUFFER
    CLR             (R1)+
    MOV             #14,(R1)+     ;LENGTH OF MESSAGE BUFFER
    CLR             (R1)+
    CLR             (R1)
    CLR             T7BFR        ;CLEAR 1ST LOC IN MESSAGE BUFFER
    RTS             PC           ;RETURN

```

6093  
6094  
6095  
6096  
6097  
6098

```

: +
:
: ROUTINE TO RESTORE COMMAND PACKET #2 TO START-UP (DEFAULT) VALUES
:
: -

```

6099 036630  
6100 036630  
6101 036634 012701 035672  
6102 036640 012721 100204  
6103 036644 012721 035702  
6104 036650 005021  
6105 036652 012721 000010  
6106 036656 012721 035714  
6107 036662 005021  
6108 036664 012721 000016  
6109 036670 005021  
6110 036672 005011  
6111 036674 005037 035714  
6112 036700 000207  
6113 036702  
036702  
036702 104401

```

T7RT2:
    SAVREG          ;SAVE THE REGISTERS
    MOV             #T7PKT,R1    ;START OF THE PACKET
    MOV             #100204,(R1)+ ;WRITE CHARACTERISTICS WITH ACK, IE
    MOV             #T7DTA,(R1)+ ;ADDRESS OF CHAR DATA BLOCK
    CLR             (R1)+         ;EXTENDED ADDRESS
    MOV             #8,(R1)+      ;SIZE OF DATA BLOCK IN BYTES
    MOV             #T7BUFR,(R1)+ ;ADDRESS OF MESSAGE BUFFER
    CLR             (R1)+
    MOV             #14,(R1)+     ;LENGTH OF MESSAGE BUFFER
    CLR             (R1)+
    CLR             (R1)
    CLR             T7BUFR        ;CLEAR 1ST LOC IN MESSAGE BUFFER
    RTS             PC           ;RETURN
    ENDTST

```

L10061: TRAP CSETST





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TEST 8: NON-TAPE MOTION COMMANDS

```

6208
6209
6210
6211
6212
6213
6214
6215
6216
6217 037202          BGNSUB           :////////////////// BEGIN SUBTEST ////////////////////
      037202          .                T8.2:
      037202 104402          .                TRAP      CSBSUB
6218
6219 037204          SETPRI #PRI00      ;LOWER PRIORITY TO ALLOW INTERRUPTS
      037204 012700 000000    .                MOV      #PRI00,R0
      037210 104441          .                TRAP      CSSPRI
6220 037212          BGNSEG           ;>>>>>>>>>>>>> BEGIN SEGMENT >>>>>>>>>>>>>>>
      037212 104404          .                TRAP      CSBSEG
6221
6222
6223 037214 004737 016464    JSR      PC,SOFINIT      ;DO SOFT INIT OF CONTROLLER
6224 037220 103405          BCS     3$              ;BR IF SOFT INIT = OK
6228 037222 010001          MOV     R0,R1           ;SAVE CONTENTS OF TSSR
6229 037224          ERRDF   ERRNO,SFIERR,SFIMSG ;DEVICE FATAL ERROR DURING INIT
      037224 104455          .                TRAP      CSERDF
      037226 001447          .WORD     807
      037230 003550          .WORD     SFIERR
      037232 011506          .WORD     SFIMSG
6230 037234          3$:
6231 037234 012704 037510    MOV     #T8PK2,R4       ;WRITE CHARACTERISTICS PACKET
6232 037240 004737 010152    JSR     PC,WRTCHR       ;ISSUE WRITE CHARACTERISTICS
6233 037244 103404          BCS     4$              ;BR, IF COMMAND ISSUED OK
6237 037246          ERRHRD  ERRNO,WRTMSG,SFIMSG ;WRITE CHARACTERISTIC FAILED
      037246 104456          .                TRAP      CSERHRD
      037250 001450          .WORD     808
      037252 004754          .WORD     WRTMSG
      037254 011506          .WORD     SFIMSG
6238 037256          4$:
6239 037256 004737 040314    JSR     PC,T8REST       ;SET UP PACKET FOR COMMAND
6240 037262 012704 037440    MOV     #T8PACKET,R4   ;GET THE ADDRESS OF COMMAND PACKET
6241 037266          5$:
6242 037266 005037 002172    CLR     INTRECV         ;CLEAR INTERRUPT RECEIVED FLAG
6243 037272 052714 007400    BIS     #P.MODE,(R4)    ;NON-ZERO COMMAND MODE BITS
6244 037276 010465 177776    MOV     R4,TSDB(R5)     ;SET THE PACKET ADDRESS
6245 037302 004737 017054    JSR     PC,CHKTSSR      ;WAIT FOR SSR TO SET
6246 037306 103405          BCS     15$            ;BR IF CARRY SET (GOOD RETURN)
6247 037310 010001          MOV     R0,R1           ;SAVE CONTENTS OF TSSR
6251 037312          ERRDF   ERRNO,T8SSR,PKTSSR ;DEVICE FATAL SSR FAILED TO SET
      037312 104455          .                TRAP      CSERDF
      037314 001451          .WORD     809
      037316 040004          .WORD     T8SSR
      037320 011520          .WORD     PKTSSR
6252 037322          15$: CKLOOP          ;LOOP ON ERROR, IF FLAG SET
      037322 104406          .                TRAP      CSCLP1
6253 037324          ESCAPE  SEG           ;BY-PASS CHECKS IF FATAL ERROR
      037324 104410          .                TRAP      C$ESCAPE
      037326 000074          .WORD     100008-

```



```

6284 037426          EXIT   TST          ;ALL DONE THIS TEST
      037426 104432          TRAP      CSEXIT
      037430 000770          .WORD    L10066-
6285
6286
6287      ;+
6288      ;LOCAL STORAGE FOR THIS TEST
6289      ;-
6291 037432          .BLKB   10-<.-TUV2A&7>
6293 037440          T8PACKET:      ;COMMAND PACKET FOR TEST
6294 037440 100204          .WORD   100204      ;WRITE CHAR COMMAND, WITH IE, ACK
6295 037442 037450          .WORD   T8DATA      ;ADDRESS OF CHARACTERISTICS BLOCK
6296 037444 000000          .WORD   0
6297 037446 000010          .WORD   8.          ;STARTING VALUE OF BLOCK SIZE
6298
6299 037450          T8DATA:          ;CHARACTERISTICS DATA BLOCK
6300 037450 037462          .WORD   T8BFR      ;ADDRESS OF MESSAGE BUFFER
6301 037452 000000          .WORD   0
6302 037454 000016          .WORD   14.         ;LENGTH OF MESSAGE BUFFER
6303 037456 000000 000000 .WORD   0,0
6304
6305 037462          T8BFR:   .BLKW   8.          ;MESSAGE BUFFER
6306
6307
6309 037502          .BLKB   10-<.-TUV2A&7>
6311 037510          T8PK2:          ;COMMAND PACKET FOR TEST
6312 037510 100204          .WORD   100204      ;WRITE CHAR COMMAND, WITH IE, ACK
6313 037512 037520          .WORD   T8DTA      ;ADDRESS OF CHARACTERISTICS BLOCK
6314 037514 000000          .WORD   0
6315 037516 000010          .WORD   8.          ;STARTING VALUE OF BLOCK SIZE
6316
6317 037520          T8DTA:          ;CHARACTERISTICS DATA BLOCK
6318 037520 037532          .WORD   T8BF2      ;ADDRESS OF MESSAGE BUFFER
6319 037522 000000          .WORD   0
6320 037524 000016          .WORD   14.         ;LENGTH OF MESSAGE BUFFER
6321 037526 000000 000000 .WORD   0,0
6322
6323 037532          T8BF2:   .BLKW   8.          ;MESSAGE BUFFER
6324
6325
6326
6327      ;+
6328      ;LOCAL TEXT MESSAGES FOR TEST
6329      ;-
6330
6331 037552          111      116      111  T8NBA:  .ASCIZ  'INITIALIZE Command Not Accepted'
6332 037612          111      116      111  T82REJ: .ASCIZ  'INITIALIZE Not Rejected With Non-Zero Mode Field'
6333 037673          107      105      124  T83REJ: .ASCIZ  'GET STATUS Not Accepted'
6334 037723          107      105      124  T84REJ: .ASCIZ  'GET STATUS Not Rejected With Non-Zero Mode Field'
6335 040004          103      157      156  T8SSR:  .ASCIZ  'Contents of TSSR Incorrect After INITIALIZE'
6336 040060          103      157      156  T8SR2:  .ASCIZ  'Contents of TSSR Incorrect After GET STATUS'
6337 040134          105      170      160  T8NINT: .ASCIZ  'Expected Interrupt Not Received On INITIALIZE'
6338 040212          111      156      143  T8TSBA: .ASCIZ  'Incorrect TSBA Address After INITIALIZE'
6339 040262          116      157      156  T8TBID: .ASCIZ  'Non-Tape Motion Commands'
6340          .EVEN
6341
    
```

CZTUWAO TUBO FRONT END PRT A MACRO M1200 29-MAR-83 13:24 PAGE 95  
TEST 8: NON-TAPE MOTION COMMANDS

```

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6351 040314
6352 040314
6353 040320 012701 037440
6354 040324 012721 100213
6355 040330 005021
6356 040332 005021
6357 040334 005021
6358 040336 005021
6359 040340 005021
6360 040342 005021
6361 040344 005021
6362 040346 005011
6363 040350 005037 037462
6364 040354 000207
6365
6366
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6371
6372 040356
6373 040356
6374 040362 012701 037440
6375 040366 012721 100217
6376 040372 005021
6377 040374 005021
6378 040376 005021
6379 040400 005021
6380 040402 005021
6381 040404 005021
6382 040406 005021
6383 040410 005011
6384 040412 005037 037462
6385 040416 000207
6386 040420
      040420 104401

```

```

: +
: ROUTINE TO RESTORE COMMAND PACKET TO START-UP (DEFAULT) VALUES
: INITIALIZE COMMAND
: -

```

```

T8REST:
      SAVREG      ;SAVE THE REGISTERS
      MOV #T8PACKET,R1 ;START OF THE PACKET
      MOV #100213,(R1)+ ;INITIALIZE WITH ACK, IE
      CLR (R1)+ ;ADDRESS OF CHAR DATA BLOCK
      CLR (R1)+ ;EXTENDED ADDRESS
      CLR (R1)+ ;SIZE OF DATA BLOCK IN BYTES
      CLR (R1)+ ;ADDRESS OF MESSAGE BUFFER
      CLR (R1)+ ;LENGTH OF MESSAGE BUFFER
      CLR (R1)
      CLR (R1)
      CLR T8BFR ;CLEAR 1ST LOC IN MESSAGE BUFFER
      RTS PC ;RETURN

```

```

: +
: ROUTINE TO RESTORE COMMAND PACKET TO START-UP (DEFAULT) VALUES
: GET STATUS COMMAND
: -

```

```

T8RT2:
      SAVREG      ;SAVE THE REGISTERS
      MOV #T8PACKET,R1 ;START OF THE PACKET
      MOV #100217,(R1)+ ;GET STATUS WITH ACK, IE
      CLR (R1)+ ;ADDRESS OF CHAR DATA BLOCK
      CLR (R1)+ ;EXTENDED ADDRESS
      CLR (R1)+ ;SIZE OF DATA BLOCK IN BYTES
      CLR (R1)+ ;ADDRESS OF MESSAGE BUFFER
      CLR (R1)+ ;LENGTH OF MESSAGE BUFFER
      CLR (R1)
      CLR (R1)
      CLR T8BFR ;CLEAR 1ST LOC IN MESSAGE BUFFER
      RTS PC ;RETURN
      ENDTST

```

```

L10066: TRAP CSETST

```

CZTUWAO TUBO FRONT END PRT A MACRO M1200 29-MAR-83 13:24 PAGE 97  
TEST 9: DMA MEMORY ADDRESSING

.SBTTL TEST 9: DMA MEMORY ADDRESSING

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:++  
: TEST 1  
: TEST DESCRIPTION  
: This test verifies that the controller can properly address and  
: access all available CPU memory (other than that occupied by the  
: diagnostic and diagnostic supervisor code) for both reading (DATI)  
: and writing (DATO). Verified are the LSI-11 Bus drivers for all  
: available address lines. Up to this point only 16 bits have been  
: used for DMA transfers.  
: TEST STEPS  
: REPEAT FROM 1 TO LOOPCNT  
: BEGIN  
: Do Subtest 1 - Verify GET STATUS selected locations  
: Do Subtest 2 - Verify message packets selected locations  
: Do Subtest 3 - Verify Characteristic data selected locations  
: Do Subtest 4 - Verify NXM to selected invalid addresses  
: END  
:--

BGNTST

040422  
040422 005037 002170  
040426 012737 005672 002146  
040434 005037 003100  
040440 012700 042070  
040444 004737 017226  
040450 012737 000002 002164  
040456

T9LOOP:

CLR FATFLG ;CLEAR FATAL ERROR FLAG  
MOV #EPRT1,EPRTSW ;SET UP ERROR MESSAGE SWITCH  
CLR KTFLG ;HOLD OFF KT11  
MOV #TST9ID,RO ;ASCII MESSAGE TO IDENTIFY TEST  
JSR PC,TSTSETUP ;DO INITIAL TEST SETUP  
MOV #2.,LOOPCNT ;PERFORM 2 ITERATIONS  
;LOOP ON TEST LABEL

T9::





CZTUWAO TUBO FRONT END PRT A MACRO M1200 29-MAR-83 13:24 PAGE 98-1  
TEST 9: SUBTEST 1: GET STATUS SELECTED LOCATIONS

```

6480 040552 010001          MOV     RO,R1          ;SAVE CONTENTS OF TSSR
6481 040554                NEXT.ERRNO
6482 040554          17$:  ERRDF  ERRNO,T9WRTSSR,PKTSSR ;DEVICE FATAL SSR FAILED TO SET
                                TRAP  CSERDF
                                .WORD 902
                                .WORD T9WRTSSR
                                .WORD PKTSSR
6483 040554          104455
6484 040556          001606
6485 040560          042172
6486 040562          011520

6483
6484          ;Verify a Get Status can be fetched from each address
6485          ;Get a valid modulo-4 test address
6486          ;Do a GET STATUS command from the test address
6487 040564          005037 002170          20$:  CLR     FATFLG          ;CLEAR FATAL ERROR FLAG
6488 040570          005037 041730          CLR     T9KT           ;TEST ABOVE 28K SWITCH
6489 040574          012702 041734          MOV     #T9BLK,R2      ;POINT TO TEST PATTERN TABLE
6490 040600          T91LOOP:
6491 040600          005037 003102          CLR     KTENABLE       ;TURN OFF ABOVE 28K TEST FLAG
6492 040604          012201          MOV     (R2)+,R1       ;GET TEST PATTERN ADDRESS
6493 040606          005000          CLR     R0             ;ASSUME NO TEST ABOVE 28K
6494 040610          005737 041730          TST     T9KT           ;TEST ABOVE 28K THIS TIME?
6495 040614          001407          BEQ     25$            ;BR IF NO
6496 040616          016200 177776          MOV     -2(R2),R0      ;GET TEST PATTERN AGAIN
6497 040622          042700 177774          BIC     #^C<A1716>,R0  ;SAVE 18 BIT ADDRESS ONLY
6498 040626          012737 000001 003102          MOV     #1,KTENABLE    ;TURN ON ABOVE 28K TEST FLAG
6499 040634          004737 042736          25$:  JSR     PC,T9CONVERT ;CONVERT TEST PATTERN TO TEST ADDRESS
6500 040640          103034          BCC     65$            ;BR IF INVALID PACKET ADDRESS
6501 040642          013704 041724          MOV     T9LOADD,R4     ;COPY CURRENT PACKET LOW ADDRESS
6502 040646          013703 041722          MOV     T9HIADD,R3     ;COPY CURRENT PACKET HIGH ADDRESS
6503 040652          004737 043474          JSR     PC,T9SETGET    ;SETUP CURRENT PACKET TO GET STATUS
6504 040656          042703 177774          BIC     #^C<A1716>,R3  ;SAVE ADDRESS BITS 17+16
6505 040662          050304          BIS     R3,R4          ;SETUP 18 BIT PACKET ADDRESS
6506 040664          004737 020064          JSR     PC,KTOFF       ;TURN OFF KT-11
6507 040670          010465 177776          MOV     R4,TSDB(R5)    ;SET THE PACKET ADDRESS TO EXECUTE
6508 040674          004737 017054          JSR     PC,CHKTSSR     ;WAIT FOR SSR TO SET
6509 040700          FORCERROR 32$
6510 040714          103405          BCS     40$            ;BR IF SSR SET IN CHKTSSR
6511 040716          010001          MOV     RO,R1          ;SAVE CONTENTS OF TSSR
6512 040720          NEXT.ERRNO
6513 040720          32$:  ERRDF  ERRNO,T9GETSSR,PKTGETS ;DEVICE FATAL SSR FAILED TO SET
                                TRAP  CSERDF
                                .WORD 903
                                .WORD T9GETSSR
                                .WORD PKTGETS
6514 040730          40$:  CKLOOP          ;LOOP ON ERROR, IF FLAG SET
                                TRAP  CSCLP1
6515 040732          104406          65$:
6516 040732          FORCEEXIT 80$
6517 040742          020227 042066          CMP     R2,#T9TBE      ;DONE ALL TSTBLK TEST PATTERNS?
6518 040746          103002          BHIS   70$            ;BR IF YES
6519 040750          000137 040600          JMP     T91LOOP        ;DO ANOTHER MODULO- 4 ADDRESS
6520 040754          005737 041730          70$:  TST     T9KT           ;DONE ABOVE 28K TESTING TOO?
6521 040760          003012          BGT     80$            ;BR IF YES
6522 040762          005737 003100          TST     KTFLG         ;ANY MEMORY ABOVE 28K ON SYSTEM?
6523 040766          001407          BEQ     80$            ;BR IF NO
6524 040770          012737 000001 041730          MOV     #1,T9KT        ;SET SWITCH
6525 040776          012702 041734          MOV     #T9BLK,R2     ;RESET TEST PATTERN TABLE
6526 041002          000137 040600          JMP     T91LOOP        ;DO ABOVE 28K TESTING
                                .TURN OFF KT11

```

CZTUWAO TUBO FRONT END PRT A MACRO M1200 29-MAR-83 13:24 PAGE 98-2  
TEST 9: SUBTEST 1: GET STATUS SELECTED LOCATIONS

6528 041012  
041012  
041012 104403  
6529 041014 005737 002170  
6530 041020 001402  
6531 041022 004737 017772  
6532 041026  
6533

ENDSUB

TST FATFLG  
BEQ 100s  
JSR PC,CKDROP

100s:

;////////// END SUBTEST //////////  
L10072: TRAP CSESUB  
:ANY FATAL ERRORS ?  
:BRANCH IF NOT  
:TRY TO DROP THE UNIT

CZTUWAO TUBO FRONT END PRT A MACRO M1200 29-MAR-83 13:24 PAGE 99  
TEST 9: SUBTEST 2: MESSAGE PACKETS TO SELECTED LOCATIONS

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041026  
041026  
041026 104402  
  
041030 012700 125252  
041034 004737 020212  
  
041040 004737 016464  
041044 103405  
041046  
041046 010001  
041050  
041050 104455  
041052 001610  
041054 003550  
041056 011506  
  
041060  
041060 012704 041660  
041064 004737 043426  
041070 004737 020064

```
.SBTTL TEST 9: SUBTEST 2: MESSAGE PACKETS TO SELECTED LOCATIONS
**
TEST 9: SUBTEST 2:
SUBTEST DESCRIPTION:
    This subtest verifies the controller can deposit message packets
    to all available memory locations.
    First all available memory is set to a background pattern
    of 125252.
    Write Characteristics commands are then executed with message
    buffer addresses set to various addresses in each available
    memory location.
    The various addresses are determined by floating a 1 then a 0
    through the address bits.
TEST STEPS:
BEGIN
    Fill Memory with background pattern of 125252
    Write to TSSR to soft initialize
    Do a WRITE CHARACTERISTICS to setup a message buffer to compare
REPEAT FOR SELECTED ADDRESSES IN DIAGNOSTIC FREE SPACE AND ABOVE 32K
BEGIN
    Get a valid modulo-4 test address
    Set the packet message buffer to the TEST ADDRESS
    Do a WRITE CHARACTERISTICS
    Restore the test message buffer to background pattern
END
END
--
BGNSUB                               :////////// BEGIN SUBTEST //////////
T9.2:                                TRAP    CSBSUB

:Fill Memory with background pattern of 125252
MOV    #125252,R0                    :BACKGROUND DATA
JSR    PC,FILLMEM                    :FILL MEMORY WITH BACKGROUND DATA

:Write to TSSR to soft initialize
JSR    PC,SOFINIT                    :DO SOFT INIT OF CONTROLLER
BCS    15$                            :BR IF SOFT INIT = OK
NEXT.ERRNO
MOV    R0,R1                          :SAVE CONTENTS OF TSSR
ERRDF  ERRNO,SFIERR,SFMSG            :DEVICE FATAL ERROR DURING INIT
                                           TRAP    CSERDF
                                           .WORD  904
                                           .WORD  SFIERR
                                           .WORD  SFMSG

:Do a WRITE CHARACTERISTICS to setup a message buffer to compare
15$:
MOV    #T9PACKET,R4                  :GET THE ADDRESS OF COMMAND PACKET
JSR    PC,T9SWRT                      :SET PACKET TO WRITE CHARACTERISTICS
JSR    PC,KTOFF                        :TURN OFF KI-11
```

CZTUWAO TUBO FRONT END PRT A MACRO M1200 29-MAR-83 13:24 PAGE 99-1  
 TEST 9: SUBTEST 2: MESSAGE PACKETS TO SELECTED LOCATIONS

```

6586 041074 010465 177776      MOV      R4,TSDB(R5)      ;SET THE PACKET ADDRESS
6587 041100 004737 017054      JSR      PC,CHKTSSR      ;WAIT FOR SSR TO SET
6588 041104          FORCERROR 178
6589 041120 103405          BCS      208             ;BR IF SSR SET IN CHKTSSR
6590 041122 010001          MOV      R0,R1          ;SAVE CONTENTS OF TSSR
6591 041124          NEXT.ERRNO
6592 041124          178:   ERRDF  ERRNO,T9WRTSSR,PKTSSR ;DEVICE FATAL SSR FAILED TO SET
        041124 104455          TRAP      C$ERDF
        041126 001611          .WORD    905
        041130 042172          .WORD    T9WRTSSR
        041132 011520          .WORD    PKTSSR

6593          ;Get a valid modulo-4 test address
6594          ;Set the packet message buffer to the test address
6595          ;Do a WRITE CHARACTERISTICS
6596 041134 005037 002170      208:   CLR      FATFLG      ;CLEAR FATAL ERROR FLAG
6597 041140 012703 041734          MOV      #T9BLK,R3      ;POINT TO TEST PATTERN TABLE
6598 041144          T92LOOP:
6599 041144 012301          MOV      (R3)+,R1       ;GET TEST PATTERN ADDRESS
6600 041146 010100          MOV      R1,R0         ;GET ADDRESS ALL '18 BITS'
6601 041150 042700 177774          BIC      #177774,R0    ;LEAVE ONLY A17 AND A16
6602 041154 042701 000001          BIC      #1,R1         ;ALWAYS ON A WORD BOUNDRY
6603 041160 004737 043130          JSR      PC,T9CT2      ;CONVERT TEST PATTERN TO TEST ADDRESS
6604 041164 103402          BCS      258           ;BR IF VALID MESSAGE BUFFER ADDRESS
6605 041166 000137 041264          JMP      1508          ;GET ANOTHER TEST PATTERN TO TRY
6606 041172 012704 041660          258:   MOV      #T9PACKET,R4 ;SET THE COMMAND PACKET ADDRESS
6607 041176 004737 043426          JSR      PC,T9SWRT     ;SETUP T9PACKET TO WRITE CHAR.
6608 041202 013737 041724 041670      MOV      T9LOADD,T9DATA ;SETUP LOW ORDER MESSAGE BUFFER ADD.
6609 041210 013737 041722 041672      MOV      T9HIADD,T9DATA+2 ;SETUP HIGH ORDER MESSAGE BUFFER ADD.
6610 041216 004737 020064          JSR      PC,KTOFF     ;TURN OFF KT-11
6611 041222 010465 177776          MOV      R4,TSDB(R5)  ;SET THE PACKET ADDRESS TO EXECUTE
6612 041226 004737 017054          JSR      PC,CHKTSSR   ;WAIT FOR SSR TO SET
6613 041232          FORCERROR 328
6614 041246 103405          BCS      508           ;BR IF SSR SET IN CHKTSSR
6615 041250 010001          MOV      R0,R1          ;SAVE CONTENTS OF TSSR
6616 041252          NEXT.ERRNO
6617 041252          328:   ERRDF  ERRNO,T9WRTSSR,PKTSSR ;DEVICE FATAL SSR FAILED TO SET
        041252 104455          TRAP      C$ERDF
        041254 001612          .WORD    906
        041256 042172          .WORD    T9WRTSSR
        041260 011520          .WORD    PKTSSR

6618 041262          508:   CKLOOP          ;LOOP ON ERROR, IF FLAG SET
        041262 104406          TRAP      C$CLP1

6619 041264          1508:  FORCEXIT 1608
6620 041274 020327 042066          CMP      R3,#T9TBE     ;DONE ALL TST9BLK TEST PATTERNS?
6621 041300 103002          BHIS    1608          ;BR IF YES
6622 041302 000137 041144          JMP      T92LOOP      ;DO ANOTHER MODULO- 4 ADDRESS
6623 041306 004737 020064          1608:  JSR      PC,KTOFF     ;TURN OFF KT11
6624 041312          ENDSUB          ;////////// END SUBTEST ////////////
        041312          L10073:
        041312 104403          TRAP      C$ESUB
6625 041314 005737 002170          TST      FATFLG      ;ANY FATAL ERRORS ?
6626 041320 001402          BEQ     1808          ;BRANCH IF NOT
6627 041322 004737 017772          JSR      PC,CKDROP    ;TRY TO DROP THE UNIT
6628 041326          1808:
    
```

CZTUWAO TUBO FRONT END PRT A MACRO M1200 29-MAR-83 13:24 PAGE 100  
TEST 9: SUBTEST 3: CHARACTERISTIC DATA SELECTED LOCATIONS

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041326  
041326 104402  
  
041330 012700 125252  
041334 004737 020212  
  
041340 004737 016464  
041344 103405  
041346  
041346 010001  
041350  
041350 104455  
041352 001613  
041354 003550  
041356 011506  
  
041360 005037 002170  
041364 005037 041730  
041370 012703 041734  
041374

.SBTTL TEST 9: SUBTEST 3: CHARACTERISTIC DATA SELECTED LOCATIONS

++  
TEST 9: SUBTEST 3:

SUBTEST DESCRIPTION:

This subtest verifies the controller can fetch a Write Characteristics data block from all available memory locations. First all available memory is set to a background pattern of 125252. Then Write Characteristics commands are executed with characteristic data blocks at various memory addresses. The various memory addresses are determined by floating a 1 then a 0 through the address bits.

TEST STEPS:

BEGIN  
Fill Memory with background pattern of 125252  
Write to TSSR to soft initialize  
  
REPEAT FOR SELECTED VALID ADDRESSES IN DIAGNOSTIC FREE SPACE AND ABOVE 32K  
BEGIN  
Get a valid test address  
Set the test packet characteristics data pointer to the test address.  
Store expected characteristic data in test address block  
Do a WRITE CHARACTERISTIC command  
  
END  
END

BGNSUB ;////////// BEGIN SUBTEST ///////////  
T9.3: TRAP CSBSUB

;Fill Memory with background pattern of 125252  
MOV #125252,R0 ;BACKGROUND DATA  
JSR PC,FILLMEM ;FILL MEMORY WITH BACKGROUND DATA

;Write to TSSR to soft initialize  
JSR PC,SOFINIT ;DO SOFT INIT OF CONTROLLER  
BCS 20\$ ;BR IF SOFT INIT = OK  
NEXT.ERRNO  
MOV R0,R1 ;SAVE CONTENTS OF TSSR  
ERRDF ERRNO,SFIERR,SFIMSG ;DEVICE FATAL ERROR DURING INIT  
TRAP CSERDF  
.WORD 907  
.WORD SFIERR  
.WORD SFIMSG

;Get a valid test address  
20\$: CLR FATFLG ;CLEAR FATAL ERROR FLAG  
CLR T9KT ;TEST ABOVE 28K SWITCH  
MOV #T9BLK,R3 ;POINT TO TEST PATTERN TABLE

T93LOOP:

CZTUWAO TUBO FRONT END PRT A MACRO M1200 29-MAR-83 13:24 PAGE 100-1  
TEST 9: SUBTEST 3: CHARACTERISTIC DATA SELECTED LOCATIONS

```

6681 041374 005037 003102 CLR KTENABLE ;TURN OFF ABOVE 28K TEST FLAG
6682 041400 012301 MOV (R3)+,R1 ;GET TEST PATTERN ADDRESS
6683 041402 010100 MOV R1,R0 ;GET ADDRESS ALL '18 BITS'
6684 041404 042700 177774 BIC #177774,R0 ;LEAVE ONLY A17 AND A16
6685 041410 042701 000003 BIC #3,R1 ;GET RID OF A17 AND A16
6686 041414 005737 041730 TST T9KT ;TEST ABOVE 28K THIS TIME?
6687 041420 001407 BEQ 25$ ;BR IF NO
6688 041422 016300 177776 MOV -2(R3),R0 ;GET TEST PATTERN AGAIN
6689 041426 042700 177774 BIC #C<A1716>,R0 ;SAVE 18 BIT ADDRESS ONLY
6690 041432 012737 000001 003102 MOV #1,KTENABLE ;TURN ON ABOVE 28K TEST FLAG
6691 041440 004737 042736 25$: JSR PC,T9CONVERT ;CONVERT TEST PATTERN TO TEST ADDRESS
6692 041444 103402 BCS 30$ ;BR IF VALID TEST ADDRESS
6693 041446 000137 041550 JMP 60$ ;GET NEXT TEST PATTERN
6694 ;Set the test packet characteristics data pointer to the test address
6695 041452 012704 041660 30$: MOV #T9PACKET,R4 ;GET THE ADDRESS OF COMMAND PACKET
6696 041456 004737 043426 JSR PC,T9SWRT ;RESTORE PACKET TO STARTING VALUES
6697 041462 013764 041724 000002 MOV T9LOADD,PKLOW(R4) ;STORE CHAR. DATA PTR LOW ADDRESS
6698 041470 013764 041722 000004 MOV T9HIADD,PKHI(R4) ;STORE CHAR. DATA PTR HIGH ADDRESS
6699 041476 004737 043536 JSR PC,T9CHAR ;STORE EXPECTED DATA IN DATA BLOCK
6700 ;Do a WRITE CHARACTERISTIC command
6701 041502 004737 020064 JSR PC,KTOFF ;TURN OFF KT-11
6702 041506 010465 177776 MOV R4,TSDB(R5) ;SET THE PACKET ADDRESS TO EXECUTE
6703 041512 004737 017054 JSR PC,CHKTSSR ;WAIT FOR SSR TO SET
6704 041516 FORCERROR 32$
6705 041532 103405 BCS 40$ ;BR IF SSR SET IN CHKTSSR
6706 041534 010001 MOV R0,R1 ;SAVE CONTENTS OF TSSR
6707 041536 NEXT.ERRNO
6708 041536 32$: ERRDF ERRNO,T9WRTSSR,PKTSSR ;DEVICE FATAL SSR FAILED TO SET
; TRAP C$ERDF
; .WORD 908
; .WORD T9WRTSSR
; .WORD PKTSSR
6709 041546 40$: CKLOOP ;LOOP ON ERROR, IF FLAG SET
; TRAP C$CLP1
6710 041550 60$:
6711 041550 020327 042066 CMP R3,#T9TBE ;DONE ALL TSTBLK TEST PATTERNS?
6712 041554 103002 BHIS 65$ ;BR IF YES
6713 041556 000137 041374 JMP T93LOOP ;DO ANOTHER MODULO- 4 ADDRESS
6714 041562 005737 041730 65$: TST T9KT ;DONE ABOVE 28K TESTING TOO?
6715 041566 003012 BGT 70$ ;BR IF YES
6716 041570 005737 003100 TST KTFLG ;ANY MEMORY ABOVE 28K ON SYSTEM?
6717 041574 001407 BEQ 70$ ;BR IF NO
6718 041576 012737 000001 041730 MOV #1,T9KT ;SET SWITCH
6719 041604 012703 041734 MOV #T9BLK,R3 ;RESET TEST PATTERN TABLE
6720 041610 000137 041374 JMP T93LOOP ;DO ABOVE 28K TESTING
6721 041614 004737 020064 70$: JSR PC,KTOFF ;TURN OFF KT11
6722 041620 ENDSUB ;////////// END SUBTEST ////////////
; L10074: TRAP C$ESUB
6723 041622 005737 002170 TST FATFLG ;ANY FATAL ERRORS ?
6724 041626 001402 BEQ 75$ ;BRANCH IF NOT
6725 041630 004737 017772 JSR PC,CKDROP ;TRY TO DROP THE UNIT
6726 041634 75$:
6727 041634 004737 017174 100$: JSR PC,TSTLOOP ;SHOULD WE DO ITERATIONS?
6728 041640 103002 BCC 105$ ;BR IF NO
6729 041642 000137 040456 105$: JMP T9LOOP ;LOOP UNTIL ITERATION COUNT DONE
6730 041646

```

CZTUWAO TUBO FRONT END PRT A MACRO M1200 29-MAR-83 13:24 PAGE 100-2  
TEST 9: SUBTEST 3: CHARACTERISTIC DATA SELECTED LOCATIONS

```

6731 041646 004737 020064      JSR      PC,KTOFF      ;TURN OFF MEMORY MANAGEMENT
6732 041652      EXIT      TST        ;ALL DONE THIS TEST
      041652 104432      TRAP      C$EXIT
      041654 001724      .WORD     L10071-.

6733
6734
6735      ;+
6736      ;LOCAL STORAGE FOR THIS TEST
6737      ;-
6738
6740 041656      .BLKB    10-<.-TUV2A87>
6742 041660      T9PACKET:
6743 041660 100004      .WORD    100004      ;COMMAND PACKET FOR TEST
6744 041662 041670      .WORD    T9DATA     ;WRITE CHARACTERISTICS COMMAND, WITH ACK
6745 041664 000000      .WORD    0           ;ADDRESS OF CHARACTERISTICS BLOCK
6746 041666 000010      .WORD    8.         ;STARTING VALUE OF BLOCK SIZE
6747
6748 041670      T9DATA:
6749 041670 041702      .WORD    T9BFR      ;CHARACTERISTICS DATA BLOCK
6750 041672 000000      .WORD    0           ;LOW ADDRESS OF MESSAGE BUFFER
6751 041674 000016      .WORD    14.        ;HIGH ORDER OF MESSAGE BUFFER
6752 041676 000000 000000      .WORD    0,0        ;LENGTH OF MESSAGE BUFFER
6753
6754 041702      T9BFR:  .BLKW    8.         ;MESSAGE BUFFER
6755
6756 041722 000000      T9HIADD: .WORD    0           ;HIGH ADDRESS
6757 041724 000000      T9LOADD: .WORD    0           ;LOW ADDRESS
6758 041726 000000      T9PAR6:  .WORD    0           ;ADDRESS IN PAR FORMAT
6759 041730 000000      T9KT:    .WORD    0           ;TEST ABOVE 28K SWITCH
6760 041732 000000      T94TST: .WORD    0           ;ADDRESS TEST BIT
6761
6762      ;+
6763      ;TABLE OF ADDRESSES
6764      ;-
6765
6766 041734 000001      T9BLK:  .WORD    000001
6767 041736 000002      .WORD    000002
6768 041740 000003      .WORD    000003
6769 041742 000005      .WORD    000005
6770 041744 000006      .WORD    000006
6771 041746 000007      .WORD    000007
6772 041750 000011      .WORD    000011
6773 041752 000012      .WORD    000012
6774 041754 000013      .WORD    000013
6775 041756 000021      .WORD    000021
6776 041760 000022      .WORD    000022
6777 041762 000023      .WORD    000023
6778 041764 000041      .WORD    000041
6779 041766 000042      .WORD    000042
6780 041770 000043      .WORD    000043
6781 041772 000101      .WORD    000101
6782 041774 000102      .WORD    000102
6783 041776 000103      .WORD    000103
6784 042000 000201      .WORD    000201
6785 042002 000202      .WORD    000202
6786 042004 000203      .WORD    000203
6787 042006 000401      .WORD    000401

```



CZTUWAO TUBO FRONT END PRT A MACRO M1200 29-MAR-83 13:24 PAGE 100-3  
TEST 9: SUBTEST 3: CHARACTERISTIC DATA SELECTED LOCATIONS

6788	042010	000402	.WORD	000402
6789	042012	000403	.WORD	000403
6790	042014	001001	.WORD	001001
6791	042016	001002	.WORD	001002
6792	042020	001003	.WORD	001003
6793	042022	002001	.WORD	002001
6794	042024	002002	.WORD	002002
6795	042026	002003	.WORD	002003
6796	042030	004001	.WORD	004001
6797	042032	004002	.WORD	004002
6798	042034	004003	.WORD	004003
6799	042036	010001	.WORD	010001
6800	042040	010002	.WORD	010002
6801	042042	010003	.WORD	010003
6802	042044	020001	.WORD	020001
6803	042046	020002	.WORD	020002
6804	042050	020003	.WORD	020003
6805	042052	040001	.WORD	040001
6806	042054	040002	.WORD	040002
6807	042056	040003	.WORD	040003
6808	042060	100001	.WORD	100001
6809	042062	100002	.WORD	100002
6810	042064	100003	.WORD	100003
6811	042066	177777	.WORD	177777

T9TBE: .WORD 177777  
;+  
;LOCAL TEXT MESSAGES FOR TEST  
;-

6816	042070	104	115	101	T9T9ID:	.ASCIZ	'DMA Memory Addressing'
6817	042116	103	157	156	T9GETSSR:	.ASCIZ	'Contents of TSSR Incorrect After GET STATUS'
6818	042172	103	157	156	T9WRTSSR:	.ASCIZ	'Contents of TSSR Incorrect After WRITE CHARACTERISTICS'
6819	042261	115	145	163	T9MSGBUF:	.ASCIZ	'Message Buffer Contents Incorrect After WRITE CHARACTERISTICS'
6820	042357	102	141	143	T9BKGNB:	.ASCIZ	'Background Pattern Disturbed By WRITE CHARACTERISTICS'
6821	042445	105	170	160	T9NINT:	.ASCIZ	'Expected Interrupt Not Received On WRITE CHARACTERISTICS'
6822	042536	127	162	151	T9DPR:	.ASCIZ	'Write Characteristic data in ram does not match expected'
6823	042627	124	123	123	T9NXM:	.ASCIZ	'TSSR NXM bit failed to set when nonexistent memory address specified'
6824						.EVEN	
6825							

CZTUWAO TUBO FRONT END PRT A MACRO M1200 29-MAR-83 13:24 PAGE 101  
 TEST 9: SUBTEST 3: CHARACTERISTIC DATA SELECTED LOCATIONS

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6843 042736
6844 042736
6845 042742 005037 041724
6846 042746 005037 041722
6847 042752 005037 041726
6848 042756 042701 170000
6849 042762 010005
6850 042764 004737 020064
6851 042770 013702 003072
6852 042774 062702 000020
6853 043000 060102
6854 043002 042702 000003
6855 043006 013703 003076
6856 043012 162703 000020
6857 043016 010237 041724
6858 043022 010237 041726
6859 043026 020203
6860 043030 101007
6861 043032 020237 003072
6862 043036 103007
6863 043040 005737 003102
6864 043044 001004
6865 043046 000424
6866 043050 162702 000020
6867 043054 000754
6868 043056 005737 003102
6869 043062 001420
6870 043064 005737 003100
6871 043070 001413
6872 043072 004737 020046
6873 043076 010500
6874 043100 010037 041722
6875 043104 010201
6876 043106 004737 020106
6877 043112 010037 041726
6878 043116 103403
6879 043120 000241
6880 043122 000401
6881 043124 000261
6882 043126 000207
    
```

```

:ROUTINE TO CONVERT A TEST PATTERN TO A VALID ADDRESS IN DIAGNOSTIC FREE SPACE
:DIAGNOSTIC FREE SPACE IS BETWEEN THE END OF THE DIAGNOSTIC AND THE
:BEGINNING OF THE SUPERVISOR. THIS IS ALWAYS BELOW 24K.
:IF MEMORY ABOVE 28K SPECIFIED (VIA R1) THEN PAR 6 IS SET
:TO THE RELOCATION BASE.
:
:INPUTS:
:R0      HIGH ORDER ADDRESS BITS
:R1      LOW ORDER ADDRESS BITS
:
:OUPUTS:
:T9PAR6 = ADDRESS BIASED TO PAR6 IF >28K UNDER TEST
:T9HIADD = HIGH ORDER ADDRESS IN NON PAR6 FORMAT
:T9LOADD = LOW ORDER ADDRESS IN NON PAR6 FORMAT
:C BIT = 1 IF GOOD ADDRESS RETURNED
:C BIT = 0 IF TEST PATTERN DID NOT YIELD A VALID ADDRESS
:
:T9CONVERT:
:SAVREG      ;SAVE R1-R5 UNTIL NEXT RETURN
:CLR T9LOADD ;CLEAR LOW ADDRESS
:CLR T9HIADD ;CLEAR HIGH ADDRESS
:CLR T9PAR6  ;CLEAR PAR6 BIASED ADDRESS
:BIC #^C<7777>,R1 ;FORCE TO LOWER 12 BITS OF ADDRESS
:MOV R0,R5   ;SAVE HIGH ORDER ADDRESS BITS
:JSR PC,KTOFF ;SHUTOFF MEMORY MANAGEMENT
:MOV FREE,R2 ;GET FIRST FREE ADDRESS
:ADD #16.,R2 ;IN CASE TEST PATTERN=0
:ADD R1,R2   ;ADD IN TEST PATTERN
:BIC #3,R2   ;MAKE IT MODULO-4
25$: :MOV FREEHI,R3 ;GET LAST FREE ADDRESS
:SUB #16.,R3 ;SAVE AT LEAST 8 WORDS (IN CASE MESSAGE BUFFER)
:MOV R2,T9LOADD ;SAVE POSSIBLE LOW ADDRESS
:MOV R2,T9PAR6 ;SAVE IT IN PAR6 BIASED TOO
:CMP R2,R3     ;IS THIS ADDRESS ABOVE FREE SPACE?
:BHI 35$      ;BR IF YES
:CMP R2,FREE  ;IS IT IN FREE SPACE?
:BHIS 50$     ;BR IF YES- ITS GOOD
:TST KTENABLE ;TESTING ABOVE 28K?
:BNE 50$     ;BR IF YES
:BR 90$     ;BR IF NOT IN FREE SPACE
35$: :SUB #16.,R2 ;FORCE FIT THE TEST PATTERN
:BR 25$    ;TRY THIS TEST PATTERN ADDRESS
50$: :TST KTENABLE ;TESTING ABOVE 28K?
:BEQ 100$  ;BR IF NO
:TST KFLG  ;ANY MEMORY ABOVE 28K?
:BEQ 90$  ;BR IF NO
:JSR PC,KTON ;TURN ON MEMORY MANAGEMENT
:MOV R5,R0  ;GET HIGH ORDER ADDRESS
:MOV R0,T9HIADD ;SAVE POSSIBLE HIGH ADDRESS
:MOV R2,R1  ;GET COMPUTED LOW ORDER ADDRESS
:JSR PC,SETMAP ;RETURN PAR6 BIASED ADDRESS IN R0
:MOV R0,T9PAR6 ;COPY PAR6 BIASED ADDRESS
:BCS 105$  ;BR IF VALID ADDRESS
90$: :CLC      ;CLR C BIT FOR FAILURE
:BR 105$  ;
100$: :SEC      ;SET SUCCESS
105$: :RTS      ;RETURN
    
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 6901 043130  
 6902 043134 005037 041724  
 6903 043140 005037 041722  
 6904 043144 005037 041726  
 6905 043150 042701 170000  
 6906 043154 010005  
 6907 043156 004737 020064  
 6908 043162 013702 003072  
 6909 043166 062702 000020  
 6910 043172 060102  
 6911 043174 013703 003076  
 6912 043200 162703 000020  
 6913 043204 010237 041724  
 6914 043210 010237 041726  
 6915 043214 020203  
 6916 043216 101007  
 6917 043220 020237 003072  
 6918 043224 103007  
 6919 043226 005737 003102  
 6920 043232 001004  
 6921 043234 000424  
 6922 043236 162702 000020  
 6923 043242 000754  
 6924 043244 005737 003102  
 6925 043250 001420  
 6926 043252 005737 003100  
 6927 043256 001413  
 6928 043260 004737 020046  
 6929 043264 010500  
 6930 043266 010037 041722  
 6931 043272 010201  
 6932 043274 004737 020106  
 6933 043300 010037 041726  
 6934 043304 103403  
 6935 043306 000241  
 6936 043310 000401  
 6937 043312 000261  
 6938 043314 000207

```

:↑
: ONLY FOR MESSAGE BUFFER ADDRESSES
: ROUTINE TO CONVERT A TEST PATTERN TO A VALID ADDRESS IN DIAGNOSTIC FREE SPACE
: DIAGNOSTIC FREE SPACE IS BETWEEN THE END OF THE DIAGNOSTIC AND THE
: BEGINNING OF THE SUPERVISOR. THIS IS ALWAYS BELOW 24K.
: IF MEMORY ABOVE 28K SPECIFIED (VIA R1) THEN PAR 6 IS SET
: TO THE RELOCATION BASE.
:
: INPUTS:
:   R0      HIGH ORDER ADDRESS BITS
:   R1      LOW ORDER ADDRESS BITS
:
: OUPUTS:
:   T9PAR6 = ADDRESS BIASED TO PAR6 IF >28K UNDER TEST
:   T9HIADD = HIGH ORDER ADDRESS IN NON PAR6 FORMAT
:   T9LOADD = LOW ORDER ADDRESS IN NON PAR6 FORMAT
:   C BIT = 1 IF GOOD ADDRESS RETURNED
:   C BIT = 0 IF TEST PATTERN DID NOT YIELD A VALID ADDRESS
:
: T9CT2: SAVREG          ;SAVE R1-R5 UNTIL NEXT RETURN
: CLR      T9LOADD      ;CLEAR LOW ADDRESS
: CLR      T9HIADD      ;CLEAR HIGH ADDRESS
: CLR      T9PAR6       ;CLEAR PAR6 BIASED ADDRESS
: BIC      #^C<7777>,R1 ;FORCE TO LOWER 12 BITS OF ADDRESS
: MOV      R0,R5        ;SAVE HIGH ORDER ADDRESS BITS
: JSR     PC,KTOFF      ;SHUTOFF MEMORY MANAGEMENT
: MOV      FREE,R2      ;GET FIRST FREE ADDRESS
: ADD      #16.,R2      ;IN CASE TEST PATTERN=0
: ADD      R1,R2        ;ADD IN TEST PATTERN
: 25$: MOV     FREEH1,R3  ;GET LAST FREE ADDRESS
: SUB      #16.,R3      ;SAVE AT LEAST 8 WORDS (IN CASE MESSAGE BUFFER)
: MOV      R2,T9LOADD   ;SAVE POSSIBLE LOW ADDRESS
: MOV      R2,T9PAR6    ;SAVE IT IN PAR6 BIASED TOO
: CMP      R2,R3        ;IS THIS ADDRESS ABOVE FREE SPACE?
: BHI     35$           ;BR IF YES
: CMP      R2,FREE      ;IS IT IN FREE SPACE?
: BHIS   50$           ;BR IF YES- ITS GOOD
: TST     KTENABLE      ;TESTING ABOVE 28K?
: BNE     50$           ;BR IF YES
: BR      90$          ;BR IF NOT IN FREE SPACE
: 35$: SUB     #16.,R2   ;FORCE FIT THE TEST PATTERN
: BR      25$          ;TRY THIS TEST PATTERN ADDRESS
: 50$: TST     KTENABLE  ;TESTING ABOVE 28K?
: BEQ     100$         ;BR IF NO
: TST     KTFLG        ;ANY MEMORY ABOVE 28K?
: BEQ     90$          ;BR IF NO
: JSR     PC,KTON      ;TURN ON MEMORY MANAGEMENT
: MOV     R5,R0        ;GET HIGH ORDER ADDRESS
: MOV     R0,T9HIADD   ;SAVE POSSIBLE HIGH ADDRESS
: MOV     R2,R1        ;GET COMPUTED LOW ORDER ADDRESS
: JSR     PC,SETMAP    ;RETURN PAR6 BIASED ADDRESS IN R0
: MOV     R0,T9PAR6    ;COPY PAR6 BIASED ADDRESS
: BCS     105$         ;BR IF VALID ADDRESS
: 90$:  CLC           ;CLR C BIT FOR FAILURE
: BR      105$
: 100$: SEC           ;SET SUCCESS
: 105$:  RTS          ;RETURN
  
```

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 TEST 9: SUBTEST 3: CHARACTERISTIC DATA SELECTED LOCATIONS

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6956 043316  
6957 043316  
6958 043322 012701 002206  
6959 043326 012702 000020  
6960 043332 005003  
6961 043334 004737 017054  
6962 043340 112765 000000 177776  
6963 043346 004737 017054 10\$:  
6964 043352 010265 177776  
6965 043356 004737 017054  
6966 043362 116511 177776  
6967 043366 122124  
6968 043370 001401  
6969 043372 005203  
6970 043374 005202 20\$:  
6971 043376 020227 000022  
6972 043402 002761  
6973 043404 005703  
6974 043406 001402  
6975 043410 000241  
6976 043412 000401  
6977 043414 000261 30\$:  
6978 043416 012737 000002 002246 50\$:  
6979 043424 000207  
6980  
6981  
6982  
6983  
6984 043426  
6985 043426  
6986 043432 012701 041660  
6987 043436 012721 100004  
6988 043442 012721 041670  
6989 043446 005021  
6990 043450 012721 000010  
6991 043454 012721 041702  
6992 043460 005021  
6993 043462 012721 000016  
6994 043466 005021  
6995 043470 005011

```

: *
: ROUTINE TO READ THE FIRST 2 BYTES FROM RAM
: MEMORY AND COMPARE THIS DATA TO A COMMAND PACKET.
: INPUT:
:     R4     ADDRESS OF THE COMMAND PACKET
:     R5     FIRST DEVICE UNIBUS ADDRESS
: OUTPUT:
:     CARRY  SET - RAM MATCHES PACKET
:           CLR - RAM DOES NOT MATCH PACKET
: IMPLICIT OUTPUT:
:     THE TABLE RAMDATA IS FILLED WITH THE
:     DATA HELD IN RAM.
:     RAMSIZ SET TO 2 FOR PRAMPKT ROUTINE
: SIDE EFFECTS:
:     THE SUBSYSTEM IS LEFT IN MAINTENANCE MODE
: -
T9CKRAM::
: SAVREG           : SAVE THE GENERAL REGISTERS
MOV #RAMDATA,R1   : ADDRESS TO SAVE THE RAM DATA
MOV #RMPKTBEGR2  : BYTE ADDRESS OF FIRST RAM DATA
CLR R3            : CLEAR THE ERROR FLAG
JSR PC,CHKTSSR   : WAIT FOR SSR
MOVB #0,TSDB(R5) : SET MAINTENANCE MODE
10$: JSR PC,CHKTSSR : WAIT FOR SSR TO SET
MOV R2,TSDB(R5)  : SELECT NEXT RAM ADDRESS
JSR PC,CHKTSSR   : WAIT FOR SSR TO SET
MOVB TSBA(R5),(R1) : READ THE RAM DATA
CMPB (R1)+,(R4)+ : COMPARE TO EXPECTED
BEQ 20$          : BRANCH IF OK
INC R3           : SET ERROR FLAG
20$: INC R2       : ADDRESS OF NEXT RAM LOCATION
CMP R2,#RMPKTBEGR2+2 : DONE 2 BYTES?
BLT 10$         : BR IF NO
TST R3          : WAS AN ERROR FOUND ?
BEQ 30$         : BRANCH IF NOT
CLC             : CLEAR CARRY TO SHOW ERROR
BR 50$         : AND EXIT
30$: SEC        : SHOW GOOD COMPARE
50$: MOV #2,RAMSIZ : SETUP RAMSIZ
RTS PC         : RETURN
: *
: ROUTINE TO SETUP PACKET TO WRITE CHARACTERISTICS
: -
T9SWRT:
: SAVREG           : SAVE THE REGISTERS
MOV #T9PACKET,R1 : START OF THE PACKET
MOV #100004,(R1)+ : WRITE CHARACTERISTICS WITH ACK
MOV #T9DATA,(R1)+ : ADDRESS OF CHAR DATA BLOCK
CLR (R1)+         : EXTENDED ADDRESS
MOV #8,(R1)+      : SIZE OF DATA BLOCK IN BYTES
MOV #T9BFR,(R1)+ : ADDRESS OF MESSAGE BUFFER
CLR (R1)+         :
MOV #14,(R1)+     : LENGTH OF MESSAGE BUFFER
CLR (R1)+         :
CLR (R1)          :
BYE               : RETURN
    
```

CZTUWAO TU80 FRONT END PRT A MACRO M1200 29-MAR-83 13:24 PAGE 103-1  
TEST 9: SUBTEST 3: CHARACTERISTIC DATA SELECTED LOCATIONS

```

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6999
7000
7001
7002
7003 043474
7004 043474
7005 043500 010401
7006 043502 005737 003102
7007 043506 001404
7008 043510 010300
7009 043512 004737 020106
7010 043516 010001
7011 043520 012700 000017
7012 043524 052700 100000
7013 043530 010021
7014 043532 005021
7015 043534 000207
7016
7017
7018
7019 043536
7020 043536
7021 043542 012700 041670
7022 043546 013701 041724
7023 043552 005737 003102
7024 043556 001402
7025 043560 013701 041726
7026 043564 012021
7027 043566 012021
7028 043570 012021
7029 043572 012021
7030 043574 012021
7031 043576 000207
7032 043600
      043600
      043600 104401

```

```

:ROUTINE TO SETUP A GET STATUS COMMAND PACKET AT CURRENT PACKET ADDRESS
:
R3 HIGH ORDER PACKET ADDRESS
R4 LOW ORDER PACKET ADDRESS
NOTE: R3 IS IGNORED IF KENABLE FLAG CLEAR
:
T9SETGET:
SAVREG ;SAVE THE REGISTERS
MOV R4,R1 ;GET LOW ORDER ADDRESS
TST KENABLE ;TESTING ABOVE 28K?
BEQ 10$ ;BR IF NO
MOV R3,R0 ;GET HIGH ORDER ADDRESS
JSR PC,SETHAP ;RETURN ADDRESS BIASED TO PAR6 IN R0
MOV R0,R1 ;GET ADDRESS
10$: MOV #P.GETSTATUS,R0 ;GET STATUS COMMAND CODE NO IE
BIS #P.ACK,R0 ;SET ACK
MOV R0,(R1)+ ;STORE GET STATUS IN PACKET
CLR (R1)+ ;CLEAR UNUSED WORD
RTS PC ;RETURN
:
:ROUTINE TO SETUP A CHARACTERISTIC DATA BLOCK AT A TEST ADDRESS
:
T9CHAR:
SAVREG ;SAVE R1-R5 UNTIL NEXT RETURN
MOV #T9DATA,R0 ;GET T9PACKET DATA POINTER
MOV T9LOAD,R1 ;ASSUME NOT ABOVE 28K
TST KENABLE ;TESTING ABOVE 28K?
BEQ 10$ ;BR IF NO
MOV T9PAR6,R1 ;SET TEST ADDRESS ABOVE 28K
10$: MOV (R0)+,(R1)+ ;STORE DATA WORD 1
MOV (R0)+,(R1)+ ;STORE DATA WORD 2
MOV (R0)+,(R1)+ ;STORE DATA WORD 3
MOV (R0)+,(R1)+ ;STORE DATA WORD 4
MOV (R0)+,(R1)+ ;STORE DATA WORD 5
RTS PC ;RETURN
ENDTST

```

L10071: TRAP CSETST

CZTUWAO TUBO FRONT END PRT A MACRO M1200 29-MAR-83 13:24 PAGE 104  
TEST 10: INITIALIZE AFTER WRITE CHARACTERISTICS

7034  
7035  
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7051  
7052  
7053  
7054

.SBTTL TEST 10: INITIALIZE AFTER WRITE CHARACTERISTICS

TEST DESCRIPTION:

This test verifies that a Hardware Initialize command invoked after a Write Characteristics command sets up the Command, Message and Characteristic image blocks in the controller ram correctly.

TEST STEPS:

REPEAT FOR LOOPCNT  
BEGIN

Do WRITE CHARACTERISTICS command.  
If the MBA bit in the TSSR register is NOT=0 then Print Error.  
Write to TSSR register to soft initialize the controller  
If controller RAM 310-377 NOT=0 then Print Error

END

7055 043602

BGNTST

T10::

7056 043602 005037 002170  
7057 043606 012737 005672 002146  
7058 043614 005037 003100  
7064 043620 012700 044252  
7065 043624 004737 017226  
7066 043630 012737 000002 002164  
7067 043636  
7068 043636 004737 044526  
7069  
7070 043642 012703 002732  
7071 043646 012704 044210  
7072 043652 012764 000010 000006  
7073 043660  
7074 043660 004737 016464  
7075 043664 103405  
7076 043666 010001  
7077 043670  
043670 104455  
043672 001750  
043674 003550  
043676 011506

CLR FATFLG ;CLEAR FATAL ERROR FLAG  
MOV #EPRT1,EPRTSW ;SET UP ERROR MESSAGE SWITCH  
CLR KTFLG ;HOLD OFF KT11  
MOV #TST10ID,R0 ;ASCII MESSAGE TO IDENTIFY TEST  
JSR PC,TSTSETUP ;DO INITIAL TEST SETUP  
MOV #2.,LOOPCNT ;PERFORM 2 ITERATIONS  
T10LOOP: JSR PC,T10REST ;SET PACKET TO START-UP VALUES  
MOV #TSTBLK+10.,R3 ;START OF TEST DATA  
MOV #T10PACKET,R4 ;GET THE ADDRESS OF COMMAND PACKET  
MOV #8.,PKBCNT(R4) ;START WITH MINIMUM ALLOWABLE VALUE  
SS: JSR PC,SOFINIT ;WRITE TO TSSR TO SOFT INITIALIZE  
BCS 10S ;BR IF SOFT INIT OKAY  
MOV R0,R1 ;SAVE CONTENTS OF TSSR  
ERRDF ERRNO,SFIERR,SFIMSG ;DEVICE FATAL DURING INIT

TRAP CSERDF  
.WORD 1000  
.WORD SFIERR  
.WORD SFIMSG

:Do WRITE CHARACTERISTICS command.

7078  
7079  
7080 043700 005037 002170  
7081 043704 010465 177776  
7082 043710 004737 017054  
7083 043714  
7084 043730 103407  
7085 043732 010001  
7086 043734  
7087 043734

10S: CLR FATFLG ;CLEAR FATAL ERROR FLAG  
MOV R4,TSDB(R5) ;SET THE PACKET ADDRESS TO EXECUTE  
JSR PC,CHKTSSR ;WAIT FOR SSR TO SET  
FORCERROR 12S ;ADD FORCE ERROR IF FCRCER=1  
BCS 15S ;BR IF CARRY SET (GOOD RETURN)  
MOV R0,R1 ;SAVE CONTENTS OF TSSR  
NEXT.ERRNO  
12S: ERRDF ERRNO,T10SSR,PKTSSR ;DEVICE FATAL SSR FAILED TO SET

TRAP CSERDF  
.WORD 1001  
.WORD T10SSR

043736 001751  
043738 011506

CZTUWAO TUBO FRONT END PRT A MACRO M1200 29-MAR-83 13:24 PAGE 104-1  
 TEST 10: INITIALIZE AFTER WRITE CHARACTERISTICS

7088	043742	011520							
7089	043744	004737	017720	158:	JSR PC,FATCHK	:INC AND CHECK FOR MORE THAN 25	.WORD	PKTSSR	
	043750				CKLOOP	:LOOP ON ERROR, IF FLAG SET		ERRORS	
	043750	104406						TRAP	C\$CLP1
7090	043752	016501	000000		MOV TSSW(R5),R1	:GET THE CONTENTS OF TSSR			
7091	043756	012702	000~90		MOV #SSR,R2	:EXPECTED CONTENTS OF TSSR			
7092	043762	032701	00C 30		BIT #OFL,R1	:IS OFF-LINE BIT SET ?			
7093	043766	001402			BEQ 258	:BRANCH IF NOT OFF-LINE			
7094	043770	052702	000100		BIS #OFL,R2	:SET OFF-LINE IN EXPECTED DATA			
7095									
7096									
7097	043774								
7098	043774								
7099	044010	020201			FORCERROR 278	:DOES EXPECTED MATCH RECEIVED ?			
7100	044012	001404			CMP R2,R1	:OKAY IF MATCH			
7101	044014				BEQ 308				
7102	044014				NEXT.ERRNO				
	044014	104456		278:	ERRHRD ERRNO,T10NBA,PKTSSR	:NBA NOT ZERO			
	044016	001752						TRAP	C\$ERHRD
	044020	044364						.WORD	1002
	044022	011520						.WORD	T10NBA
7103	044024							.WORD	PKTSSR
	044024	104406		308:	CKLOOP	:LOOP ON ERROR ?			
								TRAP	C\$CLP1
7104									
7105									
7106	044026								
7107	044026	004737	016464	408:	JSR PC,SOFINIT	:WRITE TO TSSR TO SOFT INITIALIZE			
7108	044032				FORCERROR 428				
7109	044046	103405			BCS 508	:BR IF SOFT INIT OKAY			
7110	044050	010001			MOV RO,R1	:SAVE CONTENTS OF TSSR			
7111	044052				NEXT.ERRNO				
7112	044052								
	044052	104455		428:	ERRDF ERRNO,SFIERR,SFIMSG	:DEVICE FATAL DURING INIT			
	044054	001753						TRAP	C\$ERDF
	044056	003550						.WORD	1003
	044060	011506						.WORD	SFIERR
								.WORD	SFIMSG
7113									
7114									
7115	044062	012704	000310	508:	MOV #310,R4	:START WITH LOC 310			
7116	044066	005002			CLR R2	:MEMORY EXPECTED SHOULD BE 000000			
7117	044070	004737	017054		JSR PC,CHKTSSR	:WAIT FOR SSR READY			
7118	044074	110465	177777	608:	MOVB R4,TSDBH(R5)	:SELECT RAM ADDRESS			
7119	044100	116501	177776		MOVB TSBAL(R5),R1	:READ LOC CONTENTS			
7120	044104				FORCERROR 628,NOTSSR				
7121	044114	120102			CMPB R1,R2	:CHECK MEMORY FOR 000000			
7122	044116	001406			BEQ 708	:BRANCH IF DATA OKAY			
7123	044120				NEXT.ERRNO				
7124	044120			628:	ERRDF ERRNO,T10MEM,RAEXP	:MEMORY NOT ZERO AFTER INIT.			
	044120	104455						TRAP	C\$ERDF
	044122	001754						.WORD	1004
	044124	044325						.WORD	T10MEM
	044126	016220						.WORD	RAEXP
7125	044130	004737	017720		JSR PC,FATCHK	:INC AND CHECK FOR MORE THAN 25		ERRORS	
7126	044134			708:	CKLOOP			TRAP	C\$CLP1
	044134	104406							
7127	044136				ESCAPE TST	:EXIT ON FATAL ERROR			
	044136	104410						TRAP	C\$ESCAPE

CZTUWAO TUBO FRONT END PRT A MACRO M1200 29-MAR-83 17:24 PAGE 104-2  
 TEST 10: INITIALIZE AFTER WRITE CHARACTERISTICS

```

      044140 000434                                .WORD L10075-.
7128
7129 044142 005204                                828:  INL  R4          ;LOOK AT NEXT RAM LOC.
7130 044144 020427 000377                        CMP  R4.#377         ;AT TOP OF RAM ADDRESS SPACE
7131 044150 001351                                BNE  608             ;BRANCH TILL ALL MEMORY TESTED
7132
7133
7134 044152 005737 002170                        TST  FATFLG         ;ANY FATAL ERRORS ?
7135 044156 001402                                BEQ  1608           ;BRANCH IF NOT
7136 044160 004737 017772                        JSR  PC,CKDROP      ;TRY TO DROP THE UNIT
7137 044164 004737 017174                        1608: JSR  PC,TSTLOOP ;DONE ALL ITERATIONS?
7138 044170 103002                                BCC  1658          ;BR IF YES
7139 044172 000137 043636                        JMP  T10LOOP        ;LOOP UNTIL ITERATION COUNT DONE
7140 044176
7141 044176                                EXIT  TST
      044176 104432                                TRAP C$EXIT
      044200 000374                                .WORD L10075-.

7142
7143
7144
7146 044202                                ;+
7148 044210                                ;LOCAL STORAGE FOR THIS TEST
7149 044210 100004                                ;-
7150 044212 044220                                .BLKB 10-<.-TUV2AB7>
7151 044214 000000                                T10PACKET:
7152 044216 000010                                .WORD 100004       ;COMMAND PACKET FOR TEST
7153
7154 044220                                .WORD T10DATA      ;WRITE CHARACTERISTICS COMMAND, WITH ACK
7155 044220 044232                                .WORD 0             ;ADDRESS OF CHARACTERISTICS BLOCK
7156 044222 000000                                .WORD 8             ;STARTING VALUE OF BLOCK SIZE
7157 044224 000016                                .WORD 8             ;CHARACTERISTICS DATA BLOCK
7158 044226 000000 000000                        T10DATA:           ;ADDRESS OF MESSAGE BUFFER
7159
7160 044232                                .WORD T10BFR       ;LENGTH OF MESSAGE BUFFER
7161
7162
7163
7164 044252 111 156 151 T10BFR: .BLKW 8.      ;MESSAGE BUFFER
7165 044325 111 156 143 ;LOCAL TEXT MESSAGES FOR TEST
7166
7167 044364 127 122 111 T10ID: .ASCIZ 'Initialization After WRITE CHARACTERISTICS'
7168 044437 103 157 136 T10MEM: .ASCIZ 'Incorrect RAM Data After Init'
7169
                                .EVEN
                                T10MBA: .ASCIZ 'WRITE CHARACTERISTICS Command Not Accepted'
                                T10SSR: .ASCIZ 'Contents of TSSR Incorrect After WRITE CHARACTERISTICS'

```



CZTUWAO TUBO FRONT END PRT A MACRO M1200 29-MAR-83 13:24 PAGE 105  
 TEST 10: INITIALIZE AFTER WRITE CHARACTERISTICS

```

7171
7172
7173
7174
7175
7176
7177
7178
7179
7180 044526
7181 044526
7182 044532 012701 044210
7183 044536 012721 100004
7184 044542 012721 044220
7185 044546 005021
7186 044550 012721 000010
7187 044554 012721 044232
7188 044560 005021
7189 044562 012721 000016
7190 044566 005021
7191 044570 005011
7192 044572 000207
7193 044574
    044574
    044574 104401

: +
: ROUTINE TO RESTORE COMMAND PACKET TO START-UP (DEFAULT) VALUES
: -
.EVEN
T10REST:
    SAVREG                ;SAVE THE REGISTERS
    MOV #T10PACKET,R1     ;START OF THE PACKET
    MOV #100004,(R1)+     ;WRITE CHARACTERISTICS WITH ACK
    MOV #T10DATA,(R1)+   ;ADDRESS OF CHAR DATA BLOCK
    CLR (R1)+             ;EXTENDED ADDRESS
    MOV #8,(R1)+         ;SIZE OF DATA BLOCK IN BYTES
    MOV #T10BFR,(R1)+    ;ADDRESS OF MESSAGE BUFFER
    CLR (R1)+
    MOV #14,(R1)+       ;LENGTH OF MESSAGE BUFFER
    CLR (R1)+
    RTS PC               ;RETURN
    ENDTST

L10075: TRAP CSETST
    
```





CZTUWAO TUBO FRONT END PRT A MACRO M1200 29-MAR-83 13:24 PAGE 106-2  
 TEST 11: BASIC WRITE SUBSYSTEM MEMORY COMMAND

```

7301 045474 000000      .WORD 0
7302 045476 000010      .WORD 8.          ;STARTING VALUE OF BLOCK SIZE
7303
7304
7305 045500      T11DTA:          ;SELECT DATA BLOCK
7306 045500 045066      .WORD T11BFR      ;ADDRESS OF MESSAGE BUFFER
7307 045502 000000      .WORD 0
7308 045504 000400      .WORD 256.        ;LENGTH OF MESSAGE BUFFER
7309 045506 000000 000000 .WORD 0,0
7310
7311
7312
7313      ;+
7314      ;LOCAL TEXT MESSAGES FOR TEST
7315      ;-
7316 045512      127      122      111 T11NBA: .ASCIZ 'WRITE SUBSYSTEM MEMORY Command Not Accepted'
7317 045566      105      170      160 T11NINT: .ASCIZ 'Expected Interrupt Not Received On WRITE SUBSYSTEM MEMORY'
7318 045660      102      141      163 TST11ID: .ASCIZ 'Basic WRITE SUBSYSTEM MEMORY Command'
7319
7320      .EVEN

```

CZTUWAO TUBO FRONT END PRT A MACRO M1200 29-MAR-83 13:24 PAGE 107  
 TEST 11: BASIC WRITE SUBSYSTEM MEMORY COMMAND

```

7322
7323
7324
7325
7326
7327
7328
7329
7330 045726
7331 045726
7332 045732 012701 045050
7333 045736 012721 100206
7334 045742 012721 045060
7335 045746 005021
7336 045750 012721 000006
7337 045754 005021
7338 045756 005021
7339 045760 005011
7340 045762 000207
7341
7342
7343 045764
7344 045764
7345 045770 012701 045470
7346 045774 012721 100204
7347 046000 012721 045500
7348 046004 005021
7349 046006 012721 000010
7350 046012 012721 045066
7351 046016 005021
7352 046020 012721 000400
7353 046024 005021
7354 046026 005011
7355 046030 005037 045066
7356 046034 000207
7357 046036
      046036
      046036 104401
  
```

```

: +
: ROUTINE TO RESTORE COMMAND PACKET TO START-UP (DEFAULT) VALUES
: WRITE SUBSYSTEM MEMORY COMMAND
: -
T11REST:
  SAVREG          ;SAVE THE REGISTERS
  MOV #T11PACKET,R1 ;START OF THE PACKET
  MOV #100206,(R1)+ ;WRITE SUBSYSTEM MEM. WITH ACK, IE
  MOV #T11DATA,(R1)+ ;ADDRESS OF DATA BLOCK
  CLR (R1)+        ;EXTENDED ADDRESS
  MOV #6.,(R1)+    ;SIZE OF DATA BLOCK IN BYTES
  CLR (R1)+        ;CLEAR BSEL0 AND BSEL1
  CLR (R1)+        ;CLEAR SEL2
  CLR (R1)         ;CLEAR DATA AREA
  RTS PC          ;RETURN
T11RST:
  SAVREG          ;SAVE THE REGISTERS
  MOV #T11PK2,R1  ;START OF THE PACKET
  MOV #100204,(R1)+ ;WRITE CHARA. WITH ACK, IE
  MOV #T11DTA,(R1)+ ;ADDRESS OF CHARAISTICS DATA BLOCK
  CLR (R1)+        ;EXTENDED ADDRESS
  MOV #8.,(R1)+    ;SIZE OF DATA BLOCK IN BYTES
  MOV #T11BFR,(R1)+ ;MESSAGE BUFFER ADDRESS
  CLR (R1)+        ;LENGTH OF MESSAGE BUFFER
  CLR (R1)         ;CLEAR 1ST LOC IN MESSAGE BUFFER
  RTS PC          ;RETURN
  ENDTST
L10076:
      TRAP      CSETST
  
```

CZTUWAO TUBO FRONT END PRT A  
DISPLAY BREAKPOINT SETTINGS

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8432  
8437  
8443  
8444  
8445  
8446  
8447  
8448  
8449  
8450  
8451  
8452  
8453  
8454  
8455

.SBTTL HARDWARE PARAMETER CODING SECTION

:+  
: THE HARDWARE PARAMETER CODING SECTION CONTAINS MACROS  
: THAT ARE USED BY THE SUPERVISOR TO BUILD P-TABLES. THE  
: MACROS ARE NOT EXECUTED AS MACHINE INSTRUCTIONS BUT ARE  
: INTERPRETED BY THE SUPERVISOR AS DATA STRUCTURES. THE  
: MACROS ALLOW THE SUPERVISOR TO ESTABLISH COMMUNICATIONS  
: WITH THE OPERATOR.  
:--

8456 052636  
052636 000015  
052640

BGNHRD  
.WORD L10100-LSHARD/2  
LSHARD::

8457  
8458 052640  
052640 000031  
052642 052672  
052644 160000  
052646 177776

GPRMA HPM1,0,0,160000,177776,YES :GET TSBA/TSDB REGISTER ADDRESS.  
.WORD TSCODE  
.WORD HPM1  
.WORD TSLOLIM  
.WORD TSHILIM

8459 052650  
052650 001031  
052652 052721  
052654 000000  
052656 000776

GPRMA HPM2,2,0,0,776,YES :GET VECTOR ADDRESS.  
.WORD TSCODE  
.WORD HPM2  
.WORD TSLOLIM  
.WORD TSHILIM

8460 052660  
052660 002032  
052662 052745  
052664 000340  
052666 000000  
052670 000007

GPRMD HPM3,4,0,340,0,7,YES :GET INTERRUPT PRIORITY.  
.WORD TSCODE  
.WORD HPM3  
.WORD 340  
.WORD TSLOLIM  
.WORD TSHILIM

8461 052672

ENDHRD  
.EVEN

052672  
8462 052672 104 105 126  
8463 052721 111 116 124  
8464 052745 111 116 124

L10100:  
HPM1: .ASCIZ 'DEVICE ADDRESS (TSSR) '  
HPM2: .ASCIZ 'INTERRUPT VECTOR '  
HPM3: .ASCIZ 'INTERRUPT PRIORITY '  
.EVEN

8465  
8466

CZTUWAO TUBO FRONT END PRT A MACRO M1200 29-MAR-83 13:24 PAGE 132  
SOFTWARE PARAMETER CODING SECTION

8468  
8469  
8470  
8471  
8472  
8473  
8474  
8475  
8476  
8477  
8478 052776  
052776 000006  
053000  
8479 053000  
053000 000130  
053002 053014  
053004 177777  
8480 053006  
053006 001130  
053010 053053  
053012 177777  
8481  
8482  
8483 053014  
  
053014  
8484  
8485  
8486 053014 105 116 101  
8487 053053 111 116 110  
8488 053103 120 105 122  
8489 053133 120 105 122  
8490  
8491  
8492  
8493  
8494  
8495  
8496  
8497  
8498 053164  
053164 000013  
053166  
053166 023464  
053170 023722  
053172 024674  
053174 026154  
053176 030206  
053200 031416  
053202 033472  
053204 036704  
053206 040422  
053210 043602  
053212 044576

.SBTTL SOFTWARE PARAMETER CODING SECTION

:+  
: THE SOFTWARE PARAMETER CODING SECTION CONTAINS MACROS  
: THAT ARE USED BY THE SUPERVISOR TO BUILD P-TABLES. THE  
: MACROS ARE NOT EXECUTED AS MACHINE INSTRUCTIONS BUT ARE  
: INTERPRETED BY THE SUPERVISOR AS DATA STRUCTURES. THE  
: MACROS ALLOW THE SUPERVISOR TO ESTABLISH COMMUNICATIONS  
: WITH THE OPERATOR.  
:--

BGNSFT  
.WORD L10101-LSSOFT/2  
LSSOFT::  
GPRML SPM1,0,-1,YES ;GET RAM DUMP TEST FLAG  
.WORD TSCODE  
.WORD SPM1  
.WORD -1  
GPRML SPM4,2,-1,YES ; GET ITERATION CONTROL.  
.WORD TSCODE  
.WORD SPM4  
.WORD -1  
GPRMD SPM6,4,D,7777,0,7777,YES ; GET LOCAL ERROR LIMIT  
GPRMD SPM7,6,D,7777,0,7777,YES ; GET GLOBAL ERROR LIMIT  
ENDSFT  
.EVEN

L10101:

SPM1: .ASCIZ 'ENABLE M7454 RAM DUMP ON ERROR'  
SPM4: .ASCIZ 'INHIBIT ITERATIONS'  
SPM6: .ASCIZ 'PER TEST ERROR LIMIT'  
SPM7: .ASCIZ 'PER UNIT ERROR LIMIT'  
.EVEN  
.SBTTL PATCH AREA

:+  
:DISPATCH TABLE  
: \*\*\* MOVE TO FRONT OF PROGRAM FOR RELEASE \*\*\*  
:--

DISPATCH TESTNO  
.WORD 11  
LSDISPATCH::  
.WORD T1  
.WORD T2  
.WORD T3  
.WORD T4  
.WORD T5  
.WORD T6  
.WORD T7  
.WORD T8  
.WORD T9  
.WORD T10  
.WORD T11

8499  
8500  
8501  
: FINALLY A GENEROUS PATCH AREA.

8502  
 8503  
 8504  
 8505  
 8506  
 8507 053214  
 8508  
 8509  
 8510  
 8511 053214  
  
 053214 053232  
 053216 000005  
 053220  
 8512  
 8513  
 8514  
 8515  
 8516 053220  
 8517 053220  
 053220 000000  
 053222 000003  
 053224  
 8518 053224 172522  
 8519 053226 000224  
 8520 053230 000240  
 8521 053232  
 053232  
 8522 053232  
 8523  
 8524 000001

```

: AND AN ADJUSTMENT TO ACCOUNT FOR THE 'LASTAD BIT7' HACK
: DESCRIBED IN 'SUPPRG.MEM' (FOR REV C).
:
PATCH::
: .IF NZ..8377
:   =.!377+1
: .ENDC
LASTAD ;SET LAST USED ADDRESS.
.EVEN
.WORD T$FREE
.WORD T$SIZE
L$LAST::
.SBTTL HARD CODED P-TABLE
:++
: DIAGNOSTIC IS PRE-PARAMETERIZED PER THIS TABLE
:--
BGNSETUP 1
BGNPTAB
.WORD 0
.WORD L10104-./2-1
L10102:
.WORD 172522
.WORD 224
.WORD PRI05
ENDPTAB
L10104:
ENDSETUP
.END

```



CZTUWAO 1U80 FRONT END PRT A MACRO M1200 29-MAR-83 13:24 PAGE 132-2  
SYMBOL TABLE

ADDSSR 011612 G	CSAU = 000052	DEBUGM 011304	FATERR= 000060	HIADDR= 001400
ADR = 000020 G	CSAUTO= 000061	DEVCNT 002166 G	FATFLG 002170 G	HIMEM = 007776
AMBTSS 006151	CSBRK = 000022	DEVDR0 023414	FERCM 011374	HOE = 100000 G
ASSEMB= 000010	CSBSEG= 000004	DEVNRD 023333	FIFEXP 011642 G	HPM1 052672
A1716 = 000003	CSBSUB= 000002	DEVNXR 023251	FIF1MS 011714	HPM2 052721
RADDAT 003110 G	CSCEFG= 000045	DEVONL 023201	FIF2MS 011763	HPM3 052745
RADSSR 016370 G	CSCLCK= 000062	DEVSUM 023144	FILLME 020212	IBE = 010000 G
BAR = 174402	CSCLEA= 000012	DFPTBL 002124 G	FNOINT 004113	IDU = 000040 G
BENBSW 002174 G	CSCLOS= 000035	DIAGMC= 000000	FORCER 002144 G	IER = 020000 G
BIE = 040000	CSCLP1= 000006	DICEA = 000001	FREE 003072 G	IFault 004154
BIT0 = 000001 G	CSCVEC= 000036	DLCYL = 000177	FREEHI 003076	INCERK 017562
BIT00 = 000001 G	CSDCLM= 000044	DLNER= 100200	FRESIZ 003074 G	INTCPC 016640
BIT01 = 000002 G	CSDODU= 000051	DLERR = 177730	FUSI 004015	INTFLA 016635
BIT02 = 000004 G	CSDRPT= 000024	DLGETS= 000004	FSAU = 000015	INTMAS 016634
BIT03 = 000010 G	CSDU = 000053	DLRDHD= 000010	FSAUTO= 000020	INTR 016706 G
BIT04 = 000020 G	CSEDIT= 000003	DLRDNH= 000016	FSBGN = 000040	INTREC 002172 G
BIT05 = 000040 G	CSEDF= 000055	DLSR = 000013	FSCLEA= 000007	INTVEC 016636
BIT06 = 000100 G	CSEHR= 000056	DLUN = 000006	FSDU = 000016	INTX 004176
BIT07 = 000200 G	CSEHR= 000056	DSBINT 016674	FSEND = 000041	IOKCKI= 000200
BIT08 = 000400 G	CSEHR= 000056	DUAD12 004541	FSHARD= 000004	IOKSTP= 000001
BIT09 = 001000 G	CSEHR= 000056	DUFLG 003060 G	FSHW = 000013	IPRI 002160 G
BIT1 = 000002 G	CSEHR= 000056	DUMMY 003030	FSINIT= 000006	ISR = 000100 G
BIT10 = 002000 G	CSEHR= 000056	EF.CON= 000036 G	FSJMP = 000050	IVEC 002156 G
BIT11 = 004000 G	CSEHR= 000056	EF.NEW= 000035 G	FSMOD = 000000	IXE = 004000 G
BIT12 = 010000 G	CSEHR= 000056	EF.PWR= 000034 G	FMSG = 000011	ISAU = 000041
BIT13 = 020000 G	CSEHR= 000056	EF.RES= 000037 G	FSPROT= 000021	ISAUTO= 000041
BIT14 = 040000 G	CSEHR= 000056	EF.STA= 000040 G	FSPWR = 000017	ISCLN = 000041
BIT15 = 100000 G	CSEHR= 000056	EMAXDU 017515	FSRPT = 000012	ISDU = 000041
BIT2 = 000004 G	CSEHR= 000056	EN = 000000	FSSEG = 000003	ISHRD = 000041
BIT3 = 000010 G	CSEHR= 000056	ENAIN 016642	FSSOFT= 000005	ISINIT= 000041
BIT4 = 000020 G	CSEHR= 000056	ENVIRN 021352	FSSRV = 000010	ISMOD = 000040
BIT5 = 000040 G	CSEHR= 000056	EPRTSW 002146 G	FSSUB = 000002	ISMSG = 000041
BIT6 = 000100 G	CSEHR= 000056	EPRT1 005672	FSSW = 000014	ISPROT= 000040
BIT7 = 000200 G	CSEHR= 000056	EPRT2 005672	FSTEST= 000001	ISPTAB= 000041
BIT8 = 000400 G	CSEHR= 000056	EPRT3 005672	GDDAT 003112 G	ISPWR = 000041
BIT9 = 001000 G	CSEHR= 000056	ERRCM 011405	GERRMA 002142 G	ISRPT = 000041
BOE = 000400 G	CSEHR= 000056	ERRHI 002202 G	GETPAT 020716 G	ISSEG = 000041
BRINIT 004355	CSEHR= 000056	ERRK 017474	GETSEL 021000 G	ISSETU= 000041
BSELO = 000000	CSEHR= 000056	ERRLO 002204 G	GSCNT0= 000200	ISSFT = 000041
BSEL1 = 000001	CSEHR= 000056	ERRNO = 002120	GSDELM= 000372	ISSRV = 000041
CHKAMB 016534	CSEHR= 000056	ERRVEC= 000004 G	GSDISP= 000003	ISSUB = 000041
CHKMAN 021222 G	CSEHR= 000056	ERTABE 003330	GSEXCP= 000400	ISTST = 000041
CHKTSS 017054	CSEHR= 000056	ERTABL 003130	GSHILI= 000002	JSJMP = 000167
CKDROP 017772	CSEHR= 000056	ESUM 017476	GSLOLI= 000001	KIPAR0= 172340
CKEMAX 017620	CSEHR= 000056	EVL = 000004 G	GSNO = 000000	KIPAR1= 172342
CKMSG 011032 G	CSEHR= 000056	EXBCNT= 000010	GSOFFS= 000400	KIPAR2= 172344
CKMSG2 011152 G	CSEHR= 000056	EXPBRE 016172 G	GSOF SI= 000376	KIPAR3= 172346
CKRAM 010354 G	CSEHR= 000056	EXPD 002176 G	GSPRMA= 000001	KIPAR4= 172350
CKRAM2 010730 G	CSEHR= 000056	EXPGOT 004431	GSPRMD= 000002	KIPAR5= 172352
CMEM 020376	CSEHR= 000056	EXPGT2 004465	GSPRML= 000000	KIPAR6= 172354
CONFIG 020040	CSEHR= 000056	EXMSG 002266 G	GSRADA= 000140	KIPAR7= 172356
COUNT 002254 G	CSEHR= 000056	EXPREC 016164 G	GSRADB= 000000	KIPDR0= 172300
CSR = 174400	CSEHR= 000056	EXTA 005232	GSRADD= 000040	KIPDR1= 172302
CSRADD 002154 G	CSEHR= 000056	EXTEND 005230	GSRADL= 000120	KIPDR2= 172304
CTAB 003116 G	CSEHR= 000056	ESEND = 002100	GSRADO= 000020	KIPDR3= 172306
CTABE 003130 G	CSEHR= 000056	ESLOAD= 000035	GSXFER= 000000	KIPDR4= 172310
CTABM 003114 G	CSEHR= 000056	FATCHK 017720	GSYES = 000010	KIPDR5= 172312

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SYMBOL TABLE

KIPDR6=	172314	LSREV	002010	G	L10057	032114	MULCR	004426	O.GET	051630
KIPDR7=	172316	LSRPT	022702	G	L10060	032302	NXM	= 004000	O.GO	047660
KTENAB	003102	LSSOFT	053000	G	L10061	036702	NXR	003636	O.GO1	047736
KTFLG	003100	LSSPC	002056	G	L10062	034110	NXRERR	005176	O.GO2	047742
KTINIT	021440	LSSPCP	002020	G	L10063	034502	NXRX	003675	O.HIGH	052540
KTOFF	020064	LSSPTP	002024	G	L10064	035372	NXTU	022044	O.LG	= 000010
KTON	020046	LSSTA	002030	G	L10065	035620	OFL	= 000100	O.LGCH	052133
LERRMA	002140	LSSW	002134	G	L10066	040420	ONEFIL	= 000000	O.LGDR	046530
LERRNO=	000000	LSTEST	002114	G	L10067	037166	OSAPTS	= 000000	O.LOW	052536
LISTAL=	000001	LSTIML	002014	G	L10070	037424	OSAU	= 000001	O.MOVE	050242
LOE	= 040000	LSUNIT	002012	G	L10071	043600	OSBGNR	= 000001	O.MSK	052534
LOOPCN	002164	L10000	002132		L10072	041012	OSBGNS	= 000001	O.ODT	046040
LCJPCO	012600	L10001	002144		L10073	041312	OSDU	= 000001	O.OFST	047360
LOOPFL	003114	L10002	005226		L10074	041620	OSERRT	= 000000	O.OLD	046756
LOT	= 000010	L10003	011516		L10075	044574	OSGNSW	= 000001	O.OP1	046762
LSACP	002110	L10004	011546		L10076	046036	OSPOIN	= 000001	O.OP2	047026
LSAPT	002036	L10005	011564		L10077	045022	OSSETU	= 000001	O.OP2A	047034
LSAU	022400	L10006	011572		L10100	052672	O.ADR1	052550	O.ORAB	046266
LSAUT	002070	L10007	011610		L10101	053014	O.ALL	051134	O.ORPC	046244
LSAUTO	022604	L10010	011626		L10102	053224	O.AS	046630	O.ORRB	046276
LSCCP	002106	L10011	011640		L10104	053232	O.ASC	052117	O.P	052113
LSCLEA	022660	L10012	011712		MEMADD	013426	O.ASCI	050144	O.PCS	046256
LSCO	002032	L10013	012062		MENASC	021171	O.BACK	047114	O.PRNT	050402
LSEDEPO	002011	L10014	012576		MENERR	021116	O.BALL	051020	O.PROC	047760
LSDESC	003342	L10015	013424		MENRES	021220	O.BD	052120	O.PROM	052126
LSDESCP	002076	L10016	013446		MESBFA	002716	O.BKP	= 000016	O.RALL	047304
LSDEVP	002060	L10017	016170		MESBFM	014453	O.BKPT	047142	O.RCSR	= 177560
LSDISP	053166	L10020	016176		MESHEA	014636	O.BRK	050450	O.RDB	= 177562
LSDLY	002116	L10021	016204		MIVEC	= 000250	O.BW	052100	O.REG	052032
LSDTP	002040	L10022	016216		MPR	= 174406	O.BYT	046666	O.REGT	046156
LSDTYP	002034	L10023	016240		MSA.FR	= 000006	O.BYT1	046660	O.REM	051304
LSDU	022476	L10024	016266		MSA.NO	= 000000	O.CAD	052102	O.RSB	051240
LSDUT	002072	L10025	016426		MSA.NR	= 000004	O.CADV	051446	O.RSR	051210
LSDVTY	003334	L10026	016736		MSA.VO	= 000002	O.CLGT	= 000035	O.RSTT	051400
LSEF	002052	L10030	022330		MSGEXP	011630	O.CLSE	051744	O.S	052111
LSENV1	002044	L10031	022474		MSGLOO	012536	O.COMP	050304	O.SCAN	046422
LSETP	002102	L10032	022602		MSGSTA	012022	O.CR	052123	O.SEMI	046622
LSEXP1	002046	L10033	022656		MSGSUB	013414	O.CRET	046750	O.SEQ	052116
LSEXP4	002064	L10034	022700		MS.ATT	= 000006	O.CRLF	051776	O.SNGL	046346
LSEXP5	002066	L10035	023142		MS.EXT	= 000200	O.CRLS	052012	O.SPAC	051732
LSHARD	052640	L10036	023720		MS.RSD	= 000001	O.CSR1	052114	O.STM	= 000340
LSHIME	002120	L10037	023566		MS.RSF	= 000020	O.CSR2	052115	O.SVR	051150
LSMPCP	002016	L10040	023650		MS.RST	= 000010	O.CT	052572	O.SVTT	051352
LSMPTP	002022	L10041	024672		NBA	= 002000	O.C1	050032	O.SWCH	052542
LSMW	002124	L10042	024112		NEWPAS	022032	O.DCD	046376	O.T	052112
LSICP	002104	L10043	024304		NODEV	003062	O.DCDA	046754	O.TBIT	047710
LSINIT	021602	L10044	024504		NOINIT	004233	O.DCDB	047302	O.TBT	= 000020
LSLADP	002026	L10045	026152		NOINTR	004117	O.DCD1	046416	O.TCLS	046320
LSLAST	053220	L10046	025234		NOITS	002136	O.DCD2	046412	O.TCSR	= 177564
LSLOAD	002100	L10047	025554		NOMAN	021256	O.DOT	052104	O.TDB	= 177566
LSLUN	002074	L10050	030204		NP.IR	= 000200	O.DUMP	050064	O.TL	052170
LSREV	002050	L10051	026476		NP.LOO	= 000040	O.EFF	047470	O.TRTC	052200
LSNAME	002000	L10052	026762		NP.OUT	= 000100	O.ERR	046366	O.TVEC	= 000014
LSPRIO	002042	L10053	027210		NP.WRP	= 000020	O.ERR1	047464	O.TYPE	051716
LSPROT	021572	L10054	031414		NSI	004050	O.FCHR	052544	O.UIN	052614
LSPRT	002112	L10055	033470		NSINIT	004305	O.FCNT	052546	O.UPC	052530
							O.FYD	051542	O.UPS	052532

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SYMBOL TABLE

O.URO	052512	PRMSG2	015525	RECMSG	002432	G	S1.IEO=	010000	TST10I	044252		
O.USP	052526	PROASC	014533	RECV	002200	G	S1.IFM=	001000	TST11I	045660		
O.WB1	046674	PR1ASC	014600	REGSAV	020656		S1.IHE=	000400	TST2ID	024666		
O.WDFG	052110	PST32W	003104	REWIND	010254	G	S1.IID=	004000	TST3ID	026133		
O.WRD	046644	PUNIT	022332	RMCHBE=	000167		S1.IIR=	020000	TST4ID	030107		
O.WRD1	046710	PW.D11=	000021	RMCHEN=	000200		S1.IZR=	040000	TST5ID	031377		
O.WSCH	047474	PW.D13=	000022	RMMSGB=	000104		S1.PAR=	100000	TST6ID	033377		
O.XXX	052106	PW.D22=	000020	RMPKTB=	000117		S2.ATI=	000010	TST7ID	036527		
PASRPT	022076	PW.NOP=	000000	RMPKTE=	000020		S2.BTI=	000004	TST8ID	040262		
PATCH	053214	PW.NO1=	000023	RMR	=	010000	S2.DIM=	000200	TST9ID	042070		
PATDAT	020752	PW.RDE=	000024	RUPACK	010350		S2.ILW=	000100	TTIBFR=	177562	G	
PC.ERA=	002400	PW.RDR=	000001	SC	=	100000	S2.INR=	000020	TTICSR=	177560	G	
PC.IER=	002000	PW.RDS=	000005	SCE	=	020000	S2.OUT=	000040	TTIVEC=	000060	G	
PC.MOO=	001000	PW.RFI=	000003	SCME	004711		S2.UND=	000003	TTOBFR=	177566		
PC.REL=	000000	PW.WCT=	000006	SDELAY	010150		TBLEND=	003030	TTOCSR=	177564		
PC.REW=	000400	PW.WFI=	000004	SEEK	=	000006	TCOASC	006012	TUV2A	002000	G	
PKBCNT=	000006	PW.WFM=	000007	SELASC	021164		TCOCOD	006212	TSARGC=	000003		
PKHI	=	PW.WMI=	000010	SELDAT=	000004		TEMP1	003064	TSCODE=	001130		
PKLOW	=	PW.WNP=	000011	SEL2	=	000002	TEMP2	003066	TSERRN=	002120		
PKTADD	007110	PW.WTR=	000002	SETMAP	020106		TERCLS=	000016	TSEXCP=	000000		
PKTFRM	007052	P.ACK	=	SETU	022130		TESTNO=	000013	TSFLAG=	000040		
PKTGET	011550	P.CMD	=	SFFMSG	011566	G	TEXASC	005751	TSFREE=	053232		
PKTMES	011574	P.CONT=	000012	SFHERR	003603		TFCASC	006053	TSGMAN=	000000		
PKTNEW	007145	P.CVC	=	SFIERR	003550		TIMEXP	016242	TSHILI=	000007		
PKTRAM	004643	P.FMT	=	SFIMSG	011506	G	TIMSGO	016270	TSLAST=	000001		
PKTSSR	011520	P.FORM=	000011	SFPTBL	002134	G	TINERR	011473	TSLOLI=	000000		
PNT	=	P.GETS=	000017	SIFLAG	003106	G	TKB	=	177562	TLSYM=	010000	
PRAMPK	013450	P.IE	=	SIMSG	011440		TKS	=	177560	TSLTNO=	000013	
PRBEXP	016160	P.INIT=	000013	SKIPT	003332		TMPBFR	002576	TSNEST=	000000		
PRBMSG	016026	P.MODE=	007400	SOFINI	016464	G	TNAM	017422	TSNSO	=	000000	
PRBREC	016162	P.OPP	=	SPACE	007752	G	TPB	=	177566	TSNS1	=	000005
PRBTOT	016113	P.POSI=	000010	SPM1	053014		TPS	=	177564	TSNS2	=	000002
PRBYTE	015612	P.READ=	000001	SPM4	053053		TRANST	002134	TSNS3	=	000003	
PRI	=	P.SWB	=	SPM6	053103		TSBA	=	177776	TSPCNT=	000000	
PRIADD	007524	P.WRIT=	000005	SPM7	053133		TSBAH	=	177777	TSPTAB=	010103	
PRIAO	007574	P.WRTC=	000004	SRO	=	177572	TSBAL	=	177776	TSPTHV=	000001	
PRIBXO	007156	P.WRTS=	000006	SR1	=	177574	TSBAM2	024524	TSPTNU=	000001		
PRIEQU	007424	QVP	002152	SR2	=	177576	TSBAM3	024606	TSSAVL=	177777		
PRIPKT	006704	RAMASC	013616	SR3	=	172516	TSDB	=	177776	TSSEGL=	177777	
PRIRAM	007432	RAMDAT	002206	SSR	=	000200	TSDBH	=	177777	TSSEK0=	010000	
PRITAD	007640	RAMER	010456	STATCO	012064		TSDBL	=	177776	TSSIZE=	000005	
PRITSS	005264	RAMERR	016200	SVCGBL=	000000		TSFCOD	006552	TSSUBN=	000001		
PRITO	007710	RAMEXP	016220	SVCINS=	000000		TSREJ	=	000006	TSTAGL=	177777	
PRIXOR	007306	RAMFHR	014362	SVCSUB=	000001		TSSDEF	006122	TSTAGN=	010105		
PRI00	=	RAMFOR	007462	SVCTAG=	000000		TSSR	=	000000	TSTEMP=	000014	
PRI01	=	RAMHLD	010640	SVCTST=	000001		TSSRBI	003400	TSTEST=	000013		
PRI02	=	RAMIOP	010644	SLSYM=	010000		TSSRFO	005731	TSTSTM=	177777		
PRI03	=	RAMPD	010715	SO.IDB=	000010		TSSRH	=	000001	TSTSTS=	000001	
PRI04	=	RAMRSH	010642	SO.IFB=	000002		TSSX	003716	TSSAU	=	010031	
PRI05	=	RAMSIZ	002246	SO.IFP=	000001		TSTBLK	002720	TSSAUT=	010033		
PRI06	=	RAMTAD	016206	SO.ILD=	000020		TSTCNT	002162	TSSCLE=	010034		
PRI07	=	RBPCRA	014750	SO.ION=	000040		TSTEND	017436	TSSDAT=	010104		
PRMESS	013702	RCVHIA	002250	SO.IRW=	000004		TSTFLA	002260	TSSDU	=	010032	
PRMND	002264	RCVLQA	002252	SO.IRD=	000100		TSTLOO	017174	TSSHAR=	010100		
PRMSG2	015242	RDERR	005104	SO.ISP=	000200		TSTPTR	002262	TSSHW	=	010000	
PRMSGO	015422	READ	=	SI.ICF=	002000		TSTSET	017226	TSSINI=	010030		
PRMSG1	015447	READY	=				TST1ID	023700	TSSMSG=	010025		

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SYMBOL TABLE

TSSPC = 000001	T3.2 025250	T7BUFR 035714	T9LOAD 041724	XSOCON 015015
TSSPRO= 010027	T4 026154 G	T7DATA 035640	T9LOOP 040456	XSOEOT= 000001
TSSPTA= 010103	T4BFR 027264	T7DTA 035702	T9MSG8 042261	XSOIE = 000040
TSSRPT= 010035	T4DATA 027250	T7INT 036440	T9NINT 042445	XSOILA= 000400
TSSSEG= 010000	T4INT 027735	T7LOOP 033526	T9NXM 042627	XSOILC= 001000
TSSSOF= 010101	T4LOOP 026210	T7MBF 035734	T9PACK 041660	XSOLET= 020000
TSSSRV= 010026	T4NBA 027376	T7NBA 036031	T9PAR6 041726	XSOMOT= 000200
TSSSUB= U10077	T4PACK 027240	T7NINT 036347	T9SETG 043474	XSONEF= 002000
TSSSW = 010001	T4REST 030136	T7NNBA 036113	T9SWRT 043426	XSOONL= 000100
TSSTES= 010076	T4SP 027260	T7PACK 035630	T9TBE 042066	XSOPED= 000010
T1 023464 G	T4SSR 027646	T7PKT 035672	T9WRTS 042172	XSORLL= 010000
T1LOOP 023520	T4TSBA 030024	T7RST 036556	T9.1 040456	XSORLS= 040000
T1.1 023522	T4.1 026210	T7RT2 036630	T9.2 041026	XSOTMK= 100000
T1.2 023602	T4.2 026512	T7SSR 036260	T9.3 041326	XSOVCK= 000020
T10 043602 G	T4.3 026764	T7SSRM 036170	T91LOO 040600	XSOWLE= 004000
T10BFR 044232	T42DAT 027304	T7.1 033526	T92LOO 041144	XSCWLK= 000004
T10DAT 044220	T42DON= 027320	T7.2 034112	T93LOO 041374	XS1CON 015062
T10LOO 043636	T42NBA 027320	T7.3 034504	T94TST 041732	XS2CON 015127
T10MEM 044325	T42REJ 027451	T7.4 035374	UAM = 000200 G	XS3CON 015174
T10NBA 044364	T44REJ 027550	T8 036704 G	UNITN 002150 G	XXCOMM 003070 G
T10PAC 044210	T5 030206 G	T8BFR 037462	UNREC = 000006	XSALWA= 000000
T10RES 044526	T5BFR 030732	T8BF2 037532	USI 004021	XSALS= 000040
T10SSR 044437	T5DATA 030720	T8DATA 037450	WAITF 016740 G	XSOFFS= 000400
T11 044576 G	T5LOOP 030242	T8DTA 037520	WC.IFA= 000200	XSTRUE= 000020
T11BFR 045066	T5MSG 031300	T8LOOP 036740	WC.IFE= 000002	X1.COR= 020000
T11BS0 045060	T5NVCK 031121	T8NBA 037552	WC.IGO= 000001	X1.DLT= 100000
T11BS1 045061	T5PACK 030710	T8NINT 040134	WC.IRE= 000010	X1.MBZ= 017375
T11BS2 045062	T5SSR 031211	T8PACK 037440	WC.IRW= 000004	X1.RBP= 000400
T11DAT 045060	T5VCK 030752	T8PK2 037510	WC.IOT= 000100	X1.SPA= 040000
T11DTA 045500	T5VCK2 031045	T8REST 040314	WC.IIT= 000040	X1.UNC= 000002
T11LOO 044632	T6 031416 G	T8RT2 040356	WC.I5R= 000020	X2.BUF= 000100
T11NBA 045512	T6BFR 032342	T8SR2 040060	WF.IED= 000010	X2.EXT= 000200
T11NIN 045566	T6DATA 032330	T8SSR 040004	WF.IER= 000004	X2.OPM= 100000
T11PAC 045050	T6INT 033217	T8TSBA 040212	WF.INI= 000200	X2.RCE= 040000
T11PK2 045470	T6LOOP 031452	T8.1 036740	WF.IRE= 000040	X2.REV= 000077
T11RES 045726	T6NBA 032376	T8.2 037202	WF.IWF= 000020	X2.SPA= 035400
T11RST 045764	T6NINT 033126	T82REJ 037612	WF.IWR= 000100	X2.UNI= 000007
T11.1 044632	T6PACK 032320	T83REJ 037673	WF.I3R= 000002	X2.WCF= 002000
T2 023722 G	T6REST 033416	T84REJ 037723	WF.I4R= 000001	X3.DCK= 000010
T2LOOP 023760	T6SSR 033037	T9 040422 G	WRTCHR 010152 G	X3.MBZ= 000006
T2.1 023740	T6TSBA 033306	T9BFR 041702	WRERR 005011	X3.MDE= 177400
T2.2 024114	T6.1 031452	T9BKGN 042357	WRTMSG 004754	X3.OPI= 000100
T2.3 024306	T6.2 031704	T9BLK 041734	XFERAS 016430	X3.REV= 000040
T3 024674 G	T6.3 032116	T9CHAR 043536	XNXM 017114	X3.RIB= 000001
T3INT 025723	T62DAT 032362	T9CKRA 043316 G	XORBFO 007240	X3.SPA= 000200
T3LOOP 024730	T62DON= 032376	T9CONV 042736	XORFOR 007356	X3.TRF= 000020
T3NBA 025620	T62REJ 032451	T9CT2 043130	XST0 = 000006 G	X4.HSP= 100000
T3NINT 026001	T63REJ 032550	T9DATA 041670	XST1 = 000010 G	X4.MBZ= 017400
T3PACK 025610	T64REJ 032643	T9DPR 042536	XST2 = 000012 G	X4.RCE= 040000
T3SSR 025645	T65REJ 032741	T9GETS 042116	XST3 = 000014 G	X4.TSM= 020000
T3TSBA 026061	T7 033472 G	T9HIAD 041722	XST4 = 000016 G	X4.WRC= 000377
T3.1 024730	T7BFR 035652	T9KT 041730	XSOBOT= 000002	

. ABS. 053232 000  
000000 001  
ERRORS DETECTED: 0

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SYMBOL TABLE

VIRTUAL MEMORY USED: 36168 WORDS ( 142 PAGES)

DYNAMIC MEMORY: 20060 WORDS ( 77 PAGES)

ELAPSED TIME: 00:08:01

CZTUWA.BIC,CZTUWA/-SP=SVC.MLB/ML,CZTUWA