

DEUNA

REPAIR DIAG
CZUAAA0

AH-T364A-MC
FICHE 1 OF 3

MAY 1983
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The main body of the document is a large, dense grid of technical data. Each cell in the grid contains a small, structured table or diagram, likely representing a component's specifications or a diagnostic procedure. The text within these cells is extremely small and difficult to read, but the overall layout is organized into a regular grid pattern. The grid covers most of the page area below the header and above the footer.

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Technical data tables, including component specifications and wiring diagrams, visible on the left side of the page. The text is faint and difficult to read.

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IDENTIFICATION

PRODUCT CODE: AC-T363A-MC
PRODUCT NAME: CZUAAAD REPAIR DIAG
PRODUCT DATE: JANUARY 1983
MAINTAINER: DISTRIBUTED SYSTEMS DIAGNOSTIC ENGINEERING
AUTHOR: MICHAEL CINNAMON

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1.0 GENERAL INFORMATION
1.1 PROGRAM ABSTRACT

THIS PRODUCT IS THE PDP-11 REPAIR LEVEL DIAGNOSTIC FOR THE UNIBUS TO NI ADAPTER (DEUNA). THIS DIAGNOSTIC WAS DESIGNED TO DETECT STATIC AND DYNAMIC HARDWARE FAILURES IN THE DEUNA BOARDSET. THE DEUNA BOARDSET IS THE TWO MODULES WHICH PLUG INTO THE PDP-11 UNIBUS. THE TWO MODULES ARE THE M7792 PORT MODULE AND THE M7793 LINK MODULE. THIS DIAGNOSTIC IS CAPABLE OF TESTING EIGHT SUCH BOARDSETS ON A SINGLE PDP-11 UNIBUS.

THIS DIAGNOSTIC WILL ONLY RUN IN A STANDALONE, OFFLINE ENVIRONMENT. THE DEUNA IS LOGICALLY REMOVED FROM THE 'WIRE' BY THE DIAGNOSTIC, SO NO MESSAGES FROM OTHER NODES ON THE NETWORK, TO THE DEUNA UNDER TEST, WILL DISRUPT THE TESTING PROCESS. HOWEVER, BECAUSE THIS DIAGNOSTIC RUNS THE DEUNA SELF-TEST IN TEST 9, AND THE SELF-TEST PERFORMS AN EXTERNAL LOOPBACK AS PART OF ITS TESTING PROCEDURE, IT IS RECOMMENDED THAT THE DEUNA TRANSCEIVER CABLE BE REMOVED FROM THE H4000 TRANSCEIVER AND PLUGGED INTO A FIELD SERVICE EXTERNAL LOOPBACK CONNECTOR.

THIS DIAGNOSTIC HAS BEEN WRITTEN FOR USE WITH THE DIAGNOSTIC

RUNTIME SERVICES SOFTWARE (SUPERVISOR). THESE SERVICES PROVIDE THE INTERFACE TO THE OPERATOR AND TO THE SOFTWARE ENVIRONMENT. THIS PROGRAM CAN BE USED WITH XXDP+, ACT, APT, SLIDE AND PAPER TAPE. FOR A COMPLETE DESCRIPTION OF THE RUNTIME SERVICES, REFER TO THE XXDP+ USER'S MANUAL. THERE IS A BRIEF DESCRIPTION OF THE RUNTIME SERVICES IN SECTION 2 OF THIS DOCUMENT

1.2 SYSTEM REQUIREMENTS

THE FOLLOWING LIST OF HARDWARE IS REQUIRED TO RUN THIS DIAGNOSTIC:

PDP-11 CPU
28K WORDS OF MEMORY
CONSOLE TERMINAL
DEUNA BOARDSET (M7792, M7793)
PLUS, ONE OF THE FOLLOWING:
-LINK BOARD TO BULKHEAD CABLE CONNECTED AND BULKHEAD TO
TRANSCEIVER TAP CABLE CONNECTED (NORMAL ONLINE CONFIGURATION)
OR
-LINK BOARD TO BULKHEAD CABLE CONNECTED AND BULKHEAD TO FIELD
SERVICE EXTERNAL LOOPBACK CONNECTOR INSTALLED (OFFLINE CONFIGURATION)

1.3 RELATED DOCUMENTS AND STANDARDS

XXDP+ USERS MANUAL CHQUS
XXDP+ PROGRAMMERS MANUAL
DEUNA LINK BOARD FUNCTIONAL SPECIFICATION
DEUNA PORT BOARD FUNCTIONAL SPECIFICATION
DEUNA PROGRAMMING SPECIFICATION

1.4 DIAGNOSTIC HIERARCHY PREREQUISITES

THIS DIAGNOSTIC ASSUMES THAT THE PDP-11 PROCESSOR AND MEMORY ARE IN WORKING CONDITION AND IS CAPABLE OF EXECUTING PDP-11 INSTRUCTIONS NORMALLY. THE UNIBUS IS EXPECTED TO BE FULLY OPERATIONAL I.E. ANY PROBLEMS REPORTED BY THIS DIAGNOSTIC, ABOUT THE INTEGRITY OF THE UNIBUS, ARE ASSUMED TO BE THE RESULT OF A FAILURE ON THE DEUNA AND NOT THE FAULT OF OTHER DEVICES CONNECTED TO THE UNIBUS.

THIS DIAGNOSTIC DOES NOT REQUIRE ANY PRELIMINARY TESTS BE EXECUTED ON THE DEUNA, NOR DOES RUNNING OF ANY OTHER TESTS PRIOR TO RUNNING THIS DIAGNOSTIC, AFFECT THE OPERATION OF THE TESTS IN THIS DIAGNOSTIC.

FOR A COMPLETE TEST OF THE DEUNA, ALL THE AVAILABLE DIAGNOSTIC SOFTWARE SHOULD BE RUN. THIS WOULD INCLUDE RUNNING THE DEUNA FUNCTIONAL DIAGNOSTIC AND THE DECX-11 SYSTEM EXERCISOR WITH THE DEUNA MODULE SELECTED.

1.5 ASSUMPTIONS

THIS DIAGNOSTIC ASSUMES THAT THE DEUNA WILL NOT HANG THE UNIBUS WHEN AN ACCESS IS MADE TO ANY ONE OF THE PCSR REGISTERS. THE DEUNA IS CAPABLE OF ASSERTING ACLO ON THE UNIBUS, THIS FEATURE COULD, IF BROKEN, CAUSE THE UNIBUS TO HANG. THIS TYPE OF FAILURE IS NOT DETECTED BY THE DIAGNOSTIC.

PORTIONS OF THIS DIAGNOSTIC USE SPECIAL DIAGNOSTIC MICROCODE THAT IS LOADED INTO THE DEUNA WRITEABLE CONTROL STORE. THIS MICROCODE ALLOWS THE DIAGNOSTIC MORE VISIBILITY INTO THE INTERNALS OF THE DEUNA HARDWARE AS WELL AS NOT RELYING AS HEAVILY ON THE COMPLEX OPERATIONAL MICROCODE IN ROM, HOWEVER, THIS INCREASES THE DIAGNOSTIC'S COMPLEXITY SOMEWHAT. THEREFORE, IT IS ASSUMED THAT THE USER OF THIS DIAGNOSTIC IS FAMILIAR WITH THE DEUNA ENOUGH TO READ DEUNA MICROCODE SHOULD AN ERROR OCCUR.

2.0 OPERATING INSTRUCTIONS

THIS SECTION CONTAINS A BRIEF DESCRIPTION OF THE RUNTIME SERVICES. FOR DETAILED INFORMATION, REFER TO THE XXDP+ USER'S MANUAL (CHQUS).

2.1 COMMANDS

THERE ARE ELEVEN LEGAL COMMANDS FOR THE DIAGNOSTIC RUNTIME SERVICES (SUPERVISOR). THIS SECTION LISTS THE COMMANDS AND GIVES A VERY BRIEF DESCRIPTION OF THEM. THE XXDP+ USER'S MANUAL HAS MORE DETAILS.

COMMAND	EFFECT
START	START THE DIAGNOSTIC FROM AN INITIAL STATE
RESTART	START THE DIAGNOSTIC WITHOUT INITIALIZING
CONTINUE	CONTINUE AT BTEST THAT WAS INTERRUPTED (AFTER ^C)
PROCEED	CONTINUE FROM AN ERROR HALT
EXIT	RETURN TO XXDP+ MONITOR (XXDP+ OPERATION ONLY!)
A.	ACTIVATE A UNIT FOR TESTING (ALL UNITS ARE CONSIDERED TO BE ACTIVE AT START TIME)
DROP	DEACTIVATE A UNIT
DISPLAY	TYPE A LIST OF ALL DEVICE INFORMATION
FLAGS	TYPE THE STATE OF ALL FLAGS (SEE SECTION 2.3)
ZFLAGS	CLEAR ALL FLAGS (SEE SECTION 2.3)

A COMMAND CAN BE RECOGNIZED BY THE FIRST THREE CHARACTERS. SO YOU MAY, FOR EXAMPLE, TYPE "STA" INSTEAD OF "START".

2.2 SWITCHES

THERE ARE SEVERAL SWITCHES WHICH ARE USED TO MODIFY SUPERVISOR OPERATION. THESE SWITCHES ARE APPENDED TO THE LEGAL COMMANDS. ALL OF THE LEGAL SWITCHES ARE TABULATED BELOW WITH A BRIEF DESCRIPTION OF EACH. IN THE DESCRIPTIONS BELOW, A DECIMAL NUMBER IS DESIGNATED BY "DDDD".

SWITCH	EFFECT
/TESTS:LIST	EXECUTE ONLY THOSE TESTS SPECIFIED IN THE LIST. LIST IS A STRING OF TEST NUMBERS, FOR EXAMPLE - /TESTS:1:5:7-10. THIS LIST WILL CAUSE TESTS 1,5,7,8,9,10 TO BE RUN. ALL OTHER TESTS WILL NOT BE RUN.
/PASS:DDDD	EXECUTE DDDDD PASSES (DDDD = 1 TO 64000)
/FLAGS:FLGS	SET SPECIFIED FLAGS. FLAGS ARE DESCRIBED IN SECTION 2.3.
/EOP:DDDD	REPORT END OF PASS MESSAGE AFTER EVERY DDDDD PASSES ONLY. (DDDD = 1 TO 64000)

/UNITS:LIST TEST/ADD/DROP ONLY THOSE UNITS SPECIFIED
IN THE LIST. LIST EXAMPLE - /UNITS:0:5:10-12
USE UNITS 0,5,10,11,12 (UNIT NUMBERS = 0-63)

EXAMPLE OF SWITCH USAGE:

START/TESTS:1-5/PASS:1000/EOP:100

THE EFFECT OF THIS COMMAND WILL BE: 1) TESTS 1 THROUGH 5 WILL BE EXECUTED, 2) ALL UNITS WILL TESTED 1000 TIMES AND 3) THE END OF PASS MESSAGES WILL BE PRINTED AFTER EACH 100 PASSES ONLY. A SWITCH CAN BE RECOGNIZED BY THE FIRST THREE CHARACTERS. YOU MAY, FOR EXAMPLE, TYPE "/TES:1-5" INSTEAD OF "/TESTS:1-5".

BELOW IS A TABLE THAT SPECIFIES WHICH SWITCHES CAN BE USED BY EACH COMMAND.

	TESTS	PASS	FLAGS	EOP	UNITS
START	X	X	X	X	X
RESTART	X	X	X	X	X
CONTINUE		X	X	X	
PROCEED			X		
DROP					X
ADD					X
DISPLAY					X
FLAGS					
ZFLAGS					
EXIT					

2.3 FLAGS

FLAGS ARE USED TO SET UP CERTAIN OPERATIONAL PARAMETERS SUCH AS LOOPING ON ERROR. ALL FLAGS ARE CLEARED AT STARTUP AND REMAIN CLEARED UNTIL EXPLICITLY SET USING THE FLAGS SWITCH. FLAGS ARE ALSO CLEARED AFTER A START COMMAND UNLESS SET USING THE FLAG SWITCH. THE ZFLAGS COMMAND MAY ALSO BE USED TO CLEAR ALL FLAGS. WITH THE EXCEPTION OF THE START AND ZFLAGS COMMANDS, NO COMMANDS AFFECT THE STATE OF THE FLAGS; THEY REMAIN SET OR CLEARED AS SPECIFIED BY THE LAST FLAG SWITCH.

FLAG	EFFECT
HOE	HALT ON ERROR - CONTROL IS RETURNED TO RUNTIME SERVICES COMMAND MODE
LOE	LOOP ON ERROR
IER*	INHIBIT ALL ERROR REPORTS
IBE*	INHIBIT ALL ERROR REPORTS EXCEPT FIRST LEVEL (FIRST LEVEL CONTAINS ERROR TYPE, NUMBER, PC, TEST AND UNIT)
IXE*	INHIBIT EXTENDED ERROR REPORTS (THOSE CALLED BY PRINTX MACRO'S)
PRI	DIRECT MESSAGES TO LINE PRINTER
PNT	PRINT TEST NUMBER AS TEST EXECUTES
BOE	"BELL" ON ERROR

UAM	UNATTENDED MODE (NO MANUAL INTERVENTION)
IDR	INHIBIT PROGRAM DROPPING OF UNITS
ADR	EXECUTE AUTODROP CODE
LOT	LOOP ON TEST

*ERROR MESSAGES ARE DESCRIBED IN SECTION 3.1

SEE THE XXDP+ USER'S MANUAL FOR MORE DETAILS ON FLAGS. YOU MAY SPECIFY MORE THAN ONE FLAG WITH THE FLAG SWITCH. FOR EXAMPLE, TO CAUSE THE PROGRAM TO LOOP ON ERROR, INHIBIT ERROR REPORTS AND TYPE A 'BELL' ON ERROR, YOU MAY USE THE FOLLOWING STRING:

/FLAGS:LOE ER:BDE

2.4 HARDWARE QUESTIONS

WHEN A DIAGNOSTIC IS STARTED, THE RUNTIME SERVICES WILL PROMPT THE USER FOR HARDWARE INFORMATION BY TYPING "CHANGE HW (L) ?" YOU MUST ANSWER "Y" AFTER A START COMMAND UNLESS THE HARDWARE INFORMATION HAS BEEN "PRELOADED" USING THE SETUP UTILITY (SEE CHAPTER 6 OF THE XXDP+ USER'S MANUAL). WHEN YOU ANSWER THIS QUESTION WITH A "Y", THE RUNTIME SERVICES WILL ASK FOR THE NUMBER OF UNITS (IN DECIMAL). YOU WILL THEN BE ASKED THE FOLLOWING QUESTIONS FOR EACH UNIT.

WHAT IS THE PCSRO ADDRESS?
THIS IS THE ADDRESS AT WHICH PCSRO RESIDES ON THE UNIBUS.
THIS ADDRESS IS SWITCH SELECTABLE ON THE PORT MODULE.
THE ALLOWABLE RANGE IS 160000-177776.

WHAT IS THE VECTOR ADDRESS?
THIS IS THE INTERRUPT VECTOR ADDRESS. THIS ADDRESS IS ALSO SWITCH SELECTABLE ON THE PORT MODULE. THE ALLOWABLE RANGE IS 000-776.

SAMPLE DIALOGUE:

DR>START

CHANGE HW (L) ? Y

UNITS (D) ? 2

UNIT 0

WHAT IS THE PCSRO ADDRESS? (0) ? 170000

WHAT IS THE VECTOR ADDRESS? (0) ? 700

UNIT 1

WHAT IS THE PCSRO ADDRESS? (0) ? 170010

WHAT IS THE VECTOR ADDRESS? (0) ? 710

2.7 QUICK START-UP PROCEDURE (XXDP+)

TO START-UP THIS PROGRAM:

1. BOOT XXDP+
2. GIVE THE DATE AND ANSWER ANY QUESTIONS THE MONITOR ASKS
3. TYPE 'R NAME', WHERE NAME IS THE NAME OF THE BIN OR BIC FILE FOR THIS PROGRAM
4. TYPE 'START'
5. ANSWER THE 'CHANGE HW' QUESTION WITH 'Y'
6. ANSWER ALL THE HARDWARE QUESTIONS

WHEN YOU FOLLOW THIS PROCEDURE YOU WILL BE USING ONLY THE DEFAULTS FOR FLAGS AND SOFTWARE PARAMETERS. THESE DEFAULTS ARE DESCRIBED IN SECTIONS 2.3 AND 2.5.

3.0 ERROR INFORMATION

3.1 TYPES OF ERROR MESSAGES

THERE ARE THREE LEVELS OF ERROR MESSAGES THAT MAY BE ISSUED BY A DIAGNOSTIC: GENERAL, BASIC AND EXTENDED. GENERAL ERROR MESSAGES ARE ALWAYS PRINTED UNLESS THE 'IER' FLAG IS SET (SECTION 2.3). THE GENERAL ERROR MESSAGE IS OF THE FORM:

```
NAME TYPE NUMBER ON UNIT NUMBER TST NUMBER PC:XXXXXX  
ERROR MESSAGE
```

WHERE: NAME = DIAGNOSTIC NAME
TYPE = ERROR TYPE (SYS FATAL, DEV FATAL, HARD OR SOFT)
NUMBER = ERROR NUMBER
UNIT NUMBER = 0 - N (N IS LAST UNIT IN PTABLE)
TST NUMBER = TEST AND SUBTEST WHERE ERROR OCCURRED
PC:XXXXXX = ADDRESS OF ERROR MESSAGE CALL

BASIC ERRCR MESSAGES ARE MESSAGES THAT CONTAIN SOME ADDITIONAL INFORMATION ABOUT THE ERROR. THESE ARE ALWAYS PRINTED UNLESS THE 'IER' OR 'IBR' FLAGS ARE SET (SECTION 2.3). THESE MESSAGES ARE PRINTED AFTER THE ASSOCIATED GENERAL MESSAGE.

EXTENDED ERROR MESSAGES CONTAIN SUPPLEMENTARY ERROR INFORMATION SUCH AS REGISTER CONTENTS OR GOOD/BAD DATA. THESE ARE ALWAYS PRINTED UNLESS THE 'IER', 'IBE' OR 'IXE' FLAGS ARE SET (SECTION 2.3). THESE MESSAGES ARE PRINTED AFTER THE ASSOCIATED GENERAL ERROR MESSAGE AND ANY ASSOCIATED BASIC ERROR MESSAGES.

4.0 PERFORMANCE AND PROGRESS REPORTS

AT THE END OF EACH PASS, THE PASS COUNT IS GIVEN ALONG WITH THE TOTAL NUMBER OF ERRORS REPORTED SINCE THE DIAGNOSTIC WAS STARTED. THE 'EOP' SWITCH CAN BE USED TO CONTROL HOW OFTEN THE END

OF PASS MESSAGE IS PRINTED. SECTION 2.2 DESCRIBES SWITCHES.

5.0 DEVICE INFORMATION TABLES

AT THE COMPLETION OF THE FIRST PASS OF EACH DEUNA BEING TESTED, INFORMATION FOR THAT DEUNA IS PRINTED. THIS PRINTOUT CONTAINS THE ETHERNET DEFAULT ADDRESS (OBTAINED BY READING THE PHYSICAL ADDRESS ROM), THE OPERATIONAL MICROCODE ROM VERSION NUMBER, AND THE SWITCH PACK SETTINGS FOR SELF TEST LOOPING AND REMOTE BOOTING.

EXAMPLE PRINTOUT:

ETHERNET DEFAULT ADDRESS (HEX): AA-00-03-00-00-02

ROM MICROCODE VERSION (DECIMAL): 1

SWITCH PACK SET FOR :

SELF TEST LOOP DISABLED

REMOTE BOOT ENABLED

6.0 TEST SUMMARIES

TEST 1: PCSR0 READ ACCESS TEST

THIS TEST WILL VERIFY THAT THE PORT CONTROL AND STATUS REGISTER 0 CAN BE READ FROM THE UNIBUS AND THAT THE PREDETERMINED BITS APPEAR IN THE EXPECTED BIT POSITIONS.

TEST 2: PCSR1 READ ACCESS TEST

THIS TEST WILL VERIFY THAT THE PORT CONTROL AND STATUS REGISTER CAN BE READ FROM THE UNIBUS AND THAT THE PREDETERMINED BITS APPEAR IN THE EXPECTED BIT POSITIONS.

TEST 3: PCSR2 READ ACCESS TEST

THIS TEST WILL VERIFY THAT THE PORT CONTROL AND STATUS REGISTER 2 CAN BE READ FROM THE UNIBUS

TEST 4: PCSR3 READ ACCESS TEST

THIS TEST WILL VERIFY THAT THE PORT CONTROL AND STATUS REGISTER 3 CAN BE READ FROM THE UNIBUS

TEST 5: RESET TEST

THIS TEST WILL VERIFY THE RESET STATE FOR ALL DEUNA UNIBUS REGISTERS

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TEST 6: PCSR2 REGISTER READ/WRITE TEST

THIS TEST WILL CHECK THE REGISTER FOR ALL SA0 AND SA1 ERRORS (STUCK AT 0 AND STUCK AT 1 ERRORS). THE HOST WILL WRITE PATTERNS TO THE REGISTER AND READ THEM BACK TO VERIFY. THE PATTERNS TO BE USED ARE AT THE LABEL PATERN:: IN THE GLOBAL DATA SECTION OF THIS PROGRAM.

NOTE: SINCE PCSR2 BIT 00 IS ALWAYS PRESET TO LOGIC 0, THE LOWEST ORDER BIT OF THE PATTERN WILL BE MASKED BEFORE DOING THE COMPARISON.

TEST 7: REGISTER PCSR3 READ/WRITE TEST

THIS TEST WILL WRITE PATTERNS TO THE WRITEABLE FIELD OF PCSR3 AND WILL READ THESE BACK FOR VERIFICATION.

TEST 8: NOP TEST

THIS TEST WILL VERIFY THAT THE DEUNA PROCESSOR IS ALIVE AND CAN RESPOND TO A PORT COMMAND ISSUED. THE NOP PORT COMMAND WILL BE ISSUED TO THE DEUNA IN PCSRO BITS 3:0 AND WILL WAIT FOR THE 'DNI' BIT TO SET IN PCSRO.

THE NOP PORT COMMAND USES A MINIMUM OF HARDWARE BUT FORCES THE T11 TO EXECUTE THE PORT COMMAND SEQUENCE.

TEST 9: SELF TEST

THIS TEST VERIFIES THAT THE ROM BASED SELF TEST CAN BE RUN SUCCESSFULLY WHEN INVOKED VIA THE SELF TEST PORT COMMAND.

TEST 10: DEUNA ROM DUMP TEST

THIS TEST WILL VERIFY THAT THE DATA PATH FROM THE T11 PROCESSOR TO THE UNIBUS INTERFACE IS INTACT AND ABLE TO TRANSFER DATA RELIABLY. THIS DATA PATH IS CRUCIAL FOR FURTHER TESTING BECAUSE IT IS NECESSARY FOR LOADING REPAIR-LEVEL DIAGNOSTICS INTO THE WCS.

THE TEST STRATEGY IS TO TRANSFER KNOWN DATA OVER THE DATA PATH AND TO VERIFY THE TRANSFERRED DATA.

THE DATA SOURCE FOR THE DUMP TEST IS THE ROM MICROCODE RESIDENT ON THE DEUNA PORT BOARD. A DUMP OF THE ROM WILL EXERCISE THE DATA PATH NEEDED FOR LOADING WCS AND THE ROM CONTENTS CAN BE VERIFIED. THE ROM MICROCODE WILL BE CHECKED BY VERIFYING THE CRC BYTES. THE CRC BYTES CHARACTERIZE THE DATA CONTENTS OF THE ROM AND ARE BURNED INTO THE ROM AT THE TIME OF MANUFACTURE. A FAILURE TO VERIFY THE CRC CALCULATION ON THE DUMPED ROM DATA DUMP WILL BE INTERPRETED AS AN ERROR IN THE DATA PATH.

TEST 11: WCS LOAD/DUMP TEST

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THIS TEST WILL USE THE LOAD/DUMP PORT COMMAND TO VERIFY THE DATA PATHWAY TO/FROM THE WCS. PATTERNS WILL BE USED TO CHECK THE DATA PATHWAY FOR ALL SA0 AND SA1 ERRORS.

BECAUSE THE OPERATIONAL MICROCODE NEEDS THE LOWER 2K OF WCS ONLY THE TOP HALF OF WCS WILL BE LOADED WITH A DATA PATTERN THEN DUMPED BACK TO MEMORY FOR VERIFICATION. THIS PROCEDURE WILL BE REPEATED FOR ALL PATTERNS.

TEST 12: LOAD AND START FUNCTION TEST

THIS TEST WILL VERIFY THAT THE LOAD AND START MICROADDRESS PORT COMMAND IS OPERATIONAL.

THE PROCESS IS TO LOAD WCS WITH MICROCODE THAT WHEN STARTED WILL WRITE A PATTERN OF DATA TO THE LITE-BYTE FIELD OF PCSR1 REGISTER WHICH CAN BE READ FROM THE UNIBUS AND BE VERIFIED.

NOTE: THIS TEST USES MICROCODE MODULE 'A'

TEST 13: COMPREHENSIVE WCS MEMORY TEST

THIS TEST WILL EXHAUSTIVELY TEST THE WCS MEMORY. CUSTOM MICROCODE MODULE B, MICROTEST #1 IS USED TO DO THE ACTUAL TESTING. MICROTEST #1 RUNS A SERIES OF MICROSUBTESTS TESTS ON THE WCS MEMORY CHECKING FOR BOTH ADDRESS AND DATA ERRORS. IF AN ERROR DOES OCCUR THE PORT CONTROL BLOCK WILL CONTAIN THE INFORMATION ABOUT THE ERROR.

PCBB+0: CONTAINS THE MICROSUBTEST THAT FAILED
 PCBB+1: 0 = DATA ERROR, 1 = ADDRESS ERROR
 PCBB+2: CONTAINS THE ADDRESS OF THE LOCATION
 PCBB+4: CONTAINS THE DATA THAT WAS WRITTEN
 PCBB+6: CONTAINS THE DATA THAT WAS READ

TEST 14: INTERRUPT VECTOR TEST

THIS TEST WILL VERIFY THAT THE INTERRUPT INTERFACE LOGIC OF THE DEUNA IS CAPABLE OF GENERATING AN INTERRUPT VECTOR AND ARBITRATING FOR CONTROL OF THE UNIBUS.

THE DEUNA INTERRUPT ENABLE BIT WILL BE SET AND AN INTERRUPT WILL BE CAUSED BY ISSUING A NOP PORT COMMAND. AN INTERRUPT IS EXPECTED AT THE CORRECT VECTOR AND AT THE CORRECT PRIORITY.

TEST 15: PCSRO INTERRUPT BIT TEST

THIS TEST WILL VERIFY THAT EACH OF THE INTERRUPT BITS IN REGISTER PCSRO CAN CAUSE AN INTERRUPT.

EACH OF THE INTERRUPTS OF REGISTER PCSRO IS SET UNDER THE CONTROL OF THE T11 AND NOT DIRECTLY BY HARDWARE. THE T11 THEREFORE CAN INITIATE UNIBUS INTERRUPTS BY SETTING BITS IN REGISTER PCSRO.

THIS TEST USES MICROMODULE C, MICROTEST #1.

MICROCODE MODULE C IS LOADED IF NOT ALREADY DONE SO BY A PREVIOUS TEST.

THE DEUNA INTERRUPT VECTOR IS SETUP TO STORE THE CONTENTS OF PCSRO WHEN THE INTERRUPT OCCURS. PCBB+0 IS LOADED WITH THE INTERRUPT BIT THAT IS TO BE TESTED THEN PCSRO COMMAND BITS ARE LOADED WITH A 1 TO TELL THE T11 TO EXECUTE MICROTEST #1. WE WAIT FOR THE INTERRUPT TO OCCUR THEN SEE IF THE CONTENTS OF PCSRO AT THE TIME OF THE INTERRUPT CONTAINED THE CORRECT INTERRUPT BIT. THE TEST IS REPEATED FOR ALL THE INTERRUPT BITS.

TEST 16: TIMER TEST

THIS TEST WILL USE THE CUSTOM MICROCODE MODULE 'C' TO CHECK THE OPERATION OF THE TIMER.
THE TIMER IS ACCESSIBLE ONLY TO THE T11 PROCESSOR. THE HOST PROCESSOR CAN START THE TIMER ONLY WITH THE ASSISTANCE OF THE T11 PROCESSOR.

FOR THIS TEST THE MICROCODE WILL BE LOADED ONLY IF IT HAS NOT ALREADY BEEN DONE BY A PREVIOUS TEST.

WHEN THE MICROCODE IS STARTED THE T11 WILL START THE TIMER AND WILL SET 'DNI' WHEN THE TIMING INTERVAL HAS EXPIRED. THE INTERVAL IS 10 SECONDS.

ANY TIME FROM 8 TO 12 SECONDS IS AN ACCEPTABLE RANGE.

TEST 17: LINK MEMORY TEST

THIS TEST WILL EXHAUSTIVELY TEST THE LINK MEMORY.

THE LINK MEMORY OCCUPIES THE 16-32K ADDRESS SPACE OF THE T-11. CUSTOM MICROCODE MODULE C MICROTEST #3 IS USED TO DO THE ACTUAL TESTING. MICROTEST #3 RUNS A SERIES OF MICROSUBTESTS TESTS ON THE LINK MEMORY CHECKING FOR BOTH ADDRESS AND DATA ERRORS. IF AN ERROR DOES OCCUR THE PORT CONTROL BLOCK WILL CONTAIN THE INFORMATION ABOUT THE ERROR.

PCBB+0: CONTAINS THE MICROSUBTEST THAT FAILED
PCBB+1: 0 - DATA ERROR, 1 = ADDRESS ERROR
PCBB+2: CONTAINS THE ADDRESS OF THE LOCATION
PCBB+4: CONTAINS THE DATA THAT WAS WRITTEN
PCBB+6: CONTAINS THE DATA THAT WAS READ

MICROSUBTEST #	DESCRIPTION
1	ACCESS TEST
2	ADDRESS SHIFT TEST
3	DATA LATCH TEST
4	ADDRESS BIT SHIFT #1
5	ADDRESS BIT SHIFT #2
6	MARCH TEST

TEST 18: DMA 'TO' ADDRESS TEST

THIS TEST WILL VERIFY THAT THE INTERNAL REGISTER 'DMATO' CAN BE READ AND WRITTEN. THE T11 WILL BE USED TO WRITE AND READ THIS REGISTER.

THIS TEST REQUIRES THE USE OF CUSTOM MICROCODE MODULE C MICROTEST #4. PCBB+0 WILL BE WRITTEN WITH THE DATA PATTERN TO TEST, THE T11 WILL WRITE THIS PATTERN TO THE 'DMATO' REGISTER AND READ IT BACK AND PUT THE DATA READ INTO PCBB+2. THE DATA AT PCBB+2 WILL BE VERIFIED.

TEST 19: DMA 'FROM' ADDRESS REGISTER TEST

THIS TEST CHECKS THE OPERATION OF THE REGISTER/COUNTER THAT CONTAINS THE ADDRESS OF THE LINK MEMORY WORD TO BE MOVED TO THE HOST DURING DMA OPERATIONS. THE REGISTER CAN BE WRITTEN BY THE T11 BUT IT CAN NOT BE READ BACK FOR VERIFICATION, THEREFORE IT MUST BE CHECKED INDIRECTLY.

THE METHOD USED IS TO LOAD MICROCODE MODULE C IF IT HAS NOT ALREADY BEEN DONE. THE MICROTEST #5 LOADS EACH LOCATION OF LINK MEMORY WITH ITS ADDRESS THEN IT TAKES THE CONTENTS OF PCBB+0 AND LOADS IT INTO THE DMA 'FROM' ADDRESS REGISTER, THE 'TO' REGISTER IS LOADED WITH THE ADDRESS OF PCBB+2, THE WORD COUNT IS LOADED FOR A ONE WORD TRANSFER AND THE DMA ENGINE IS STARTED. THE HOST VERIFIES PCBB+2 = PCBB+0

TEST 20: DMA BLOCK TRANSFER TEST

THIS TEST WILL VERIFY THAT THE DMA ENGINE CAN TRANSFER A MAXIMUM SIZE DATA BLOCK TO HOST MEMORY.

THIS TEST USES CUSTOM MICROCODE MODULE C, MICROTEST #6. THE MICROTEST FILLS EACH LOCATION OF LINK MEMORY WITH ITS ADDRESS AND THEN SETS UP A TRANSFER FROM LINK MEMORY TO THE ADDRESS POINTED TO BY PCBB+0. THE TRANSFER SIZE IS 1776 WORDS. AFTER THE MICROTEST FINISHES THE BUFFER IS CHECKED TO SEE IF IT CONTAINS THE INCREMENTING ADDRESS PATTERN.

TEST 21: TRANSMIT DONE TEST

THE TRANSMIT STATE MACHINE INFORMS THE PORT MODULE PROCESSOR OF A 'TRANSMIT DONE' CONDITION. IT DOES THIS BY GENERATING AN INTERRUPT WHENEVER IT FINISHES TRANSMITTING A DATAGRAM. SINCE THE 'TRANSMIT DONE' INTERRUPT IS A NECESSARY CONDITION OF EVERY DATAGRAM TRANSMISSION, THIS TEST WILL USE THE INTERRUPT TO INDICATE THAT THE TRANSMIT STATE MACHINE IS FUNCTIONING.

MICROCODE MODULE D MICROTEST #1 WILL BE USED FOR THIS TEST. IT SETS UP THE T-11 FOR AN INTERRUPT, STARTS A DATAGRAM LOOPBACK AND WAITS FOR A TRANSMIT INTERRUPT. THE T-11 WILL BE RELEASED FROM THE LOOP IF THE XMIT DONE INTERRUPT OCCURS. UPON RELEASE THE DNI BIT WILL BE SET IN PCSRO SIGNALING THAT THE TEST IS COMPLETE.

TEST 22: RECEIVER DONE TEST

THE LINK HARDWARE INCLUDES LOGIC TO TELL THE DEUNA PROCESSOR WHEN A LINK MEMORY BUFFER HAS BEEN FILLED AND DATA IS AVAILABLE FOR PROCESSING. THE HARDWARE INTERRUPTS THE DEUNA PROCESSOR. BECAUSE THE INTERRUPT HAPPENS WHEN A LINK MEMORY BUFFER IS FULL AND THE LINK MEMORY IS FILLED BY THE OPERATION OF THE RECEIVE STATE MACHINE, THE INTERRUPT CAN BE USED TO CHECK IF THE STATE MACHINE WORKS.

MICROCODE MODULE D MICROTEST #2 WILL BE USED FOR THIS TEST. IT SETS UP THE T-11 FOR AN INTERRUPT, STARTS A DATAGRAM LOOPBACK AND WAITS FOR A

RECEIVER INTERRUPT. THE T-11 WILL BE RELEASED FROM THE LOOP IF THE INTERRUPT OCCURS. UPON RELEASE THE DNI BIT WILL BE SET IN PCSRO SIGNALING THAT THE TEST IS COMPLETE.

TEST 23: DATA BYTE FRAMING TEST

THIS TEST WILL CHECK THE LINK MODULE DATA PATH FOR BYTE DATA BOUNDARY CONDITIONS.

THE T-11 PROCESSOR WILL TRANSMIT DATA IN LOOPBACK MODE. THE DATA WILL BE ORGANIZED SUCH THAT DATA BOUNDARIES ARE CREATED BETWEEN ADJACENT BYTES IN THE DATA STREAM (I.E. 11111111000000011...) THE T-11 PROCESSOR WILL VERIFY THE CONDITION OF THE DATA AFTER IT IS LOOPED BACK TO THE RECEIVER DATA BUFFER.

THIS TEST WILL USE MICROCODE MODULE 'D' MICROTEST #3. TESTING OF THE DATA FRAMING WILL BE DONE BY THE T-11 PROCESSOR. THE HOST PROCESSOR, MEANWHILE, WILL WAIT FOR A 'DNI' IN REGISTER PCSRO. IF 'DNI' APPEARS, THE HOST PROCESSOR WILL CHECK PCSR1 FOR AN ERROR CONDITION. IF AN ERROR CONDITION IS SET, ADDITIONAL ERROR INFORMATION WILL BE FOUND IN THE PCBB AS FOLLOWS:

PCBB+0: RECEIVER STATUS WORD
PCBB+2: DATA TRANSMITTED
PCBB+4: DATA RECEIVED
PCBB+6: WORD OFFSET INTO RECEIVER BUFFER OF BAD DATA

TEST 24: DATA WORD FRAMING TEST

THIS TEST WILL CHECK THE LINK MODULE DATA PATH FOR WORD DATA BOUNDARY CONDITIONS.

THE T-11 PROCESSOR WILL TRANSMIT DATA IN LOOPBACK MODE. THE DATA WILL BE ORGANIZED SUCH THAT DATA BOUNDARIES ARE CREATED BETWEEN ADJACENT WORDS IN THE DATA STREAM (I.E. 111111111111111100000000000000011...) THE T-11 PROCESSOR WILL VERIFY THE CONDITION OF THE DATA AFTER IT IS LOOPED BACK TO THE RECEIVER DATA BUFFER.

THIS TEST WILL USE MICROCODE MODULE 'D' MICROTEST #4. TESTING OF THE DATA FRAMING WILL BE DONE BY THE T-11 PROCESSOR. THE HOST PROCESSOR, MEANWHILE, WILL WAIT FOR A 'DNI' IN REGISTER PCSRO. IF 'DNI' APPEARS, THE HOST PROCESSOR WILL CHECK PCSR1 FOR AN ERROR CONDITION. IF AN ERROR CONDITION IS SET, ADDITIONAL ERROR INFORMATION WILL BE FOUND IN THE PCBB AS FOLLOWS:

PCBB+0: RECEIVER STATUS WORD
PCBB+2: DATA TRANSMITTED
PCBB+4: DATA RECEIVED
PCBB+6: WORD OFFSET INTO RECEIVER BUFFER OF BAD DATA

TEST 25: DATA PATH PATTERN TEST

THIS TEST WILL CHECK THE LINK MODULE DATA PATH FOR ALL 'STUCK AT 0' AND 'STUCK AT 1' ERRORS.

THE T-11 PROCESSOR WILL TRANSMIT DATAGRAMS OF MAXIMUM LENGTH IN LOOPBACK MODE. THIS PATTERN LOOPBACK PROCEDURE WILL BE USED FOR ALL PATTERNS OF UP TO WORD WIDTH.

THIS TEST USES MICROMODULE 'D' MICROTEST #5 TO DO THE TESTING. THE HOST PROCESSOR WILL PASS A DATA PATTERN TO THE T-11 PROCESSOR THROUGH THE PCBB. THE T-11 WILL FILL A XMIT BUFFER WITH THE DATA PATTERN AND TRANSMIT THE DATAGRAM OVER THE LOOPBACK. THE T-11 PROCESSOR WILL VERIFY THE PATTERN IN THE RECEIVER BUFFER. IF THE T-11 FINDS AN ERROR, IT WILL WRITE THE FAILING PATTERN TO THE PCBB ALONG WITH THE OFFSET INTO THE RECEIVER BUFFER AT WHICH THE PATTERN WAS FOUND. IT WILL INFORM THE HOST OF THE ERROR BY SETTING PCSR1 TO AN ERROR CONDITION. THE PCBB IS FORMATTED AS FOLLOWS:

PCBB+0: DATA PATTERN
 PCBB+2: RECEIVER STATUS WORD
 PCBB+4: BAD DATA PATTERN
 PCBB+6: OFFSET INTO RECEIVER BUFFER WHERE BAD DATA WAS FOUND

TEST 26: STATUS MUX VERIFICATION TEST

THE LINK WRITES STATUS IN LINK MEMORY AFTER EACH TRANSMIT ATTEMPT. THE STATUS GIVES INFORMATION ABOUT THE ATTEMPTED OPERATION. THE STATUS INFORMATION IS WRITTEN INTO THE FIRST TWO LOCATIONS OF THE TRANSMIT BUFFER. THIS INFORMATION IS ACCESSIBLE TO THE T-11 BY SIMPLY READING IT FROM LINK MEMORY.

THIS TEST WILL VERIFY THAT THE STATUS INFORMATION IS WRITTEN INTO THE FIRST LOCATION OF THE TRANSMIT BUFFER. THE TEST WILL ALSO CHECK THE SECOND WORD OF THE TRANSMIT BUFFER.

THIS TEST WILL USE MICROMODULE 'D' MICROTEST #6. WHEN THE TEST IS STARTED, THE T-11 PROCESSOR WILL SET UP THE LINK FOR LOOPBACK OF A DATA PATTERN. A BACKGROUND PATTERN WILL BE WRITTEN INTO THE FIRST WORD OF THE TRANSMIT BUFFER. THIS WORD SHOULD BE OVER-WRITTEN BY THE STATUS WHEN THE BUFFER IS TRANSMITTED. THE SECOND WORD OF THE TRANSMIT BUFFER CAN NOT BE WRITTEN WITH A BACKGROUND BECAUSE IT MUST DESIGNATE THE TRANSMIT BYTE COUNT.

WHEN THE DATAGRAM HAS BEEN LOOPED BACK, THE T-11 PROCESSOR WILL PASS THE FIRST TWO WORDS OF THE TRANSMIT BUFFER TO THE HOST THRU THE PCBB+0 AND PCBB+2.

PCBB+0: FIRST WORD OF TRANSMIT BUFFER
 PCBB+2: SECOND WORD OF TRANSMIT BUFFER

THE CORRECT STATUS SHOULD BE:

TRANSMIT STATUS WORD 0 BITS 15:09:00 SHOULD BE ALL 0 AND
 BIT 13 SHOULD BE A 1

TRANSMIT STATUS WORD 1 BITS 15:13 SHOULD ALL BE 0

TEST 27: LINK BYTE COUNTER TEST

BYTE COUNTERS ARE INVOLVED BOTH WITH THE LINK TRANSMIT FUNCTION AND THE LINK RECEIVE FUNCTION. WHEN THE T-11 PREPARES A TRANSMIT BUFFER FOR TRANSMISSION OF A DATAGRAM, IT WRITES THE INTENDED BYTE COUNT IN THE SECOND WORD OF THE TRANSMIT BUFFER. WHEN TRANSMISSION OF THE TRANSMIT BUFFER BEGINS, THE BYTE COUNT VALUE IS USED TO LOAD THE TRANSMIT BYTE COUNTER. THIS COUNTER IS DECREMENTED BY THE TRANSMIT STATE MACHINE AS THE DATAGRAM IS TRANSMITTED. THE DATAGRAM TRANSMISSION WILL CONTINUE UNTIL THE BYTE COUNTER IS DECREMENTED

TO ZERO.

THE RECEIVER ALSO HAS A BYTE COUNTER. THIS COUNTER IS CLEARED AT THE START OF A DATAGRAM RECEPTION AND IS INCREMENTED BY THE RECEIVE STATE MACHINE AS THE DATAGRAM IS RECEIVED. THE VALUE IN THIS COUNTER IS WRITTEN INTO WORD TWO OF THE RECEIVE BUFFER AT THE END OF RECEPTION.

THIS TEST WILL USE MICROMODULE 'D' MICROTEST #7. THIS TEST WILL VERIFY THE BYTE COUNT LOGIC BY LOOPING MESSAGES AND VERIFYING THAT THE BYTE COUNT APPEARING IN THE RECEIVE BUFFER CORRESPONDS TO THE BYTE COUNT THAT WAS WRITTEN TO THE TRANSMIT BYTE COUNTER. THE TEST WILL ALSO VERIFY THAT THE ACTUAL NUMBER OF BYTES TRANSFERRED TO THE RECEIVE BUFFER CORRESPONDS TO THE INTENDED BYTE COUNT.

THE TRANSMIT BYTE COUNT IS PASSED TO THE T-11 VIA THE PCBB+0. AFTER THE DATAGRAM LOOPBACK THE RECEIVE BYTE COUNT IS PLACED INTO PCBB+2 BY THE T-11 PROCESSOR. PCBB+4 IS LOADED BY THE T-11 PROCESSOR WITH THE ACTUAL NUMBER OF BYTES THAT WERE TRANSFERRED TO THE RECEIVER BUFFER.

PCBB+0: TRANSMIT BYTE COUNT
 PCBB+2: RECEIVE BYTE COUNT
 PCBB+4: ACTUAL NUMBER OF BYTES RECEIVED

TEST 28: ODD BYTE TEST

THIS TEST WILL VERIFY THAT THE LINK CAN TRANSMIT AND RECEIVE DATAGRAMS HAVING ONLY ODD BYTE COUNTS.

THIS TEST IS IDENTICAL TO THE PREVIOUS BYTE COUNTER TEST WITH THE ONLY EXCEPTION THAT IT PASSES ONLY ODD BYTE COUNTS TO THE MICROCODE. IT ALSO USES MICROMODULE 'D' MICROTEST #7

TEST 29: LINK MAXIMUM BYTE COUNTER TEST

THE RECEIVE BYTE COUNTER IS A 12 BIT BINARY COUNTER THAT COUNTS THE NUMBER OF BYTES THAT WERE RECEIVED DURING A DATAGRAM TRANSMISSION. THE BYTE COUNTER IS INCREMENTED AS EACH BYTE IS RECEIVED. THE RECEIVE BYTE COUNTER HAS LOGIC THAT DISABLES THE COUNTER IF THE MAXIMUM VALUE IS REACHED AND PREVENTS THE COUNTER FROM ROLLING OVER TO ZERO.

THIS TEST WILL CHECK THAT THE COUNTER 'TOPS OUT' AT THE MAXIMUM COUNTER VALUE. IT DO THIS MICROMODULE 'D' MICROTEST #9 IS USED. IT WILL TRANSMIT A DATAGRAM OF MAXIMUM COUNTER LENGTH OVER THE LOOPBACK. THE LINK CRC HARDWARE WILL BE ALLOCATED TO THE TRANSMIT SIDE SO THAT CRC BYTES WILL APPENDED TO THE DATAGRAM. THE LENGTH OF THE DATAGRAM WILL THEREFORE EXCEED THE LENGTH OF THE RECEIVE BYTE COUNTER. THE RECEIVE COUNTER WILL BE CHECKED TO INSURE THAT THE COUNTER HAS REMAINED AT THE MAXIMUM VALUE, IF NOT AN ERROR IS PASSED TO THE HOST.

TEST 30: FIFO TEST

THERE ARE TWO FIFO'S USED IN THE DEUNA TO KEEP TRACK OF RECEIVER BUFFERS. THE FIRST IS CALLED THE 'RECEIVER BUFFER AVAILABLE FIFO' AND THE SECOND IS CALLED THE 'RECEIVER BUFFER DONE FIFO'.

THE T11 LOADS THE RECEIVER BUFFER AVAILABLE FIFO WITH A LIST OF UNUSED 1K BUFFERS IN LINK MEMORY. WHEN THE DEUNA SENSES THAT A PACKET IS COMING IN IT PULLS AN AVAILABLE BUFFER ADDRESS FROM THE OUTPUT OF THE RECEIVER BUFFER AVAILABLE FIFO AND USES IT TO ADDRESS LINK MEMORY FOR THE STORAGE OF THE RECEIVED DATA. AFTER THE DATA HAS BEEN LOADED THE RECEIVER STATE MACHINE PUTS THE USED BUFFER ADDRESS INTO THE RECEIVER BUFFER DONE FIFO WHERE AN INTERRUPT IS GENERATED TO THE T11 WHEN IT BUBBLES TO THE TOP OF THE FIFO.

THESE FIFO'S ARE 64 DEEP BY 4 BITS WIDE. THE OPERATIONAL MICROCODE ONLY FILLS THE FIFO TO A MAXIMUM OF 16. THE 4 BIT WIDTH REPRESENTS BITS 14-11 OF THE LINK MEMORY ADDRESS. THESE BITS ALLOW THE ADDRESSING OF A 1K BUFFER IN LINK MEMORY.

THIS TEST WILL VERIFY THAT THE RECEIVE BUFFER AVAILABLE FIFO AND THE RECEIVER BUFFER DONE FIFO OPERATE CORRECTLY. THIS WILL BE DONE BY LOADING THE RECEIVER BUFFER AVAILABLE FIFO WITH A 1K BUFFER ADDRESS THEN A PACKET WILL BE TRANSMITTED IN LOOPBACK MODE. AFTER THE RECEIVER INTERRUPT OCCURS THE RECEIVER BUFFER DONE FIFO IS READ AND THE ADDRESS IS COMPARED WITH WHAT WAS GIVEN THE RECEIVER BUFFER AVAILABLE FIFO. THEY SHOULD BE THE SAME IF EVERYTHING IS WORKING CORRECTLY. THE OPERATION IS PERFORMED WITH THE TRANSMITTER BUFFER SET TO 0 AND WILL BE REPEATED WITH RECEIVER BUFFERS 1-15. THIS TEST WILL USE MICROMODULE 'D' MICROTEST #10.

PARAMETERS PASSED TO THE MICROCODE WILL BE FORMATTED IN THE PCBB AS FOLLOWS:

PCBB+0: RECEIVE BUFFER ADDRESS
 PCBB+2: RECEIVE BUFFER COMPLETED ADDRESS

TEST 31: RECEIVER LINK MEMORY ADDRESS TEST

THIS TEST WILL VERIFY THAT BUFFERS 1-15 OF LINK MEMORY CAN BE ADDRESSED CORRECTLY BY THE RECEIVER. THIS WILL BE DONE BY DIRECTING THE MICROCODE TO TRANSMIT A DATA PATTERN FROM BUFFER 0 AND TO RECEIVE THE DATA IN BUFFER X WHERE X = 1-15. THEN A CHECK WILL BE MADE TO SEE IF THE PATTERN NOT ONLY ARRIVED IN THE CORRECT RECEIVER BUFFER BUT THAT THE PATTERN DOES NOT SHOW UP ANYWHERE ELSE IN LINK MEMORY EXCEPT WHERE IT WAS SUPPOSE TO.

THIS TEST WILL USE MICROMODULE 'D' MICROTEST #11. THIS MICROTEST ACCEPTS 2 PARAMETERS: THE TRANSMIT BUFFER AND THE RECEIVER BUFFER. IT WILL SET UP A DATA PATTERN IN THE TRANSMIT BUFFER AND TELL THE LINK TO TRANSMIT, IN LOOPBACK MODE, FROM THE TRANSMIT BUFFER GIVEN TO THE RECEIVER BUFFER GIVEN. AFTER THE RECEIVER INTERRUPT, THE DATA IS CHECKED IN THE EXPECTED RECEIVER BUFFER FOR THE CORRECT DATA PATTERN. THEN ALL OF LINK MEMORY (EXCEPT FOR THE TRANSMIT BUFFER) IS CHECKED TO SEE IF THE PATTERN ENDS UP ELSEWHERE. IF AN ERROR IS FOUND THE MICROCODE PASSES THE ADDRESS OF LINK MEMORY WHERE THE ERROR WAS FOUND, THE DATA THAT WAS FOUND THERE ALONG WITH THE DATA THAT SHOULD HAVE BEEN THERE.

THE PARAMETERS FOR THE MICROCODE ARE FORMATED IN THE PCBB AS FOLLOWS:

PCBB+0: RECEIVER BUFFER ADDRESS
 PCBB+2: TRANSMIT BUFFER ADDRESS
 PCBB+4: LINK MEMORY ADDRESS (IF ERROR)
 PCBB+6: GOOD DATA PATTERN (IF ERROR)

PCBB+10: BAD DATA PATTERN (IF ERROR)

TEST 32: TRANSMITTER LINK MEMORY ADDRESS TEST

THIS TEST WILL VERIFY THAT BUFFERS 1-15 CAN BE CORRECTLY ADDRESSED BY THE TRANSMITTER. THIS TEST IS IDENTICAL TO THE RECEIVER ADDRESS TEST IN THAT IT USES THE SAME MICROCODE (MICROMODULE 'D' MICROTEST #11) EXCEPT IT FIXES THE RECEIVER BUFFER AT 0 AND VARIES THE TRANSMIT BUFFER FROM 1-15.

TEST 33: LINK MEMORY ARBITRATION TEST

THE LINK MEMORY CAN BE ACCESSED BY FOUR PROCESSES; THE T-11 PROCESSOR, THE DMA ENGINE, THE RECEIVE STATE MACHINE AND THE TRANSMIT STATE MACHINE. THE PORT MODULE HAS ARBITRATION CIRCUITRY TO MANAGE LINK MEMORY ACCESSES. THIS CIRCUITRY PREVENTS CONFLICTS BETWEEN PROCESSES AND ASSURES THAT HIGHER PRIORITY PROCESSES GET PRECEDENCE.

THIS TEST WILL VERIFY THE ABILITY OF THE LINK MEMORY ARBITRATOR TO HANDLE SIMULTANEOUS REQUESTS BY FOUR PROCESSES. EACH OF THESE PROCESSES WILL INVOLVE TASKS THAT REQUIRE HEAVY ACCESSES OF LINK MEMORY. DATA WILL BE MOVED INTO OR OUT OF LINK MEMORY BY EACH. WHEN THAT TASKS ARE FINISHED THE DATA WILL BE VERIFIED.

THE FOUR PROCESSES ARE:

1-TRANSMIT STATE MACHINE

WILL TRANSMIT A DATAGRAM OF MAXIMUM DATA LENGTH IN LOOPBACK MODE. THE DATA FIELD WILL CONTAIN A BIT PATTERN STRING OF TWO 1'S FOLLOWED BY TWO 0'S I.E. 31463 (OCTAL).

2-RECEIVE STATE MACHINE

WILL RECEIVE A DATAGRAM OF MAXIMUM DATA LENGTH OVER THE LOOPBACK. THE RECEIVE DATA BUFFER WILL BE FILLED WITH 0'S PRIOR TO THE RECEPTION.

3-T-11 MICROPROCESSOR DMA

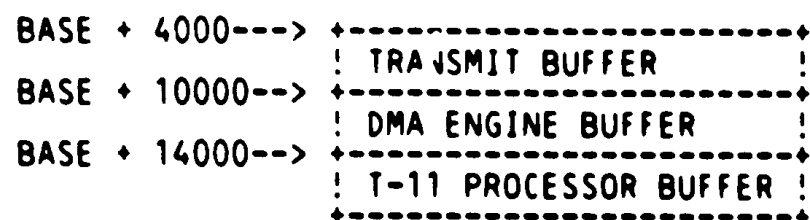
A 1K BUFFER IN LINK MEMORY WILL BE FILLED WITH AN ALL 1'S DATA PATTERN PRIOR TO THE OPERATION THEN ALTERNATING 1'S AND 0'S DATA PATTERN WILL BE WRITTEN.

4-DMA ENGINE

WILL TRANSFER A 1K BLOCK OF DATA FROM LINK MEMORY TO UNIBUS MEMORY. THE DATA IN LINK MEMORY WILL A BIT PATTERN STRING OF FOUR 1'S FOLLOWED BY A STRING OF FOUR 0'S. THE BUFFER IN UNIBUS MEMORY WILL BE CLEARED PRIOR TO THE OPERATION.

THE FOUR PROCESSES WILL WORK OUT OF FOUR SEPARATE AREAS OF LINK MEMORY.

BASE OF LINK MEMORY--> +-----+
! RECEIVE BUFFER !



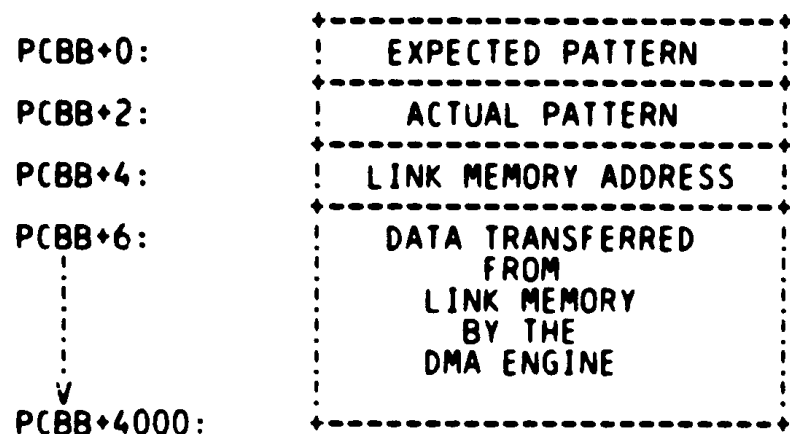
THIS WILL ALLOW THE ARITRATION CIRCUITRY TO BE TESTED AND YET ALLOWS THE DATA TO BE VERIFIED EASILY AND ASSOCIATED WITH A SINGLE PROCESS.

A DATAGRAM WILL BE LOOPED BACK FROM THE TRANSMIT BUFFER TO THE RECEIVE BUFFER. AS THE DATAGRAM IS BEING TRANSFERRED, THE T-11 PROCCESOR WILL FILL IT'S BUFFER AND THE DMA ENGINE WILL TRANSFER IT'S BUFFER FROM LINK MEMORY TO UNIBUS MEMORY.

WHEN THE RECEIVE STATE MACHINE IS DONE, THE T-11 WILL VERIFY THE DATA IN THE RECEIVE BUFFER. IF AN ERROR IS FOUND PCSR1 WILL BE SET TO INDICATE AN ERROR.

THE HOST WILL WAIT FOR THE MICROCODE TO FINISH AND WHEN DONE, IT WILL VERIFY THE DATA TRANSFERRED BY THE DMA ENGINE TO UNIBUS MEMORY.

THE FIRST 3 WORDS OF THE PCBB ARE USED FOR ERROR INFORMATION, THE REST WILL BE THE UNIBUS ADDRESS THAT THE DMA ENGINE WILL TRANSFER TO.



TEST 34: STATION ADDRESS PATTERN TEST

WITHOUT EITHER THE PROMISCUIOUS MODE OR THE MULTICAST MODE ENABLED, THE LINK LOGIC WILL RECOGNIZE DATAGRAM ADDRESSES ONLY IF THE ADDRESS IS CONTAINED IN THE STATION ADDRESS RAM.

WHEN A DATAGRAM ARRIVES, THE LINK LOGIC COMPARES THE DATAGRAM DESTINATION ADDRESS FIELD TO THE 12 ADDRESSES WRITTEN IN THE STATION ADDRESS RAM. IF THE INCOMING ADDRESS MATCHES ONE OF THESE, THEN THE DATAGRAM WILL BE ACCEPTED BY THE LINK. THE 'MATCH' BIT IS SET IN THE TRANSMIT BUFFER AND THE RECEIVING PROCESS BEGINS.

THIS TEST WILL VERIFY THAT THE LINK CAN RECOGNIZE A DATAGRAM WHEN THE DESTINATION ADDRESS OF THE DATAGRAM MATCHES ONE OF THE ADDRESSES STORED IN THE STATION ADDRESS RAM.

THIS TEST WILL USE MICROMODULE 'E' MICROTEST #1. PATTERNS WILL BE USED FOR ADDRESSES IN CHECKING THE STATION ADDRESS LOGIC. THE PATTERNS WILL BE SUPPLIED TO THE T-11 THROUGH THE PCBB. THE MICROCODE WILL BE RESTARTED FOR EACH DIFFERENT PATTERN TO BE TESTED. UPON START-UP, THE T-11 PROCESSOR WILL PICK UP THE CURRENT PATTERN/ADDRESS, LOAD THE SAME PATTERN INTO ALL 12 LOCATIONS OF THE STATION ADDRESS RAM, FORMAT THE TRANSMIT BUFFER AND LOGIC FOR A LOOPBACK, PRESET PCSR1 TO AN ERROR CONDITION, START THE LINK AND WAIT FOR THE MATCH BIT IN THE TRANSMIT BUFFER. IF THE MATCH BIT SETS THE PCSR1 ERROR CONDITION IS CLEARED AND THE T-11 WAITS FOR BOTH THE TRANSMITTER AND RECEIVER INTERRUPTS BEFORE IT SETS 'DNI' TO INDICATE THE TEST WAS SUCCESSFUL

THE PCBB WILL BE USED TO PASS THE 48 BIT STATION ADDRESS PATTERN:

```

PCBB+0:  :-----:
          : STATION ADDRESS LOW :
          :-----:
PCBB+2:  :-----:
          : STATION ADDRESS MIDDLE :
          :-----:
PCBB+4:  :-----:
          : STATION ADDRESS HIGH :
          :-----:

```

THE FOLLOWING PATTERNS WILL BE USED:

- ALTERNATING 1'S AND 0'S
- ALTERNATING 0'S AND 1'S
- PAIR OF 0'S FOLLOWED BY PAIR OF 1'S
- FOUR 0'S FOLLOWED BY FOUR 1'S
- EIGHT 0'S FOLLOWED BY EIGHT 1'S
- SIXTEEN 1'S FOLLOWED BY SIXTEEN 0'S FOLLOWED BY 16 1'S
- TWENTYFOUR 1'S FOLLOWED BY TWENTYFOUR 0'S

TEST 35: STATION ADDRESS REJECTION TEST

THIS TEST WILL VERIFY THAT THE STATION ADDRESS DETECTION LOGIC DOES NOT RECOGNIZE A DATAGRAM WHEN THE DATAGRAM ADDRESS IS NOT CONTAINED IN THE STATION ADDRESS RAM.

THE MICROCODE WILL FILL THE STATION ADDRESS RAM WITH 0'S. THE DESTINATION FIELD OF THE TRANSMIT BUFFER IS FILLED WITH 1'S. A TRANSMISSION IS STARTED IN LOOPBACK MODE AND THE T-11 WILL WAIT FOR A RECEIVER INTERRUPT. OF COURSE, THE RECEIVER INTERRUPT SHOULD NEVER HAPPEN BECAUSE THE STATION ADDRESS LOGIC SHOULD NOT GET A SUCCESSFUL COMPARISON BETWEEN 0'S IN THE DESTINATION ADDRESS OF THE INCOMING DATAGRAM AND THE 1'S IN THE STATION ADDRESS RAM. THE T-11 WILL BE PUT INTO A LOOP WAITING FOR A RECEIVER INTERRUPT AND THE DEUNA TIMER IS STARTED. IF THE LOOP IS BROKEN BY THE RECEIVER INTERRUPT AN ERROR WILL BE PRESENTED IN PCSR1 BY THE MICROCODE. IF THE LOOP IS BROKEN BY THE TIMER THEN THE TEST WAS SUCCESSFUL.

TEST 36: STATION ADDRESS RAM POSITION TEST

THE STATION ADDRESS RAM CAN HOLD UP TO 12 STATION ADDRESSES. WHEN A DATAGRAM IS RECEIVED THE STATION ADDRESS COMPARISON LOGIC DOES A BIT-WISE COMPARISON OF ALL 12 RAM STATION ADDRESS WITH THE INCOMING DATAGRAM STATION ADDRESS.

THIS TEST WILL VERIFY THAT THE DEUNA CAN RECOGNIZE A STATION ADDRESS REGARDLESS OF THE LOCATION OF THE ADDRESS IN THE STATION ADDRESS RAM.

THIS TEST WILL USE MICROMODULE 'E' MICROTEST #4. THE MICROCODE WILL WRITE A STATION ADDRESSES OF ALL 1'S INTO A SINGLE LOCATION OF THE STATION ADDRESS RAM. THE OTHER ELEVEN LOCATION WILL BE LOADED WITH 0'S. A DATAGRAM WITH AN ALL 1'S DESTINATION ADDRESS WILL BE TRANSMITTED IN LOOPBACK MODE. THE TEST WILL VERIFY THAT THE DATAGRAM IS RECEIVED. THE TEST WILL BE REPEATED FOR ALL TWELVE LOCATIONS OF THE STATION ADDRESS RAM.

THE MICROTEST WILL BE REPEATED FOR EACH OF THE 12 TEST ITERATIONS. THE PCBB WILL BE USED TO PASS TO THE MICROCODE WHICH POSITION IS TO BE LOADED WITH 1'S. WHEN THE STATION ADDRESS IS LOADED, THE STATION ADDRESSES MUST BE ROTATED ORTHOGONALLY, I.E. BIT 0 OF ALL STATION ADDRESSES LOADED TOGETHER, THEN BIT 1, THEN BIT 2 ETC. THIS MAKES IT DIFFICULT TO DESCRIBE THE POSITION OF ANY SINGLE STATION ADDRESS IN TERMS OF AN OFFSET FROM THE RAM BASE ADDRESS.

THE PCBB IS FORMATTED AS FOLLOWS:

```
PCBB+0:      +-----+
              | RAM ADDRESS POSTION |
              +-----+
```

TEST 37: MULTICAST ADDRESS TEST

MULTICAST ADDRESSING PERMITS THE DEUNA TO RESPOND TO MESSAGES AIMED AT LOGICALLY RELATED DEVICES ON THE NETWORK. THE MSB OF THE DESTINATION ADDRESS OF THESE MESSAGES IS A 1. THIS BIT IS DETECTED BY THE ADDRESS RECOGNITION LOGIC.

THIS TEST WILL VERIFY THAT THE DEUNA CAN RECOGNIZE AND ACCEPT MESSAGES WITH THE MULTICAST BIT DESIGNATION.

THIS TEST WILL USE MICROMODULE 'E' MICROTEST #4. THE MICROCODE WILL PREPARE A DATAGRAM WITH THE DESTINATION ADDRESS HAVING THE MULTICAST BIT SET. THE DEUNA WILL BE SETUP IN LOOPBACK MODE WITH 'ENABLE ALL MULTICAST'. THE DATAGRAM WILL BE TRANSMITTED AND THE T-11 WILL BE PUT IN A LOOP WAITING FOR A RECEIVER INTERRUPT. THE TIMER WILL INTERRUPT THE LOOP IF THE RECEIVER INTERRUPT DOES NOT OCCUR. IF THIS HAPPENS, PCSR1 WILL INDICATE AN ERROR, OTHERWISE WHEN THE RECEIVER INTERRUPT OCCURS IT WILL BREAK THE LOOP AND PCSR1 WILL INDICATE A SUCCESSFULL COMPLETION OF THE TEST.

TEST 38: CRC DATA PATTERN TEST

THE LINK MODULE HAS HARDWARE TO GUARANTEE THAT DATAGRAMS HAVE NOT BEEN CORRUPTED DURING TRANSMISSION AND RECEPTION. THE HARDWARE GENERATES A CRC FOR DATAGRAMS TRANSMITTED AND VERIFIES THE CRC FOR DATAGRAMS RECEIVED. THE CRC IS A 32 BIT NUMBER GENERATED BY DIVIDING THE DATAGRAM BIT STREAM BY A CRC POLYNOMIAL. THE DIVISION RESULTS IN A UNIQUE NUMBER THAT CAN ONLY BE REPRODUCED IN CRC CALCULATIONS IF THE BIT STREAM EXACTLY MATCHES THE ORIGINAL. THE CRC IS CALCULATED DURING DATAGRAM TRANSMISSION AND IS APPENDED TO THE PACKET. THE CRC IS TRANSMITTED AS PART OF THE PACKET. THE CRC IS AGAIN

CALCULATED WHEN THE DATAGRAM IS RECEIVED AND THE CALCULATED IS COMPARED TO THE CRC TRANSMITTED. IF THE DATAGRAM HAS BEEN FAITHFULLY TRANSMITTED, THE CRC'S SHOULD MATCH EXACTLY.

THE DEUNA CALCULATES THE CRC WITH DEDICATED CRC LOGIC. THE LOGIC IS EITHER DEDICATED TO THE CALCULATION OF THE OUTGOING DATAGRAM OR THE CALCULATION OF THE INCOMING DATAGRAM, BUT NOT BOTH.

THIS TEST WILL VERIFY THE OPERATION OF THE CRC CALCULATION CIRCUITRY. MICROMODULE 'F' MICROTEST #1 WILL BE USED. THE MICROCODE WILL TRANSMIT DATAGRAMS IN LOOPBACK MODE. THE CRC HARDWARE WILL BE DEDICATED TO THE TRANSMITTER. WHEN THE DATAGRAM IS RECEIVED THE T-11 WILL CALCULATE A CRC ON THE DATA RECEIVED (INCLUDING THE TRANSMITTED CRC). THE RESULT OF THIS CALCULATION WILL BE A 32 BIT CONSTANT. THIS CONSTANT IS THEN COMPARED TO WHAT WAS EXPECTED AND IF THEY DO NOT MATCH. AN ERROR IS PLACED IN PCSR1.

PATTERNS WILL BE PASSED TO THE MICROCODE THROUGH THE PCBB. THE MICROCODE WILL FILL THE TRANSMIT BUFFER WITH THIS PATTERN BEFORE EACH TRANSMISSION TAKES PLACE.

THE PCBB WILL BE FORMATTED AS FOLLOWS:

```
PCBB+0:      +-----+
              ! DATA PATTERN !
              +-----+
```

TEST 39: CRC ERROR TEST

THIS TEST WILL VERIFY THAT THE LINK CRC CIRCUITRY CAN DETECT A BAD CRC.

MICROMODULE 'F' MICROTEST #2 WILL BE USED. THE MICROCODE WILL TRANSMIT DATAGRAMS IN LOOPBACK MODE. EACH DATAGRAM WILL HAVE AN ERRONEOUS CRC APPENDED TO THE DATA FIELD. THE DEUNA CRC LOGIC WILL BE SETUP SUCH THAT THE CRC LOGIC WILL BE DEDICATED TO THE RECEIVER. THIS IS EXPECTED TO CAUSE A CRC ERROR.

THE DATA FIELDS OF EACH DATAGRAM WILL CONSIST OF PATTERNS. THE PATTERNS WILL BE PASSED TO THE MICROCODE VIA THE PCBB.

AFTER THE RECEIVER INTERRUPT THE MICROCODE WILL PASS THE RECEIVER STATUS WORD 0 BACK VIA PCBB+2. THE CRC BIT IN THIS WORD IS CHECKED TO SEE IF IT IS SET.

THE PCBB IS FORMATTED AS FOLLOWS:

```
PCBB+0:      +-----+
              ! DATA PATTERN !
              +-----+
PCBB+2:      ! RECEIVER STATUS WORD !
              +-----+
```

TEST 40: CRC PATTERN LENGTH TEST

THIS TEST WILL VERIFY THAT THE RECEIVE CRC HARDWARE CAN CALCULATE CRC FOR

DATAGRAMS OF VARYING LENGTHS.

DATAGRAMS WILL BE TRANSMITTED FOR THE TRANSMIT BUFFER TO THE RECEIVE BUFFER IN LOOPBACK MODE. THE TRANSMIT CRC WILL BE DISABLED WHICH WILL ASSIGN THE CRC LOGIC TO CALCULATION OF INCOMING DATAGRAMS. THE CRC FOR TRANSMIT DATAGRAMS WILL BE CALCULATED BY THE MICROCODE. IT IS EXPECTED THAT THE CRC LOGIC WILL VERIFY THE CRC APPENDED TO THE DATAGRAM AS IT IS BEING RECEIVED.

PATTERNS WILL BE USED TO FILL THE DATAGRAM DATA FIELD. THE PATTERNS WILL BE PASSED TO THE MICROCODE THROUGH THE PCBB ALONG WITH THE BYTE COUNT TO BE USED.

AFTER THE RECEPTION OF THE DATAGRAM THE RECEIVER STATUS WORD WILL BE PASSED BACK VIA THE PCBB SO IT CAN BE CHECKED

THE PCBB IS FORMATTED AS FOLLOWS:

```

PCBB+0:      +-----+
              | DATA PATTERN |
              +-----+
PCBB+2:      +-----+
              |  BYTE COUNT  |
              +-----+
PCBB+4:      +-----+
              | RECEIVE STATUS WORD |
              +-----+
    
```

TEST 41: RECEIVER BUFFER RECOVERY - RUNT TEST

THIS TEST WILL CHECK THE ABILITY OF THE RECEIVE STATE MACHINE TO REJECT A DATAGRAM OF LESS THAN 64 BYTES AND TO RECOVER THE RECEIVER BUFFER.

THIS TEST WILL USE MICROMODULE 'F' MICROTTEST #4. EACH TRIAL WILL CONSIST OF TWO DATAGRAM TRANSMISSIONS IN LOOPBACK MODE. EACH TRANSMISSION WILL LOOPBACK A DATAGRAM FILLED WITH UNIQUE DATA. THE FIRST DATAGRAM WILL BE A RUNT OF LESS THAN 64 BYTES. THE SECOND WILL BE A DATAGRAM OF LEGAL SIZE.

EACH TRIAL WILL START WITH THE LINK BUFFER POINTER RESET TO THE FIRST LINK BUFFER. THE RUNT WILL BE TRANSMITTED, THEN THE VALID DATAGRAM. IF THE BUFFER RECOVERY IS WORKING CORRECTLY, THE SECOND DATAGRAM IS EXPECTED TO BE WRITTEN INTO THE SAME LINK MEMORY BUFFER AS WAS THE RUNT.

THIS TEST WILL BE REPEATED WITH VARIOUS RUNT PACKET SIZES.

THE BYTE COUNT FOR THE RUNT PACKET TRANSMISSION WILL BE PASSED VIA THE PCBB. AFTER THE TWO TRANSMISSIONS, THE MICROCODE WILL PASS BACK THE CONTENTS OF THE BUFFER DONE FIFO, AND THE CONTENTS OF THE FIRST DATA WORD OF THE RECEIVER BUFFER.

THE PCBB WILL BE FORMATTED AS FOLLOWS:

```

PCBB+0:      +-----+
              | RUNT BYTE COUNT |
              +-----+
PCBB+2:      +-----+
              | BUFFER DONE FIFO CONTENTS |
              +-----+
PCBB+4:      +-----+
              | FIRST DATA WORD OF BUFFER |
              +-----+
    
```

TEST 42: HALF-DUPLEX TEST

THE LINK INCLUDES A 'HALF DUPLEX' MODE OF OPERATION. THIS MODE CAN BE ENABLED OR DISABLED THROUGH THE LINK MODE REGISTER. THE OPERATIONAL MICROCODE NORMALLY USES HALF DUPLEX MODE.

IN THE HALF-DUPLEX MODE, THE LINK WILL NOT RECEIVE MESSAGES ADDRESSED TO ITSELF. INCOMING MESSAGES LOOPED BACK WILL BE IGNORED BY THE RECEIVE STATE MACHINE. THE STATE MACHINE WILL NOT ISSUE A 'RECEIVER DONE' INTERRUPT AND THE BUFFER CAN BE RECOVERED FOR RECEIVING A LATER DATAGRAM.

THIS TEST USES MICROMODULE 'F' MICROTEST #5. THIS TEST WILL VERIFY THE OPERATION OF HALF-DUPLEX MODE. A DATAGRAM WILL BE TRANSMITTED IN LOOPBACK MODE WITH THE HALF-DUPLEX BIT SET. THE MICROCODE WILL VERIFY THAT THE RECEIVER INTERRUPT DOES NOT OCCUR. THE MICROCODE WILL THEN CLEAR THE HALF-DUPLEX BIT AND LOOP A DATAGRAM AND VERIFY THAT THE ORIGINAL BUFFER WAS RECOVERED.

THIS TEST WILL USE THE PCBB TO PASS INFORMATION. PCBB+0 WILL BE USED TO PASS THE CONTENTS OF THE BUFFER DONE FIFO AFTER THE SECOND DATAGRAM IS RECEIVED. PCBB+4 WILL BE USED TO PASS THE FIRST WORD OF DATA FROM THE RECEIVER BUFFER AFTER THE SECOND DATAGRAM IS TRANSMITTED.

PCBB+0: +-----+
 ! CONTENTS OF BUFFER DONE FIFO !
 +-----+
PCBB+2: +-----+
 ! FIRST DATA WORD OF BUFFER DONE!
 +-----+

THE CONTENTS OF THE BUFFER DONE FIFO SHOULD BE 0 AND THE FIRST DATA WORD SHOULD BE AN ALTERNATING 1'S AND 0'S PATTERN.

TEST 43: COLLISION TEST

THE RECEIVE STATE MACHINE REACTS TO COLLISIONS ON THE WIRE BY ACTIVATING THE RETRY LOGIC. THE RETRY LOGIC WAITS AN INTERVAL OF TIME BEFORE ATTEMPTING TO RETRANSMIT THE DATAGRAM. THE INTERVALS ARE NOT UNIFORM BUT ARE OF GENERALLY INCREASING PSEUDO-RANDOM DURATION. THE RETRY LOGIC WILL ATTEMPT TO RETRANSMIT UP TO 15 ADDITIONAL TIMES BEFORE GIVING UP.

THIS TEST WILL VERIFY THAT THE RECEIVE STATE MACHINE RESPONDS TO A COLLISION AND THAT THE RETRY SEQUENCE IS REPORTED CORRECTLY IN THE TRANSMIT STATUS WORD.

THIS TEST WILL USE MICROMODULE 'G' MICROTEST #1. THE LINK BOARD CONTAINS DIAGNOSTIC LOGIC THAT ALLOWS COLLISIONS TO BE SIMULATED. WITH THE FORCE COLLISIONS LOGIC ACTIVATED, THE RETRY HARDWARE CAN BE STEPPED THROUGH THE RETRY SEQUENCE. THAT IS, EVERY DATAGRAM LOOPED BACK WILL STEP THE RETRY LOGIC THROUGH ONE STEP OF THE RETRY SEQUENCE. THE RETRY SEQUENCE CAN BE VERIFIED BY CHECKING THE TRANSMIT BUFFER STATUS WORDS AFTER EACH RETRY STEP.

THE PCBB WILL BE USED TO PASS PARAMETERS BETWEEN THE MICROCODE AND THE HOST

PROCESSOR. PCBB+0 WILL BE USED TO PASS THE DATA TO BE LOADED INTO THE LINK MODE WORD. PCBB+2 WILL BE PASSED BACK BY THE MICROCODE, IT IS THE FIRST WORD OF THE TRANSMIT BUFFER (TRANSMIT STATUS WORD 0). PCBB+4 WILL ALSO BE PASSED BACK, IT IS TRANSMIT STATUS WORD 1.

THE TRANSMIT STATUS WORDS SHOULD SHOW THE FOLLOWING STATUS:

LOOPBACK STEP #	STATUS BITS			
	WORD 0			WORD 1
	ERRS (14)	MORE (12)	ONE (11)	RETRY (10)
1	0	0	1	0
2-15	0	1	0	0
16	1	0	0	1

THE PCBB IS FORMATTED AS FOLLOWS:

```

PCBB+0:  +-----+
          | LINK MODE WORD |
          +-----+
PCBB+2:  +-----+
          | TRANSMIT STATUS WORD 0 |
          +-----+
PCBB+4:  +-----+
          | TRANSMIT STATUS WORD 1 |
          +-----+

```

TEST 44: TDR COUNTER TEST

THE DEUNA HAS A COUNTER DESIGNED TO HELP LOCATE FAULTS IN THE COAXIAL CABLE. THE COUNTER IS INITIALIZED WHEN A MESSAGE IS TRANSMITTED AND INCREMENTS AS THE DATAGRAM IS TRANSMITTED. COUNTING WILL STOP IF A COLLISION OCCURS OR THE CARRIER IS LOST. COUNTING ALSO STOPS IF THE 10 BIT COUNTER REACHES ITS MODULUS.

THIS TEST WILL DETERMINE THAT THE TDR COUNTER VALUE WILL CHANGE AND THAT THE COUNTER IS NOT STUCK.

BECAUSE THE COUNTER COUNTS DURING TRANSMISSION OF A DATAGRAM AND WILL CONTINUE TO COUNT DURING THE TIME THAT THE TRANSMIT STATE MACHINE OPERATES, THE COUNT ACCUMULATED IN THE COUNTER DURING TRANSMISSION IS PROPORTIONAL TO THE LENGTH OF THE DATAGRAM. THIS TEST WILL USE THIS RELATION TO VERIFY THAT THE COUNTER IS NOT STUCK.

THIS TEST USES MICROMODULE 'G' MICROTEST #2. THE TEST WILL SEND DATAGRAMS OVER THE LOOPBACK. THE LENGTH OF THE DATAGRAM WILL BE VARIED BY USING AN INCREASING BYTE COUNT IN THE TRANSMIT BUFFER.

AFTER EACH DATAGRAM HAS BEEN LOOPED BACK, THE TRANSMIT BUFFER WORD 1 WILL BE PASSED BACK TO THE HOST TO VERIFY THAT IT IS CORRECT. THE CRITERIA FOR CORRECTNESS WILL BE: INCREASING BYTE COUNTS SHOULD RESULT IN INCREASING TDR VALUES IN TRANSMIT STATUS WORD 1.

THE PCBB WILL BE FORMATED AS FOLLOWS:

```

PCBB+0:      +-----+
              |          |
              |  BYTE COUNT  |
              |          |
              +-----+
PCBB+2:      +-----+
              |          |
              | TRANSMIT STATUS WORD 1 |
              |          |
              +-----+
  
```

TEST 45: RETRY LOGIC TEST

THE RETRY LOGIC IS ACTIVATED WHENEVER A COLLISION IS ENCOUNTERED DURING A TRANSMISSION ATTEMPT. THE LINK STOPS TRANSMISSION AND WAITS FOR A PERIOD OF TIME BEFORE ATTEMPTING TO RETRANSMIT.

THE WAIT TIME IS AN INTEGRAL NUMBER OF 'SLOT TIMES'. THE NUMBER COMES FROM A RANDOM NUMBER GENERATOR. THE NUMBER OF SLOT TIMES IS NOT EXACTLY RANDOM SINCE THE RETRY LOGIC WAITS A GENERALLY INCREASING NUMBER OF SLOT TIMES BEFORE TRYING TO RETRANSMIT. THIS TEST WILL VERIFY THAT THE RETRY LOGIC IS CAPABLE OF GENERATING VARIABILITY IN THE DURATION OF THE RETRY WAIT TIMES.

THIS TEST WILL USE MICROMODULE 'G' MICROTEST #3
 THE LINK MODULE HAS A DIAGNOSTIC MAINTENANCE FACILITY MAKING IT POSSIBLE TO SINGLE STEP THE RETRY LOGIC THROUGH THE MAXIMUM SIXTEEN RETRY STEPS. THIS FEATURE WILL ALSO MAKE IT POSSIBLE TO MEASURE THE RETRY WAIT INTERVAL.

THE MICROCODE WILL SET THE COLLISION BIT IN THE LINK MODE REGISTER AND AND TRANSMIT A DATAGRAM IN LOOPBACK MODE. THE T-11 WILL COUNT WHILE WAITING FOR THE TRANSMIT STATE MACHINE TO INTERRUPT. THE ACCUMULATED COUNT SHOULD PROVIDE A MEASURE OF TIME TAKEN FOR THE TRANSMISSION ATTEMPT TO OCCUR. SINCE THE COLLISION BIT IS SET, THIS INTERVAL WILL INCLUDE THE RETRY WAIT INTERVAL. THE ACCUMULATED COUNT WILL BE WRITTEN BY THE MICROCODE TO THE PCBB.

THE MICROTEST WILL BE EXECUTED 16 TIMES. AFTER EACH EXECUTION, THE COUNT WILL BE READ FROM THE PCBB AND STORED IN A TABLE. THE TABLE WILL BE SCANNED TO VERIFY THAT THEY ARE NOT ALL THE SAME.

THE PCBB IS FORMATED AS FOLLOWS:

```

PCBB+0:      +-----+
              |          |
              |  BYTE COUNT  |
              |          |
              +-----+
PCBB+2:      +-----+
              |          |
              | TRANSMIT WAIT COUNT |
              |          |
              +-----+
  
```

TEST 46: PRINT DEVICE PARAMETERS TEST

THIS TEST PRINTS THE DEFAULT PHYSICAL ADDRESS, THE MICROCODE REVISION AND THE SWITCH PACK SETTINGS.

1387
1388
1389
1390
1391
1392 000000'
1393
1394
1395
1396
1397
1398
1399 000000'
1400
1401 000000'
1402 000000'
1403 000000' 125
1404 000001' 116
1405 000002' 101
1406 000003' 122
1407 000004' 105
1408 000005' 120
1409 000006' 000
1410 000007' 000
1411 000010'
1412 000010' 101
1413 000011'
1414 000011' 060
1415 000012'
1416 000012' 000000
1417 000014'
1418 000014' 000000
1419 000016'
1420 000016' 053464'
1421 000020'
1422 000020' 000000
1423 000022'
1424 000022' 000262'
1425 000024'
1426 000024' 000000
1427 000026'
1428 000026' 000000G
1429 000030'
1430 000030' 000000
1431 000032'
1432 000032' 000000
1433 000034'
1434 000034' 000000
1435 000036'
1436 000036' 000000
1437 000040'
1438 000040' 000124'
1439 000042'
1440 000042' 000340
1441 000044'
1442 000044' 000000

.TITLE PROGRAM HEADER AND TABLES
.SBTTL PROGRAM HEADER

.ENABL AMA
BGNMOD

:++
: THE PROGRAM HEADER IS THE INTERFACE BETWEEN
: THE DIAGNOSTIC PROGRAM AND THE SUPERVISOR.
:--

POINTER BGNRPT,BGNAU,BGNDU
HEADER UNAREP,A,0,0,0,340

LSNAME::
 .ASCII /U/
 .ASCII /N/
 .ASCII /A/
 .ASCII /R/
 .ASCII /E/
 .ASCII /P/
 .BYTE 0
 .BYTE 0
LSREV::
 .ASCII /A/
LSDEPO::
 .ASCII /O/
LSUNIT::
 .WORD 0
LSTIML::
 .WORD 0
LSHPCP::
 .WORD LSHARD
LSSPCP::
 .WORD 0
LSHPTP::
 .WORD LSHW
LSSPTP::
 .WORD 0
LSLADP::
 .WORD LSLAST
LSSTA::
 .WORD 0
LSCO::
 .WORD 0
LSDTYP::
 .WORD 0
LSAPT::
 .WORD 0
LSDTP::
 .WORD 0
LSPRIO::
 .WORD L\$DISPATCH
LSENV1::
 .WORD 340
 .WORD 0

PROGRAM HEADER AND TABLES
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PROGRAM HEADER

GL
CZ

1443	000046'	
1444	000046'	000000
1445	000050'	
1446	000050'	003
1447	000051'	003
1448	000052'	
1449	000052'	000000
1450	000054'	000000
1451	000056'	
1452	000056'	000000
1453	000060'	
1454	000060'	000576'
1455	000062'	
1456	000062'	021110'
1457	000064'	
1458	000064'	000000
1459	000066'	
1460	000066'	000000
1461	000070'	
1462	000070'	021640'
1463	000072'	
1464	000072'	021632'
1465	000074'	
1466	000074'	000000
1467	000076'	
1468	000076'	000704'
1469	000100'	
1470	000100'	104035
1471	000102'	
1472	000102'	000000
1473	000104'	
1474	000104'	021124'
1475	000106'	
1476	000106'	021472'
1477	000110'	
1478	000110'	021470'
1479	000112'	
1480	000112'	021116'
1481	000114'	
1482	000114'	000000
1483	000116'	
1484	000116'	000000
1485	000120'	
1486	000120'	000000
1487		

L\$EXP1::		
L\$MREV::	.WORD	0
	.BYTE	C\$REVISION
	.BYTE	C\$EDIT
L\$EF::		
	.WORD	0
	.WORD	0
L\$SPC::		
	.WORD	0
L\$DEVP::		
	.WORD	L\$DVTYP
L\$REPP::		
	.WORD	L\$RPT
L\$EXP4::		
	.WORD	0
L\$EXP5::		
	.WORD	0
L\$AUT::		
	.WORD	L\$AU
L\$DUT::		
	.WORD	L\$DU
L\$LUN::		
	.WORD	0
L\$DESP::		
	.WORD	L\$DESC
L\$LOAD::		
	EMT	ESLOAD
L\$ETP::		
	.WORD	0
L\$ICP::		
	.WORD	L\$INIT
L\$CCP::		
	.WORD	L\$CLEAN
L\$ACP::		
	.WORD	L\$AUTO
L\$PRT::		
	.WORD	L\$PROT
L\$TEST::		
	.WORD	0
L\$DLY::		
	.WORD	0
L\$HIME::		
	.WORD	0

.SBTTL DISPATCH TABLE

:+
: THE DISPATCH TABLE CONTAINS THE STARTING ADDRESS OF EACH TEST.
: IT IS USED BY THE SUPERVISOR TO DISPATCH TO EACH TEST.
:--

DISPATCH 46

1488		
1489		
1490		
1491		
1492		
1493		
1494		
1495	000122'	
1496	000122'	000056
1497	000124'	
1498	000124'	021710'
1499	000126'	022122'
1500	000130'	022454'
1501	000132'	022566'
1502	000134'	022704'
1503	000136'	023366'
1504	000140'	023450'
1505	000142'	023546'
1506	000144'	023770'
1507	000146'	027332'
1508	000150'	027746'
1509	000152'	030534'
1510	000154'	031214'
1511	000156'	031632'
1512	000160'	032122'
1513	000162'	032630'
1514	000164'	033146'
1515	000166'	033614'
1516	000170'	034172'
1517	000172'	034552'
1518	000174'	035100'
1519	000176'	035410'
1520	000200'	035720'
1521	000202'	036300'
1522	000204'	036660'
1523	000206'	037254'
1524	000210'	040016'
1525	000212'	040414'
1526	000214'	041016'
1527	000216'	041336'
1528	000220'	041726'
1529	000222'	042312'
1530	000224'	042676'
1531	000226'	043336'
1532	000230'	043666'
1533	000232'	044206'
1534	000234'	044556'
1535	000236'	045076'
1536	000240'	045436'
1537	000242'	046026'
1538	000244'	046410'
1539	000246'	047042'
1540	000250'	047446'
1541	000252'	050536'
1542	000254'	051260'
1543	000256'	051674'

	.WORD	46
LSDISPATCH:	:	
	.WORD	T1
	.WORD	T2
	.WORD	T3
	.WORD	T4
	.WORD	T5
	.WORD	T6
	.WORD	T7
	.WORD	T8
	.WORD	T9
	.WORD	T10
	.WORD	T11
	.WORD	T12
	.WORD	T13
	.WORD	T14
	.WORD	T15
	.WORD	T16
	.WORD	T17
	.WORD	T18
	.WORD	T19
	.WORD	T20
	.WORD	T21
	.WORD	T22
	.WORD	T23
	.WORD	T24
	.WORD	T25
	.WORD	T26
	.WORD	T27
	.WORD	T28
	.WORD	T29
	.WORD	T30
	.WORD	T31
	.WORD	T32
	.WORD	T33
	.WORD	T34
	.WORD	T35
	.WORD	T36
	.WORD	T37
	.WORD	T38
	.WORD	T39
	.WORD	T40
	.WORD	T41
	.WORD	T42
	.WORD	T43
	.WORD	T44
	.WORD	T45
	.WORD	T46

PROGRAM HEADER AND TABLES
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DEFAULT HARDWARE P-TABLE

GL
CZ

1545
1546
1547
1548
1549
1550
1551
1552
1553
1554 000260' 000002
1555 000260' 000002
1556 000262'
1557 000262'
1558
1559 000262' C00000
1560 000264' 000000
1561
1562 000266'
1563 000266'

.SBTTL DEFAULT HARDWARE P-TABLE

:++
: THE DEFAULT HARDWARE P-TABLE CONTAINS DEFAULT VALUES OF
: THE TEST-DEVICE PARAMETERS. THE STRUCTURE OF THIS TABLE
: IS IDENTICAL TO THE STRUCTURE OF THE HARDWARE P-TABLES,
: AND IS USED AS A "TEMPLATE" FOR BUILDING THE P-TABLES.
:--

BGNHW DFPTBL

.WORD L10000-LSHW/2
LSHW::
DFPTBL::

.WORD 0 :CSR
.WORD 0 :VECTOR

ENDHW

L10000:

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CZUAAA.P11 11-JAN-83 09:29 DEFAULT HARDWARE P-TABLE

GL
CZ

1565
1566
1567
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1600
1601
1602
1603
1604
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1606
1607
1608
1609
1610
1611
1612
1613
1614
1615
1616
1617
1618
1619
1620

000266'

.TITLE GLOBAL AREAS
.SBTTL GLOBAL EQUATES SECTION

..*
: THE GLOBAL EQUATES SECTION CONTAINS PROGRAM EQUATES THAT
: ARE USED IN MORE THAN ONE TEST.
:--

EQUALS

: BIT DEFINITIONS

: BIT15== 100000
: BIT14== 40000
: BIT13== 20000
: BIT12== 10000
: BIT11== 4000
: BIT10== 2000
: BIT09== 1000
: BIT08== 400
: BIT07== 200
: BIT06== 100
: BIT05== 40
: BIT04== 20
: BIT03== 10
: BIT02== 4
: BIT01== 2
: BIT00== 1

: BIT9== BIT09
: BIT8== BIT08
: BIT7== BIT07
: BIT6== BIT06
: BIT5== BIT05
: BIT4== BIT04
: BIT3== BIT03
: BIT2== BIT02
: BIT1== BIT01
: BIT0== BIT00

: EVENT FLAG DEFINITIONS

: EF32:EF17 RESERVED FOR SUPERVISOR TO PROGRAM COMMUNICATION

: EF.START== 32. : START COMMAND WAS ISSUED
: EF.RESTART== 31. : RESTART COMMAND WAS ISSUED
: EF.CONTINUE== 30. : CONTINUE COMMAND WAS ISSUED
: EF.NEW== 29. : A NEW PASS HAS BEEN STARTED
: EF.PWR== 28. : A POWER-FAIL/POWER-UP OCCURRED

: PRIORITY LEVEL DEFINITIONS

: PRI07== 340
: PRI06== 300

GLOBAL AREAS
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GLOBAL EQUATES SECTION

1621	000240	PRI05== 240
1622	000200	PRI04== 200
1623	000140	PRI03== 140
1624	000100	PRI02== 100
1625	000040	PRI01== 40
1626	000000	PRI00== 0
1627		.
1628		:OPERATOR FLAG BITS
1629		.
1630	000004	EVL== 4
1631	000010	LOT== 10
1632	000020	ADR== 20
1633	000040	IDU== 40
1634	000100	ISR== 100
1635	000200	UAM== 200
1636	000400	BOE== 400
1637	001000	PNT== 1000
1638	002000	PRJ== 2000
1639	004000	IXE== 4000
1640	010000	IBE== 10000
1641	020000	IER== 20000
1642	040000	LOE== 40000
1643	100000	HOE== 100000
1644		
1645	000077	SECOND==63.
1646	000001	SET== 1
1647	000000	CLEAR== 0
1648	004000	SIZ1K== 4000
1649	010000	SIZ2K== SIZ1K*2
1650	020000	SIZ4K== SIZ2K*2
1651	040000	SIZ8K== SIZ4K*2
1652	020000	WCSSIZ==SIZ4K
1653	020000	IOSIZ==SIZ4K
1654	040000	ROMSIZ==SIZ8K
1655	077774	LINSIZ==SIZ8K*2-4
1656	000000	WCSADR==0
1657	020000	IOADR==WCSADR+WCSSIZ
1658	040000	ROMADR==IOADR+IOSIZ
1659	100000	LINADR==ROMADR+ROMSIZ
1660	000100	IE== BIT6
1661	177777	INITH== -1
1662	177777	INITL== -1
1663	166670	POLYH== 166670
1664	101440	POLYL== 101440
1665	120001	PGLY16== 120001
1666	000000	DATERR==0
1667	000001	ADRERR==1
1668	002756	MAXBYT==1518.
1669	000100	MINBYT==64.
1670	000004	CRCSIZ==4.
1671	000001	INMON=1
1672	000002	INTST=2
1673	000003	INERR=3
1674		

:63 LINE CLOCK TICKS = APROX. 1 SECOND

:1K WORDS
:2K WORDS
:4K WORDS
:8K WORDS

:SIZE OF THE DEUNA WRITEABLE CONTROL STORE
:SIZE OF THE DEUNA I/O PAGE
:SIZE OF THE DEUNA ROM IN WORDS
:SIZE OF THE DEUNA LINK MEMORY
:INTERNAL BASE ADDRESS OF WCS
:INTERNAL BASE ADDRESS OF THE I/O PAGE FOR THE T11
:INTERNAL BASE ADDRESS OF THE DEUNA ROM
:INTERNAL BASE ADDRESS OF THE DEUNA LINK MEMORY
:INTERRUPT ENABLE
:INITIAL HIGH WORD FOR 32 BIT CRC CALCULATOR
:INITIAL LOW WORD FOR 32 BIT CRC CALCULATOR
:FUNCTION POLYNOMIAL HIGH WORD FOR 32 BIT CRC
:FUNCTION POLYNOMIAL LOW WORD FOR 32 BIT CRC
:FUNCTION POLYNOMIAL FOR 16 BIT CRC
:DATA ERROR INDICATER FOR LINK MEMORY TESTS
:ADDRESS ERROR INDICATOR FOR LINK MEMORY TESTS
:MAXIMUM NUMBER OF BYTES RECEIVER CAN HANDLE
:MINIMUM NUMBER OF BYTES RECEIVER CAN HANDLE
:SIZE OF CRC
:IN MICROMONITOR STATE
:IN A TEST STATE
:IN ERROR STATE

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 CZUAAA.P11 11-JAN-83 09:29 GLOBAL EQUATES SECTION

```

1675
1676      ; P RO - PORT CONTROL AND STATUS REGISTER 0 (OPERATIONAL MICROCODE DEFINITIONS)
1677
1678      100000      SERI == BIT15      ; STATUS ERROR INTERRUPT
1679      040000      PCEI == BIT14      ; PORT COMMAND ERROR INTERRUPT
1680      020000      RXI  == BIT13      ; RECEIVE RING INTERRUPT
1681      010000      TXI  == BIT12      ; TRANSMIT RING INTERRUPT
1682      004000      DNI  == BIT11      ; DONE INTERRUPT
1683      002000      RCEI == BIT10      ; RECEIVE BUFFER UNAVAILABLE
1684
1685      000400      FATI == BIT08      ; FATAL ERROR INTERERUPT
1686      000200      INTR == BIT07      ; INTERRUPT SUMMARY <15:08>
1687      000100      INTE == BIT06      ; INTERRUPT ENABLE
1688      000040      RSET == BIT05      ; UNA RESET
1689
1690      ;
1691      ;PCSR0 - PORT CONTROL AND STATUS REGISTER 0 (DIAGNOSTIC MICROCODE DEFINITIONS)
1692      ;
1693      100000      NPRERR == BIT15      ;T11 NPR TIMEOUT INTERRUPT OCCURRED
1694      040000      NXMERR == BIT14      ;T11 NON-EXISTANT MEMORY TIMEOUT OCCURRED
1695      020000      UNIERR == BIT13      ;T11 UNEXPECTED INTERRUPT OCCURRED
1696      010000      PARERR == BIT12      ;T11 LINK MEMORY PARITY ERROR OCCURRED
1697      ;
1698      ;PORT COMMANDS <03:00>
1699      000001      GETPCB == BIT00      ;
1700      000002      GETCMD == BIT01      ;
1701      000006      PNOP  == BIT01!BIT02 ;
1702      000003      SLFT  == BIT00!BIT01 ;
1703      ;
1704      ;PCSR1 - PORT CONTROL AND STATUS REGISTER 1
1705      100000      XPWR  == BIT15      ; TRANSCEIVER POWER BAD
1706      040000      ICAB  == BIT14      ; PORT/LINK CABLING OK
1707      ;
1708      ;SELF TEST ERROR CODE <13:08>
1709      ;
1710      000200      PCTO  == BIT07      ; PORT COMMAND TIMEOUT
1711      ;
1712      000010      RMTC  == BIT03      ; REMOTE CONSOLE RESERVED
1713      000400      SFTB0 == BIT8       ; FIRST BIT OF SELF TEST FIELD
1714      001000      SFTB1 == BIT9       ; SECOND
1715      002000      SFTB2 == BIT10      ; THIRD
1716      004000      SFTB3 == BIT11      ; FOURTH
1717      010000      SFTB4 == BIT12      ; FIFTH
1718      020000      SFTB5 == BIT13      ; SIXTH
1719      037400      SFT   == BIT8!BIT9!BIT10!BIT11!BIT12!BIT13 ;SELF TEST FIELD
1720      ;
1721      ;PORT STATE <02:00>
1722      ;
1723      000007      PSTATE == BIT00!BIT01!BIT02 ;PORT STATE BITS OF PCSR1
1724      000000      RESET == 0           ;RESET STATE (NOT REALLY A STATE)
1725      000001      PRILD == BIT00      ; PRIMARY LOAD STATE
1726      000002      READY == BIT01      ;READY STATE
1727      000003      RUN   == BIT00!BIT01 ;RUNNING STATE
1728      000005      UNHLT == BIT00!BIT02 ;UNIBUS HALTED STATE
1729      000006      NIHLT == BIT01!BIT02 ;NI HALTED STATE
1730      000007      NIUNI == BIT00!BIT01!BIT02 ;UNIBUS AND NI HALTED STATE

```


GLOBAL AREAS
CZUAAA.P11MACY11 30(1046)
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GLOBAL EQUATES SECTION

```

1731
1732
1733      100000
1734      040000
1735      001000
1736      000400
1737
1738
1739
1740
1741      000000
1742      000001
1743      000002
1744      000003
1745      000004
1746      000005
1747      000006
1748      000007
1749      000010
1750      000011
1751      000012
1752      000013
1753      000014
1754      000015
1755      000016
1756      000017
1757      000020
1758      000021
1759      000022
1760      000023
1761      000024
1762      000025
1763
1764

```

```

:
: DESCRIPTOR RING DEFINITIONS
OWN      ==      BIT15
ERRS     ==      BIT14
STP      ==      BIT09
ENP      ==      BIT08
:
: PORT FUNCTION CODES
:
PFNOP1   ==      0      :NOP PORT FUNCTION #1
LASM     ==      1      :LOAD AND START MICROADDRESS
RDPA     ==      2      :READ DEFAULT PHYSICAL ADDRESS
PFNOP2   ==      3      :NOP PORT FUNCTION #2
RPA      ==      4      :READ PHYSICAL ADDRESS
WPA      ==      5      :WRITE PHYSICAL ADDRESS
RMAL     ==      6      :READ MULTICAST ADDRESS LIST
WMAL     ==      7      :WRITE MULTICAST ADDRESS LIST
RRF      ==      10     :READ RING FORMAT
WRF      ==      11     :WRITE RING FORMAT
RC       ==      12     :READ COUNTERS
RACC     ==      13     :READ AND CLEAR COUNTERS
RTM      ==      14     :READ THE MODE
WTM      ==      15     :WRITE THE MODE
RPS      ==      16     :READ PORT STATUS
RACPS    ==      17     :READ AND CLEAR PORT STATUS
DIM      ==      20     :DUMP INTERNAL MEMORY
LIM      ==      21     :LOAD INTERNAL MEMORY
RSIDP    ==      22     :READ SYSTEM ID PARAMETERS
WSIDP    ==      23     :WRITE SYSTEM ID PARAMETERS
RLSA     ==      24     :READ LOAD SERVER ADDRESS
WLSA     ==      25     :WRITE LOAD SERVER ADDRESS

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1765
1766
1767          .SBTTL  GLOBAL DATA SECTION
1768
1769          : **
1770          : THE GLOBAL DATA SECTION CONTAINS DATA THAT ARE USED
1771          : IN MORE THAN ONE TEST.
1772          : --
1773
1774 000266' 000000  UNACSR::WORD 0          :CSR
1775 000270' 000000  UNAVEC::WORD 0          :VECTOR
1776 000272' 000000  UNAPRI::WORD 0         :PRIORITY
1777
1778 000274'          CLKTAB::
1779 000274' 000000  CLKCSR::WORD 0          :LINE CLOCK STATUS REGISTER
1780 000276' 000000  CLKBR::WORD 0          :LINE CLOCK PRIORITY
1781 000300' 000000  CLKVEC::WORD 0         :LINE CLOCK VECTOR
1782 000302' 000000  CLKFRE::WORD 0        :LINE CLOCK FREQUENCY
1783
1784 000304' 000000  CSRNUM::WORD 0         :PCSR NUMBER
1785 000306' 000000  BITNUM::WORD 0        :BIT NUMBER
1786 000310' 000000  BITNAM::WORD 0        :POINTER TO BIT NAME ASCII STRING
1787 000312' 000000  BITSTA::WORD 0       :POINTER TO BIT STATUS ASCII STRING
1788 000314' 000000  PWHEN::WORD 0        :POINTER TO 'BEFORE' OR 'AFTER' ASCII STRING
1789 000316' 000000  PCOMND::WORD 0       :POINTER TO PORT COMMAND ASCII STRING
1790 000320' 000000  MICMOD::WORD 0       :POINTER TO MICROCODE MODULE # LOADED
1791
1792 000322' 000000  FRESIZ::WORD 0        :POINTER TO WORD CONTAINING SIZE OF FREE MEMORY
1793 000324' 000000  FREMEM::WORD 0        :POINTER TO FREE MEMORY SPACE
1794
1795 000326' 000000  MICRO::WORD 0         :CURRENT MICROCODE MODULE LOADED
1796 000330' 000000  UNIT::WORD 0         :CURRENT UNIT NUMBER BEING TESTED
1797 000332' 000000  METER::WORD 0        :CLOCK TICKS
1798 000334' 021040  SWADDR::WORD 21040   :INTERNAL ADDRESS OF SWITCH PACK
1799
1800 000336' 000000  PCSR0::WORD 0        :PORT CONTROL AND STATUS REGISTER 0
1801 000340' 000000  PCSR1::WORD 0        :PORT CONTROL AND STATUS REGISTER 1
1802 000342' 000000  PCSR2::WORD 0        :PORT CONTROL AND STATUS REGISTER 2
1803 000344' 000000  PCSR3::WORD 0        :PORT CONTROL AND STATUS REGISTER 3
1804
1805 000346' 000000  PCSR0C::WORD 0       :PCSR0 CONTENTS
1806 000350' 000000  PCSR1C::WORD 0       :PCSR1 CONTENTS
1807 000352' 000000  PCSR2C::WORD 0       :PCSR2 CONTENTS
1808 000354' 000000  PCSR3C::WORD 0       :PCSR3 CONTENTS
1809
1810 000356'          BNAMT0::          :TABLE OF POINTERS TO BIT NAME MNEMONICS FOR PCSR0
1811 000356' 001267'          $BIT0
1812 000360' 001261'          $BIT1
1813 000362' 001253'          $BIT2
1814 000364' 001245'          $BIT3
1815 000366' 001237'          $BIT4
1816 000370' 001052'          $RSET
1817 000372' 001041'          $INTE
1818 000374' 001030'          $INTR
1819 000376' 001017'          $FA11
1820 000400' 001201'          $BI'9
    
```

1821 000402' 001006'
 1822 000404' 000776'
 1823 000406' 000766'
 1824 000410' 000756'
 1825 000412' 000745'
 1826 000414' 000734'
 1827 000416' 000000'
 1828 000416' 000000'
 1829 000420' 000000'
 1830 000422' 000000'
 1831 000424' 001116'
 1832 000426' 001237'
 1833 000430' 001231'
 1834 000432' 001223'
 1835 000434' 001105'
 1836 000436' 000000'
 1837 000440' 000000'
 1838 000442' 000000'
 1839 000444' 000000'
 1840 000446' 000000'
 1841 000450' 000000'
 1842 000452' 001014'
 1843 000454' 001063'
 1844
 1845 000456'
 1846 000456' 001267'
 1847 000460' 001261'
 1848 000462' 001253'
 1849 000464' 001245'
 1850 000466' 001237'
 1851 000470' 001231'
 1852 000472' 001223'
 1853 000474' 001215'
 1854 000476' 001207'
 1855 000500' 001201'
 1856 000502' 001172'
 1857 000504' 001163'
 1858 000506' 001154'
 1859 000510' 001145'
 1860 000512' 001136'
 1861 000514' 001127'
 1862
 1863 000516'
 1864 000516' 125252
 1865 000520' 052525
 1866 000522' 031463
 1867 000524' 007417
 1868 000526' 000377
 1869 000530' 177777
 1870
 1871 000532' 125252
 1872 000534' 125252
 1873 000536' 125252
 1874 000540' 052525
 1875 000542' 052525
 1876 000544' 052525

\$RCEI
 \$DNI
 \$TXI
 \$RXI
 \$PCEI
 \$SERI
 BNAMT1::
 .WORD 0
 .WORD 0
 .WORD 0
 \$RMTC
 \$BIT4
 \$BIT5
 \$BIT6
 \$PCTO
 .WORD 0
 .WORD 0
 .WORD 0
 .WORD 0
 .WORD 0
 .WORD 0
 \$ICAB
 \$XPWR

;TABLE OF POINTERS TO BIT NAME MNEMONICS FOR PCSRI

BNAMT2::
 \$BIT0
 \$BIT1
 \$BIT2
 \$BIT3
 \$BIT4
 \$BIT5
 \$BIT6
 \$BIT7
 \$BIT8
 \$BIT9
 \$BIT10
 \$BIT11
 \$BIT12
 \$BIT13
 \$BIT14
 \$BIT15

;TABLE OF GENERIC BIT NAME MNEMONICS

PATERN::
 PAT1:: .WORD ^B1010101010101010
 PAT2:: .WORD ^B0101010101010101
 PAT3:: .WORD ^B0011001100110011
 PAT4:: .WORD ^B0000111100001111
 PAT5:: .WORD ^B0000000011111111
 PAT6:: .WORD ^B1111111111111111
 SPAT1:: .WORD ^B1010101010101010
 .WORD ^B1010101010101010
 .WORD ^B1010101010101010
 SPAT2:: .WORD ^B0101010101010101
 .WORD ^B0101010101010101
 .WORD ^B0101010101010101

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1877	000546'	031463	SPAT3::	.WORD	^B0011001100110011	
1878	000550'	031463		.WORD	^B0011001100110011	
1879	000552'	031463		.WORD	^B0011001100110011	
1880	000554'	007417	SPAT4::	.WORD	^B0000111100001111	
1881	000556'	007417		.WORD	^B0000111100001111	
1882	000560'	007417		.WORD	^B0000111100001111	
1883	000562'	000377	SPAT5::	.WORD	^B0000000011111111	
1884	000564'	000377		.WORD	^B0000000011111111	
1885	000566'	000377		.WORD	^B0000000011111111	
1886	000570'	177777	SPAT6::	.WORD	^B1111111111111111	
1887	000572'	000000		.WORD	^B0000000000000000	
1888	000574'	177777		.WORD	^B1111111111111111	
1889	000576'	177777	SPAT7::	.WORD	^B1111111111111111	
1890	000600'	000377		.WORD	^B0000000011111111	
1891	000602'	000000		.WORD	^B0000000000000000	
1892						
1893	000604'	000000	PCBB::	.WORD	0	:PORT CONTROL BLOCK
1894	000606'	000000		.WORD	0	:+2
1895	000610'	000000		.WORD	0	:+4
1896	000612'	000000		.WORD	0	:+6
1897						
1898	000614'	000000	UDBB::	.WORD	0	:UNIBUS DATA BLOCK BASE
1899	000616'	000000		.WORD	0	:+2
1900	000620'	000000		.WORD	0	:+4
1901	000622'	000000		.WORD	0	:+6
1902						
1903	000624'	000020	CNTTAB::	.BLKW	16.	:HOLDS THE 16 RETRY WAIT COUNTS FOR TEST 39
1904						
1905	000664'	000000	NEXMEM::	.WORD	0	:NON-EXISTANT MEMORY FLAG
1906	000666'	000000	ERRINT::	.WORD	0	:HOLDS PCSRO INTERRUPT FLAG BITS FROM MICROCODE
1907	000670'	000000	UNAINT::	.WORD	0	:UNA INTERRUPT FLAG
1908	000672'	000000	FRSTIM::	.WORD	0	:FLAG INDICATING FIRST TIME THROUGH TESTS FOR ALL UNITS
1909	000674'	000000	CPUPRI::	.WORD	0	:CPU PRIORITY BEFORE INTERRUPT OCCURRED
1910						
1911			.EVEN			

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.SBTTL GLOBAL TEXT SECTION

:+
: THE GLOBAL TEXT SECTION CONTAINS FORMAT STATEMENTS,
: MESSAGES, AND ASCII INFORMATION THAT ARE USED IN
: MORE THAN ONE TEST.
:--

:
: NAMES OF DEVICES SUPPORTED BY PROGRAM

:
: DEVTYP <DEUNA>

000676'
000676'
000676' 042504 047125 000101

LSDVTYP::
.ASCIZ /DEUNA/
.EVEN

:
: TEST DESCRIPTION

:
: DESCRIPT <DEUNA REPAIR DIAGNOSTIC>

000704'
000704'
000704' 042504 047125 020101
000712' 042522 040520 051111
000720' 042040 040511 047107
000726' 051517 044524 000103

L\$DESC::
.ASCIZ /DEUNA REPAIR DI

.EVEN

000734' 042523 044522 041040 \$SERI:: .ASCIZ /SERI BIT/
000742' 052111 000
000745' 120 042503 020111 \$PCEI:: .ASCIZ /PCEI BIT/
000752' 044502 000124
000756' 054122 020111 044502 \$RXI:: .ASCIZ /RXI BIT/
000764' 000124
000766' 054124 020111 044502 \$TXI:: .ASCIZ /TXI BIT/
000774' 000124
000776' 047104 020111 044502 \$DNI:: .ASCIZ /DNI BIT/
001004' 000124
001006' 041522 044505 041040 \$RCEI:: .ASCIZ /RCEI BIT/
001014' 052111 000
001017' 106 052101 020111 \$FATI:: .ASCIZ /FATI BIT/
001024' 044502 000124
001030' 047111 051124 041040 \$INTR:: .ASCIZ /INTR BIT/
001036' 052111 000
001041' 111 052116 020105 \$INTE:: .ASCIZ /INTE BIT/
001046' 044502 000124
001052' 051522 052105 041040 \$RSET:: .ASCIZ /RSET BIT/
001060' 052111 000
001063' 130 053520 020122 \$XPWR:: .ASCIZ /XPWR BIT/
001070' 044502 000124
001074' 041511 041101 041040 \$ICAB:: .ASCIZ /ICAB BIT/
001102' 052111 000
001105' 120 052103 020117 \$PCTO:: .ASCIZ /PCTO BIT/

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1968	00 112'	044502	000124				
1969	001116'	046522	041524	041040	\$RMTC::	.ASCIZ	/RMTC BIT/
1970	001124'	052111	000				
1971	001127'	102	052111	030440	\$BIT15::	.ASCIZ	/BIT 15/
1972	001134'	000065					
1973	001136'	044502	020124	032061	\$BIT14::	.ASCIZ	/BIT 14/
1974	001144'	000					
1975	001145'	102	052111	030440	\$BIT13::	.ASCIZ	/BIT 13/
1976	001152'	000063					
1977	001154'	044502	020124	031061	\$BIT12::	.ASCIZ	/BIT 12/
1978	001162'	000					
1979	001163'	102	052111	030440	\$BIT11::	.ASCIZ	/BIT 11/
1980	001170'	000061					
1981	001172'	044502	020124	030061	\$BIT10::	.ASCIZ	/BIT 10/
1982	001200'	000					
1983	001201'	102	052111	034440	\$BIT9::	.ASCIZ	/BIT 9/
1984	001206'	000					
1985	00 207'	102	052111	034040	\$BIT8::	.ASCIZ	/BIT 8/
1986	001214'	000					
1987	00 215'	102	052111	033440	\$BIT7::	.ASCIZ	/BIT 7/
1988	00 222'	000					
1989	00 223'	102	052111	033040	\$BIT6::	.ASCIZ	/BIT 6/
1990	00 230'	000					
1991	00 231'	102	052111	032440	\$BIT5::	.ASCIZ	/BIT 5/
1992	00 236'	000					
1993	001237'	102	052111	032040	\$BIT4::	.ASCIZ	/BIT 4/
1994	001244'	000					
1995	001 245'	102	052111	031440	\$BIT3::	.ASCIZ	/BIT 3/
1996	001252'	000					
1997	001 253'	102	052111	031040	\$BIT2::	.ASCIZ	/BIT 2/
1998	001 260'	000					
1999	001261'	102	052111	030440	\$BIT1::	.ASCIZ	/BIT 1/
2000	001266'	000					
2001	001 267'	102	052111	030040	\$BIT0::	.ASCIZ	/BIT 0/
2002	001274'	000					
2003	001275'	116	052117	051440	\$NSET::	.ASCIZ	/NOT SET/
2004	001302'	052105	000				
2005	001 305'	123	052105	000	\$SET::	.ASCIZ	/SET/
2006	001311'	116	052117	041440	\$NCLR::	.ASCIZ	/NOT CLEAR/
2007	001316'	042514	051101	000			
2008	001323'	103	042514	051101	\$CLR::	.ASCIZ	/CLEAR/
2009	001330'	000					
2010	001331'	102	043105	051117	\$BEFOR::	.ASCIZ	/BEFORE/
2011	001336'	000105					
2012	001340'	043101	042524	000122	\$AFTER::	.ASCIZ	/AFTER/
2013	001346'	042507	050124	041103	\$GTPCB::	.ASCIZ	/GETPCB/
2014	001354'	000					
2015	001355'	107	052105	041440	\$GTCMD::	.ASCIZ	/GET COMMAND/
2016	001 352'	046517	040515	042116			
2017	001370'	000					
2018	001371'	123	046105	020106	\$SLFT::	.ASCIZ	/SELF TEST/
2019	001376'	042524	052123	000			
2020	001403'	116	050117	000	\$NOP::	.ASCIZ	/NOP/
2021	001407'	123	040524	052122	\$STRT::	.ASCIZ	/START/
2022	001414'	000					
2023	0014 5'	104	046505	047101	\$PDNDM::	.ASCIZ	/DEMAND POLL/

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 CZUAAA.P11 11-JAN-83 09:29 GLOBAL TEXT SECTION

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2024	001422'	020104	047520	046114	
2025	001430'	000			
2026	001431'	123	047524	000120	\$STOP::.ASCIZ /STOP/
2027	001436'	042522	042523	000124	\$RESET::.ASCIZ /RESET/
2028	001444'	040504	040524	042440	\$DATER::.ASCIZ /DATA ERROR/
2029	001442'	051122	051117	000	
2030	001447'	101	042104	042522	\$ADRER::.ASCIZ /ADDRESSING ERROR/
2031	001444'	051523	047111	020107	
2032	001442'	051105	047522	000122	
2033	001500'	040520	044522	054524	\$PARER::.ASCIZ /PARITY ERROR/
2034	001506'	042440	051122	051117	
2035	001514'	000			
2036					
2037					
2038					: : FORMAT STATEMENTS USED IN PRINT CALLS :
2039					
2040					
2041	001515'	045	022516	052501	UNLOD::.ASCIZ /%N%AUNABLE TO LOAD MICROCODE MODULE %T%N/
2042	001522'	040516	046102	020105	
2043	001530'	047524	046040	040517	
2044	001536'	020104	044515	051103	
2045	001544'	041517	042117	020105	
2046	001552'	047515	052504	042514	
2047	001560'	022440	022524	000116	
2048	001566'	040503	047116	052117	NOCLK::.ASCIZ /CANNOT TEST DEUNA BECAUSE SYSTEM DOES NOT HAVE A LINE 'LOCK!/'
2049	001574'	052040	051505	020124	
2050	001602'	042504	047125	020101	
2051	001610'	042502	040503	051525	
2052	001616'	020105	054523	052123	
2053	001624'	046505	042040	042517	
2054	001632'	020123	047516	020124	
2055	001640'	040510	042526	040440	
2056	001646'	046040	047111	020105	
2057	001654'	046103	041517	020513	
2058	001662'	000			
2059	001663'	122	043505	051511	RACERR::.ASCIZ /REGISTER ACCESS TEST FAILED/
2060	001670'	042524	020122	041501	
2061	001676'	042503	051523	052040	
2062	001704'	051505	020124	040506	
2063	001712'	046111	042105	000	
2064	001717'	122	051505	052105	RSETER::.ASCIZ /RESET TEST FAILED/
2065	001724'	052040	051505	020124	
2066	001732'	040506	046111	042105	
2067	001740'	000			
2068	001741'	122	043505	051511	RRWER::.ASCIZ 'REGISTER READ/WRITE TEST FAILED'
2069	001746'	042524	020122	042522	
2070	001754'	042101	053457	044522	
2071	001762'	042524	052040	051505	
2072	001770'	020124	040506	046111	
2073	001776'	042105	000		
2074	002001'	116	050117	052040	NOPERR::.ASCIZ /NOP TEST FAILED/
2075	002006'	051505	020124	040506	
2076	002014'	046111	042105	000	
2077	002021'	123	046105	020106	SLFTST::.ASCIZ /SELF TEST FAILED/
2078	002026'	042524	052123	043040	
2079	002034'	044501	042514	000104	

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2080	002042'	047522	020115	052504	ROMDMP::ASCIZ	/ROM DUMP TEST FAILED/
2081	002050'	050115	052040	051505		
2082	002056'	020124	040506	046111		
2083	002064'	042105	000			
2084	002067'	127	051503	046040	DATALD::ASCIZ	'WCS LOAD/DUMP TEST FAILED'
2085	002074'	040517	027504	052504		
2086	002102'	050115	052040	051505		
2087	002110'	020124	040506	046111		
2088	002116'	042105	000			
2089	002121'	114	040517	020104	LASFT::ASCIZ	/LOAD AND START FUNCTION TEST FAILED/
2090	002126'	047101	020104	052123		
2091	002134'	051101	020124	052506		
2092	002142'	041516	044524	047117		
2093	002150'	052040	051505	020124		
2094	002156'	040506	046111	042105		
2095	002164'	000				
2096	002165'	127	051503	046440	WCSMEM::ASCIZ	/WCS MEMORY TEST FAILED/
2097	002172'	046505	051117	020131		
2098	002200'	042524	052123	043040		
2099	002206'	044501	042514	000104		
2100	002214'	047111	042524	051122	INTVEC::ASCIZ	/INTERRUPT VECTOR TEST FAILED/
2101	002222'	050125	020124	042526		
2102	002230'	052103	051117	052040		
2103	002236'	051505	020124	040506		
2104	002244'	046111	042105	000		
2105	002251'	120	051503	030122	INTBIT::ASCIZ	/PCSR0 INTERRUPT BIT TEST FAILED/
2106	002256'	044440	052116	051105		
2107	002264'	052522	052120	041040		
2108	002272'	052111	052040	051505		
2109	002300'	020124	040506	046111		
2110	002306'	042105	000			
2111	002311'	124	046511	051105	TIMTST::ASCIZ	/TIMER TEST FAILED/
2112	002316'	052040	051505	020124		
2113	002324'	040506	046111	042105		
2114	002332'	000				
2115	002333'	114	047111	020113	LNKMEM::ASCIZ	/LINK MEMORY TEST FAILED/
2116	002340'	042515	047515	054522		
2117	002346'	052040	051505	020124		
2118	002354'	040506	046111	042105		
2119	002362'	000				
2120	002363'	104	040515	023440	DMATO::ASCIZ	/DMA 'TO' ADDRESS REGISTER TEST FAILED/
2121	002370'	047524	020047	042101		
2122	002376'	051104	051505	020123		
2123	002404'	042522	044507	052123		
2124	002412'	051105	052040	051505		
2125	002420'	020124	040506	046111		
2126	002426'	042105	000			
2127	002431'	104	040515	023440	DMAFRM::ASCIZ	/DMA 'FROM' ADDRESS REGISTER TEST FAILED/
2128	002436'	051106	046517	020047		
2129	002444'	042101	051104	051505		
2130	002452'	020123	042522	044507		
2131	002460'	052123	051105	052040		
2132	002466'	051505	020124	040506		
2133	002474'	046111	042105	000		
2134	002501'	104	040515	041040	DMABLK::ASCIZ	/DMA BLOCK TRANSFER TEST FAILED/
2135	002506'	047514	045503	052040		

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2136	002514'	040522	051516	042506	
2137	002522'	020122	042524	052123	
2138	002530'	043040	044501	042514	
2139	002536'	000104			
2140	002540'	051124	047101	046523	TRNDON::.ASCIZ /TRANSMIT DONE TEST FAILED/
2141	002546'	052111	042040	047117	
2142	002554'	020105	042524	052123	
2143	002562'	043040	044501	042514	
2144	002570'	000104			
2145	002572'	042522	042503	053111	RCVDON::.ASCIZ /RECEIVER DONE TEST FAILED/
2146	002600'	051105	042040	047117	
2147	002606'	020105	042524	052123	
2148	002614'	043040	044501	042514	
2149	002622'	000104			
2150	002624'	040504	040524	041040	DBFRAM::.ASCIZ /DATA BYTE FRAMING TEST FAILED/
2151	002632'	052131	020105	051106	
2152	002640'	046501	047111	020107	
2153	002646'	042524	052123	043040	
2154	002654'	044501	042514	000104	
2155	002662'	040504	040524	053440	DWFRAM::.ASCIZ /DATA WORD FRAMING TEST FAILED/
2156	002670'	051117	020104	051106	
2157	002676'	046501	047111	020107	
2158	002704'	042524	052123	043040	
2159	002712'	044501	042514	000104	
2160	002720'	040504	040524	050040	DPPAT::.ASCIZ /DATA PATH PATTERN TEST FAILED/
2161	002726'	052101	020110	040520	
2162	002734'	052124	051105	020116	
2163	002742'	042524	052123	043040	
2164	002750'	044501	042514	000104	
2165	002756'	052123	052101	051525	STAMUX::.ASCIZ /STATUS MUX VERIFICATION TEST FAILED/
2166	002764'	046440	054125	053040	
2167	002772'	051105	043111	041511	
2168	003000'	052101	047511	020116	
2169	003006'	042524	052123	043040	
2170	003014'	044501	042514	000104	
2171	003022'	044514	045516	041040	LNKBYT::.ASCIZ /LINK BYTE COUNTER TEST FAILED/
2172	003030'	052131	020105	047503	
2173	003036'	047125	042524	020122	
2174	003044'	042524	052123	043040	
2175	003052'	044501	042514	000104	
2176	003060'	044514	045516	047440	ODDBYT::.ASCIZ /LINK ODD BYTE COUNTER TEST FAILED/
2177	003066'	042104	041040	052131	
2178	003074'	020105	047503	047125	
2179	003102'	042524	020122	042524	
2180	003110'	052123	043040	044501	
2181	003116'	042514	000104		
2182	003122'	044514	045516	046440	MAXCNT::.ASCIZ /LINK MAXIMUM BYTE COUNT TEST FAILED/
2183	003130'	054101	046511	046525	
2184	003136'	041040	052131	020105	
2185	003144'	047503	047125	020124	
2186	003152'	042524	052123	043040	
2187	003160'	044501	042514	000104	
2188	003166'	044506	047506	052040	FIFTST::.ASCIZ /FIFO TEST FAILED/
2189	003174'	051505	020124	040506	
2190	003202'	046111	042105	000	
2191	003207'	122	041505	044505	RLNKAD::.ASCIZ /RECEIVER LINK MEMORY ADDRESS TEST FAILED/

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2192	003214'	042526	020122	0'4514	
2193	003222'	045516	046440	046505	
2194	003230'	051117	020131	0'2101	
2195	003236'	051104	051505	020123	
2196	003244'	042524	052123	043040	
2197	003252'	044501	042514	000104	
2198	003260'	051124	047101	046523	TLNKAD::.ASCIZ /TRANSMITTER LINK MEMORY ADDRESS TEST FAILED/
2199	003266'	052111	042524	020122	
2200	003274'	044514	045516	046440	
2201	003302'	046505	051117	020131	
2202	003310'	042101	051104	051505	
2203	003316'	020123	042524	052123	
2204	003324'	043040	044501	042514	
2205	003332'	000104			
2206	003334'	044514	045516	046440	LNKARB::.ASCIZ /LINK MEMORY ARBITRATION TEST FAILED/
2207	003342'	046505	051117	020131	
2208	003350'	051101	044502	051124	
2209	003356'	052101	047511	020116	
2210	003364'	042524	052123	043040	
2211	003372'	044501	042514	000104	
2212	003400'	052123	052101	047511	STAPAT::.ASCIZ /STATION ADDRESS PATTERN TEST FAILED/
2213	003406'	020116	042101	051104	
2214	003414'	051505	020123	040520	
2215	003422'	052124	051105	020116	
2216	003430'	042524	052123	043040	
2217	003436'	044501	042514	000104	
2218	003444'	052123	052101	047511	STAREJ::.ASCIZ /STATION ADDRESS REJECTION TEST FAILED/
2219	003452'	020116	042101	051104	
2220	003460'	051505	020123	042522	
2221	003466'	042512	052103	047511	
2222	003474'	020116	042524	052123	
2223	003502'	043040	044501	042514	
2224	003510'	000104			
2225	003512'	052123	052101	047511	STAPOS::.ASCIZ /STATION ADDRESS RAM POSITION TEST FAILED/
2226	003520'	020116	042101	051104	
2227	003526'	051505	020123	040522	
2228	003534'	020115	047520	044523	
2229	003542'	044524	047117	052040	
2230	003550'	051505	020124	040506	
2231	003556'	046111	042105	000	
2232	003563'	115	046125	044524	MUCAST::.ASCIZ /MULTICAST ADDRESS TEST FAILED/
2233	003570'	040503	052123	040440	
2234	003576'	042104	042522	051523	
2235	003604'	052040	051505	020124	
2236	003612'	040506	046111	042105	
2237	003620'	000			
2238	003621'	103	041522	042040	CRCDAT::.ASCIZ /CRC DATA PATTERN TEST FAILED/
2239	003626'	052101	020101	040520	
2240	003634'	052124	051105	020116	
2241	003642'	042524	052123	043040	
2242	003650'	044501	042514	000104	
2243	003656'	051103	020103	051105	CRCERR::.ASCIZ /CRC ERROR TEST FAILED/
2244	003664'	047522	020122	042524	
2245	003672'	052123	043040	044501	
2246	003700'	042514	000104		
2247	003704'	051103	020103	040520	CRCPAT::.ASCIZ /CRC PATTERN LENGTH TEST FAILED/

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2248	003712'	052124	051105	020116	
2249	003720'	042514	043516	044124	
2250	003726'	052040	051505	020124	
2251	003734'	040506	046111	042105	
2252	003742'	000			
2253	003743'	122	041505	044505	RBRUN::.ASC /RECEIVE BUFFER RECOVERY - RUNT TEST FAILED/
2254	003750'	042526	041040	043125	
2255	003756'	042506	020122	042522	
2256	003764'	047503	042526	054522	
2257	003772'	026440	051040	047125	
2258	004000'	020124	042524	052123	
2259	004006'	043040	044501	042514	
2260	004014'	000104			
2261	004016'	040510	043114	042055	HAFDUP::.ASCIZ /HALF-DUPLEX TEST FAILED/
2262	004024'	050125	042514	020130	
2263	004032'	042524	052123	043040	
2264	004040'	044501	042514	000104	
2265	004046'	047503	046114	051511	COLTST::.ASCIZ /COLLISION TEST FAILED/
2266	004054'	047511	020116	042524	
2267	004062'	052123	043040	044501	
2268	004070'	042514	000104		
2269	004074'	042124	020122	047503	TDRCNT::.ASCIZ /TDR COUNTER TEST FAILED/
2270	004102'	047125	042524	020122	
2271	004110'	042524	052123	043040	
2272	004116'	044501	042514	000104	
2273	004124'	042522	051124	020131	RETLOG::.ASCIZ /RETRY LOGIC TEST FAILED/
2274	004132'	047514	044507	020103	
2275	004140'	042524	052123	043040	
2276	004146'	044501	042514	000104	
2277	004154'	047125	041101	042514	PRTPAR::.ASCIZ /UNABLE TO PRINT DEVICE PARAMETERS FOR THIS UNIT/
2278	004162'	052040	020117	051120	
2279	004170'	047111	020124	042504	
2280	004176'	044526	042503	050040	
2281	004204'	051101	046501	052105	
2282	004212'	051105	020123	047506	
2283	004220'	020122	044124	051511	
2284	004226'	052440	044516	000124	
2285	004234'	040445	041520	051123	FORM1::.ASCIZ /%APCSR%D1% DOES NOT EXIST%/
2286	004242'	042045	022461	020101	
2287	004250'	047504	051505	047040	
2288	004256'	052117	042440	044530	
2289	004264'	052123	047045	000	
2290	004271'	045	050101	051503	FORM2::.ASCIZ /%APCSR%D1% BIT %22% IS %1%/
2291	004276'	022522	030504	040445	
2292	004304'	041040	052111	022440	
2293	004312'	031132	040445	044440	
2294	004320'	020123	052045	047045	
2295	004326'	000			
2296	004327'	045	050101	051503	FORM3::.ASCIZ /%APCSR%D1% FAILED DATA PATTERN TEST%/
2297	004334'	022522	030504	040445	
2298	004342'	043040	044501	042514	
2299	004350'	020104	040504	040524	
2300	004356'	050040	052101	042524	
2301	004364'	047122	052040	051505	
2302	004372'	022524	000116		
2303	004376'	040445	040504	040524	FORM4::.ASCIZ /%ADATA WRITTEN: %06% DATA READ: %06%/

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2304	004404'	053440	044522	052124	
2305	004412'	047105	020072	047445	
2306	004420'	022466	020101	040504	
2307	004426'	040524	051040	040505	
2308	004434'	035104	022440	033117	
2309	004442'	047045	000		
2310	004445'	045	041501	047101	FORM5:: .ASCIZ /%ACANNOT CLEAR INTE BIT IN PCSR0%N/
2311	004452'	047516	020124	046103	
2312	004460'	040505	020122	047111	
2313	004466'	042524	041040	052111	
2314	004474'	044440	020116	041520	
2315	004502'	051123	022460	000116	
2316	004510'	052045	051445	022461	FORM6:: .ASCIZ /%T%S1%T%S1%T%S1%T%A PORT COMMAND WAS ISSUED%N/
2317	004516'	022524	030523	052045	
2318	004524'	051445	022461	022524	
2319	004532'	020101	047520	052122	
2320	004540'	041440	046517	040515	
2321	004546'	042116	053440	051501	
2322	004554'	044440	051523	042525	
2323	004562'	022504	000116		
2324	004566'	040445	051103	020103	FORM7:: .ASCIZ /%ACRC CHECK ERROR ON DATA DUMPED FROM DEUNA ROM%N/
2325	004574'	044103	041505	020113	
2326	004602'	051105	047522	020122	
2327	004610'	047117	042040	052101	
2328	004616'	020101	052504	050115	
2329	004624'	042105	043040	047522	
2330	004632'	020115	042504	047125	
2331	004640'	020101	047522	022515	
2332	004646'	000116			
2333	004650'	040445	047104	020111	FORM8:: .ASCIZ /%ADNI BIT DID NOT CLEAR AFTER WRITING A 1 TO IT%N/
2334	004656'	044502	020124	044504	
2335	004664'	020104	047516	020124	
2336	004672'	046103	040505	020122	
2337	004700'	043101	042524	020122	
2338	004706'	051127	052111	047111	
2339	004714'	020107	020101	020061	
2340	004722'	047524	044440	022524	
2341	004730'	000116			
2342	004732'	040445	041520	051123	FORM9:: .ASCIZ /%APCSR%D1%S1%T%S1%T%S1%T%A RESET WAS ISSUED%N/
2343	004740'	042045	022461	030523	
2344	004746'	052045	051445	022461	
2345	004754'	022524	030523	052045	
2346	004762'	040445	051040	051505	
2347	004770'	052105	053440	051501	
2348	004776'	044440	051523	042525	
2349	005004'	022504	000116		
2350	005010'	040445	042504	047125	FORM10:: .ASCIZ /%ADEUNA NOT IN READY STATE AFTER RESET WAS ISSUED%N/
2351	005016'	020101	047516	020124	
2352	005024'	047111	051040	040505	
2353	005032'	054504	051440	040524	
2354	005040'	042524	040440	052106	
2355	005046'	051105	051040	051505	
2356	005054'	052105	053440	051501	
2357	005062'	044440	051523	042525	
2358	005070'	022504	000116		
2359	005074'	040445	040504	040524	FORM11:: .ASCIZ /%ADATA COMPARE ERROR%N/

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2360	005102'	041440	046517	040520	
2361	005110'	042522	042440	051122	
2362	005116'	051117	047045	000	
2363	005123'	045	041101	043125	FORM12::.ASCIIZ /%ABUFFER ADDRESS: %06%S1%ADATA SB: %06%S1%ADATA WAS: %06%N/
2364	005130'	042506	020122	042101	
2365	005136'	051104	051505	035123	
2366	005144'	022440	033117	051445	
2367	005152'	022461	042101	052101	
2368	005160'	020101	041123	020072	
2369	005166'	047445	022466	030523	
2370	005174'	040445	040504	040524	
2371	005202'	053440	051501	020072	
2372	005210'	047445	022466	000116	
2373	005216'	040445	033061	041040	FORM13::.ASCIIZ /%A16 BIT CRC SHOULD BE: 0%S2%AWAS: %06%N/
2374	005224'	052111	041440	041522	
2375	005232'	051440	047510	046125	
2376	005240'	020104	042502	020072	
2377	005246'	022460	031123	040445	
2378	005254'	040527	035123	022440	
2379	005262'	033117	047045	000	
2380	005267'	045	042101	052101	FORM15::.ASCIIZ /%ADATA WRITTEN TO PCSR1 FROM T11 WAS: %06%N/
2381	005274'	020101	051127	052111	
2382	005302'	042524	020116	047524	
2383	005310'	050040	051503	030522	
2384	005316'	043040	047522	020115	
2385	005324'	030524	020061	040527	
2386	005332'	035123	022440	033117	
2387	005340'	047045	000		
2388	005343'	045	042101	052101	FORM16::.ASCIIZ /%ADATA READ FROM PCSR1 FROM UNIBUS WAS: %06%N/
2389	005350'	020101	042522	042101	
2390	005356'	043040	047522	020115	
2391	005364'	041520	051123	020061	
2392	005372'	051106	046517	052440	
2393	005400'	044516	052502	020123	
2394	005406'	040527	035123	022440	
2395	005414'	033117	047045	000	
2396	005421'	045	047101	020117	FORM17::.ASCIIZ /%ANO INTERRUPT AFTER NOP PORT COMMAND WAS ISSUED%N/
2397	005426'	047111	042524	051122	
2398	005434'	050125	020124	043101	
2399	005442'	042524	020122	047516	
2400	005450'	020120	047520	052122	
2401	005456'	041440	046517	040515	
2402	005464'	042116	053440	051501	
2403	005472'	044440	051523	042525	
2404	005500'	022504	000116		
2405	005504'	040445	047125	020101	FORM18::.ASCIIZ /%AUNA DID NOT INTERRUPT AT CORRECT PRIORITY%N/
2406	005512'	044504	020104	047516	
2407	005520'	020124	047111	042524	
2408	005526'	051122	050125	020124	
2409	005534'	052101	041440	051117	
2410	005542'	042522	052103	050040	
2411	005550'	044522	051117	052111	
2412	005556'	022531	000116		
2413	005562'	040445	047516	044440	FORM19::.ASCIIZ /%ANO INTERRUPT AFTER T11 SET %T% IN PCSR0%N/
2414	005570'	052116	051105	052522	
2415	005576'	052120	040440	052106	

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2416	005604*	051105	052040	030461	
2417	005612*	051440	052105	022440	
2418	005620*	022524	020101	047111	
2419	005626*	050040	051503	030122	
2420	005634*	047045	000		
2421	005637*	045	052101	046511	FORM20:::ASCIIZ /%TIMER DID NOT INTERRUPT T11%N/
2422	005644*	051105	042040	042111	
2423	005652*	047040	052117	044440	
2424	005660*	052116	051105	052522	
2425	005666*	052120	052040	030461	
2426	005674*	047045	000		
2427	005677*	045	052101	046511	FORM21:::ASCIIZ /%TIMER DID NOT INTERRUPT WHEN EXPECTED%N/
2428	005704*	051105	042040	042111	
2429	005712*	047040	052117	044440	
2430	005720*	052116	051105	052522	
2431	005726*	052120	053440	042510	
2432	005734*	020116	054105	042520	
2433	005742*	052103	042105	047045	
2434	005750*	000			
2435	005751*	045	042501	050130	FORM22:::ASCIIZ /%AEXPECTED INTERRUPT BETWEEN 8 AND 12 SECONDS%N/
2436	005756*	041505	042524	020104	
2437	005764*	047111	042524	051122	
2438	005772*	050125	020124	042502	
2439	006000*	053524	042505	020116	
2440	006006*	020070	047101	020104	
2441	006014*	031061	051440	041505	
2442	006022*	047117	051504	047045	
2443	006030*	000			
2444	006031*	045	051101	041505	FORM23:::ASCIIZ /%ARECIEVED INTERRUPT AT %D1%A SECONDS%N/
2445	006036*	042511	042526	020104	
2446	006044*	047111	042524	051122	
2447	006052*	050125	020124	052101	
2448	006060*	022440	030504	040445	
2449	006066*	051440	041505	047117	
2450	006074*	051504	047045	000	
2451	006101*	045	046501	041511	FORM24:::ASCIIZ /%AMICRO TEST %D2%A HUNG%N/
2452	006106*	047522	052040	051505	
2453	006114*	020124	042045	022462	
2454	006122*	020101	052510	043516	
2455	006130*	047045	000		
2456	006133*	045	042101	052101	FORM25:::ASCIIZ /%ADATA WRITTEN TO 'DMATO' REGISTER = %06%N/
2457	006140*	020101	051127	052111	
2458	006146*	042524	020116	047524	
2459	006154*	023440	046504	052101	
2460	006162*	023517	051040	043505	
2461	006170*	051511	042524	020122	
2462	006176*	020075	047445	022466	
2463	006204*	000116			
2464	006206*	040445	040504	040524	FORM26:::ASCIIZ /%ADATA READ FROM 'DMATO' REGISTER = %06%N/
2465	006214*	051040	040505	020104	
2466	006222*	051106	046517	023440	
2467	006230*	046504	052101	023517	
2468	006236*	051040	043505	051511	
2469	006244*	042524	020122	036440	
2470	006252*	022440	033117	047045	
2471	006260*	000			

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2472	006261'	045	042101	052101	FORM27:::ASCIIZ	/%ADATA SHOULD BE: %06%A DATA WAS: %06%N/
2473	006266'	020101	044123	052517		
2474	006274'	042114	041040	035105		
2475	006302'	022440	033117	040445		
2476	006310'	042040	052101	020101		
2477	006316'	040527	035123	022440		
2478	006324'	033117	047045	000		
2479	006331'	045	022524	000116	FORM28:::ASCIIZ	/%T%N/
2480	006336'	040445	042101	051104	FORM29:::ASCIIZ	/%AADDRESS = %06%S2/
2481	006344'	051505	020123	020075		
2482	006352'	047445	022466	031123		
2483	006360'	000				
2484	006361'	045	051101	041505	FORM30:::ASCIIZ	/%ARECEIVER STATUS:%06%N/
2485	006366'	044505	042526	020122		
2486	006374'	052123	052101	051525		
2487	006402'	022472	033117	047045		
2488	006410'	000				
2489	006411'	045	041101	043125	FORM31:::ASCIIZ	/%ABUFFER OFFSET:%06%N/
2490	006416'	042506	020122	043117		
2491	006424'	051506	052105	022472		
2492	006432'	033117	047045	000		
2493	006437'	045	052101	040522	FORM32:::ASCIIZ	/%ATRANSMIT STATUS WORD %D1%S1%T%A NOT CLEAR AFTER TRANSMIT%N/
2494	006444'	051516	044515	020124		
2495	006452'	052123	052101	051525		
2496	006460'	053440	051117	020104		
2497	006466'	042045	022461	030523		
2498	006474'	052045	040445	047040		
2499	006502'	052117	041440	042514		
2500	006510'	051101	040440	052106		
2501	006516'	051105	052040	040522		
2502	006524'	051516	044515	022524		
2503	006532'	000116				
2504	006534'	040445	051124	047101	FORM33:::ASCIIZ	/%ATRANSMIT BYTE COUNT = %D4%N/
2505	006542'	046523	052111	041040		
2506	006550'	052131	020105	047503		
2507	006556'	047125	020124	020075		
2508	006564'	042045	022464	000116		
2509	006572'	040445	042522	042503	FORM34:::ASCIIZ	/%ARECEIVE BYTE COUNT SHOULD BE: %D4%S3%A WAS: %D4%N/
2510	006600'	053111	020105	054502		
2511	006606'	042524	041440	052517		
2512	006614'	052116	051440	047510		
2513	006622'	046125	020104	042502		
2514	006630'	020072	042045	022464		
2515	006636'	031523	040445	040527		
2516	006644'	035123	022440	032104		
2517	006652'	047045	000			
2518	006655'	045	040501	052103	FORM35:::ASCIIZ	/%AACTUAL NUMBER OF BYTES RECEIVED SHOULD BE: %D4%A WAS: %D4%N/
2519	006662'	040525	020114	052516		
2520	006670'	041115	051105	047440		
2521	006676'	020106	054502	042524		
2522	006704'	020123	042522	042503		
2523	006712'	053111	042105	051440		
2524	006720'	047510	046125	020104		
2525	006726'	042502	020072	042045		
2526	006734'	022464	020101	020040		
2527	006742'	040527	035123	022440		

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2528	006750'	032104	047045	000	
2529	006755'	045	046101	047111	FORM36:::ASCIIZ /%ALINK MEMORY ADDRESS = %06%N/
2530	006762'	020113	042515	047515	
2531	006770'	054522	040440	042104	
2532	006776'	042522	051523	036440	
2533	007004'	022440	033117	047045	
2534	007012'	000			
2535	007013'	045	052101	040522	FORM37:::ASCII /%ATRANSMIT STATUS INFORMATION INCORRECT AFTER LOOPBACK STEP /
2536	007020'	051516	044515	020124	
2537	007026'	052123	052101	051525	
2538	007034'	044440	043116	051117	
2539	007042'	040515	044524	047117	
2540	007050'	044440	041516	051117	
2541	007056'	042522	052103	040440	
2542	007064'	052106	051105	046040	
2543	007072'	047517	041120	041501	
2544	007100'	020113	052123	050105	
2545	007106'	040			
2546	007107'	116	046525	042502	.ASCIIZ /NUMBER %D2%N/
2547	007114'	020122	042045	022462	
2548	007122'	000116			
2549	007124'	040445	051124	047101	FORM38:::ASCIIZ /%ATRANSMIT STATUS WORD %D1%A SHOULD BE: %06%A WAS: %06%N/
2550	007132'	046523	052111	051440	
2551	007140'	040524	052524	020123	
2552	007146'	047527	042122	022440	
2553	007154'	030504	040445	051440	
2554	007162'	047510	046125	020104	
2555	007170'	042502	020072	047445	
2556	007176'	022466	020101	040527	
2557	007204'	035123	022440	033117	
2558	007212'	047045	000		
2559	007215'	045	052101	051104	FORM39:::ASCIIZ /%ATDR COUNTER NOT INCREMENTING%N/
2560	007222'	041440	052517	052116	
2561	007230'	051105	047040	052117	
2562	007236'	044440	041516	042522	
2563	007244'	042515	052114	047111	
2564	007252'	022507	000116		
2565	007256'	040445	042522	051124	FORM40:::ASCIIZ /%ARETRY BACKOFF WAIT TIME INTERVAL NOT VARYING%N%N/
2566	007264'	020131	040502	045503	
2567	007272'	043117	020106	040527	
2568	007300'	052111	052040	046511	
2569	007306'	020105	047111	042524	
2570	007314'	053122	046101	047040	
2571	007322'	052117	053040	051101	
2572	007330'	044531	043516	047045	
2573	007336'	047045	000		
2574	007341'	045	051101	052105	FORM41:::ASCIIZ /%ARETRY #%S4%AWAIT INTERVAL COUNT%N%N/
2575	007346'	054522	021440	051445	
2576	007354'	022464	053501	044501	
2577	007362'	020124	047111	042524	
2578	007370'	053122	046101	041440	
2579	007376'	052517	052116	047045	
2580	007404'	047045	000		
2581	007407'	045	032504	051445	FORM42:::ASCIIZ /%D5%S7/
2582	007414'	000067			
2583	007416'	042045	022465	000116	FORM43:::ASCIIZ /%D5%N/

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2584	007424	040445	044124	020105	FORM44:::ASCIIZ	/%ATHE 48 BIT DESTINATION ADDRESS PATTERN IS:%N/
2585	007432	034064	041040	052111		
2586	007440	042040	051505	044524		
2587	007446	040516	044524	047117		
2588	007454	040440	042104	042522		
2589	007462	051523	050040	052101		
2590	007470	042524	047122	044440		
2591	007476	035123	047045	000		
2592	007503	045	046101	053517	FORM45:::ASCIIZ	/%ALOWER ORDER = %06%N/
2593	007510	051105	047440	042122		
2594	007516	051105	036440	022440		
2595	007524	033117	047045	000		
2596	007531	045	046501	042111	FORM46:::ASCIIZ	/%AMIDDLE ORDER = %06%N/
2597	007536	046104	020105	051117		
2598	007544	042504	020122	020075		
2599	007552	047445	022466	000116		
2600	007560	040445	050125	042520	FORM47:::ASCIIZ	/%AUPPER ORDER = %06%N/
2601	007566	020122	051117	042504		
2602	007574	020122	036440	022440		
2603	007602	033117	047045	000		
2604	007607	045	042101	052505	FORM48:::ASCIIZ	/%ADEUNA FAILED TO RECOGNIZE A STATION ADDRESS THAT MATCHES%N/
2605	007614	040516	043040	044501		
2606	007622	042514	020104	047524		
2607	007630	051040	041505	043517		
2608	007636	044516	042532	040440		
2609	007644	051440	040524	044524		
2610	007652	047117	040440	042104		
2611	007660	042522	051523	052040		
2612	007666	040510	020124	040515		
2613	007674	041524	042510	022523		
2614	007702	000116				
2615	007704	040445	044124	020105	FORM49:::ASCIIZ	/%ATHE STATION ADDRESS IN RAM POSITION %D2%N/
2616	007712	052123	052101	047511		
2617	007720	020116	042101	051104		
2618	007726	051505	020123	047111		
2619	007734	051040	046501	050040		
2620	007742	051517	052111	047511		
2621	007750	020116	042045	022462		
2622	007756	000116				
2623	007760	040445	042522	042503	FORM50:::ASCIIZ	/%ARECEIVER STATUS WORD 0 SHOULD BE: %06%S3%AWAS: %06%N/
2624	007766	053111	051105	051440		
2625	007774	040524	052524	020123		
2626	010002	047527	042122	030040		
2627	010010	051440	047510	046125		
2628	010016	020104	042502	020072		
2629	010024	047445	022466	031523		
2630	010032	040445	040527	035123		
2631	010040	022440	033117	047045		
2632	010046	000				
2633	010047	045	041501	041522	FORM51:::ASCIIZ	/%ACRC ERROR BIT NOT SET%N/
2634	010054	042440	051122	051117		
2635	010062	041040	052111	047040		
2636	010070	052117	051440	052105		
2637	010076	047045	000			
2638	010101	045	042501	051122	FORM52:::ASCIIZ	/%AERROR SUMMARY BIT NOT SET%N/
2639	010106	051117	051440	046525		

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2640	010114	040515	054522	041040	
2641	010122	052111	047040	052117	
2642	010130	051440	052105	047045	
2643	010136	000			
2644	010137	045	047101	046525	FORM53:::ASCIIZ /%ANUMBER OF DATA BYTES TRANSMITTED: %D4%N/
2645	010144	042502	020122	043117	
2646	010152	042040	052101	020101	
2647	010160	054502	042524	020123	
2648	010166	051124	047101	046523	
2649	010174	052111	042524	035104	
2650	010202	022440	032104	047045	
2651	010210	000			
2652	010211	045	042101	052101	FORM54:::ASCIIZ /%ADATA PATTERN: %O6%N/
2653	010216	020101	040520	052124	
2654	010224	051105	035116	022440	
2655	010232	033117	047045	000	
2656	010237	045	042101	052505	FORM55:::ASCIIZ /%ADEUNA FAILED TO REJECT A RUNT PACKET%N/
2657	010244	040516	043040	044501	
2658	010252	042514	020104	047524	
2659	010260	051040	045105	041505	
2660	010266	020124	020101	052522	
2661	010274	052116	050040	041501	
2662	010302	042513	022524	000116	
2663	010310	040445	042504	047125	FORM56:::ASCIIZ /%ADEUNA FAILED TO RECOVER RECEIVE BUFFER%N/
2664	010316	020101	040506	046111	
2665	010324	042105	052040	020117	
2666	010332	042522	047503	042526	
2667	010340	020122	042522	042503	
2668	010346	053111	020105	052502	
2669	010354	043106	051105	047045	
2670	010362	000			
2671	010363	045	047101	046525	FORM57:::ASCIIZ /%ANUMBER OF BYTES IN RUNT PACKET: %D2%N/
2672	010370	042502	020122	043117	
2673	010376	041040	052131	051505	
2674	010404	044440	020116	052522	
2675	010412	052116	050040	041501	
2676	010420	042513	035124	022440	
2677	010426	031104	047045	000	
2678	010433	045	047101	046525	FORM58:::ASCIIZ /%ANUMBER OF BYTES IN LEGITIMATE PACKET: %D4%N/
2679	010440	042502	020122	043117	
2680	010446	041040	052131	051505	
2681	010454	044440	020116	042514	
2682	010462	044507	044524	040515	
2683	010470	042524	050040	041501	
2684	010476	042513	035124	022440	
2685	010504	032104	047045	000	
2686	010511	045	051101	041505	FORM59:::ASCIIZ /%ARECEIVE BUFFER ADDRESS AVAILABLE BEFORE RECEPTION: %O6%N/
2687	010516	044505	042526	041040	
2688	010524	043125	042506	020122	
2689	010532	042101	051104	051505	
2690	010540	020123	053101	044501	
2691	010546	040514	046102	020105	
2692	010554	042502	047506	042522	
2693	010562	051040	041505	050105	
2694	010570	044524	047117	020072	
2695	010576	047445	022466	000116	

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2696	010604'	040445	042522	042503	FORM60:::ASCIIZ	/%ARECEIVE BUFFER ADDRESS COMPLETED AFTER RECEPTION%/
2697	010612'	053111	020105	052502		
2698	010620'	043106	051105	040440		
2699	010626'	042104	042522	051523		
2700	010634'	041440	046517	046120		
2701	010642'	052105	042105	040440		
2702	010650'	052106	051105	051040		
2703	010656'	041505	050105	044524		
2704	010664'	047117	047045	000		
2705	010671'	045	042101	052101	FORM61:::ASCIIZ	/%ADATA COMPARE ERROR IN RECOVERED RECEIVE BUFFER%/
2706	010676'	020101	047503	050115		
2707	010704'	051101	020105	051105		
2708	010712'	047522	020122	047111		
2709	010720'	051040	041505	053117		
2710	010726'	051105	042105	051040		
2711	010734'	041505	044505	042526		
2712	010742'	041040	043125	042506		
2713	010750'	022522	000116			
2714	010754'	052045	040445	042040	FORM62:::ASCIIZ	/%T% DID NOT CLEAR AFTER WRITING 1 TO IT%/
2715	010762'	042111	047040	052117		
2716	010770'	041440	042514	051101		
2717	010776'	040440	052106	051105		
2718	011004'	053440	044522	044524		
2719	011012'	043516	030440	052040		
2720	011020'	020117	052111	047045		
2721	011026'	000				
2722	011027'	045	051501	046105	FORM63:::ASCIIZ	/%ASELF TEST ERROR CODE = %02%/
2723	011034'	020106	042524	052123		
2724	011042'	042440	051122	051117		
2725	011051'	041440	042117	020105		
2726	011056'	020075	047445	022462		
2727	011064'	000116				
2728	011066'	047045	040445	047522	FORM64:::ASCIIZ	/%N%AROM MICROCODE VERSION (DECIMAL): %D2/
2729	011074'	020115	044515	051103		
2730	011102'	041517	042117	020105		
2731	011110'	042526	051522	047511		
2732	011116'	020116	042050	041505		
2733	011124'	046511	046101	035051		
2734	011132'	022440	031104	000		
2735	011137'	045	022516	051501	FORM65:::ASCIIZ	/%N%ASWITCH PACK = %06/
2736	011144'	044527	041524	020110		
2737	011152'	040520	045503	036440		
2738	011160'	022440	033117	000		
2739	011165'	045	047101	020117	FORM66:::ASCIIZ	/%AND INTERRUPT FROM TRANSMIT STATE MACHINE TO T-11%/
2740	011172'	047111	042524	051122		
2741	011200'	050125	020124	051106		
2742	011206'	046517	052040	040522		
2743	011214'	051516	044515	020124		
2744	011222'	052123	052101	020105		
2745	011230'	040515	044103	047111		
2746	011236'	020105	047524	052040		
2747	011244'	030455	022461	000116		
2748	011252'	040445	047516	044440	FORM67:::ASCIIZ	/%AND INTERRUPT FROM RECEIVE STATE MACHINE TO T-11%/
2749	011260'	052116	051105	052522		
2750	011266'	052120	043040	047522		
2751	011274'	020115	042522	042503		

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2752	011302'	053111	020105	052123	
2753	011310'	052101	020105	040515	
2754	011316'	044103	047111	020105	
2755	011324'	047524	052040	030455	
2756	011332'	022461	000116		
2757	011336'	040445	042522	042503	FORM68:::ASCII /%RECEIVER BYTE COUNTER FAILED TO LOCK UP AT MAXIMUM VALUE%/
2758	011344'	053111	051105	041040	
2759	011352'	052131	020105	047503	
2760	011360'	047125	042524	020122	
2761	011366'	040506	046111	042105	
2762	011374'	052040	020117	047514	
2763	011402'	045503	052440	020120	
2764	011410'	052101	046440	054101	
2765	011416'	046511	046525	053040	
2766	011424'	046101	042525	047045	
2767	011432'	000			
2768	011433'	045	042101	052505	FORM69:::ASCII /%ADEUNA DID NOT REJECT A PACKET TRANSMITTED TO ITSELF/
2769	011440'	040516	042040	042111	
2770	011446'	047040	052117	051040	
2771	011454'	045105	041505	020124	
2772	011462'	020101	040520	045503	
2773	011470'	052105	052040	040522	
2774	011476'	051516	044515	052124	
2775	011504'	042105	052040	020117	
2776	011512'	052111	042523	043114	
2777	011520'	040445	044440	020116	.ASCII /%A IN HALF-DUPLEX MODE%/
2778	011526'	040510	043114	042055	
2779	011534'	050125	042514	020130	
2780	011542'	047515	042504	047045	
2781	011550'	000			
2782	011551'	045	052101	040522	FORM70:::ASCII /%ATRANSMIT BUFFER ADDRESS = %06%/
2783	011556'	051516	044515	020124	
2784	011564'	052502	043106	051105	
2785	011572'	040440	042104	042522	
2786	011600'	051523	036440	022440	
2787	011606'	033117	047045	000	
2788	011613'	045	051101	041505	FORM71:::ASCII /%ARECEIVE BUFFER ADDRESS = %06%/
2789	011620'	044505	042526	041040	
2790	011626'	043125	042506	020122	
2791	011634'	042101	051104	051505	
2792	011642'	020123	036440	022440	
2793	011650'	033117	047045	000	
2794	011655'	045	052101	030461	FORM72:::ASCII /%AT11 LINK MEMORY PARITY ERROR OCCURRED%/
2795	011662'	046040	047111	020113	
2796	011670'	042515	047515	054522	
2797	011676'	050040	051101	052111	
2798	011704'	020131	051105	047522	
2799	011712'	020122	041517	052503	
2800	011720'	051122	042105	047045	
2801	011726'	000			
2802	011727'	045	052101	030461	FORM73:::ASCII /%AT11 NPR TIMEOUT ERROR OCCURRED%/
2803	011734'	047040	051120	052040	
2804	011742'	046511	047505	052125	
2805	011750'	042440	051122	051117	
2806	011756'	047440	041503	051125	
2807	011764'	042522	022504	000116	

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2808	011772'	040445	030524	020061	FORM74::ASCIZ	/%AT11 NON-EXISTANT MEMORY TIMEOUT OCCURRED%/
2809	012000'	047516	026516	054105		
2810	012006'	051511	040524	052116		
2811	012014'	046440	046505	051117		
2812	012022'	020131	044524	042515		
2813	012030'	052517	020124	041517		
2814	012036'	052503	051122	042105		
2815	012044'	047045	000			
2816	012047'	045	052101	030461	FORM75::ASCIZ	/%AT11 UNEXPECTED INTERRUPT OCCURRED%/
2817	012054'	052440	042516	050130		
2818	012062'	041505	042524	020104		
2819	012070'	047111	042524	051122		
2820	012076'	050125	020124	041517		
2821	012104'	052503	051122	042105		
2822	012112'	047045	000			
2823	012115'	045	046501	052101	FORM76::ASCIZ	/%AMATCH BIT FAILED TO SET%/
2824	012122'	044103	041040	052111		
2825	012130'	043040	044501	042514		
2826	012136'	020104	047524	051440		
2827	012144'	052105	047045	000		
2828	012151'	045	046501	052101	FORM77::ASCIZ	/%AMATCH BIT SET BUT NO RECEIVER INTERRUPT%/
2829	012156'	044103	041040	052111		
2830	012164'	051440	052105	041040		
2831	012172'	052125	047040	020117		
2832	012200'	042522	042503	053111		
2833	012206'	051105	044440	052116		
2834	012214'	051105	052522	052120		
2835	012222'	047045	000			
2836	012225'	045	051501	047510	FORM78::ASCIZ	/%ASHOULD BE: %06%S2%AWAS: %06%/
2837	012232'	046125	020104	042502		
2838	012240'	020072	047445	022466		
2839	012246'	031123	040445	040527		
2840	012254'	035123	022440	033117		
2841	012262'	047045	000			
2842	012265'	045	042101	052505	FORM79::ASCIZ	/%ADEUNA FAILED TO REJECT A DATAGRAM.%/
2843	012272'	040516	043040	044501		
2844	012300'	042514	020104	047524		
2845	012306'	051040	045105	041505		
2846	012314'	020124	020101	040504		
2847	012322'	040524	051107	046501		
2848	012330'	022456	000116			
2849	012334'	040445	042504	052123	FORM80::ASCIZ	/%ADESTINATION ADDRESS OF DATAGRAM IS ALL 1'S%/
2850	012342'	047111	052101	047511		
2851	012350'	020116	042101	051104		
2852	012356'	051505	020123	043117		
2853	012364'	042040	052101	043501		
2854	012372'	040522	020115	051511		
2855	012400'	040440	046114	030440		
2856	012406'	051447	047045	000		
2857	012413'	045	051501	040524	FORM81::ASCIZ	/%ASTATION ADDRESS RAM IS ALL 0'S%/
2858	012420'	044524	047117	040440		
2859	012426'	042104	042522	051523		
2860	012434'	051040	046501	044440		
2861	012442'	020123	046101	020114		
2862	012450'	023460	022523	000116		
2863	012456'	040445	044524	042515	FORM82::ASCIZ	/%ATIMEOUT WAITING FOR MICROCODE TO ENTER MICROMONITOR%/

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2864	012464'	052517	020124	040527
2865	012472'	052111	047111	020107
2866	012500'	047506	020122	044515
2867	012506'	051103	041517	042117
2868	012514'	020105	047524	042440
2869	012522'	052116	051105	046440
2870	012530'	041511	047522	047515
2871	012536'	044516	047524	022522
2872	012544'	000116		
2873				
2874				
2875				

.EVEN

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GLOBAL ERROR REPORT SECTION

.SBTTL GLOBAL ERROR REPORT SECTION

: THE GLOBAL ERROR REPORT SECTION CONTAINS MESSAGE PRINTING AREAS
: USED BY MORE THAN TEST TO OUTPUT ADDITIONAL ERROR INFORMATION. PRINTB
: (BASIC) AND PRINTX (EXTENDED) CALLS ARE USED TO CALL PRINT SERVICES.
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012546' 013746 000304'
012546' 012746 004234'
012546' 012746 000002
012546' 010600
012564' 104414
012566' 062706 000C06
012572'
012572' 104423
012574'
012574'
012574'
012574' 013746 000312'
012600' 013746 000306'
012604' 013746 000304'
012610' 012746 004271'
012614' 012746 000004
012620' 010600
012622' 104414
012624' 062706 000012
012630'
012630' 104423
012632'
012632'
012632'
012632' 013746 000304'
012636' 012746 004327'
012642' 012746 000002
012646' 010600
012650' 104414
012652' 062706 000006
012656'
012656' 010446
012660' 010346
012662' 012746 004376'
012666' 012746 000003
012672' 010600
012674' 104414
012676' 062706 000010

BGNMSG RACMG1
PRINTB #FORM1,CSRNUM
ENDMSG
BGNMSG RACMG2
PRINTB #FORM2,CSRNUM,BITNUM,BITSTA
ENDMSG
BGNMSG RACMG3
PRINTB #FORM3,CSRNUM
PRINTB #FORM4,R3,R4

RACMG1::
MOV CSRNUM,-(SP)
MOV #FORM1,-(SP)
MOV #2,-(SP)
MOV SP,R0
TRAP C\$PNTB
ADD #6,SP
L10001: TRAP C\$MSG
RACMG2::
MOV BITSTA,-(SP)
MOV BITNUM,-(SP)
MOV CSRNUM,-(SP)
MOV #FORM2,-(SP)
MOV #4,-(SP)
MOV SP,R0
TRAP C\$PNTB
ADD #12,SP
L10002: TRAP C\$MSG
RACMG3::
MOV CSRNUM,-(SP)
MOV #FORM3,-(SP)
MOV #2,-(SP)
MOV SP,R0
TRAP C\$PNTB
ADD #6,SP
MOV R4,-(SP)
MOV R3,-(SP)
MOV #FORM4,-(SP)
MOV #3,-(SP)
MOV SP,R0
TRAP C\$PNTB
ADD #10,SP

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GLOBAL ERROR REPORT SECTION

2932	012702'			ENDMSG			
2933	012702'					L10003:	
2934	012702'	104423				TRAP	C\$MSG
2935							
2936	012704'			BGNMSG	RACMG4		
2937	012704'					RACMG4::	
2938	012704'				PRINTB	#FORM5	
2939	012704'	012746	004445'			MOV	#FORM5,-(SP)
2940	012710'	012746	000001			MOV	#1,-(SP)
2941	012714'	010600				MCV	SP,R0
2942	012716'	104414				TRAP	C\$PNTB
2943	012720'	062706	000004			ADD	#4,SP
2944	012724'			ENDMSG			
2945	012724'					L10004:	
2946	012724'	104423				TRAP	C\$MSG
2947							
2948	012726'			BGNMSG	RACMG7		
2949	012726'					RACMG7::	
2950	012726'				PRINTB	#FORM8	
2951	012726'	012746	004650'			MOV	#FORM8,-(SP)
2952	012732'	012746	000001			MOV	#1,-(SP)
2953	012736'	010600				MOV	SP,R0
2954	012740'	104414				TRAP	C\$PNTB
2955	012742'	062706	000004			ADD	#4,SP
2956	012746'			ENDMSG			
2957	012746'					L10005:	
2958	012746'	104423				TRAP	C\$MSG
2959							
2960	012750'			BGNMSG	MSG1		
2961	012750'					MSG1::	
2962	012750'				PRINTB	#FORM6,BITNAM,BITSTA,PWHEN,PCOMND	
2963	012750'	013746	000316'			MOV	PCOMND,-(SP)
2964	012754'	013746	000314'			MOV	PWHEN,-(SP)
2965	012760'	013746	000312'			MOV	BITSTA,-(SP)
2966	012764'	013746	000310'			MOV	BITNAM,-(SP)
2967	012770'	012746	004510'			MOV	#FORM6,-(SP)
2968	012774'	012746	000005			MOV	#5,-(SP)
2969	013000'	010600				MOV	SP,R0
2970	013002'	104414				TRAP	C\$PNTB
2971	013004'	062706	000014			ADD	#14,SP
2972	013010'			ENDMSG			
2973	013010'					L10006:	
2974	013010'	104423				TRAP	C\$MSG
2975							
2976	013012'			BGNMSG	MSG2		
2977	013012'					MSG2::	
2978	013012'				PRINTB	#FORM7	
2979	013012'	012746	004566'			MOV	#FORM7,-(SP)
2980	013016'	012746	000001			MOV	#1,-(SP)
2981	013022'	010600				MOV	SP,R0
2982	013024'	104414				TRAP	C\$PNTB
2983	013026'	062706	000004			ADD	#4,SP
2984	013032'				PRINTB	#FORM13,R4	
2985	013032'	010446				MOV	R4,-(SP)
2986	013034'	012746	005216'			MOV	#FORM13,-(SP)
2987	013040'	012746	000002			MOV	#2,-(SP)

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2988	013044'	010600				MOV	SP,R0
2989	013046'	104414				TRAP	C\$PNTB
2990	013050'	062706	000006			ADD	#6,SP
2991	013054'			ENDMSG			
2992	013054'				L10007:		
2993	013054'	104423				TRAP	C\$MSG
2994							
2995	013056'			BGNMSG	MSG3		
2996	013056'					MSG3::	
2997	013056'						
2998	013056'	013746	000314'			MOV	PWHEN,-(SP)
2999	013062'	013746	000312'			MOV	BITSTA,-(SP)
3000	013066'	013746	000310'			MOV	BITNAM,-(SP)
3001	013072'	013746	000304'			MOV	CSRNUM,-(SP)
3002	013076'	012746	004732'			MOV	#FORM9,-(SP)
3003	013102'	012746	000005			MOV	#5,-(SP)
3004	013106'	010600				MOV	SP,R0
3005	013110'	104414				TRAP	C\$PNTB
3006	013112'	062706	000014			ADD	#14,SP
3007	013116'			ENDMSG			
3008	013116'				L10010:		
3009	013116'	104423				TRAP	C\$MSG
3010							
3011	013120'			BGNMSG	MSG4		
3012	013120'					MSG4::	
3013	013120'						
3014	013120'	012746	005010'			MOV	#FORM10,-(SP)
3015	013124'	012746	000001			MOV	#1,-(SP)
3016	013130'	010600				MOV	SP,R0
3017	013132'	104414				TRAP	C\$PNTB
3018	013134'	062706	000004			ADD	#4,SP
3019	013140'			ENDMSG			
3020	013140'				L10011:		
3021	013140'	104423				TRAP	C\$MSG
3022							
3023	013142'			BGNMSG	MSG5		
3024	013142'					MSG5::	
3025	013142'						
3026	013142'	012746	005074'			MOV	#FORM11,-(SP)
3027	013146'	012746	000001			MOV	#1,-(SP)
3028	013152'	010600				MOV	SP,R0
3029	013154'	104414				TRAP	C\$PNTB
3030	013156'	062706	000004			ADD	#4,SP
3031	013162'						
3032	013162'	011146				MOV	(R1),-(SP)
3033	013164'	011446				MOV	(R4),-(SP)
3034	013166'	010146				MOV	R1,-(SP)
3035	013170'	012746	005123'			MOV	#FORM12,-(SP)
3036	013174'	012746	000004			MOV	#4,-(SP)
3037	013200'	010600				MOV	SP,R0
3038	013202'	104414				TRAP	C\$PNTB
3039	013204'	062706	000012			ADD	#12,SP
3040	013210'			ENDMSG			
3041	013210'				L10012:		
3042	013210'	104423				TRAP	C\$MSG
3043							

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BGNMSG MSG6
      PRINTB #FORM11
      PRINTB #FORM15,R1
      PRINTB #FORM16,R2
ENDMSG

BGNMSG MSG7
      PRINTB #FORM17
ENDMSG

BGNMSG MSG8
      PRINTB #FORM18
ENDMSG

BGNMSG MSG9
      PRINTB #FORM19,@BITNAM

```

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MSG6::
      MOV #FORM11,-(SP)
      MOV #1,-(SP)
      MOV SP,R0
      TRAP C$PNTB
      ADD #4,SP

      MOV R1,-(SP)
      MOV #FORM15,-(SP)
      MOV #2,-(SP)
      MOV SP,R0
      TRAP C$PNTB
      ADD #6,SP

      MOV R2,-(SP)
      MOV #FORM16,-(SP)
      MOV #2,-(SP)
      MOV SP,R0
      TRAP C$PNTB
      ADD #6,SP

L10013: TRAP C$MSG

MSG7::
      MOV #FORM17,-(SP)
      MOV #1,-(SP)
      MOV SP,R0
      TRAP C$PNTB
      ADD #4,SP

L10014: TRAP C$MSG

MSG8::
      MOV #FORM18,-(SP)
      MOV #1,-(SP)
      MOV SP,R0
      TRAP C$PNTB
      ADD #4,SP

L10015: TRAP C$MSG

MSG9::
      MOV @BITNAM,-(SP)
      MOV #FORM19,-(SP)
      MOV #2,-(SP)

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3044 013212'
3045 013212'
3046 013212'
3047 013212' 012746 005074'
3048 013216' 012746 000001'
3049 013222' 010600
3050 013224' 104414
3051 013226' 062706 000004
3052 013232'
3053 013232' 010146
3054 013234' 012746 005267'
3055 013240' 012746 000002
3056 013244' 010600
3057 013246' 104414
3058 013250' 062706 000006
3059 013254'
3060 013254' 010246
3061 013256' 012746 005343'
3062 013262' 012746 000002
3063 013266' 010600
3064 013270' 104414
3065 013272' 062706 000006
3066 013276'
3067 013276'
3068 013276' 104423
3069
3070 013300'
3071 013300'
3072 013300'
3073 013300' 012746 005421'
3074 013304' 012746 000001'
3075 013310' 010600
3076 013312' 104414
3077 013314' 062706 000004
3078 013320'
3079 013320'
3080 013320' 104423
3081
3082 013322'
3083 013322'
3084 013322'
3085 013322' 012746 005504'
3086 013326' 012746 000001'
3087 013332' 010600
3088 013334' 104414
3089 013336' 062706 000004
3090 013342'
3091 013342'
3092 013342' 104423
3093
3094 013344'
3095 013344'
3096 013344'
3097 013344' 017746 164740
3098 013350' 012746 005562'
3099 013354' 012746 000002

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3100	013340	010600							
3101	013362	104414							MOV SP,R0
3102	013364	062706	000006						TRAP C\$PNTB
3103	013370			ENDMSG					ADD #6,SP
3104	013370							L10016:	
3105	013370	104423							TRAP C\$MSG
3106									
3107	013372			BGNMSG	MSG10				
3108	013372							MSG10::	
3109	013372				PRINTB	#FORM20			
3110	013372	012746	005637						MOV #FORM20,-(SP)
3111	013376	012746	000001						MOV #1,-(SP)
3112	013402	010600							MOV SP,R0
3113	013404	104414							TRAP C\$PNTB
3114	013406	062706	000004						ADD #4,SP
3115	013412			ENDMSG					
3116	013412							L10017:	
3117	013412	104423							TRAP C\$MSG
3118									
3119	013414			BGNMSG	MSG11				
3120	013414							MSG11::	
3121	013414				PRINTB	#FORM21			
3122	013414	012746	005677						MOV #FORM21,-(SP)
3123	013420	012746	000001						MOV #1,-(SP)
3124	013424	010600							MOV SP,R0
3125	013426	104414							TRAP C\$PNTB
3126	013430	062706	000004						ADD #4,SP
3127	013434				PRINTB	#FORM22			
3128	013434	012746	005751						MOV #FORM22,-(SP)
3129	013440	012746	000001						MOV #1,-(SP)
3130	013444	010600							MOV SP,R0
3131	013446	104414							TRAP C\$PNTB
3132	013450	062706	000004						ADD #4,SP
3133	013454				PRINTB	#FORM23,R1			
3134	013454	010146							MOV R1,-(SP)
3135	013456	012746	006031						MOV #FORM23,(SP)
3136	013462	012746	000002						MOV #2,-(SP)
3137	013466	010600							MOV SP,R0
3138	013470	104414							TRAP C\$PNTB
3139	013472	062706	000006						ADD #6,SP
3140	013476			ENDMSG					
3141	013476							L10020:	
3142	013476	104423							TRAP C\$MSG
3143									
3144	013500			BGNMSG	MSG12				
3145	013500							MSG12::	
3146	013500				PRINTB	#FORM24,R2			
3147	013500	010246							MOV R2,-(SP)
3148	013502	012746	006101						MOV #FORM24,-(SP)
3149	013506	012746	000002						MOV #2,-(SP)
3150	013512	010600							MOV SP,R0
3151	013514	104414							TRAP C\$PNTB
3152	013516	062706	000006						ADD #6,SP
3153	013522			ENDMSG					
3154	013522							L10021:	
3155	013522	104423							TRAP C\$MSG

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3156
3157 013524'
3158 013524'
3159 013524'
3160 013524' 010346
3161 013526' 012746 006133'
3162 013532' 012746 000002
3163 013536' 010600
3164 013540' 104414
3165 013542' 062706 000006
3166 013546'
3167 013546' 010446
3168 013550' 012746 006206'
3169 013554' 012746 000002
3170 013560' 010600
3171 013562' 104414
3172 013564' 062706 000006
3173 013570'
3174 013570'
3175 013570' 104423
3176
3177 013572'
3178 013572'
3179 013572'
3180 013572' 010446
3181 013574' 010346
3182 013576' 012746 006261'
3183 013602' 012746 000003
3184 013606' 010600
3185 013610' 104414
3186 013612' 062706 000010
3187 013616'
3188 013616'
3189 013616' 104423
3190
3191 013620'
3192 013620'
3193 013620'
3194 013620' 011246
3195 013622' 011146
3196 013624' 010246
3197 013626' 012746 005123'
3198 013632' 012746 000004
3199 013636' 010600
3200 013640' 104414
3201 013642' 062706 000012
3202 013646'
3203 013646'
3204 013646' 104423
3205
3206 013650'
3207 013650'
3208 013650'
3209 013650' 010246
3210 013652' 012746 006331'
3211 013656' 012746 000002

BGNMSG MSG13
PRINTB #FORM25,R3

PRINTB #FORM26,R4

ENDMSG

BGNMSG MSG14
PRINTB #FORM27,R3,R4

ENDMSG

BGNMSG MSG15
PRINTB #FORM12,R2,(R1),(R2)

ENDMSG

BGNMSG MSG16
PRINTB #FORM28,R2

MSG13::
MOV R3,-(SP)
MOV #FORM25,-(SP)
MOV #2,-(SP)
MOV SP,R0
TRAP C\$PNTB
ADD #6,SP

MOV R4,-(SP)
MOV #FORM26,-(SP)
MOV #2,-(SP)
MOV SP,R0
TRAP C\$PNTB
ADD #6,SP

L10022: TRAP C\$MSG

MSG14::
MOV R4,-(SP)
MOV R3,-(SP)
MOV #FORM27,-(SP)
MOV #3,-(SP)
MOV SP,R0
TRAP C\$PNTB
ADD #10,SP

L10023: TRAP C\$MSG

MSG15::
MOV (R2),-(SP)
MOV (R1),-(SP)
MOV R2,-(SP)
MOV #FORM12,-(SP)
MOV #4,-(SP)
MOV SP,R0
TRAP C\$PNTB
ADD #12,SP

L10024: TRAP C\$MSG

MSG16::
MOV R2,-(SP)
MOV #FORM28,-(SP)
MOV #2,-(SP)

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3212 013662 010600
 3213 013664 104414
 3214 013666 062706 000006
 3215 013672
 3216 013672 010146
 3217 013674 012746 006336
 3218 013700 012746 000002
 3219 013704 010600
 3220 013706 104414
 3221 013710 062706 000006
 3222 013714
 3223 013714 010446
 3224 013716 010346
 3225 013720 012746 006261
 3226 013724 012746 000003
 3227 013730 010600
 3228 013732 104414
 3229 013734 062706 000010
 3230 013740
 3231 013740
 3232 013740 104423
 3233
 3234 013742
 3235 013742
 3236 013742
 3237 013742 010446
 3238 013744 012746 006361
 3239 013750 012746 000002
 3240 013754 010600
 3241 013756 104414
 3242 013760 062706 000006
 3243 013764
 3244 013764 010146
 3245 013766 012746 006411
 3246 013772 012746 000002
 3247 013776 010600
 3248 014000 104414
 3249 014002 062706 000006
 3250 014006
 3251 014006 010346
 3252 014010 010246
 3253 014012 012746 006261
 3254 014016 012746 000003
 3255 014022 010600
 3256 014024 104414
 3257 014026 062706 000010
 3258 014032
 3259 014032
 3260 014032 104423
 3261
 3262 014034
 3263 014034
 3264 014034
 3265 014034 013746 000310
 3266 014040 010146
 3267 014042 012746 006437

PRINTB #FORM29,R1

PRINTB #FORM27,R3,R4

ENDMSG

BGNMSG MSG17

PRINTB #FORM30,R4

PRINTB #FORM31,R1

PRINTB #FORM27,R2,R3

ENDMSG

BGNMSG MSG18

PRINTB #FORM32,R1,BITNAM

MOV SP,R0
TRAP (\$PNTB
ADD #6,SP

MOV R1,-(SP)
MOV #FORM29,-(SP)
MOV #2,-(SP)
MOV SP,R0
TRAP (\$PNTB
ADD #6,SP

MOV R4,-(SP)
MOV R3,-(SP)
MOV #FORM27,-(SP)
MOV #3,-(SP)
MOV SP,R0
TRAP (\$PNTB
ADD #10,SP

L10025: TRAP (\$MSG

MSG17::

MOV R4,-(SP)
MOV #FORM30,-(SP)
MOV #2,-(SP)
MOV SP,R0
TRAP (\$PNTB
ADD #6,SP

MOV R1,-(SP)
MOV #FORM31,-(SP)
MOV #2,-(SP)
MOV SP,R0
TRAP (\$PNTB
ADD #6,SP

MOV R3,-(SP)
MOV R2,-(SP)
MOV #FORM27,-(SP)
MOV #3,-(SP)
MOV SP,R0
TRAP (\$PNTB
ADD #10,SP

L10026: TRAP (\$MSG

MSG18::

MOV BITNAM,-(SP)
MOV R1,-(SP)
MOV #FORM32,-(SP)

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3268 014046' 012746 000003
 3269 014052' 010600
 3270 014054' 104414
 3271 014056' 062706 000010
 3272 014062'
 3273 014062'
 3274 014062' 104423
 3275
 3276 014064'
 3277 014064'
 3278 014064'
 3279 014064' 010146
 3280 014066' 012746 006534'
 3281 014072' 012746 000002
 3282 014076' 010600
 3283 014100' 104414
 3284 014102' 062706 000006
 3285 014106'
 3286 014106' 010346
 3287 014110' 010246
 3288 014112' 012746 006572'
 3289 014116' 012746 000003
 3290 014122' 010600
 3291 014124' 104414
 3292 014126' 062706 000010
 3293 014132'
 3294 014132' 010446
 3295 014134' 010146
 3296 014136' 012746 006655'
 3297 014142' 012746 000003
 3298 014146' 010600
 3299 014150' 104414
 3300 014152' 062706 000010
 3301 014156'
 3302 014156'
 3303 014156' 104423
 3304
 3305 014160'
 3306 014160'
 3307 014160'
 3308 014160' 012746 005074'
 3309 014164' 012746 000001
 3310 014170' 010600
 3311 014172' 104414
 3312 014174' 062706 000004
 3313 014200'
 3314 014200' 010346
 3315 014202' 012746 006755'
 3316 014206' 012746 000002
 3317 014212' 010600
 3318 014214' 104414
 3319 014216' 062706 000006
 3320 014222'
 3321 014222' 010246
 3322 014224' 010146
 3323 014226' 012746 006261'

ENDMSG

BGNMSG MSG19

PRINTB #FORM33,R1

PRINTB #FORM34,R2,R3

PRINTB #FORM35,R1,R4

ENDMSG

BGNMSG MSG20

PRINTB #FORM11

PRINTB #FORM36,R3

PRINTB #FORM27,R1,R2

L10027:

MOV #3,-(SP)
 MOV SP,R0
 TRAP C\$PNTB
 ADD #10,SP

TRAP C\$MSG

MSG19::

MOV R1,-(SP)
 MOV #FORM33,-(SP)
 MOV #2,-(SP)
 MOV SP,R0
 TRAP C\$PNTB
 ADD #6,SP

MOV R3,-(SP)
 MOV R2,-(SP)
 MOV #FORM34,-(SP)
 MOV #3,-(SP)
 MOV SP,R0
 TRAP C\$PNTB
 ADD #10,SP

MOV R4,-(SP)
 MOV R1,-(SP)
 MOV #FORM35,-(SP)
 MOV #3,-(SP)
 MOV SP,R0
 TRAP C\$PNTB
 ADD #10,SP

L10030:

TRAP C\$MSG

MSG20::

MOV #FORM11,-(SP)
 MOV #1,-(SP)
 MOV SP,R0
 TRAP C\$PNTB
 ADD #4,SP

MOV R3,-(SP)
 MOV #FORM36,-(SP)
 MOV #2,-(SP)
 MOV SP,R0
 TRAP C\$PNTB
 ADD #6,SP

MOV R2,-(SP)
 MOV R1,-(SP)
 MOV #FORM27,-(SP)

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3324	014232'	012746	000003				
3325	014236'	010600				MOV	#3,-(SP)
3326	014240'	104414				MOV	SP,R0
3327	014242'	062706	000010			TRAP	C\$PNTB
3328	014246'			ENDMSG		ADD	#10,SP
3329	014246'						
3330	014246'	104423				L10031:	TRAP
3331							C\$MSG
3332	014250'			BGNMSG	MSG21		
3333	014250'					MSG21::	
3334	014250'				PRINTB	#FORM11	
3335	014250'	012746	005074'				
3336	014254'	012746	000001			MOV	#FORM11,-(SP)
3337	014260'	010600				MOV	#1,-(SP)
3338	014262'	104414				MOV	SP,R0
3339	014264'	062706	000004			TRAP	C\$PNTB
3340	014270'					ADD	#4,SP
3341	014270'	010146			PRINTB	#FORM29,R1	
3342	014272'	012746	006336'			MOV	R1,-(SP)
3343	014276'	012746	000002			MOV	#FORM29,-(SP)
3344	014302'	010600				MOV	#2,-(SP)
3345	014304'	104414				MOV	SP,R0
3346	014306'	0627^	000006			TRAP	C\$PNTB
3347	014312'					ADD	#6,SP
3348	014312'	010346			PRINTB	#FORM27,R2,R3	
3349	014314'	010246				MOV	R3,-(SP)
3350	014316'	012746	006261'			MOV	R2,-(SP)
3351	014322'	012746	000003			MOV	#FORM27,-(SP)
3352	014326'	010600				MOV	#3,-(SP)
3353	014330'	104414				MOV	SP,R0
3354	014332'	062706	000010			TRAP	C\$PNTB
3355	014336'			ENDMSG		ADD	#10,SP
3356	014336'						
3357	014336'	104423				L10032:	TRAP
3358							C\$MSG
3359	014340'			BGNMSG	MSG22		
3360	014340'					MSG22::	
3361	014340'	010146			MOV	R1,-(SP)	
3362	014342'				PRINTB	#FORM37,R2	
3363	014342'	010246					
3364	014344'	012746	007013'			MOV	R2,-(SP)
3365	014350'	012746	000002			MOV	#FORM37,-(SP)
3366	014354'	010600				MOV	#2,-(SP)
3367	014356'	104414				MOV	SP,R0
3368	014360'	062706	000006			TRAP	C\$PNTB
3369	014364'	005001				ADD	#6,SP
3370	014366'				CLR	R1	;TRANSMIT STATUS WORD 0
3371	014366'	013746	000606'		PRINTB	#FORM38,R1,R3,PCBB+2	
3372	014372'	010346					
3373	014374'	010146				MOV	PCBB+2,-(SP)
3374	014376'	012746	007124'			MOV	R3,-(SP)
3375	014402'	012746	000004			MOV	R1,-(SP)
3376	014406'	010600				MOV	#FORM38,-(SP)
3377	014410'	104414				MOV	#4,-(SP)
3378	014412'	062706	000012			MOV	SP,R0
3379	014416'	005201				TRAP	C\$PNTB
				INC	R1		#12,SP
							;TRANSMIT STATUS WORD 1

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3492 015014'
3493 015014'
3494 015014'
3495 015014' 012746 007607'
3496 015020' 012746 000001
3497 015024' 010600
3498 015026' 104414
3499 015030' 062706 000004
3500 015034'
3501 015034' 010146
3502 015036' 012746 007704'
3503 015042' 012746 000002
3504 015046' 010600
3505 015050' 104414
3506 015052' 062706 000006
3507 015056'
3508 015056'
3509 015056' 104423
3510
3511 015060'
3512 015060'
3513 015060'
3514 015060' 012746 010047'
3515 015064' 012746 000001
3516 015070' 010600
3517 015072' 104414
3518 015074' 062706 000004
3519 015100'
3520 015100' 010346
3521 015102' 010446
3522 015104' 012746 007760'
3523 015110' 012746 000003
3524 015114' 010600
3525 015116' 104414
3526 015120' 062706 000010
3527 015124'
3528 015124'
3529 015124' 104423
3530
3531 015126'
3532 015126'
3533 015126'
3534 015126' 012746 010101'
3535 015132' 012746 000001
3536 015136' 010600
3537 015140' 104414
3538 015142' 062706 000004
3539 015146'
3540 015146' 010346
3541 015150' 010446
3542 015152' 012746 007760'
3543 015156' 012746 000003
3544 015162' 010600
3545 015164' 104414
3546 015166' 062706 000010
354 015172'

BGNMSG MSG26
PRINTB #FORM48
PRINTB #FORM49,R1
ENDMSG
BGNMSG MSG27
PRINTB #FORM51
PRINTB #FORM50,R4,R3
ENDMSG
BGNMSG MSG28
PRINTB #FORM52
PRINTB #FORM50,R4,R3
ENDMSG

MSG26::
MOV #FORM48,-(SP)
MOV #1,-(SP)
MOV SP,R0
TRAP C\$PNTB
ADD #4,SP
MOV R1,-(SP)
MOV #FORM49,-(SP)
MOV #2,-(SP)
MOV SP,R0
TRAP C\$PNTB
ADD #6,SP
Li0037:
TRAP C\$MSG
MSG27::
MOV #FORM51,-(SP)
MOV #1,-(SP)
MOV SP,R0
TRAP C\$PNTB
ADD #4,SP
MOV R3,-(SP)
MOV R4,-(SP)
MOV #FORM50,-(SP)
MOV #3,-(SP)
MOV SP,R0
TRAP C\$PNTB
ADD #10,SP
L10040:
TRAP C\$MSG
MSG28::
MOV #FORM52,-(SP)
MOV #1,-(SP)
MOV SP,R0
TRAP C\$PNTB
ADD #4,SP
MOV R3,-(SP)
MOV R4,-(SP)
MOV #FORM50,-(SP)
MOV #3,-(SP)
MOV SP,R0
TRAP C\$PNTB
ADD #10,SP

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3548 015172*
3549 015172* 104423
3550
3551 015174*
3552 015174*
3553 015174*
3554 015174* 013746 000610*
3555 015200* 012746 000000
3556 015204* 012746 007760*
3557 015210* 012746 000003
3558 015214* 010600
3559 015216* 104414
3560 015220* 062706 000010
3561 015224*
3562 015224* 013746 000606*
3563 015230* 012746 010137*
3564 015234* 012746 000002
3565 015240* 010600
3566 015242* 104414
3567 015244* 062706 000006
3568 015250*
3569 015250* 013746 000604*
3570 015254* 012746 010211*
3571 015260* 012746 000002
3572 015264* 010600
3573 015266* 104414
3574 015270* 062706 000006
3575 015274*
3576 015274* 104423
3577 015274*
3578
3579 015276*
3580 015276*
3581 015276*
3582 015276* 012746 010237*
3583 015302* 012746 000001
3584 015306* 010600
3585 015310* 104414
3586 015312* 062706 000004
3587 015316*
3588 015316* 013746 000604*
3589 015322* 012746 010363*
3590 015326* 012746 000002
3591 015332* 010600
3592 015334* 104414
3593 015336* 062706 000006
3594 015342*
3595 015342*
3596 015342* 104423
3597
3598 015344*
3599 015344*
3600 015344*
3601 015344* 012746 010310*
3602 015350* 012746 000001
3603 015354* 010600

BGNMSG MSG29
PRINTB #FORM50,#0,PCBB+4

PRINTB #FORM53,PCBB+2

PRINTB #FORM54,PCBB

ENDMSG

BGNMSG MSG30
PRINTB #FORM55

PRINTB #FORM57,PCBB

ENDMSG

BGNMSG MSG31
PRINTB #FORM56

L10041: TRAP C\$MSG
MSG29::
MOV PCBB+4,-(SP)
MOV #0,-(SP)
MOV #FORM50,-(SP)
MOV #3,-(SP)
MOV SP,R0
TRAP C\$PNTB
ADD #10,SP

MOV PCBB+2,-(SP)
MOV #FORM53,-(SP)
MOV #2,-(SP)
MOV SP,R0
TRAP C\$PNTB
ADD #6,SP

MOV PCBB,-(SP)
MOV #FORM54,-(SP)
MOV #2,-(SP)
MOV SP,R0
TRAP C\$PNTB
ADD #6,SP

L10042: TRAP C\$MSG
MSG30::
MOV #FORM55,-(SP)
MOV #1,-(SP)
MOV SP,R0
TRAP C\$PNTB
ADD #4,SP

MOV PCBB,-(SP)
MOV #FORM57,-(SP)
MOV #2,-(SP)
MOV SP,R0
TRAP C\$PNTB
ADD #6,SP

L10043: TRAP C\$MSG
MSG31::
MOV #FORM56,-(SP)
MOV #1,-(SP)
MOV SP,R0

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3604 015356* 104414
3605 015360* 062706 000004
3606 015364*
3607 015364* 010146
3608 015366* 012746 010363*
3609 015372* 012746 000002
3610 015376* 010600
3611 015400* 104414
3612 015402* 062706 000006
3613 015406*
3614 015406* 012746 000150
3615 015412* 012746 010433*
3616 015416* 012746 000002
3617 015422* 010600
3618 015424* 104414
3619 015426* 062706 000006
3620 015432*
3621 015432* 012746 100000
3622 015436* 012746 010511*
3623 015442* 012746 000002
3624 015446* 010600
3625 015450* 104414
3626 015452* 062706 000006
3627 015456*
3628 015456* 013746 000606*
3629 015462* 012746 100000
3630 015466* 012746 010604*
3631 015472* 012746 000003
3632 015476* 010600
3633 015500* 104414
3634 015502* 062706 000010
3635 015506*
3636 015506*
3637 015506* 104423
3638
3639 015510*
3640 015510*
3641 015510*
3642 015510* 012746 010671*
3643 015514* 012746 000001
3644 015520* 010600
3645 015522* 104414
3646 015524* 062706 000004
3647 015530*
3648 015530* 013746 000610*
3649 015534* 012746 052525*
3650 015540* 012746 006261*
3651 015544* 012746 000003
3652 015550* 010600
3653 015552* 104414
3654 015554* 062706 000010
3655 015560*
3656 015560* 013746 000604*
3657 015564* 012746 010363*
3658 015570* 012746 000002
3659 015574* 010600

PRINTB #FORM57,R1

PRINTB #FORM58,#104.

PRINTB #FORM59,#100000

PRINTB #FORM60,#100000,PCBB+2

ENDMSG

BGNMSG MSG32

PRINTB #FORM61

PRINTB #FORM27,#52525,PCBB+4

PRINTB #FORM57,PCBB

TRAP C\$PNTB
ADD #4,SP
MOV R1,-(SP)
MOV #FORM57,-(SP)
MOV #2,-(SP)
MOV SP,R0
TRAP C\$PNTB
ADD #6,SP
MOV #104,-(SP)
MOV #FORM58,-(SP)
MOV #2,-(SP)
MOV SP,R0
TRAP C\$PNTB
ADD #6,SP
MOV #100000,-(SP)
MOV #FORM59,-(SP)
MOV #2,-(SP)
MOV SP,R0
TRAP C\$PNTB
ADD #6,SP
MOV PCBB+2,-(SP)
MOV #100000,-(SP)
MOV #FORM60,-(SP)
MOV #3,-(SP)
MOV SP,R0
TRAP C\$PNTB
ADD #10,SP
L10044:
TRAP C\$MSG
MSG32::
MOV #FORM61,-(SP)
MOV #1,-(SP)
MOV SP,R0
TRAP C\$PNTB
ADD #4,SP
MOV PCBB+4,-(SP)
MOV #52525,-(SP)
MOV #FORM27,-(SP)
MOV #3,-(SP)
MOV SP,R0
TRAP C\$PNTB
ADD #10,SP
MOV PCBB,-(SP)
MOV #FORM57,-(SP)
MOV #2,-(SP)
MOV SP,R0

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3660	015576'	104414				TRAP	C\$PNTB
3661	015600'	062706	000006			ADD	#6,SP
3662	015604'			ENDMSG			
3663	015604'				L10045:		
3664	015604'	104423				TRAP	C\$MSG
3665							
3666	015606'			BGNMSG	MSG33		
3667	015606'					MSG33::	
3668	015606'			PRINTB	#FORM62,@BITNAM		
3669	015606'	017746	162476			MOV	@BITNAM,-(SP)
3670	015612'	012746	010754'			MOV	#FORM62,-(SP)
3671	015616'	012746	000002			MOV	#2,-(SP)
3672	015622'	010600				MOV	SP,RO
3673	015624'	104414				TRAP	C\$PNTB
3674	015626'	062706	000006			ADD	#6,SP
3675	015632'			ENDMSG			
3676	015632'				L10046:		
3677	015632'	104423				TRAP	C\$MSG
3678							
3679	015634'			BGNMSG	MSG34		
3680	015634'					MSG34::	
3681	015634'			PRINTB	#FORM63,R4		
3682	015634'	010446				MOV	R4,-(SP)
3683	015636'	012746	011027'			MOV	#FORM63,-(SP)
3684	015642'	012746	000002			MOV	#2,-(SP)
3685	015646'	010600				MOV	SP,RO
3686	015650'	104414				TRAP	C\$PNTB
3687	015652'	062706	000006			ADD	#6,SP
3688	015656'			PRINTB	#FORM28,STMSG		
3689	015656'	013746	024202'			MOV	STMSG,-(SP)
3690	015662'	012746	006331'			MOV	#FORM28,-(SP)
3691	015666'	012746	000002			MOV	#2,-(SP)
3692	015672'	010600				MOV	SP,RO
3693	015674'	104414				TRAP	C\$PNTB
3694	015676'	062706	000006			ADD	#6,SP
3695	015702'			ENDMSG			
3696	015702'				L10047:		
3697	015702'	104423				TRAP	C\$MSG
3698							
3699	015704'			BGNMSG	MSG35		
3700	015704'					MSG35::	
3701	015704'			PRINTB	#FORM59,PCBB		
3702	015704'	013746	000604'			MOV	PCBB,-(SP)
3703	015710'	012746	010511'			MOV	#FORM59,-(SP)
3704	015714'	012746	000002			MOV	#2,-(SP)
3705	015720'	010600				MOV	SP,RO
3706	015722'	104414				TRAP	C\$PNTB
3707	015724'	062706	000006			ADD	#6,SP
3708	015730'			PRINTB	#FORM60		
3709	015730'	012746	010604'			MOV	#FORM60,-(SP)
3710	015734'	012746	000001			MOV	#1,-(SP)
3711	015740'	010600				MOV	SP,RO
3712	015742'	104414				TRAP	C\$PNTB
3713	015744'	062706	000004			ADD	#4,SP
3714	015750'			PRINTB	#FORM78,PCBB,PCBB+2		
3715	015750'	013746	000606'			MOV	PCBB+2,-(SP)

3716	015754'	013746	000604'				
3717	015760'	012746	012225'			MOV	PCBB,-(SP)
3718	015764'	012746	000003			MOV	#FORM78,-(SP)
3719	015770'	010600				MOV	#3,-(SP)
3720	015772'	104414				MOV	SP,RC
3721	015774'	062706	000010			TRAP	C\$PNTB
3722	016000'			ENDMSG		ADD	#10,SP
3723	016000'						
3724	016000'	104423				L10050:	
3725						TRAP	C\$MSG
3726	016002'			BGNMSG	MSG36		
3727	016002'						
3728	016002'				PRINTB	MSG36::	#FORM66
3729	016002'	012746	011165'				
3730	016006'	012746	000001			MOV	#FORM66,-(SP)
3731	016012'	010600				MOV	#1,-(SP)
3732	016014'	104414				MOV	SP,R0
3733	016016'	062706	000004			TRAP	C\$PNTB
3734	016022'			ENDMSG		ADD	#4,SP
3735	016022'						
3736	016022'	104423				L10051:	
3737						TRAP	C\$MSG
3738	016024'			BGNMSG	MSG37		
3739	016024'						
3740	016024'				PRINTB	MSG37::	#FORM67
3741	016024'	012746	011252'				
3742	016030'	012746	000001			MOV	#FORM67,-(SP)
3743	016034'	010600				MOV	#1,-(SP)
3744	016036'	104414				MOV	SP,R0
3745	016040'	062706	000004			TRAP	C\$PNTB
3746	016044'			ENDMSG		ADD	#4,SP
3747	016044'						
3748	016044'	104423				L10052:	
3749						TRAP	C\$MSG
3750	016046'			BGNMSG	MSG38		
3751	016046'						
3752	016046'				PRINTB	MSG38::	#FORM68
3753	016046'	012746	011336'				
3754	016052'	012746	000001			MOV	#FORM68,-(SP)
3755	016056'	010600				MOV	#1,-(SP)
3756	016060'	104414				MOV	SP,R0
3757	016062'	062706	000004			TRAP	C\$PNTB
3758	016066'			ENDMSG		ADD	#4,SP
3759	016066'						
3760	016066'	104423				L10053:	
3761						TRAP	C\$MSG
3762	016070'			BGNMSG	MSG39		
3763	016070'						
3764	016070'				PRINTB	MSG39::	#FORM69
3765	016070'	012746	011433'				
3766	016074'	012746	000001			MOV	#FORM69,-(SP)
3767	016100'	010600				MOV	#1,-(SP)
3768	016102'	104414				MOV	SP,R0
3769	016104'	062706	000004			TRAP	C\$PNTB
3770	016110'			ENDMSG		ADD	#4,SP
3771	016110'						
						L10054:	

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3772	016110'	104423			TRAP	C\$MSG
3773						
3774	016112'			BGNMSG	MSG40	
3775	016112'					MSG40::
3776	016112'			PRINTB	#FORM56	
3777	016112'	012746	010310'			MOV #FORM56,-(SP)
3778	016116'	012746	000001			MOV #1,-(SP)
3779	016122'	010600				MOV SP,RO
3780	016124'	104414				TRAP C\$PNTB
3781	016126'	062706	000004			ADD #4,SP
3782	016132'			PRINTB	#FORM59,#100000	
3783	016132'	012746	100000			MOV #100000,-(SP)
3784	016136'	012746	010511'			MOV #FORM59,-(SP)
3785	016142'	012746	000002			MOV #2,-(SP)
3786	016146'	010600				MOV SP,RO
3787	016150'	104414				TRAP C\$PNTB
3788	016152'	062706	000006			ADD #6,SP
3789	016156'			PRINTB	#FORM60,#100000,PCBB	
3790	016156'	013746	000604'			MOV PCBB,-(SP)
3791	016162'	012746	100000			MOV #100000,-(SP)
3792	016166'	012746	010604'			MOV #FORM60,-(SP)
3793	016172'	012746	000003			MOV #3,-(SP)
3794	016176'	010600				MOV SP,RO
3795	016200'	104414				TRAP C\$PNTB
3796	016202'	062706	000010			ADD #10,SP
3797	016206'			ENDMSG		
3798	016206'					L10055:
3799	016206'	104423			TRAP	C\$MSG
3800						
3801	016210'			BGNMSG	MSG41	
3802	016210'					MSG41::
3803	016210'			PRINTB	#FORM61	
3804	016210'	012746	010671'			MOV #FORM61,-(SP)
3805	016214'	012746	000001			MOV #1,-(SP)
3806	016220'	010600				MOV SP,RO
3807	016222'	104414				TRAP C\$PNTB
3808	016224'	062706	000004			ADD #4,SP
3809	016230'			PRINTB	#FORM27,#52525,PCBB+2	
3810	016230'	013746	000606'			MOV PCBB+2,-(SP)
3811	016234'	012746	052525			MOV #52525,-(SP)
3812	016240'	012746	006261'			MOV #FORM27,-(SP)
3813	016244'	012746	000003			MOV #3,-(SP)
3814	016250'	010600				MOV SP,RO
3815	016252'	104414				TRAP C\$PNTB
3816	016254'	062706	000010			ADD #10,SP
3817	016260'			ENDMSG		
3818	016260'					L10056:
3819	016260'	104423			TRAP	C\$MSG
3820						
3821	016262'			BGNMSG	MSG42	
3822	016262'					MSG42::
3823	016262'			PRINTB	#FORM70,PCBB+2	
3824	016262'	013746	000606'			MOV PCBB+2,-(SP)
3825	016266'	012746	011551'			MOV #FORM70,-(SP)
3826	016272'	012746	000002			MOV #2,-(SP)
3827	016276'	010600				MOV SP,RO

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3828	016300'	104414				TRAP	C\$PNTB
3829	016302'	062706	000006			ADD	#6,SP
3830	016306'			PRINTB	#FORM71,PCBB		
3831	016306'	013746	000604'			MOV	P(CBB,-(SP)
3832	016312'	012746	011613'			MOV	#FORM71,-(SP)
3833	016316'	012746	000002			MOV	#2,-(SP)
3834	016322'	010600				MOV	SP,R0
3835	016324'	104414				TRAP	C\$PNTB
3836	016326'	062706	000006			ADD	#6,SP
3837	016332'			PRINTB	#FORM33,R1		
3838	016332'	010146				MOV	R1,-(SP)
3839	016334'	012746	006534'			MOV	#FORM33,-(SP)
3840	016340'	012746	000002			MOV	#2,-(SP)
3841	016344'	010600				MOV	SP,R0
3842	016346'	104414				TRAP	C\$PNTB
3843	016350'	062706	000006			ADD	#6,SP
3844	016354'			PRINTB	#FORM36,PCBB+4		
3845	016354'	013746	000610'			MOV	P(CBB+4,-(SP,
3846	016360'	012746	006755'			MOV	#FORM36,-(SP)
3847	016364'	012746	000002			MOV	#2,-(SP)
3848	016370'	010600				MOV	SP,R0
3849	016372'	104414				TRAP	C\$PNTB
3850	016374'	062706	000006			ADD	#6,SP
3851	016400'			PRINTB	#FORM27,PCBB+6,PCBB+10		
3852	016400'	013746	000614'			MOV	P(CBB+10,-(SP)
3853	016404'	013746	000612'			MOV	P(CBB+6,-(SP)
3854	016410'	012746	006261'			MOV	#FORM27,-(SP)
3855	016414'	012746	000003			MOV	#3,-(SP)
3856	016420'	010600				MOV	SP,R0
3857	016422'	104414				TRAP	C\$PNTB
3858	016424'	062706	000010			ADD	#10,SP
3859	016430'			ENDMSG			
3860	016430'					L10057:	
3861	016430'	104423				TRAP	C\$MSG
3862							
3863	016432'			BGNMSG	MSG43		
3864	016432'					MSG43::	
3865	016432'			PRINTB	#FORM72		
3866	016432'	012746	011655'			MOV	#FORM72,-(SP)
3867	016436'	012746	000001			MOV	#1,-(SP)
3868	016442'	010600				MOV	SP,R0
3869	016444'	104414				TRAP	C\$PNTB
3870	016446'	062706	000004			ADD	#4,SP
3871	016452'			ENDMSG			
3872	016452'					L10060:	
3873	016452'	104423				TRAP	C\$MSG
3874							
3875	016454'			BGNMSG	MSG44		
3876	016454'					MSG44::	
3877	016454'	032737	100000 000666'	BIT	#NPRERR,ERRINT		
3878	016462'	001410		BEQ	10\$		
3879	016464'			PRINTB	#FORM73		
3880	016464'	012746	011727'				
3881	016470'	012746	000001			MOV	#FORM73,-(SP)
3882	016474'	010600				MOV	#1,-(SP)
3883	016476'	104414				MOV	SP,R0
						TRAP	C\$PNTB

```

;NPR ERROR OCCUR?
;NO
;YES, PRINT NPR ERROR MESSAGE

```


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```

3884 016500' 062706 000004
3885 016504' 032737 040000 000666' 10$: BIT #NXMERR,ERRINT ;NON-EXISTANT MEMORY OCCUR?
3886 016512' 001410 BEQ 20$ ;NO
3887 016514' PRINTB #FORM74 ;YES, PRINT NON-EXISTANT MEMORY MESSAGE
3888 016514' 012746 011772' MOV #FORM74,-(SP)
3889 016520' 012746 000001 MOV #1,-(SP)
3890 016524' 010600 MOV SP,RO
3891 016526' 104414 TRAP (SPNTB)
3892 016530' 062706 000004 ADD #4,SP
3893 016534' 032737 020000 000666' 20$: BIT #UNIERR,ERRINT ;UNEXPECTED INTERRUPT OCCUR?
3894 016542' 001410 BEQ 30$ ;NO
3895 016544' PRINTB #FORM75 ;YES, PRINT UNEXPECTED INTERRUPT MESSAGE
3896 016544' 012746 012047' MOV #FORM75,-(SP)
3897 016550' 012746 000001 MOV #1,-(SP)
3898 016554' 010600 MOV SP,RO
3899 016556' 104414 TRAP (SPNTB)
3900 016560' 062706 000004 ADD #4,SP
3901 016564' 032737 010000 000666' 30$: BIT #PARERR,ERRINT ;PARITY ERROR OCCUR?
3902 016572' 001410 BEQ 40$ ;NO
3903 016574' PRINTB #FORM72 ;YES, PRINT PARITY ERROR MESSAGE
3904 016574' 012746 011655' MOV #FORM72,-(SP)
3905 016600' 012746 000001 MOV #1,-(SP)
3906 016604' 010600 MOV SP,RO
3907 016606' 104414 TRAP (SPNTB)
3908 016610' 062706 000004 ADD #4,SP
3909 016614' 40$:
3910 016614' ENDMSG
3911 016614'
3912 016614' 104423 L10061: TRAP (MSG)
3913
3914 016616' BGNMSG MSG45
3915 016616'
3916 016616' PRINTB #FORM79 MSG45::
3917 016616' 012746 012265' MOV #FORM79,-(SP)
3918 016622' 012746 000001 MOV #1,-(SP)
3919 016626' 010600 MOV SP,RO
3920 016630' 104414 TRAP (SPNTB)
3921 016632' 062706 000004 ADD #4,SP
3922 016636' PRINTB #FORM80
3923 016636' 012746 012334' MOV #FORM80,-(SP)
3924 016642' 012746 000001 MOV #1,-(SP)
3925 016646' 010600 MOV SP,RO
3926 016650' 104414 TRAP (SPNTB)
3927 016652' 062706 000004 ADD #4,SP
3928 016656' PRINTB #FORM81
3929 016656' 012746 012413' MOV #FORM81,-(SP)
3930 016662' 012746 000001 MOV #1,-(SP)
3931 016666' 010600 MOV SP,RO
3932 016670' 104414 TRAP (SPNTB)
3933 016672' 062706 000004 ADD #4,SP
3934 016676' ENDMSG
3935 016676'
3936 016676' 104423 L10062: TRAP (MSG)
3937
3938 016700' BGNMSG MSG46
3939 016700' MSG46::

```

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GLOBAL ERROR REPORT SECTION

SEQ 74

3940	016700*		
3941	016700*	012746	012456*
3942	016704*	012746	000001
3943	016710*	010600	
3944	016712*	104414	
3945	016714*	062706	000004
3946	016720*		
3947	016720*		
3948	016720*	104423	
3949			

PRINTB #FORMB2

ENDMSG

```

MOV #FORMB2,-(SP)
MOV #1,-(SP)
MOV SP,R0
TRAP C$PNTB
ADD #4,SP

```

L10063:

TRAP C\$MSG

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GLOBAL SUBROUTINES SECTION

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.SBTTL GLOBAL SUBROUTINES SECTION

..
: THE GLOBAL SUBROUTINES SECTION CONTAINS THE SUBROUTINES
: THAT ARE USED IN MORE THAN ONE TEST.

..
:SBTTL 32 BIT CRC CALCULATOR

..
: FUNCTIONAL DESCRIPTION:
: SUBROUTINE TO CALCULATE A 32 BIT CRC ON A BLOCK OF DATA

..
: INPUTS:
: R0 = ADDRESS OF DATA BLOCK
: R2 = BYTE COUNT

..
: IMPLICIT INPUTS: NONE

..
: OUTPUTS:
: R4 = CRC HIGH WORD
: R5 = CRC LOW WORD

..
: SUBORDINATE ROUTINES USED: GETCRC

..
: FUNCTIONAL SIDE EFFECTS: NONE

..
: CALLING SEQUENCE. PUT ADDRESS OF DATA TO PERFORM CRC ON IN R0
: PUT NUMBER OF BYTES TO CHECK IN R2
: JSR PC,BLKCRC

..

```
CRC32::
      MOV     R0,-(SP)      ;SAVE REGISTERS 0-3
      MOV     R1,-(SP)
      MOV     R2,-(SP)
      MOV     R3,-(SP)
      MOV     #INITH,R4    ;INITIAL CRC HIGH WORD
      MOV     #INITL,R5    ;INITIAL CRC LOW WORD
10$:  MOVB    (R0)+,R1       ;GET NEXT BYTE OF DATA
      JSR     PC,GETCRC    ;CALCULATE THE CRC
      SOB    R2,10$       ;LOOP TILL DONE
      MOV    (SP)+,R3      ;RESTORE REGISTERS
      MOV    (SP)+,R2
      MOV    (SP)+,R1
      MOV    (SP)+,R0
      RTS     PC           ;RETURN
```

016722' 010046
016722' 010146
016724' 010246
016726' 010346
016730' 010346
016732' 012704 177777
016736' 012705 177777
016742' 112001
016744' 004737 016764'
016750' 077204
016752' 012603
016754' 012602
016756' 012601
016760' 012600
016762' 000207

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```

3999
4000
4001
4002
4003
4004
4005
4006
4007
4008
4009
4010
4011
4012
4013 016764*
4014 016764* 010146
4015 016766* 010246
4016 016770* 010346
4017 016772* 042701 177400
4018 016776* 074105
4019 017000* 012702 16667C
4020 017004* 012703 101440
4021 017010* 012701 000010
4022 017014* 000241
4023 017016* 006004
4024 017020* 006005
4025 017022* 103002
4026 017024* 074204
4027 017026* 074305
4028 017030* 077107
4029 017032* 012603
4030 017034* 012602
4031 017036* 012601
4032 017040* 000207
  
```

```

:++
:BYTE WISE 32-BIT CRC CALCULATOR
:INPUTS:
:   R1 = NEW BYTE TO ADD TO CRC
:   R4,R5 = CURRENT PARTIAL CRC CODE
:OUTPUTS:
:   R4,R5 = UPDATED CRC
:NOTE: THIS ROUTINE IS ONLY USED BY BLKCRC
:--
GETCRC:
      MOV     R1,-(SP)      ;SAVE R1-3
      MOV     R2,-(SP)
      MOV     R3,-(SP)
      BIC     #^C377,R1    ;CLEAR HIGH BYTE
      XOR     R1,R5        ;MERGE NEW BYTE WITH OLD CRC
      MOV     #POLYH,R2    ;GET CRC POLYNOMIAL HIGH WORD
      MOV     #POLYL,R3    ;GET CRC POLYNOMIAL LOW WORD
      MOV     #8,R1        ;LOOP COUNT
1$:   CLC
      ROR     R4
      ROR     R5
      BCC     2$
      XOR     R2,R4        ;EXCLUSIVE OR IN THE POLY
      XOR     R3,R5        ;BOTH HIGH AND LOW WORDS
2$:   SOB     R1,1$        ;AND LOOP ON ALL 8 BITS
      MOV     (SP)+,R3     ;RESTORE REGISTERS
      MOV     (SP)+,R2
      MOV     (SP)+,R1
      RTS     PC
      ;RETURN
  
```

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.SBTTL 16 BIT CRC CALCULATOR

```

.....
SUBROUTINE - CRC16

THIS SUBROUTINE CALCULATES A 16 BIT CRC
ON A BLOCK OF DATA.

INPUTS:      R0 = ADDRESS OF DATA BLOCK
              R2 = BYTE COUNT
              R4 = INITIAL CRC VALUE

OUTPUTS:     R4 = CRC

CALLING SEQUENCE:
              JSR  PC,CRC16
.....

```

```

CRC16::
MOV  R0,-(SP)      ; SAVE R0
MOV  R1,-(SP)      ; SAVE R1
MOV  R2,-(SP)      ; SAVE R2
MOV  R3,-(SP)      ; SAVE R3
MOV  R5,-(SP)      ; SAVE R5
MOV  R2,R3         ; R3 = BYTE COUNT
MOV  R0,R2         ; R2 = ADDRESS OF DATA BLOCK
MOV  #POLY16,R5    ; CRC POLYNOMIAL
1$:  MOVB (R2)+,R0  ; GET NEXT BYTE
     BIC #^C377,R0 ; CLEAR HIGH BYTE
     XOR R0,R4     ; MERGE BYTE WITH OLD CRC
     MOV #8.,R1    ; LOOP COUNT
2$:  CLC          ; CLEAR CARRY
     ROR R4       ; SHIFT RIGHT THE CRC
     BCC 3$      ; SKIP IF BIT ZERO NOT SET
     XOR R5,R4   ; EXCLUSIVE OR IN THE POLY
3$:  SOB R1,2$   ; AND LOOP ON ALL 8 BITS
     MOV (SP)+,R5 ; RESTORE R5
     MOV (SP)+,R3 ; RESTORE R3
     MOV (SP)+,R2 ; RESTORE R2
     MOV (SP)+,R1 ; RESTORE R1
     MCV (SP)+,R0 ; RESTORE R0
     RTS PC      ; AND RETURN

```

120001

177400

000010

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4101
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4108
4109
4110
4111
4112
4113
4114
4115
4116
4117
4118
4119
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4121
4122

.SBTTL HEXIDECIMAL CONVERTER FOR DEFAULT PHYSICAL ADDRESS

```

*****
SUBROUTINE - HEXDPA
THIS SUBROUTINE LOADS DEFADR WITH THE ASCII HEX VALUE
FOR THE DEFAULT PHYSICAL ADDRESS DPA.
INPUTS:          NONE
IMPLICIT
INPUTS:          DPA = DEFAULT PHYSICAL ADDRESS
OUTPUTS:         DEFADR = ASCII HEX VALUE FOR DPA
CALLING SEQUENCE:
                  JSR    PC,HEXDPA
*****

```

```

HEXDPA::
      MOV    R0,-(SP)          ; SAVE R0
      MOV    R3,-(SP)          ; SAVE R3
      MOV    R5,-(SP)          ; SAVE R5
      :
      MOV    #6,R0             ; DO LOOP = 6 BYTES
      MOV    #DEFADR,R3        ; POINT TO ASCII MESSAGE
      MOV    #DPA,R5           ; POINT TO DEFAULT PHYSICAL ADDR
      :
10$:  MOVB   (R5)+,HEXDAT       ; LOAD BYTE FOR CONVERSION
      JSR   PC,HEXH            ; CONVERT HIGH NIBBLE
      MOVB  HEXVAL,(R3)+       ; LOAD INTO ASCII MESSAGE
      JSR   PC,HEXL            ; CONVERT LOW NIBBLE
      MOVB  HEXVAL,(R3)+       ; LOAD INTO ASCII MESSAGE
      TSTB  (R3)+              ; SKIP U.FR HYPHEN IN MESSAGE
      SOB   R0,10$            ; LOOP TIL ALL 6 BYTES ARE DONE
      :
      MOV   (SP)+,R5           ; RESTORE R5
      MOV   (SP)+,R3           ; RESTORE R3
      MOV   (SP)+,R0           ; RESTORE R0
      RTS   PC                 ; AND RETURN

```

```

017130*
017130* 010046
017132* 010346
017134* 010546
:
017136* 012700 000006
017142* 012703 053033*
017146* 012705 052750*
:
017152* 112537 052766*
017156* 004737 017212*
017162* 113723 052767*
017166* 004737 017250*
017172* 113723 052767*
017176* 105723
017200* 077014
:
017202* 012605
017204* 012603
017206* 012600
017210* 000207

```


GLOBAL AREAS
CZUAAA.P11

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HEXIDECIMAL CONVERTER FOR DEFAULT PHYSICAL ADDRESS

GL
CZ

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017212'
017212' 010146
017214' 013701 052766'
017220' 042701 177417
017224' 006201
017226' 006201
017230' 006201
017232' 006201
017234' 062701 053057'
017240' 111137 052767'
017244' 012601
017246' 000207

```
*****
SUBROUTINE - HEXH
THIS SUBROUTINE LOADS HEXVAL WITH THE ASCII HEX VALUE
FOR THE HIGH NIBBLE IN HEXDAT
INPUTS:          NONE
IMPLICIT
INPUTS:          HEYDAT = BYTE TO BE CONVERTED
OUTPUTS:         HEXVAL = ASCII HEX VALUE FOR THE HIGH NIBBLE
CALLING SEQUENCE:
                  JSR      PC,HEXH
*****
```

```
HEXH::
MOV      R1,-(SP)          ; SAVE R1
:
MOV      HEXDAT,R1        ; LOAD DATA FOR CONVERSION
BIC      #177417,R1       ; MASK HIGH NIBBLE
:
ASR      R1                ; SHIFT RIGHT
ASR      R1
ASR      R1
ASR      R1
:
ADD      #HEXTBL,R1        ; GET INDEX INTO HEXTBL
MOVB     (R1),HEXVAL       ; AND LOAD HEXVAL
:
MOV      (SP)+,R1         ; RESTORE R1
RTS      PC                ; AND RETURN
```

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HEXIDECIMAL CONVERTER FOR DEFAULT PHYSICAL ADDRESS

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017250'
017250' 010146
017252' 013701 052766'
017256' 042701 177760
017262' 062701 053057'
017266' 111137 052767'
017272' 012601
017274' 000207

```

.....
SUBROUTINE - HEXL
THIS SUBROUTINE LOADS HEXVAL WITH THE ASCII HEX VALUE
FOR THE LOW NIBBLE IN HEXDAT
INPUTS:          NONE
IMPLICIT
INPUTS:          HEXDAT = BYTE TO BE CONVERTED
OUTPUTS:         HEXVAL = ASCII HEX VALUE FOR THE LOW NIBBLE
CALLING SEQUENCE:
                JSR      PC,HEXL
.....

```

```

HEXL::
MOV      R1,-(SP)          ; SAVE R1
:
MOV      HEXDAT,R1        ; LOAD DATA FOR CONVERSION
BIC      #177760,R1       ; MASK LOW NIBBLE
:
ADD      #HEXTBL,R1       ; GET INDEX INTO HEXTBL
MOVB    (R1),HEXVAL       ; AND LOAD HEXVAL
:
MOV      (SP)+,R1         ; RESTORE R1
RTS     PC                ; AND RETURN

```

GLOBAL AREAS
CZUAAA.P11

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HEXIDECIMAL CONVERTER FOR DEFAULT PHYSICAL ADDRESS

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4199 017276'
4200 017276' 012700 000240
4201 017302' 104441
4202 017304' 012777 000100 160762
4203 017312' 000207
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4208
4209
4210
4211
4212 017314' 005077 160762
4213 017320'
4214 017320' 012700 000340
4215 017324' 104441
4216 017326' 000207

.SBTTL TURN ON THE CLOCK
:*****
:THIS ROUTINE TURNS ON THE CLOCK
:*****
TIMON:: SETPRI #PRI05 ;SET PROCESSOR PRIORITY TO 5
;MOV #PRI05,R0
;TRAP C$SPRI
MOV #IE,@CLKCSR ;ENABLE CLOCK INTERRUPTS
RTS PC

:*****
:THIS ROUTINE TURNS THE CLOCK OFF
:*****
TIMOFF::CLR @CLKCSR ;CLEAR INTERRUPT ENABLE
SETPRI #PRI07 ;PUT PRIORITY BACK UP
;MOV #PRI07,R0
;TRAP C$SPRI
RTS PC

```

GLOBAL AREAS
CZUAAA.P11

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CHECK FOR DONE INTERRUPT

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```

.SBTTL CHECK FOR DONE INTERRUPT
:*****
:FUNCTIONAL DESCRIPTION:
:   ROUTINE TO WAIT FOR THE 'DONE INTERRUPT' BIT TO SET IN PCSRO
:INPUTS: NONE
:IMPLICIT INPUTS: METER
:OUTPUTS: C BIT SET IN PSW IF 'DNI' NOT SET
:          C BIT CLEAR IN PSW IF 'DNI' SET
:SUBORDINATE ROUTINES USED: TIMON, TIMOFF
:FUNCTIONAL SIDE EFFECTS: PSW CHANGED, LINE CLOCK INTERRUPTS ARE ENABLED
:CALLING SEQUENCE: PUT NUMBER OF CLOCK TICKS TO WAIT FOR IN--->METER
:                   JSR PC,CHKDNI
:*****

```

```

017330 004737 017276  CHKDNI::JSR PC,TIMON ;TURN ON THE LINE CLOCK
017334 032777 004000 160774 10$: BIT #DNI,@PCSRO ;IS 'DNI' SET?
017342 001010 BNE 20$ ;YES
017344 BREAK ;RETURN TO THE DRS FOR A MOMENT
017344 104422 ;TRAP C$BRK
017346 005737 000332 TST METER ;HAS THE TIME EXPIRED?
017352 001370 BNE 10$ ;NO, KEEP WAITING
017354 004737 017314 JSR PC,TIMOFF ;TURN THE CLOCK OFF
017360 000261 SEC ;INDICATE 'DNI' DID NOT SET
017362 000403 BR 30$ ;LEAVE
017364 004737 017314 20$: JSR PC,TIMOFF ;STOP THE CLOCK
017370 000241 CLC ;INDICATE 'DNI' SET
017372 000207 30$: RTS PC

```

GLOBAL AREAS MACY11 30(1046) 11-JAN-83 10:13 PAGE 87
CZUAAA.P11 11-JAN-83 09:29 CLEAR DONE INTERRUPT

```

4254 .SBTTL CLEAR DONE INTERRUPT
4255
4256 :*****
4257 :
4258 :FUNCTIONAL DESCRIPTION:
4259 :    ROUTINE TO CLEAR THE 'DNI' BIT IN PCSRO
4260 :
4261 :INPUTS: NONE
4262 :
4263 :IMPLICIT INPUTS: PCSRO
4264 :
4265 :OUTPUTS: C BIT SET IN PCSW IF 'DNI' WILL NOT CLEAR
4266 :          C BIT CLEAR IN PSW IF 'DNI' CLEARED SUCCESSFULLY
4267 :
4268 :SUBORDINATE ROUTINES CALLED: NONE
4269 :
4270 :FUNCTIONAL SIDE EFFECTS: PSW CHANGED
4271 :CALLING SEQUENCE: JSR PC,CLRDNI
4272 :
4273 :*****

```

```

4274
4275 017374' 012777 004000 160734 CLRDNI::MOV #DNI,@PCSRO ;CLEAR 'DNI' BIT
4276 017402' 032777 004000 160726 ;BIT #DNI,@PCSRO ;DID IS CLEAR?
4277 017410' 001402 ;BEQ 10$ ;YES
4278 017412' 000261 ;SEC ;NO, INDICATE ERROR
4279 017414' 000401 ;BR 20$
4280 017416' 000241 10$: CLC ;YES, INDICATE SUCCESS
4281 017420' 000207 20$: RTS PC

```

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CZUAAA.P11 11-JAN-83 09:29

CLEAR OUTSTANDING INTERRUPT BITS

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.SBTTL CLEAR OUTSTANDING INTERRUPT BITS
:*****8
:FUNCTIONAL DESCRIPTION:
:ROUTINE TO CLEAR ALL INTERRUPT BITS IN PCSRO
:INPUTS: NONE
:IMPLICIT INPUTS: NONE
:OUTPUTS: C BIT SET IF UNABLE TO CLEAR A INTERRUPT BIT
: C BIT CLEARED IF SUCCESSFUL
:SUBORDINATE ROUTINES USED: NONE
:FUNCTIONAL SIDE EFFECTS: ANY OUTSTANDING INTERRUPT IS CLEARED
:CALLING SEQUENCE: JSR PC,CLRINT
:*****

```
4304 017422' 032777 100000 160706 CLRINT: BIT #SERI,@PCSRO :IS 'SERI' BIT SET?
4305 017430' 001413 BEQ 10$ :NO
4306 017432' 012777 100000 160676 MOV #SERI,@PCSRO :WRITE ONE TO CLEAR 'SERI'
4307 017440' 032777 100000 160670 BIT #SERI,@PCSRO :DID IT CLEAR?
4308 017446' 001404 BEQ 10$ :YES
4309 017450' 012737 000734' 000310' MOV #SERI,BITNAM :NO, GET POINTER TO BIT NAME STRING
4310 017456' 000531 BR 70$ :LEAVE
4311 017460' 032777 040000 160650 10$: BIT #PCEI,@PCSRO :IS 'PCEI' BIT SET?
4312 017466' 001413 BEQ 20$ :NO
4313 017470' 012777 040000 160640 MOV #PCEI,@PCSRO :YES, WRITE ONE TO CLEAR 'PCEI'
4314 017476' 032777 040000 160632 BIT #PCEI,@PCSRO :DID IT CLEAR?
4315 017504' 001404 BEQ 20$ :YES
4316 017506' 012737 000745' 000310' MOV #PCEI,BITNAM :NO, GET POINTER TO BIT NAME STRING
4317 017514' 000512 BR 70$ :LEAVE
4318 017516' 032777 020000 160612 20$: BIT #RXI,@PCSRO :IS 'RXI' BIT SET?
4319 017524' 001413 BEQ 30$ :NO
4320 017526' 012777 020000 160602 MOV #RXI,@PCSRO :YES, WRITE ONE TO CLEAR 'RXI'
4321 017534' 032777 020000 160574 BIT #RXI,@PCSRO :DID IT CLEAR?
4322 017542' 001404 BEQ 30$ :YES
4323 017544' 012737 000756' 000310' MOV #RXI,BITNAM :NO, GET POINTER TO BIT NAME STRING
4324 017552' 000473 BR 70$ :LEAVE
4325 017554' 032777 010000 160554 30$: BIT #TXI,@PCSRO :IS 'TXI' BIT SET?
4326 017562' 001413 BEQ 40$ :NO
4327 017564' 012777 010000 160544 MOV #TXI,@PCSRO :YES, WRITE ONE TO CLEAR 'TXI'
4328 017572' 032777 010000 160536 BIT #TXI,@PCSRO :DID IT CLEAR?
4329 017600' 001404 BEQ 40$ :YES
4330 017602' 012737 000766' 000310' MOV #TXI,BITNAM :NO, GET POINTER TO BIT NAME STRING
4331 017610' 000454 BR 70$ :LEAVE
4332 017612' 032777 004000 160516 40$: BIT #DNI,@PCSRO :IS 'DNI' BIT SET?
4333 017620' 001413 BEQ 50$ :NO
4334 017622' 012777 004000 160506 MOV #DNI,@PCSRO :YES, WRITE ONE TO CLEAR 'DNI'
4335 017630' 032777 004000 160500 BIT #DNI,@PCSRO :DID IT CLEAR?
4336 017636' 001404 BEQ 50$ :YES
4337 017640' 012737 000776' 000310' MOV #DNI,BITNAM :NO, GET POINTER TO BIT NAME STRING
```

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```

4338 017646' 000435 BR 70$
4339 017650' 032777 002000 160460 50$: BIT #RCEI,@PCSRC
4340 017656' 001413 BEQ 60$
4341 017660' 012777 002000 160450 MOV #RCEI,@PCSRC
4342 017666' 032777 002000 160442 BIT #RCEI,@PCSRC
4343 017674' 001404 BEQ 60$
4344 017676' 012737 001006' 000310' MOV #RCEI,BITNAM
4345 017704' 000416 BR 70$
4346 017706' 032777 000400 160422 60$: BIT #FATI,@PCSRC
4347 017714' 001426 BEQ 80$
4348 017716' 012777 000400 160412 MOV #FATI,@PCSRC
4349 017724' 032777 000400 160404 BIT #FATI,@PCSRC
4350 017732' 001417 BEQ 80$
4351 017734' 012737 001017' 000310' MOV #FATI,BITNAM
4352 017742' PRINTF #FORM62,BITNAM 70$:
4353 017742' 013746 000310'
4354 017746' 012746 010754'
4355 017752' 012746 000002
4356 017756' 010600
4357 017760' 104417
4358 017762' 062706 000006
4359 017766' 000261 SEC
4360 017770' 000401 BR 100$
4361 017772' 000241 80$: CLC
4362 017774' 000207 100$: RTS PC
4363

```

```

:LEAVE
:IS 'RCEI' BIT SET?
:NO
:WRITE ONE TO CLEAR 'RCEI'
:DID IT CLEAR?
:YES
:NO, GET POINTER TO BIT NAME STRING
:LEAVE
:IS 'FATI' BIT SET?
:NO
:WRITE ONE TO CLEAR 'FATI'
:DID IS CLEAR?
:YES
:NO, GET POINTER TO BIT NAME STRING
:PRINT ERROR MESSAGE
MOV BITNAM,-(SP)
MOV #FORM62,-(SP)
MOV #2,-(SP)
MOV SP,R0
TRAP C$PNTF
ADD #6,SP
:INDICATE ERROR TO CALLER
:LEAVE
:INDICATE SUCCESS

```


GLOBAL AREAS MACY11 30(1046) 11-JAN-83 10:13 PAGE 90
CZUAAA.P11 11-JAN-83 09:29 CHECK MICROMONITOR

```

4364 .SBTTL CHECK MICROMONITOR
4365
4366 :*****
4367 :
4368 :FUNCTIONAL DESCRIPTION:
4369 :ROUTINE TO WAIT FOR THE MICROCODE TO ENTER THE MICROMONITOR
4370 :
4371 :INPUTS: NONE
4372 :
4373 :IMPLICIT INPUTS: PCSR1
4374 :
4375 :OUTPUTS: C BIT SET IN PSW IF TIMEOUT WAITING FOR MICROMONITOR
4376 :          C BIT CLEAR IN PSW IF MICROCODE IN MICROMONITOR
4377 :
4378 :SUBORDINATE ROUTINES CALLED: TIMON, TIMOFF
4379 :
4380 :FUNCTIONAL SIDE EFFECTS: PSW CHANGED, LINE CLOCK INTERRUPTS ARE ENABLED
4381 :
4382 :CALLING SEQUENCE: JSR PC,CHKMON
4383 :
4384 :*****
4385
4386 017776* 012737 000077 000332* CHKMON:MOV #1*SECOND,METER ;TIMEOUT PERIOD IS 1 SECOND
4387 020004* 004737 017276* JSR PC,TIMON ;TURN ON THE CLOCK
4388 020010* 122777 000001 160322 10$:CMPB #INMON,@PCSR1 ;IS THE MICROCODE IN THE MICROMONITOR?
4389 020016* 001410 BEQ 20$ ;YES
4390 020020* 020020* BREAK ;RETURN TO DRS FOR A MOMENT
4391 020020* 104422 TRAP CSBRK
4392 020022* 005737 000332* TST METER ;HAS THE TIMER EXPIRED?
4393 020026* 001370 BNE 10$ ;NOT YET, KEEP CHECKING MICROCODE
4394 020030* 004737 017314* JSR PC,TIMOFF ;TIMER HAS EXPIRED TURN OFF THE TIMER
4395 020034* 000261 SEC ;INDICATE ERROR TO CALLER
4396 020036* 000403 BR 30$ ;LEAVE
4397 020040* 004737 017314* 20$:JSR PC,TIMOFF ;STOP THE CLOCK
4398 020044* 000241 CLC ;INDICATE TO CALLER MICROCODE IS IN
4399 ;MICROMONITOR
4400 020046* 000207 30$:RTS PC

```

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.SBTTL CHECK INTERRUPT ERROR BITS
:*****
:FUNCTIONAL DESCRIPTION:
:ROUTINE TO CHECK FOR ANY T11 INTERRUPT ERROR BITS IN PCSRO
:INPUTS: NONE
:IMPLICIT INPUTS: PCSRO
:OUTPUTS: C BIT SET IN PSW IF AN INTERRUPT BIT IS SET
:C BIT CLEAR IN NO INTERRUPT BITS ARE SET
:'ERRINT' CONTAINS COPY OF PCSRO
:SUBORDINATE ROUTINES CALLED: NONE
:FUNCTIONAL SIDE EFFECTS: ANY INTERRUPT BIT SET IS CLEARED
:CALLING SEQUENCE: JSR PC,CHKINT
:*****

020050' 017737 160262 000666' CHKINT::MOV @PCSRO,ERRINT ;GET PCSRO CONTENTS
020056' 032737 170000 000666' BIT #NPRERR!NXMERR!UNIERR!PARERR,ERRINT ;ANY INTERRUPT ERRORS SET?
020064' 001405 BEQ 10\$;NO
020066' 012777 170000 160242 MOV #NPRERR!NXMERR!UNIERR!PARERR,@PCSRO ;CLEAR ANY ERROR INTERRUPT
020074' 000261 SEC ;INDICATE ERROR
020076' 000491 BR 20\$
020100' 000241 10\$: CLC ;INDICATE NO ERRORS
020102' 000207 20\$: RTS PC

GLOBAL AREAS MACY11 30(1046) 11-JAN-83 10:13 PAGE 92
CZUAAA.P11 11-JAN-83 09:29 CHECK INTERRUPT ERROR BITS

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.SBTTL RESET UNA

.....
:FUNCTIONAL DESCRIPTION:
:ROUTINE TO RESET DEUNA
:INPUTS: NONE
:IMPLICIT INPUTS: NONE
:OUTPUTS: C BIT SET IF ERROR OCCURRED
:C BIT CLEARED IF SUCCESS
:SUBORDINATE ROUTINES USED: CHKDNI, CLRDNI
:FUNCTIONAL SIDE EFFECTS: PSW CHANGED, LINE CLOCK IS TURNED ON, OPERATIONAL
:MICROCODE IS STARTED.
:CALLING SEQUENCE: JSR PC,REUNA
:.....8

```
REUNA:: MOV #RSET,@PCSR0 ;RESET DEUNA BACK TO OPERATIONAL MICRO
        MOV #10*SECOND,METER ;PUT SOME TIME ON THE METER
        JSR PC,CHKDNI ;WAIT FOR 'DNI'
        BCC 10$ ;OK
        ;ERROR DNI NOT SET AFTER RESET!
        ;SETUP ERROR MESSAGE
        MOV PWHEN,-(SP)
        MOV BITSTA,-(SP)
        MOV BITNAM,-(SP)
        MOV CSRNUM,-(SP)
        MOV #FORM9,-(SP)
        MOV #5,-(SP)
        MOV SP,RO
        TRAP C$PNTF
        ADD #14,SP
        ;INDICATE ERROR
10$: SEC 20$
        JSR PC,CLRDNI ;GO CLEAR DNI
        BCC 20$ ;OK
        PRINTF #FORM8
        MOV #FORM8,-(SP)
        MOV #1,-(SP)
        MOV SP,RO
        TRAP C$PNTF
        ADD #4,SP
        SEC
```

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4488 020250* 000207 20\$: RTS PC

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C

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4489
4490          .SBTTL  LOAD MICROMODULE
4491
4492          ;*****
4493          ;THIS SUBROUTINE LOADS THE MICROMODULE FOR TESTS THAT REQUIRE CUSTOM
4494          ;MICROCODE.
4495          ;*****
4496
4497
4498
4499 020252' 012737 000326' 000320' LODMIC: MOV      #MICRO,MICMOD      ;POINT TO MICRO MODULE NAME
4500 020260' 004737 020104'          JSR      PC,REUNA          ;GO START OPERATION MICROCODE
4501 020264' 103015          BCC     5$              ;OK
4502 020266'          PRINTF #UNLOD,MICMOD      ;PRINT ERROR MESSAGE
4503 020266' 013746 000320'          MOV      MICMOD,-(SP)
4504 020272' 012746 001515'          MOV      #UNLOD,-(SP)
4505 020276' 012746 000002'          MOV      #2,-(SP)
4506 020302' 010600          MOV      SP,R0
4507 020304' 104417          TRAP   C$PNTF
4508 020306' 062706 000006'          ADD     #6,SP
4509 020312' 000261          SEC
4510 020314' 000137 021106'          JMP     200$
4511 020320' 023727 000326' 000101 5$:  CMP     MICRO,#'A
4512 020326' 001007          BNE     10$
4513
4514 020330' 013737 000000G 000614'          MOV     MICASZ,UDBB
4515 020336' 012737 000000G 000616'          MOV     #MICROA,UDBB+2
4516 020344' 000475          BR      70$
4517
4518 020346' 023727 000326' 000102 10$:  CMP     MICRO,#'B
4519 020354' 001007          BNE     20$
4520
4521 020356' 013737 000000G 000614'          MOV     MICBSZ,UDBB
4522 020364' 012737 000000G 000616'          MOV     #MICROB,UDBB+2
4523 020372' 000462          BR      70$
4524
4525 020374' 023727 000326' 000103 20$:  CMP     MICRO,#'C
4526 020402' 001007          BNE     30$
4527
4528 020404' 013737 000000G 000614'          MOV     MICCSZ,UDBB
4529 020412' 012737 000000G 000616'          MOV     #MICROC,UDBB+2
4530 020420' 000447          BR      70$
4531
4532 020422' 023727 000326' 000104 30$:  CMP     MICRO,#'D
4533 020430' 001007          BNE     40$
4534
4535 020432' 013737 000000G 000614'          MOV     MICDSZ,UDBB
4536 020440' 012737 000000G 000616'          MOV     #MICROD,UDBB+2
4537 020446' 000434          BR      70$
4538
4539 020450' 023727 000326' 000105 40$:  CMP     MICRO,#'E
4540 020456' 001007          BNE     50$
4541
4542 020460' 013737 000000G 000614'          MOV     MICESZ,UDBB
4543 020466' 012737 000000G 000616'          MOV     #MICROE,UDBB+2
4544 020474' 000421          BR      70$

```

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 CZUAAA.P11 11-JAN-83 09:29 LOAD MICROMODULE

H/
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```

4545
4546 020476' 023727 000326' 000106 50$: CMP MICRO,#'F ;LOAD MICROCODE MODULE F?
4547 020504' 001007 BNE 60$ ;NO
4548 ;YES
4549 020506' 013737 000000G 000614' MOV MICFSZ,UDBB ;SIZE OF MODULE F
4550 020514' 012737 000000G 000616' MOV #MICROF,UDBB+2 ;BASE ADDRESS OF MODULE F
4551 020522' 000406 BR 70$
4552
4553 020524' 013737 000000G 000614' 60$: MOV MICGSZ,UDBB ;SIZE OF MODULE G
4554 020532' 012737 000000G 000616' MOV #MICROG,UDBB+2 ;BASE ADDRESS OF MODULE G
4555
4556 020540' 005037 000620' 70$: CLR UDBB+4
4557 020544' 012737 010000 000622' MOV #WCSADR+<WCSSIZ/2>,UDBB+6 ;LOAD INTO TOP HALF OF WCS
4558
4559 ;SETUP PCB
4560 020552' 012737 000021 000604' MOV #LIM,PCBB ;'LOAD INTERNAL MEMORY' FUNCTION
4561 020560' 012737 000614' 000606' MOV #UDBB,PCBB+2 ;SET ADDRESS OF UDBB
4562 020566' 005037 000610' CLR PCBB+4
4563 020572' 012777 000604' 157542 MOV #PCBB,@PCSR2 ;TELL DEUNA WHERE PCBB IS
4564 020600' 005077 157540 CLR @PCSR3
4565
4566 020604' 004737 017422' JSR PC,CLRINT ;CLEAR ANY OUTSTANDING INTERRUPT BITS
4567 020610' 103014 BCC 75$ ;OK
4568 020612' PRINTF #UNLOD,MICMOD ;CAN'T CONTINUE WITH INTERRUPT BITS SET
4569 020612' 013746 000320' MOV MICMOD,-(SP)
4570 020616' 012746 001515' MOV #UNLOD,-(SP)
4571 020622' 012746 000002 MOV #2,-(SP)
4572 020626' 010600 MOV SP,R0
4573 020630' 104417 TRAP C$PNTF
4574 020632' 062706 000006 ADD #6,SP
4575 020635' 000261 SEC
4576 020640' 000522 BR 200$
4577
4578 020642' 012777 000001 157466 75$: MOV #GETPCB,@PCSR0 ;ISSUE 'GET PCB' PORT COMMAND
4579 020650' 012737 000770 000332' MOV #10*SECOND,METER ;SETUP TIMER
4580 020656' 004737 017330' JSR PC,CHKDNI ;WAIT FOR 'DNI' TO SET
4581 020662' 103014 BCC 80$ ;OK
4582 ;ERROR DNI NOT SET!
4583 PRINTF #UNLOD,MICMOD ;PRINT MESSAGE
4584 020664' MOV MICMOD,-(SP)
4585 020670' 013746 000320' MOV #UNLOD,-(SP)
4586 020674' 012746 001515' MOV #2,-(SP)
4587 020700' 010600 MOV SP,R0
4588 020702' 104417 TRAP C$PNTF
4589 020704' 062706 000006 ADD #6,SP
4590 020710' 000261 SEC ;INDICATE ERROR
4591 020712' 000475 BR 200$
4592 020714' 80$:
4593 020714' 004737 017374' JSR PC,CLRDN1 ;GO CLEAR 'DNI'
4594 020720' 103014 BCC 90$ ;OK
4595 ;ERROR 'DNI' NOT CLEAR!
4596 PRINTF #UNLOD,MICMOD ;PRINT MESSAGE
4597 020722' MOV MICMOD,-(SP)
4598 020726' 013746 000320' MOV #UNLOD,-(SP)
4599 020732' 012746 001515' MOV #2,-(SP)
4600 020736' 010600 MOV SP,R0
    
```

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4601 020740' 104417
4602 020742' 062706 000006
4603 020746' 000261
4604 020750' 000456
4605 020752'
4606 020752' 012777 000002 157356 90$:
4607 020760' 012737 000770 000332'
4608 020766' 004737 017330'
4609 020772' 103014
4610
4611 020774'
4612 020774' 013746 000320'
4613 021000' 012746 001515'
4614 021004' 012746 000002
4615 021010' 010600
4616 021012' 104417
4617 021014' 062706 000006
4618 021020' 000261
4619 021022' 000431
4620 021024'
4621 021024' 004737 017374' 100$:
4622 021030' 103014
4623
4624 021032'
4625 021032' 013746 000320'
4626 021036' 012746 001515'
4627 021042' 012746 000002
4628 021046' 010600
4629 021050' 104417
4630 021052' 062706 000006
4631 021056' 000261
4632 021060' 000412
4633 021062'
4634 021062' 012737 000001 000604' 110$:
4635 021070' 012737 010000 000606'
4636 021076' 012777 000002 157232
4637 021104' 000241
4638
4639 021106' 000207 200$:
4640

```

```

          SEC
          BR          200$          :INDICATE ERROR
          TRAP
          ADD          C$PNTF
          #6,SP
          :ISSUE <GET COMMAND> PORT COMMAND
          :SETUP TIMER
          :WAIT FOR 'DNI'
          :OK
          :ERROR 'DNI' NOT SET:
          :PRINT MESSAGE
          MOV          MICMOD,-(SP)
          MOV          #UNLOD,-(SP)
          MOV          #2,-(SP)
          MOV          SP,R0
          TRAP          C$PNTF
          ADD          #6,SP
          :INDICATE ERROR
          SEC
          BR          200$
          JSR          PC,CLRDN1
          BCC          110$
          :GO CLEAR 'DNI'
          :OK
          :ERROR 'DNI' NOT CLEAR
          :PRINT ERROR MESSAGE
          MOV          MICMOD,-(SP)
          MOV          #UNLOD,-(SP)
          MOV          #2,-(SP)
          MOV          SP,R0
          TRAP          C$PNTF
          ADD          #6,SP
          :INDICATE ERROR
          SEC
          BR          200$
          MOV          #LASM,PCBB
          MOV          #WCSADR+<WCSSIZ/2>,PCBB+2
          MOV          #GETCMD,@PCSRO
          CLC
          :LOAD PCBB WITH 'LOAD AND START' FUNCTION CODE
          :STARTING MICROADDRESS
          :ISSUE <GET COMMAND> PORT COMMAND
          :INDICATE SUCCESS
          RTS          PC
          :RETURN

```


MISCELLANEOUS SECTIONS MACY11 30(1046)
(ZUAAA.P11 11-JAN-83 09:29

11-JAN-83 10:13 PAGE 97
LOAD MICROMODULE

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4641
4642
4643 021110'
4644 021110'
4645 021110'
4646 021110' 000167
4647 021112' 000000
4648 021114'
4649 021114'
4650 021114' 104425
4651
4652
4653
4654
4655
4656
4657
4658
4659

.TITLE MISCELLANEOUS SECTIONS
.SBTTL REPORT CODING SECTION
BGNRPT
EXIT RPT
ENDRPT

LSRPT::
.WORD JSJMP
.WORD L10064-2-
L10064:
TRAP CSRPT

4660 021116'
4661 021116'
4662
4663 021116' 177777
4664 021120' 177777
4665 021122' 177777
4666
4667 021124'
4668

.SBTTL PROTECTION TABLE
:++
: THIS TABLE IS USED BY THE RUNTIME SERVICES
: TO PROTECT THE LOAD MEDIA.
:--

BGNPROT
LSPROT::
-1 ;OFFSET INTO P-TABLE FOR CSR ADDRESS
-1 ;OFFSET INTO P-TABLE FOR MASSBUS ADDRESS
-1 ;OFFSET INTO P-TABLE FOR DRIVE NUMBER
ENDPROT

MISCELLANEOUS SECTIONS MACV11 30(1046) 11-JAN-83 10:13 PAGE 98
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INITIALIZE SECTION

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.SBTTL INITIALIZE SECTION

: THE INITIALIZE SECTION CONTAINS THE CODING THAT IS PERFORMED
: AT THE BEGINNING OF EACH PASS.
:--

```

4669
4670
4671
4672
4673
4674
4675
4676 021124'          BGNINIT
4677 021124'
4678
4679
4680
4681 021124'          READEF #EF.CONTINUE          ;WAS A CONTINUE COMMAND ENTERED
4682 021124' 012700 000036          MOV #EF.CONTINUE,RO
4683 021130' 104447          TRAP CSREFG
4684 021132'          BCOMPLETE          30$          ;YES, LEAVE INIT CODE
4685 021132' 103555          READEF #EF.PWR          ;WAS THERE A POWER FAILURE?
4686 021134'          BCS          30$
4687 021134' 012700 JJ0034          MOV #EF.PWR,RO
4688 021140' 104447          TRAP CSREFG
4689 021142'          BCOMPLETE          30$          ;YES, LEAVE INIT CODE
4690 021142' 103555          READEF #EF.NFW          ;IS THIS A NEW PASS?
4691 021144'          BCS          30$
4692 021144' 012700 000035          MOV #EF.NFW,RO
4693 021150' 104447          TRAP CSREFG
4694 021152'          BNCOMPLETE          10$          ;NO SKIP THE NEW PASS TIME STUFF
4695 021152' 103072          BCC          10$
4696 021154'          READEF #EF.START          ;IS THIS THE VERY FIRST TIME THOUGH?
4697 021154' 012700 000040          MOV #EF.START,RO
4698 021160' 104447          TRAP CSREFG
4699 021162'          BNCOMPLETE          6$          ;NO SKIP THE FIRST TIME STUFF
4700 021162' *J3061          BCC          6$
4701 021164' 012737 000001 000672' MOV #1,FRSTIM          ;SET THE FIRST TIME FLAG
4702 021172'          MEMORY FRESIZ          ;GET FREE MEMORY INFO
4703 021172' 104431          TRAP CSMEM
4704 021174' 010037 000322' MOV RO,FRESIZ
4705 021200' 013737 000322' 000324' MOV FRESIZ,FREMEM          ;SIZE OF FREE MEMORY IN FRESIZ
4706 021206' 062737 000002 000324' ADD #2,FREMEM          ;START OF FREE MEMORY IN FREMEM
4707 021214'          CLOCK L,R1          ;GET LINE CLOCK INFO
4708 021214' 012700 000114          MOV #L,RO
4709 021220' 104462          TRAP CSCLK
4710 021222' 010001          MOV RO,R1
4711 021224'          BCOMPLETE          1$
4712 021224' 103411          BCS          1$
4713 021226'          PRINTF #NOCLK          ;ERROR MESSAGE
4714 021226' 012746 001566' MOV #NOCLK,-(SP)
4715 021232' 012746 000001          MOV #1,-(SP)
4716 021236' 010600          MOV SP,RO
4717 021240' 104417          TRAP CSPTF
4718 021242' 062706 000004          ADD #4,SP
4719 021246' 000504
4720 021250' 012137 000274' 1$: BR 20$          ;CANNOT CONTINUE
4721 021254' 012102          MOV (R1)+,CLKCSR          ;LINE CLOCK CSR
4722 021256' 072227 000005          MOV (R1)+,R2          ;GET CLOCK PRIORITY
4723 021262' 010237 000276' ASH #5,R2
4724 021266' 012137 000300' MOV R2,CLKBR          ;VECTOR
          MOV (R1)+,CLKVEC

```

MISCELLANEOUS SECTIONS MACY11 30(1046) 11-JAN-83 10:13 PAGE 99
CZUAAA.P11 11-JAN-83 09:29 INITIALIZE SECTION

```

4725 021272' 012137 000302'      MOV      (R1)+,CLKFRE      :FREQUENCY
4726 021276'      SETVEC  CLKVEC,#CLKSRV,CLKBR :SETUP CLOCK INTERRUPT VECTOR
4727 021276' 013746 000276'      MOV      CLKBR,-(SP)
4728 021302' 012746 021674'      MOV      #CLKSRV,-(SP)
4729 021306' 013746 000300'      MOV      CLKVEC,-(SP)
4730 021312' 012746 000003'      MOV      #3,-(SP)
4731 021316' 104437      TRAP    C$SVEC
4732 021320' 062706 000010'      ADD     #10,SP
4733 021324' 000402'      BR      5$
4734 021326' 005037 000672'      CLR    FRSTIM      :INDICATE NOT THE FIRST TIME THROUGH
4735 021332' 012737 177777' 000330' 5$:  MOV    #-1,UNIT    :YES, INITIALIZE UNIT #
4736 021340' 005237 000330' 10$: INC    UNIT        :SETUP FOR NEXT UNIT
4737 021344' 023737 000330' 000012' CMP    UNIT,LSUNIT  :WE TESTED ALL AVAILABLE UNITS?
4738 021352' 003042'      BGT    20$         :YES, LEAVE
4739 021354'      GPHARD  UNIT,R1   :GET P-TAB POINTER FOR THIS UNIT
4740 021354' 013700 000330'      MOV    UNIT,R0
4741 021360' 104442'      TRAP  C$GPHRD
4742 021362' 010001'      MOV    R0,R1
4743 021364'      BNCOMPLETE 10$      :THIS ONE IS NOT AVAILABLE
4744 021364' 103365'      BCC   10$
4745 021366' 012137 000266'      MOV    (R1)+,UNACSR  :SAVE CSR
4746 021372' 012137 000270'      MOV    (R1)+,UNAVEC  :SAVE VECTOR
4747 021376' 012737 000240' 000272' MOV    #PRIOS,UNAPRI  :SAVE PRIORITY
4748 021404' 013737 000266' 000336' MOV    UNACSR,PCSR0  :PCSR0
4749 021412' 013737 000336' 000340' MOV    PCSRO,PCSR1
4750 021420' 062737 000002' 000340' ADD    #2,PCSR1      :PCSR1
4751 021426' 013737 000340' 000342' MOV    PCRSR1,PCSR2
4752 021434' 062737 000002' 000342' ADD    #2,PCSR2      :PCSR2
4753 021442' 013737 000342' 000344' MOV    PCRSR2,PCSR3
4754 021450' 062737 000002' 000344' ADD    #2,PCSR3      :PCSR3
4755 021456' 00J403'      BR     30$         :LEAVE
4756 021460' 005037 000672' 20$: CLR    FRSTIM      :CLEAR FIRST TIME THROUGH FLAG
4757 021464'      DOCLN
4758 021464' 104444'      TRAP  C$DCLN      :ABORT PASS
4759 021466'      30$:
4760 021466'      ENDINIT
4761 021466'
4762 021466' 104411'      L10066: TRAP    C$INIT
4763
4764
4765
4766
4767

```

.EVEN

MISCELLANEOUS SECTIONS M'CY11 30(1046) 11-JAN-83 10:13 PAGE 100
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AUTODROP SECTION

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4768 .SBTTL AUTODROP SECTION
4769
4770 021470' BGNAUTO
4771 021470'
4772 021470' LSAUTO::
4773 021470' ENDAUTO
4774 021470' 104461 L10067: TRAP CSAUTO
4775

```

4776 .SBTTL CLEANUP CODING SECTION

```

4777 :++
4778 : THE CLEANUP CODING SECTION CONTAINS THE CODING THAT IS PERFORMED
4779 : AFTER THE HARDWARE TESTS HAVE BEEN PERFORMED.
4780 :--
4781

```

```

4782 021472' BGNCLN
4783 021472'
4784 021472' 005737 000664' TST NEXMEM ;DOES PCSRO EXIST?
4785 021476' 001023 BNE 20$ ;NO SKIP RESET
4786 021500' 012777 000040 156630 MOV #RSET,@PCSRO ;RESTORE DEUNA TO OPERATIONAL
4787 ;STATE
4788 021506' 012737 000770 000332' MOV #10*SECOND,METER ;SETUP TIMER
4789 021514' 004737 017330' JSR PC,CHKDNI ;DNI SET WHEN DONE
4790 021520' 103010 BCC 10$
4791 021522' PRINT# #CLNERR ;ERROR
4792 021522' 012746 021556' MOV #CLNERR,-(SP)
4793 021526' 012746 000001 MOV #1,-(SP)
4794 021532' 010600 MOV SP,R0
4795 021534' 104417 TRAP C$PNTF
4796 021536' 062706 000004 ADD #4,SP
4797 021542' 004737 017374' 10$: JSR PC,CLRDN1 ;CLEAR DNI BIT
4798 021546' 004737 017314' 20$: JSR PC,TIMOFF ;TURN OFF THE LINE CLOCK
4799 021552' EXIT CLN ;CC
4800 021552' 104432 TRAP C$EXIT
4801 021554' 000054 .WORD L10070-
4802

```

```

4803 021556' 047045 040445 051105 CLNERR: .ASCIZ /%N%AERROR OCCURRED DURING DEVICE RESET%/
4804 021564' 047522 020122 041517
4805 021572' 052503 051122 042165
4806 021600' 042040 051125 047111
4807 021606' 020107 042504 044526
4808 021614' 042503 051040 051505
4809 021622' 052105 047045 000

```

4810 .EVEN

4811 ENDCLN

```

4812 021630'
4813 021630' L10070: TRAP CSAUTO
4814 021630' 104412

```

MISCELLANEOUS SECTIONS MACY11 30(1046) 11-JAN-83 10:13 PAGE 101
CZUAAA.P11 11-JAN-83 09:29 DROP UNIT SECTION

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4815 .SBTTL DROP UNIT SECTION
4816
4817 021632' BGNDU
4818 021632'
4819 021632'
4820 021632' 000167
4821 021634' 000000
4822 021636' ENDDU
4823 021636'
4824 021636' 104453
4825

```

```

4826 .SBTTL ADD UNIT SECTION
4827
4828 021640' BGNAU
4829 021640'
4830 021640'
4831 021640' 000167
4832 021642' 000000
4833 021644' ENDAU
4834 021644'
4835 021644' 104452
4836

```

```

4837 .TITLE GLOBAL INTERRUPT SERVICE ROUTINES
4838
4839 .SBTTL NON-EXISTANT MEMORY INTERRUPT SERVICE ROUTINE
4840

```

```

4841 :*****
4842 :
4843 :FUNCTIONAL DESCRIPTION:
4844 :
4845 :       THIS ROUTINE IS ASSIGNED TO VECTOR 4 BY THE ACCESS TESTS
4846 :       IT SETS THE NEXMEM FLAG SIGNALING THAT AN ACCESS WAS
4847 :       ATTEMPTED ON NON-EXISTANT MEMORY.
4848 :
4849 :*****
4850

```

```

4851 021646' BGNSRV TRAP4
4852 021646'
4853 021646' 012737 000001 000664' ENDSRV MOV #1,NEXMEM ;SET FLAG TRAP4::
4854 021654'
4855 021654'
4856 021654' 000002 L10073: RTI

```

GLOBAL INTERRUPT SERVICE ROUTINES
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NON-EXISTANT MEMORY INTERRUPT SERVICE ROUTINE

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4906

.SBTTL UNA INTERRUPT SERVICE ROUTINE

.....
:FUNCTIONAL DESCRIPTION:
:CONTROL GOES HERE WHEN THE INTERRUPT ENABLE BIT IS SET
:AND ANY OF THE INTERRUPT BITS SET.
:A COPY OF PCSRO IS STORED AT 'UNAINI:' AND A COPY OF THE
:PSW AT THE TIME OF THE INTERRUPT IS STORED AT '(P)PRI:'.
:.....

```
BGNSRV UNASRV
                                UNASRV::
MOV @PCSRO,UNAINI              ;INDICATE UNA INTERRUPTED
MOV 2(SP),(P)PRI               ;GET CPU PRIORITY AT TIME OF INTERRUPT
ENDSRV
                                L10074:
                                RTI
```

.SBTTL CLOCK INTERRUPT SERVICE ROUTINE

.....
:FUNCTIONAL DESCRIPTION:
:THIS ROUTINE COUNTS A PRESET NUMBER OF CLOCK TICKS THEN IT
:TURNS THE CLOCK OFF
:INPUTS: METER
:OUTPUTS:METER
:ROUTINES CALLED: NONE
:.....

```
BGNSRV CLKSRV
                                CLKSRV::
TST METER                      ;HAS THE METER EXPIRED?
BEQ 20$                         ;YES, STOP COUNTING
DEC METER                       ;COUNT TICKS
20$:
ENDSRV
                                L10075:
                                RTI
```

HARDWARE TESTS MACV11 30(1046) 11-JAN-83 09:29
CZUAAA.P11

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CLOCK INTERRUPT SERVICE ROUTINE

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4962

.TITLE HARDWARE TESTS

.SBTTL TEST 1: PCSRO READ ACCESS TEST

.....
: THIS TEST WILL VERIFY THAT THE PORT CONTROL AND STATUS REGISTER 0
: CAN BE READ FROM THE UNIBUS AND THAT THE PREDETERMINED BITS APPEAR
: IN THE EXPECTED BIT POSITIONS.
: TEST SEQUENCE: 1-READ PCSRO
: 2-VERIFY BITS 9 AND 4 - 0
:

BGNTST

SETVEC #4,#TRAP4,#PRI07 ; SETUP TIME-OUT TRAP T1:
MOV #PRI07,-(SP)
MOV #TRAP4,-(SP)
MOV #4,-(SP)
MOV #3,-(SP)
TRAP C\$SVEC
ADD #10,SP

BGNSUB ;#1

T1.1: TRAP C\$BSUB

: CHECK TO SEE IF PCSRO EXISTS

CLR NEXMEM ; CLEAR NON-EXISTANT MEMORY FLAG
CLR CSRNUM ; HOLDS WHICH PCSR WE ARE DOING
TST @PCSRO ; DOES PCSRO EXIST?
TST NEXMEM
BEQ 10\$; YES
ERRDF 001,RACERR,RACMG1 ; NO, PRINT FATAL ERROR MESSAGE

TRAP C\$ERDF
.WORD 1
.WORD RACERR
.WORD RACMG1

CKLOOP ; LOOP BACK FROM HERE IF ERROR

TRAP C\$CLP1

CLRVEC #4 ; RELEASE TRAP 4 VECTOR

MOV #4,R0
TRAP C\$CVEC

DODU UNIT ; DROP UNIT

MOV UNIT,R0
TRAP C\$DODU

DOCLN ; ABORT SUB-PASS

TRAP C\$DCLN

10\$:
ENDSUB ;#1

L10077: TRAP C\$ESUB

BGNSUB ;#2

T1.2:

HARDWARE TESTS MACY11 30(1046) 11-JAN-83 10:13 PAGE 104
CZUAAA.P11 11-JAN-83 09:29 TEST 1: PCSRO READ ACCESS TEST

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```

4963 022014* 104402                                TRAP      CSBSUB
4964
4965          :OK, PCSRO EXISTS NOW CHECK SOME BITS
4966          :
4967 022016*                                BGNSEG
4968 022016* 104404                                TRAP      CSBSEG
4969
4970          :CHECK BIT 4 = 0
4971          :
4972 022020* 012737 001311* 000312*          MOV      #SNCLR,BITSTA          :TESTING FOR CLEARED BITS
4973 022026* 012737 000004 000306*          MOV      #4,BITNUM             :TESTING BIT 4
4974 022034* 032777 000020 156274          BIT      #BIT4,@PCSRO          :IS BIT 4=0?
4975 022042* 001404          BEQ      20$                   :YES
4976 022044*          ERRHRD 002,RACERR,RACMG2      :NO,PRINT HARD ERROR MESSAGE
4977 022044* 104456                                TRAP      CSERHRD
4978 022046* 000002                                .WORD    2
4979 022050* 001663*                                .WORD    RACERR
4980 022052* 012574*                                .WORD    RACMG2
4981 022054*          20$:
4982 022054*          ENDSEG
4983 022054*
4984 022054* 104405                                10000$: TRAP      CSSESEG
4985 022056*          BGNSEG
4986 022056* 104404                                TRAP      CSBSEG
4987
4988          :CHECK BIT 9 = 0
4989          :
4990 022060* 012737 000011 000306*          MOV      #9,BITNUM             :TESTING BIT 9 NOW
4991 022066* 032777 001000 156242          BIT      #BIT9,@PCSRO          :IS BIT 9=0?
4992 022074* 001404          BEQ      30$                   :YES
4993 022076*          ERRHRD 003,RACERR,RACMG2      :NO,PRINT HARD ERROR MESSAGE
4994 022076* 104456                                TRAP      CSERHRD
4995 022100* 000003                                .WORD    3
4996 022102* 001663*                                .WORD    RACERR
4997 022104* 012574*                                .WORD    RACMG2
4998 022106*          30$:
4999 022106*          ENDSEG
5000 022106*
5001 022106* 104405                                10001$: TRAP      CSSESEG
5002 022110*          ENDSUB ;#2
5003 022110*
5004 022110* 104403                                L10100: TRAP      CSSESUB
5005 022112*          CLRVEC #4          :FREE TRAP 4 VECTOR
5006 022112* 012700 000004          MOV      #4,RO
5007 022116* 104436          TRAP      CSVEC
5008 022120*          ENDTST
5009 022120*
5010 022120* 104401                                L10076: TRAP      CSSETST

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.SBTTL TEST 2: PCSR1 READ ACCESS TEST

.....
: THIS TEST WILL VERIFY THAT THE PORT CONTROL AND STATUS REGISTER
: CAN BE READ FROM THE UNIBUS AND THAT THE PREDETERMINED BITS
: APPEAR IN THE EXPECTED BIT POSITIONS.
: TEST SEQUENCE: 1-READ PCSR1
: 2-VERIFY BITS 4,5,6,14,15 = 0
:

BGNTST

SETVEC #4,#TRAP4,#PRI07 ;SETUP TIMEOUT TRAP T2::
MOV #PRI07,-(SP)
MOV #TRAP4,-(SP)
MOV #4,-(SP)
MOV #3,-(SP)
TRAP C\$SVEC
ADD #10,SP
BGNSUB ;#1
T2.1: TRAP C\$BSUB

:CHECK TO SEE IF PCSR1 EXISTS

CLR NEXMEM ;CLEAR NON-EXISTANT MEMORY FLAG
MOV #1,CSRNUM ;TESTING PCSR1
TST @PCSR1 ;DOES PCSR1 EXIST?
TST NEXMEM
BEQ 10\$;YES
ERRDF 004,RACERR,RACMG1 ;NO,PRINT FATAL ERROR MESSAGE
TRAP C\$ERDF
.WORD 4
RACERR
RACMG1
CKLOOP ;LOOP FROM HERE IF ERROR
TRAP C\$CLP1
CLRVEC #4 ;RELEASE TRAP 4 VECTOR
MOV #4,R0
TRAP C\$CVEC
DODU UNIT ;DROP THE UNIT
MOV UNIT,R0
TRAP C\$DODU
DOCLN ;ABORT SUB-PASS
TRAP C\$DCLN

10\$:
ENDSUB ;#1

BGNSUB ;#2

L10102: TRAP C\$ESUB
T2.2: TRAP C\$BSUB

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CZUAAA.P11 11-JAN-83 09:29 TEST 2: PCSR1 READ ACCESS TEST

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5070 022232'
5071 022232' 104404
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5075 022234' 012737 001311' 000312'
5076 022242' 012737 000004 000306'
5077 022250' 032777 000020 156062
5078 022256' 001404
5079 022260'
5080 022260' 104456
5081 022262' 000005
5082 022264' 001663'
5083 022266' 012574'
5084 022270'
5085 022270'
5086 022270'
5087 022270' 104405
5088 022272'
5089 022272' 104404
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5093 022274' 012737 000005 000306'
5094 022302' 032777 000040 156030
5095 022310' 001404
5096 022312'
5097 022312' 104456
5098 022314' 000006
5099 022316' 001663'
5100 022320' 012574'
5101 022322'
5102 022322'
5103 022322'
5104 022322' 104405
5105 022324'
5106 022324' 104404
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5110 022326' 012737 000006 000306'
5111 022334' 032777 000100 155776
5112 022342' 001404
5113 022344'
5114 022344' 104456
5115 022346' 000007
5116 022350' 001663'
5117 022352' 012574'
5118 022354'
5119 022354'
5120 022354'
5121 022354' 104405
5122 022356'

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```

:OK, PCSR1 EXISTS NOW CHECK SOME BITS
:
BGNSEG
TRAP CSBSEG
:CHECK BIT 4 = 0
:
MOV #SNCLR,BITSTA ;TESTING CLEARED BITS
MOV #4,BITNUM ;TESTING BIT 4
BIT #BIT4,@PCSR1 ;IS BIT 4=0?
BEQ 20$ ;YES
ERRHRD 005,RACERR,RACMG2 ;NO, PRINT HARD ERROR MESSAGE
TRAP CSERHRD
WORD 5
WORD RACERR
WORD RACMG2
20$:
ENDSEG
10000$:
TRAP CSESEG
BGNSEG
TRAP CSBSEG
:CHECK BIT 5 = 0
:
MOV #5,BITNUM ;TESTING BIT 5
BIT #BIT5,@PCSR1 ;IS BIT 5=0?
BEQ 30$ ;YES
ERRHRD 006,RACERR,RACMG2 ;NO,PRINT HARD ERROR MESSAGE
TRAP CSERHRD
WORD 6
WORD RACERR
WORD RACMG2
30$:
ENDSEG
10001$:
TRAP CSESEG
BGNSEG
TRAP CSBSEG
:CHECK BIT 6 = 0
:
MOV #6,BITNUM ;TESTING BIT 6
BIT #BIT6,@PCSR1 ;IS BIT 6=0?
BEQ 40$ ;YES
ERRHRD 007,RACERR,RACMG2 ;NO,PRINT HARD ERROR MESSAGE
TRAP CSERHRD
WORD 7
WORD RACERR
WORD RACMG2
40$:
ENDSEG
10002$:
TRAP CSESEG
BGNSEG

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TEST 3: PCRS2 READ ACCESS TEST

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.SBTTL TEST 3: PCRS2 READ ACCESS TEST

:*****
: THIS TEST WILL VERIFY THAT THE PORT CONTROL AND STATUS REGISTER 2
: CAN BE READ FROM THE UNIBUS
: TEST SEQUENCE: 1-READ PCRS2
:*****

BGNTST

T3: :
SETVEC #4,#TRAP4,PRI07 ;SETUP TIMEOUT TRAP VECTOR
MOV PRI07,-(SP)
MOV #TRAP4,-(SP)
MOV #4,-(SP)
MOV #3,-(SP)
TRAP C\$SVEC
ADD #10,SP
CLR NEXMEM ;CLEAR NON-EXISTANT MEMORY FLAG
MOV #2,CSRNUM ;TESTING PCRS2
TST @PCRS2 ;DOES PCRS2 EXIST?
TST NEXMEM
BEQ 10% ;YES
ERRDF 010,RACERR,RACMG1 ;NO,PRINT FATAL ERROR MESSAGE
TRAP C\$ERDF
.WORD 10
.WORD RACERR
.WORD RACMG1
CKLOOP ;LOOP BACK FROM HERE IF ERROR
TRAP C\$CLP1
CLRVEC #4 ;RELEASE TRAP 4 VECTOR
MOV #4,RO
TRAP C\$CVEC
DODU UNIT ;DROP THE UNIT
MOV UNIT,RO
TRAP C\$DODU
DOCLN ;ABORT SUBPASS
TRAP C\$DCLN
10%: CLRVEC #4 ;RELEASE TRAP 4 VECTOR
MOV #4,RO
TRAP C\$CVEC
L10104: TRAP C\$E1ST

ENDTST

000304'

000004

000330'

000004

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.SBTTL TEST 4: PCRS3 READ ACCESS TEST

.....
: THIS TEST WILL VERIFY THAT THE PORT CONTROL AND STATUS REGISTER 3
: CAN BE READ FROM THE UNIBUS
: TEST SEQUENCE: 1-READ PCRS3
:

BGNTST

SETVEC #4,#TRAP4,#PRI07 ;SETUP VECTOR FOR TIME-OUT T4::
MOV #PRI07,-(SP)
MOV #TRAP4,-(SP)
MOV #4,-(SP)
MOV #3,-(SP)
TRAP C\$SVEC
ADD #10,SP

BGNSEG

TRAP C\$BSEC
CLR NEXMEM ;CLEAR NON-EXISTANT MEMORY FLAG
MOV #3,CSRNUM ;TESTING PCRS3
TST @PCRS3 ;DOES PCRS3 EXIST?
TST NEXMEM
BEQ 10\$;YES
ERRDF 011,RACERR,RACMG1 ;NO,PRINT FATAL ERKOR MESSAGE
TRAP C\$ERDF
.WORD 11
.WORD RACERR
.WORD RACMG1
CKLOOP ;LOOP BACK FROM HERE IF ERROR
TRAP C\$CLP1
CLRVEC #4 ;RELEASE TRAP 4 VECTOR
MOV #4,R0
TRAP C\$CVEC
DODU UNIT ;DROP UNIT
MOV UNIT,R0
TRAP C\$DODU
DOCLN ;ABORT SUB-PASS
TRAP C\$DCLN

10\$:
ENDSEG

10000\$:
TRAP C\$ESEG
CLRVEC #4 ;RETURN TIME-OUT TRAP
MOV #4,R0
TRAP C\$CVEC

ENDTST

L10105:
TRAP C\$E1ST

000304'

000330'

000004

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.SBTTL TEST 5: RESET TEST

.....
: THIS TEST WILL VERIFY THE RESET STATE FOR ALL DEUNA UNIBUS REGISTERS
: TEST SEQUENCE:
: 1-WRITE A 1 TO PCSRO BIT 5
: 2-READ PCSRO
: -VERIFY DNI SET
: -VERIFY INTR SET
: 3-VERIFY PCSRO BITS 15:12 AND 10:08 AND 06:00 FOR LOGICAL 0
: 4-READ PCSR1
: 5-VERIFY PCSR1 BITS 07:03 AND 14 AND 15 FOR LOGICAL 0 AND
: PORT STATE FIELD BITS 02:00 FOR READY STATE
.....

BGNTST

CLR CSRNUM T5: :
MOV #AFTER,PWHEN ; TESTING PCSRO FIRST
; SETUP PART OF ERROR MESSAGE
; FOR THE FOLLOWING TEST SEGMENTS

BGNSUB :#1

T5.1: TRAP C\$BSUB

: SET THE RESET BIT IN PCSPG AND WAIT FOR 'DNI' TO SET

MOV #RSET,@PCSRO ; RESET DEUNA
MOV #10*SECOND,METER ; PUT SOME TIME ON THE METER
JSR PC,CHKDNI ; WAIT FOR DNI TO SET
BCC 10\$; OK

MOV #DNI,BITNAM ; ERROR 'DNI' DID NOT SET
MOV #NSET,BITSTA ; SETUP ERROR MESSAGE
ERRHRD 012,RSETER,MSG3 ; PRINT ERROR MESSAGE

TRAP C\$ERHRD
.WORD 12
.WORD RSETER
.WORD MSG3

ESCAPE TST ; NO POINT IN CONTINUING TEST

TRAP C\$ESCAPE
.WORD L10106-

10\$:
ENDSUB :#1

L10107: TRAP C\$ESUB

BGNSUB :#2

T5.2: TRAP C\$BSUB

: CHECK ALL THE BITS AFFECTED BY RESET

BGNSEG

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5322 022776' 104404                                TRAP      C$BSEG
5323
5324          :CHECK THE INTERRUPT SUMMARY BIT = 0
5325          :
5326 023000' 032777 000200 155330          BIT      #INTR,@PCSR0          :IS THE INTERRUPT SUMMARY BIT SET?
5327 023006' 001012          BNE      11$                  :YES
5328          :ERROR, INTERRUPT SUMMARY NOT SET!
5329 023010' 012737 001030' 000310'          MOV      #$INTR,BITNAM        :PREPARE ERROR MESSAGE
5330 023016' 012737 001275' 000312'          MOV      #SNSET,BITSTA
5331 023024'          ERRHRD 013,RSETER,MSG3          :PRINT ERROR MESSAGE
5332 023024' 104456                                TRAP      C$ERHRD
5333 023026' 000015                                .WORD    13
5334 023030' 001717'                                .WORD    RSETER
5335 023032' 013056'                                .WORD    MSG3
5336 023034'
5337 023034'          11$:
5338 023034'          ENDSEG
5339 023034' 104405                                10000$: TRAP      C$ESEG
5340
5341          :CHECK BITS 15:12 AND 10:08 AND 06:00 OF PCSR0 FOR 0
5342          :
5343 023036' 0'2701 000356'          MOV      #BNAMTO,R1          :POINT TO BIT NAME MNEMONICS
5344 023042' 012702 000001          MOV      #BIT0,R2           :START TESTING AT LSB
5345 023046' 012737 001311' 000312'          MOV      #SNCLR,BITSTA      :TESTING FOR CLEARED BITS
5346 023054'          15$:
5347 023054'          BGNSEG
5348 023054' 104404                                TRAP      C$BSEG
5349 023056' 030277 155254          BIT      R2,@PCSR0          :IS THIS BIT CLEARED?
5350 023062' 001406          BEQ      20$                  :YES
5351          :ERROR BIT FAILED TO CLEAR
5352 023064' 011137 000310'          MOV      (R1),BITNAM        :GET MNEMONIC FOR THIS BIT
5353 023070'          ERRHRD 014,RSETER,MSG3          :PRINT ERROR MESSAGE
5354 023070' 104456                                TRAP      C$ERHRD
5355 023072' 000016                                .WORD    14
5356 023074' 001717'                                .WORD    RSETER
5357 023076' 013056'                                .WORD    MSG3
5358 023100'
5359 023100'          20$:
5360 023100'          ENDSEG
5361 023100' 104405                                10001$: TRAP      C$ESEG
5362 023102' 005702          21$:  TST      R2              :WE TESTED ALL BITS IN PCSR0?
5363 023104' 100411          BM!     25$                  :YES
5364 023106' 006302          ASL     R2                  :POINT TO NEXT BIT TO TEST
5365 023110' 062701 000002          ADD     #2,R1              :POINT TO NEXT MNEMONIC
5366 023114' 105702          TSTB   R2                  :ARE WE POINTING TO 'INTR' BIT?
5367 023116' 100771          BMI     21$                :YES, SKIP IT
5368 023120' 022702 004000          CMP     #DNI,R2           :ARE WE POINTING TO 'DNI' BIT?
5369 023124' 001766          BEQ     21$                :YES SKIP IT
5370 023126' 000752          BR      15$                :CONTINUE TESTING
5371
5372 023130'          25$:
5373 023130'          BGNSEG
5374 023130' 104404                                TRAP      C$BSEG
5375
5376          :CHECK PCSR1 BITS 02:00 FOR THE READY STATE
5377          :

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CZUAAA.P11 11-JAN-83 09:29 TEST 5: RESET TEST

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5378 023132' 012737 000001 000304'      MOV      #1,CSRNUM      :TESTING PCSR1
5379 023140' 017701 155174      MOV      @PCSR1,R1      :GET PCSR1 CONTENTS
5380 023144' 042701 177770      BIC      #^CPSTATE,R1   :MASK ALL BUT PORT STATUS BITS
5381 023150' 022701 000002      CMP      #READY,R1      :IS THE DEUNA IN THE READY STATE?
5382 023154' 001404      BEQ      30$            :YES
5383                                     :ERROR, DEUNA NOT IN READY STATE!
5384 023156'                                     :PRINT ERROR MESSAGE
5385 023156' 104456      ERRHRD   015,RSETER,MSG4
5386 023160' 000017      TRAP    C$ERHRD
5387 023162' 001717'      .WORD   15
5388 023164' 013120'      .WORD   RSETER
5389 023166'                                     .WORD   MSG4
5390 023166'
5391 023166'
5392 023166' 104405      10002$: TRAP    C$ESEG
5393
5394                                     :CHECK PCSR1 BITS 07:03 FOR 0
5395
5396 023170' 012701 000416'      MOV      #BNAMT1,R1     :POINT TO MNEMONIC TABLE FOR PCSR:
5397 023174' 062701 000006      ADD      #3*2,R1        :INDEX PAST STATE BITS
5398 023200' 012737 001311' 000312'  MOV      #SNCLR,BITSTA   :PREPARE ERROR MESSAGE
5399 023206' 012737 001340' 000314'  MOV      #SAFTER,PWHEN
5400 023214' 012702 000010      MOV      #BIT3,R2       :POINT TO BIT 3 TO START TESTING
5401 023220'
5402 023220'      40$: BGNSEG
5403 023220' 104404
5404 023222' 030277 155112      BIT      R2,@PCSR1      :IS THIS BIT A 0?
5405 023226' 001406      BEQ      45$            :YES
5406                                     :ERROR, BIT IS NOT A 0 AFTER RESET!
5407 023230' 011137 000310'      MOV      (R1),BITNAM    :GET MNEMONIC FOR THIS BIT
5408 023234'      ERRHRD   016,RSETER,MSG3 :PRINT ERROR MESSAGE
5409 023234' 104456      TRAP    C$ERHRD
5410 023236' 000020      .WORD   16
5411 023240' 001717'      .WORD   RSETER
5412 023242' 013056'      .WORD   MSG3
5413 023244'
5414 023244'      45$: ENDSEG
5415 023244'
5416 023244' 104405      10003$: TRAP    C$ESEG
5417 023246' 105702
5418 023250' 100404      TSTB    R2              :WE TESTED BITS 07:03?
5419 023252' 006302      BMI     50$            :YES
5420 023254' 062701 000002      ASL     R2              :NO, POINT TO NEXT BIT
5421 023260' 000757      ADD     #2,R1           :POINT TO MNEMONIC FOR NEXT BIT
5422 023262'      BR      40$           :CONTINUE TESTING
5423 023262'
5424 023262' 104404      50$: BGNSEG
5425                                     TRAP    C$BSEG
5426                                     :CHECK PCSR1 BIT 14 = 0
5427
5428 023264' 032777 040000 155046      BIT      #ICAB,@PCSR1   :IS PORT/CABLING BIT CLEAR?
5429 023272' 001412      BEQ     60$            :YES
5430                                     :ERROR, PORT/CABLING BIT NOT CLEAR!
5431 023274' 012737 001311' 000312'  MOV      #SNCLR,BITSTA   :PREPARE ERROR MESSAGE
5432 023302' 012737 001074' 000310'  MOV      #ICAB,BITNAM    :GET MNEMONIC
5433 023310'      ERRHRD   017,RSETER,MSG3 :PRINT ERROR MESSAGE

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HARDWARE TESTS MACV11 30(1046) 11-JAN-83 10:13 PAGE 113
CZUAAA.P11 11-JAN-83 09:29 TEST 5: RESET TEST

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5434 023310 104456 TRAP C$ERHRD
5435 023312 000021 .WORD 17
5436 023314 001717 .WORD RSETER
5437 023316 013056 .WORD MSG3
5438 023320
5439 023320 60$:
5440 023320 ENDSEG
5441 023320 104405 10004$:
5442 023322 BGNSEG TRAP C$ESEG
5443 023322 104404 TRAP C$BSEG
5444
5445 :CHECK PCSR1 BIT 15 = 0
5446 :
5447 023324 032777 100000 155006 BIT #XPWR,@PCSR1 ;IS TRANSCEIVER POWER BIT CLEAR?
5448 023332 001412 BEQ 70$ :YES
5449
5450 023334 012737 001311 000312 MOV #SNCLR,BITSTA ;ERROR, TRANSCEIVER POWER BIT NOT CLEAR!
5451 023342 012737 001063 000310 MOV #XPWR,BITNAM ;PREPARE ERROR MESSAGE
5452 023350 ERRHRD 018,RSETER,MSG3 ;GET MNEMONIC
5453 023350 104456 ;PRINT ERROR MESSAGE
5454 023352 000022 TRAP C$ERHRD
5455 023354 001717 .WORD 18
5456 023356 013056 .WORD RSETER
5457 023360 .WORD MSG3
5458 023360 70$:
5459 023360 ENDSEG
5460 023360 104405 10005$:
5461 023362 ENDSUB ;#2 TRAP C$ESEG
5462 023362 L10110:
5463 023362 104403 TRAP C$ESUB
5464 023364
5465 023364 L10100:
5466 023364 104401 TRAP C$ETST

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TEST 6: PCRS2 REGISTER READ/WRITE TEST

.SBTTL TEST 6: PCRS2 REGISTER READ/WRITE TEST

.....
: THIS TEST WILL CHECK THE REGISTER FOR ALL SA0 AND SA1 ERRORS (STUCK AT
: 0 AND STUCK AT 1 ERRORS). THE HOST WILL WRITE PATTERNS TO THE REGISTER
: AND READ THEM BACK TO VERIFY. THE PATTERNS TO BE USED ARE AT THE LABEL
: PATERN:: IN THE GLOBAL DATA SECTION OF THIS PROGRAM.

: NOTE: SINCE PCRS2 BIT 00 IS ALWAYS PRESET TO LOGIC 0, THE LOWEST ORDER
: BIT OF THE PATTERN WILL BE MASKED BEFORE DOING THE COMPARISON.

: TEST SEQUENCE:
: 1-WRITE PATTERN TO REGISTER PCRS2
: 2-COMPARE MASKED PATTERN WITH REGISTER PCRS2 CONTENTS
: 3-REPEAT STEPS 1 TO 2 FOR ALL PATTERNS

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023366' 012737 000002 000304'
023374' 012701 000516'
023400' 012705 000005'
023404' 012103
023406'
023406' 104404
023410' 010377 154726
023414' 017704 154722
023420' 042703 000001
023424' 020304
023426' 001404
023430'
023430' 104456
023432' 000023
023434' 001741'
023436' 012632'
023440'
023440'
023440' 104405
023442' 005305
023444' 001357
023446'
023446' 104401

BGNTST
T6::
MOV #2,CSRNUM ;TESTING PCRS2
MOV #PATERN,R1 ;GET ADDRESS OF DATA PATTERNS
MOV #5,R5 ;5 DATA PATTERNS
10\$: MOV (R1)+,R3 ;GET DATA PATTERN
BGNSEG
TRAP C\$BSEG
MOV R3,@PCSR2 ;WRITE PATTERN TO PCRS2
MOV @PCSR2,R4 ;READ PCRS2
BIC #BIT0,R3 ;MASK BIT 00
CMP R3,R4 ;COMPARE WHAT WAS WRITTEN TO...
;WHAT WAS READ
BEQ 20\$;COMPARED OK
ERRHRD 019,RRWER,RACMG3 ;PRINT ERROR MESSAGE
TRAP C\$EHRD
;WORD 19
;WORD RRWER
;WORD RACMG3
20\$:
ENDSEG
10000\$:
TRAP C\$ESEG
DEC R5 ;DONE ALL DATA PATTERNS?
BNE 10\$;NO, CONTINUE TESTING
ENDTST
L10111:
TRAP C\$ETST

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TEST 7: REGISTER PCSR3 READ/WRITE TEST

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023450'
023450'
023450' 012737 000003 000304'
023456' 012703 000001
023462'
023462' 104404
023464' 010377 154654
023470' 017704 154650
023474' 020304
023476' 001404
023500'
023500' 104456
023502' 000024
023504' 001741'
023506' 012632'
023510'
023510'
023510' 104405
023512' 006303
023514'
023514' 104404
023516' 010377 154622
023522' 017704 154616
023526' 020304
023530' 001404
023532' 104456
023534' 000025
023536' 001741'
023540' 012632'
023542'
023542'
023542' 104405
023544'
023544'
023544' 104401

```
.SBTTL TEST 7: REGISTER PCSR3 READ/WRITE TEST
:.....
:THIS TEST WILL WRITE PATTERNS TO THE WRITEABLE FIELD OF PCSR3
:AND WILL READ THESE BACK FOR VERIFICATION.
:TEST SEQUENCE:
: 1-WRITE PATTERN 000001 TO PCSR3
: 2-VERIFY PATTERN IN PCSR3
: 3-WRITE PATTERN 000002 TO PCSR3
: 4-VERIFY PATTERN IN PCSR3
:.....

BGNTEST
                                T7::
MOV #3,CSRNUM ;TESTING PCSR3
MOV #BIT00,R3 ;DATA PATTERN =1
BGNSEG
MOV R3,@PCSR3 ;WRITE PATTERN TO PCSR3 TRAP C$BSEG
MOV @PCSR3,R4 ;READ PCSR3
CMP R3,R4 ;COMPARE
BEQ 10$ ;IF == GOOD
ERRHRD 020,RRWER,RACMG3 ;ELSE PRINT ERROR MESSAGE TRAP C$ERHRD
                                .WORD 20
                                .WORD RRWER
                                .WORD RACMG3
10$:
ENDSEG
                                10000$:
ASL R3 ;DATA PATTERN =2 TRAP C$ESEG
BGNSEG
MOV R3,@PCSR3 ;WRITE PATTERN TO PCSR3 TRAP C$BSEG
MOV @PCSR3,R4 ;READ PCSR3
CMP R3,R4 ;COMPARE
BEQ 20$ ;IF = GOOD
ERRHRD 021,RRWER,RACMG3 ;ELSE PRINT ERROR MESSAGE TRAP C$ERHRD
                                .WORD 21
                                .WORD RRWER
                                .WORD RACMG3
20$:
ENDSEG
                                10001$:
ENDTEST TRAP C$ESEG
                                L10112:
TRAP C$ETST
```

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.SBTTL TEST 8: NOP TEST

.....
: THIS TEST WILL VERIFY THAT THE DEUNA PROCESSOR IS ALIVE AND CAN
: RESPOND TO A PORT COMMAND ISSUED. THE NOP PORT COMMAND WILL BE ISSUED
: TO THE DEUNA IN PCSRO BITS 3:0 AND WILL WAIT FOR THE 'DNI' BIT TO
: SET IN PCSRO.
: THE NOP PORT COMMAND USES A MINIMUM OF HARDWARE BUT FORCES THE T11
: TO EXECUTE THE PORT COMMAND SEQUENCE.
: TEST SEQUENCE:
: 1-READ REGISTER PCSRO
: 2-VERIFY INTERRUPT ENABLE BIT 06 = LOGIC 0
: 3-VERIFY DONE INTERRUPT BIT 11 = LOGIC 0
: 4-WRITE PORT COMMAND NOP TO PCSRO BITS 3:0
: 5-READ REGISTER PCSRO
: 6-VERIFY DONE INTERRUPT BIT 11 = LOGIC 1
: 7-WRITE REGISTER PCSRO BIT 11 WITH LOGIC 1
: 8-READ REGISTER PCSRO
: 9-VERIFY PCSRO BIT 11 = 0
:.....

BGNTST

BGNSUB :#1

T8::

T8.1:

TRAP C\$BSUB

: CHECK THE INTERRUPT ENABLE BIT; IF SET CLEAR IT

MOV PCSRO,R1 ;GET PCSRO ADDRESS
MOV (R1),R2 ;GET CONTENTS
BIT #INTÉ,R2 ;IS INTERRUPT ENABLE SET?
BEQ 10\$;NO
MOVB #0,(R1) ;CLEAR INTERRUPT ENABLE
MOV (R1),R2 ;GET PCSRO CONTENTS
BIT #INTÉ,R2 ;IS INTERRUPT ENABLE CLEAR NOW?
BEQ 10\$;YES
ERRHRD 022,NOPERR,RACMG4 ;NO,PRINT ERROR MESSAGE

TRAP C\$ERHRD
.WORD 22
.WORD NOPERR
.WORD RACMG4

10\$:

ENDSUB :#2

L10114:

TRAP C\$ESUB

BGNSUB :#2

T8.2:

TRAP C\$BSUB

: CHECK THE DONE INTERRUPT BIT; IF SET WRITE ONE TO CLEAR IT

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5624 023614 032702 004000 BIT #DNI,R2 ;IS DONE INTERRUPT CLEAR?
5625 023620 001413 BEQ 20$ ;YES
5626 023622 000302 SWAB R2 ;NO,ORIENT UPPER & LOWER BYTES
5627 023624 110261 000001 MOV#B R2,1(R1) ;SO WE CAN CLEAR UPPER BITS
5628 023630 111102 MOV (R1),R2 ;GET PCSRO CONTENTS
5629 023632 032702 004000 BIT #DNI,R2 ;IS DNI CLEAR NOW?
5630 023636 001404 BEQ 20$ ;YES
5631 023640 ERRHRD 023,NOPERR,RACMG7 ;NO,PRINT ERROR MESSAGE
5632 023640 104456 TRAP C$ERHRD
5633 023642 000027 .WORD 23
5634 023644 002001 .WORD NOPERR
5635 023646 012726 .WORD RACMG7
5636 023650 20$:
5637 023650 ENDSUB :#2
5638 023650 L10115:
5639 023650 104403 TRAP C$ESUB
5640 023652 BGNSUB :#3
5641 023652 T8.3:
5642 023652 104402 TRAP C$BSUB
5643
5644 ;ISSUE NOP PORT COMMAND AND CHECK FOR DNI
5645
5646 023654 012777 000006 154454 MOV #PNOP,@PCSRO ;ISSUE NOP PORT COMMAND
5647 023662 012737 000176 000332 MOV #2*SECOND,METER ;SETUP TIMER
5648 023670 004737 017330 JSR PC,CHKDNI ;WAIT FOR DNI TO SET
5649 023674 103022 BCC 30$ ;OK
5650 ;ERROR DNI DID NOT SET!
5651 ;SETUP TO PRINT ERROR MESSAGE
5652 023676 012737 000776 000310 MOV #SDNI,BITNAM ;POINT TO 'DNI' ASCII STRING
5653 023704 012737 001275 000312 MOV #NSET,BITSTA ;POINT TO 'NOT SET' ASCII STRING
5654 023712 012737 001340 000314 MOV #AFTER,PWHEN ;POINT TO 'AFTER' ASCII STRING
5655 023720 012737 001403 000316 MOV #SNOP,PCOMND ;POINT TO 'NOP' ASCII STRING
5656 023726 ERRHRD 024,NOPERR,MSG1 ;PRINT ERROR MESSAGE
5657 023726 104456 TRAP C$FRHRD
5658 023730 000030 .WORD 24
5659 023732 002001 .WORD NOPERR
5660 023734 012750 .WORD MSG1
5661 023736 ESCAPE TST ;DON'T CONTINUE TEST IF ERROR OCCURRED
5662 023736 104410 TRAP C$ESCAPE
5663 023740 000026 .WORD L10113-
5664 023742 30$:
5665 023742 BGNSEG
5666 023742 104404 TRAP C$BSEG
5667
5668 ;WRITE ONE TO CLEAR 'DNI'
5669
5670 023744 004737 017374 JSR PC,CLRDN1 ;GO CLEAR DNI BIT
5671 023750 103004 BCC 40$ ;IT CLEARED OK
5672 ;ERROR DNI DID NOT CLEAR!
5673 023752 ERRHRD 025,NOPERR,RACMG7 ;NO,PRINT ERROR MESSAGE
5674 023752 104456 TRAP C$ERHRD
5675 023754 000031 .WORD 25
5676 023756 002001 .WORD NOPERR
5677 023760 012726 .WORD RACMG7
5678 023762 40$:
5679 023762 ENDSEG

```

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5680 023762*
5681 023762* 104405
5682 023764*
5683 023764*
5684 023764* 104403
5685 023766*
5686 023766*
5687 023766* 104401
5688

ENDSUB ;#3

ENDTST

100008: TRAP CSESEG
L10116: TRAP CSESUB
L10113: TRAP CSETST

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.SBTTL TEST 9: SELF TEST

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.....
THIS TEST VERIFIES THAT THE ROM BASED SELF TEST
CAN BE RUN SUCCESSFULLY WHEN INVOKED VIA
THE SELF TEST PORT COMMAND.

TEST SEQUENCE:
1. CLEAR OUTSTANDING INTERRUPTS
2. ISSUE THE SELF TEST PORT COMMAND
3. WAIT FOR DNI
4. CHECK LITE BITE REGISTER FOR SUCCESSFUL SELF TEST
5. WRITE ONE TO CLEAR DNI
.....

```

BGNTST

```

T9::
JSR PC,CLRINT ;CLEAR ANY OUTSTANDING INTERRUPTS
BCC 10$ ;OK
ERRHRD 026,SLFTST ;ERROR OCCURRED TRYING TO CLEAR
TRAP C$ERRHRD
.WORD 26
.WORD SLFTST
.WORD 0
ESCAPE TST ;LEAVE THIS TEST
TRAP C$ESCAPE
.WORD L10117-.

MOV #10*SECOND,METER ;PUT SOME TIME ON THE METER
MOV #SLFT,@PCSR0 ; RUN SELF TEST
JSR PC,CHKDNI ; DNI ?
BCC 30$ ; YES
;ERROR DNI FAILED TO SET!

MOV #$DNI,BITNAM
MOV #$NSET,BITSTA
MOV #$AFTER,PWHEN
MOV #$SLFT,PCOMND
ERRHRD 027,SLFTST,MSG1 ;PRINT ERROR MESSAGE
TRAP C$ERRHRD
.WORD 27
.WORD SLFTST
.WORD MSG1
ESCAPE TST ; AND ABORT TEST
TRAP C$ESCAPE
.WORD L10117-.

30$: MOV @PCSR1,R3 ;GET PCSR1 CONTENTS
BIT #SFT,R3 ;WAS SELF TEST SUCCESSFUL?
BEQ 40$ ;YES
COM R3 ;GET TEST NUMBER CORRECT POLARITY
BIC #^CSFT,R3 ;MASK ALL BUT SELF TEST FIELD
MOV #8.,R1 ;SHIFT RESULT OVER 8 BIT POSTIONS
35$: ASR R3
DEC R1
BNE 35$

```



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5771
5772      : LOCAL STORAGE FOR TEST 9
5773
5774 024202' 000000      STMSG::      .WORD      0      ; SELF TEST MESSAGE ADDRESS
5775
5776      : SELF TEST MESSAGE TABLE
5777
5778 024204' 024404'      STTBL::      .WORD      SMSG00
5779 024206' 024432'      .WORD      SMSG01
5780 024210' 024457'      .WORD      SMSG02
5781 024212' 024470'      .WORD      SMSG03
5782 024214' 024525'      .WORD      SMSG04
5783 024216' 024552'      .WORD      SMSG05
5784 024220' 024613'      .WORD      SMSG06
5785 024222' 024644'      .WORD      SMSG07
5786 024224' 024660'      .WORD      SMSG10
5787 024226' 024710'      .WORD      SMSG11
5788 024230' 024721'      .WORD      SMSG12
5789 024232' 024734'      .WORD      SMSG13
5790 024234' 024746'      .WORD      SMSG14
5791 024236' 024760'      .WORD      SMSG15
5792 024240' 024772'      .WORD      SMSG16
5793 024242' 025004'      .WORD      SMSG17
5794 024244' 025016'      .WORD      SMSG20
5795 024246' 025037'      .WORD      SMSG21
5796 024250' 025051'      .WORD      SMSG22
5797 024252' 025063'      .WORD      SMSG23
5798 024254' 025075'      .WORD      SMSG24
5799 024256' 025107'      .WORD      SMSG25
5800 024260' 025121'      .WORD      SMSG26
5801 024262' 025133'      .WORD      SMSG27
5802 024264' 025145'      .WORD      SMSG30
5803 024266' 025220'      .WORD      SMSG31
5804 024270' 025270'      .WORD      SMSG32
5805 024272' 025341'      .WORD      SMSG33
5806 024274' 025403'      .WORD      SMSG34
5807 024276' 025452'      .WORD      SMSG35
5808 024300' 025513'      .WORD      SMSG36
5809 024302' 025562'      .WORD      SMSG37
5810 024304' 025623'      .WORD      SMSG40
5811 024306' 025711'      .WORD      SMSG41
5812 024310' 025774'      .WORD      SMSG42
5813 024312' 026060'      .WORD      SMSG43
5814 024314' 026135'      .WORD      SMSG44
5815 024316' 026217'      .WORD      SMSG45
5816 024320' 026273'      .WORD      SMSG46
5817 024322' 026305'      .WORD      SMSG47
5818 024324' 026317'      .WORD      SMSG50
5819 024326' 026403'      .WORD      SMSG51
5820 024330' 026464'      .WORD      SMSG52
5821 024332' 026546'      .WORD      SMSG53
5822 024334' 026621'      .WORD      SMSG54
5823 024336' 026701'      .WORD      SMSG55
5824 024340' 026753'      .WORD      SMSG56
5825 024342' 026765'      .WORD      SMSG57
5826 024344' 026777'      .WORD      SMSG60

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5827	024346'	027010'				.WORD	SMSG61
5828	024350'	027031'				.WORD	SMSG62
5829	024352'	027043'				.WORD	SMSG63
5830	024354'	027070'				.WORD	SMSG64
5831	024356'	027102'				.WORD	SMSG65
5832	024360'	027121'				.WORD	SMSG66
5833	024362'	027133'				.WORD	SMSG67
5834	024364'	027145'				.WORD	SMSG70
5835	024366'	027164'				.WORD	SMSG71
5836	024370'	027215'				.WORD	SMSG72
5837	024372'	027237'				.WORD	SMSG73
5838	024374'	027251'				.WORD	SMSG74
5839	024376'	027263'				.WORD	SMSG75
5840	024400'	027275'				.WORD	SMSG76
5841	024402'	027307'				.WORD	SMSG77
5842							
5843	024404'	047503	0501 5	042514	:ASCII MESSAGES		
5844	024412'	042524	020104	020055	SMSG00::	.ASCIZ	/COMPLETED - NO ERRORS/
5845	024420'	047516	042440	051122			
5846	024426'	051117	000123				
5847	024432'	050103	020125	047111	SMSG01::	.ASCIZ	/CPU INSTRUCTION TEST/
5848	024440'	052123	052522	052103			
5849	024446'	047511	020116	042524			
5850	024454'	052123	000				
5851	024457'	122	046517	052040	SMSG02::	.ASCIZ	/ROM TEST/
5852	024464'	051505	000124				
5853	024470'	051127	052111	040505	SMSG03::	.ASCIZ	/WRITEABLE CONTROL STORE TEST/
5854	024476'	046102	020105	047503			
5855	024504'	052116	047522	020114			
5856	024512'	052123	051117	020105			
5857	024520'	042524	052123	000			
5858	024525'	120	054510	044523	SMSG04::	.ASCIZ	/PHYSICAL ADDRESS ROM/
5859	024532'	040503	020114	042101			
5860	024540'	051104	051505	020123			
5861	024546'	047522	000115				
5862	024552'	030524	020061	047125	SMSG05::	.ASCIZ	/T11 UNIBUS ADDRESS REGISTER TEST/
5863	024560'	041111	051525	040440			
5864	024566'	042104	042522	051523			
5865	024574'	051040	043505	051511			
5866	024602'	042524	020122	042524			
5867	024610'	052123	000				
5868	024613'	122	044505	044505	SMSG06::	.ASCIZ	/RECEIVER UNIBUS DMA TEST/
5869	024620'	042526	020122	047125			
5870	024626'	041111	051525	042040			
5871	024634'	040515	052040	051505			
5872	024642'	000124					
5873	024644'	047125	041111	051525	SMSG07::	.ASCIZ	/UNIBUS TEST/
5874	024652'	052040	051505	000124			
5875	024660'	044515	051103	050117	SMSG10::	.ASCIZ	/MICROPROCESSOR DMA TEST/
5876	024666'	047522	042503	051523			
5877	024674'	051117	04 040	040515			
5878	024702'	052040	051505	000124			
5879	024710'	051503	020122	042524	SMSG11::	.ASCIZ	/CSR TEST/
5880	024716'	052123	000				
5881	024721'	124	046511	051105	SMSG12::	.ASCIZ	/TIMER TEST/
5882	024726'	052040	051505	000124			

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5883	024734	047125	042504	044506	MSG13::	.ASCIZ	/UNDEFINED/
5884	024742	042516	000104				
5885	024746	047125	042504	044506	MSG14::	.ASCIZ	/UNDEFINED/
5886	024754	042516	000104				
5887	024760	047125	042504	044506	MSG15::	.ASCIZ	/UNDEFINED/
5888	024766	042516	000104				
5889	024772	047125	042504	044506	MSG16::	.ASCIZ	/UNDEFINED/
5890	025000	042516	000104				
5891	025004	047125	042504	044506	MSG17::	.ASCIZ	/UNDEFINED/
5892	025012	042516	000104				
5893	025016	044514	045516	046440	MSG20::	.ASCIZ	/LINK MEMORY TEST/
5894	025024	046505	051117	020131			
5895	025032	042524	052123	000			
5896	025037	125	042116	043105	MSG21::	.ASCIZ	/UNDEFINED/
5897	025044	047111	042105	000			
5898	025051	125	042116	043105	MSG22::	.ASCIZ	/UNDEFINED/
5899	025056	047111	042105	000			
5900	025063	125	042116	043105	MSG23::	.ASCIZ	/UNDEFINED/
5901	025070	047111	042105	000			
5902	025075	125	042116	043105	MSG24::	.ASCIZ	/UNDEFINED/
5903	025102	047111	042105	000			
5904	025107	125	042116	043105	MSG25::	.ASCIZ	/UNDEFINED/
5905	025114	047111	042105	000			
5906	025121	125	042116	043105	MSG26::	.ASCIZ	/UNDEFINED/
5907	025126	047111	042105	000			
5908	025133	125	042116	043105	MSG27::	.ASCIZ	/UNDEFINED/
5909	025140	047111	042105	000			
5910	025145	114	041517	046101	MSG30::	.ASCIZ	/LOCAL LOOPBACK TEST - TRANSMITTER TIMEOUT/
5911	025152	046040	047517	041120			
5912	025160	041501	020113	042524			
5913	025166	052123	026440	020040			
5914	025174	051124	047101	046523			
5915	025202	052111	042524	020122			
5916	025210	044524	042515	052517			
5917	025216	000124					
5918	025220	047514	040503	020114	MSG31::	.ASCIZ	/LOCAL LOOPBACK TEST - RECEIVER TIMEOUT/
5919	025226	047514	050117	040502			
5920	025234	045503	052040	051505			
5921	025242	020124	020055	051040			
5922	025250	041505	044505	042526			
5923	025256	020122	044524	042515			
5924	025264	052517	000124				
5925	025270	047514	040503	020114	MSG32::	.ASCIZ	/LOCAL LOOPBACK TEST - BUFFER COMPARISON/
5926	025276	047514	050117	040502			
5927	025304	045503	052040	051505			
5928	025312	020124	020055	041040			
5929	025320	043125	042506	020122			
5930	025326	047503	050115	051101			
5931	025334	044523	047117	000			
5932	025341	114	041517	046101	MSG33::	.ASCIZ	/LOCAL LOOPBACK TEST - BYTE COUNT/
5933	025346	046040	047517	041120			
5934	025354	041501	020113	042524			
5935	025362	052123	026440	020040			
5936	025370	054502	042524	041440			
5937	025376	052517	052116	000			
5938	025403	114	041517	046101	MSG34::	.ASCIZ	/LOCAL LOOPBACK TEST - RECEIVER STATUS/

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5939	025410'	046040	047517	041120		
5940	025416'	041501	020113	042524		
5941	025424'	052123	026440	020040		
5942	025432'	042522	042503	053111		
5943	025440'	051105	051440	040524		
5944	025446'	052524	000123			
5945	025452'	047514	040503	020114	MSG35::	.ASCIZ /LOCAL LOOPBACK TEST - CRC ERROR/
5946	025460'	047514	050117	040502		
5947	025466'	045503	052040	051505		
5948	025474'	020124	020055	041440		
5949	025502'	041522	042440	051122		
5950	025510'	051117	000			
5951	025513'	114	041517	046101	MSG36::	.ASCIZ /LOCAL LOOPBACK TEST - MATCH BIT ERROR/
5952	025520'	046040	047517	041120		
5953	025526'	041501	020113	042524		
5954	025534'	052123	026440	020040		
5955	025542'	040515	041524	020110		
5956	025550'	044502	020124	051105		
5957	025556'	047522	000122			
5958	025562'	047514	040503	020114	MSG37::	.ASCIZ /LOCAL LOOPBACK TEST - TDR ERROR/
5959	025570'	047514	050117	040502		
5960	025576'	045503	052040	051505		
5961	025604'	020124	020055	052040		
5962	025612'	051104	042440	051122		
5963	025620'	051117	000			
5964	025623'	124	040522	051516	MSG40::	.ASCIZ /TRANSMITTER BUFFER ADDRESS TEST - TRANSMITTER TIMEOUT/
5965	025630'	044515	052124	051105		
5966	025636'	041040	043125	042506		
5967	025644'	020122	042101	051104		
5968	025652'	051505	020123	042524		
5969	025660'	052123	026440	052040		
5970	025666'	040522	051516	044515		
5971	025674'	052124	051105	052040		
5972	025702'	046511	047505	052125		
5973	025710'	000				
5974	025711'	124	040522	051516	MSG41::	.ASCIZ /TRANSMITTER BUFFER ADDRESS TEST - RECEIVER TIMEOUT/
5975	025716'	044515	052124	051105		
5976	025724'	041040	043125	042506		
5977	025732'	020122	042101	051104		
5978	025740'	051505	020123	042524		
5979	025746'	052123	026440	051040		
5980	025754'	041505	044505	042526		
5981	025762'	020122	044524	042515		
5982	025770'	052517	000124			
5983	025774'	051124	047101	046523	MSG42::	.ASCIZ /TRANSMITTER BUFFER ADDRESS TEST - BUFFER COMPARISON/
5984	026002'	052111	042524	020122		
5985	026010'	052502	043106	051105		
5986	026016'	040440	042104	042522		
5987	026024'	051523	052040	051505		
5988	026032'	020124	020055	052502		
5989	026040'	043106	051105	041440		
5990	026046'	046517	040520	051522		
5991	026054'	047511	000116			
5992	026060'	051124	047101	046523	MSG43::	.ASCIZ /TRANSMITTER BUFFER ADDRESS TEST - BYTE COUNT/
5993	026066'	052111	042524	020122		
5994	026074'	052502	043106	051105		

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5995	026102'	040440	042104	042522		
5996	026110'	051523	052040	051505		
5997	026116'	020124	020055	054502		
5998	026124'	042524	041440	052517		
5999	026132'	052116	000			
6000	026135'	124	040522	051516	MSG44::	.ASCIZ /TRANSMITTER BUFFER ADDRESS TEST - RECEIVER STATUS/
6001	026142'	044515	052124	051105		
6002	026150'	041040	043125	042506		
6003	026156'	020122	042101	051104		
6004	026164'	051505	020123	042524		
6005	026172'	052123	026440	051040		
6006	026200'	041505	044505	042526		
6007	026206'	020122	052123	052101		
6008	026214'	051525	000			
6009	026217'	124	040522	051516	MSG45::	.ASCIZ /TRANSMITTER BUFFER ADDRESS TEST - CRC ERROR/
6010	026224'	044515	052124	051105		
6011	026232'	041040	043125	042506		
6012	026240'	020122	042101	051104		
6013	026246'	051505	020123	042524		
6014	026254'	052123	026440	041440		
6015	026262'	041522	042440	051122		
6016	026270'	051117	000			
6017	026273'	125	042116	043105	MSG46::	.ASCIZ /UNDEFINED/
6018	026300'	047111	042105	000		
6019	026305'	125	042116	043105	MSG47::	.ASCIZ /UNDEFINED/
6020	026312'	047111	042105	000		
6021	026317'	122	041505	044505	MSG50::	.ASCIZ /RECEIVER BUFFER ADDRESS TEST - TRANSMITTER TIMEOUT/
6022	026324'	042526	020122	052502		
6023	026332'	043106	051105	040440		
6024	026340'	042104	042522	051523		
6025	026346'	052040	051505	020124		
6026	026354'	020055	052040	040522		
6027	026362'	051516	044515	052124		
6028	026370'	051105	052040	046511		
6029	026376'	047505	052125	000		
6030	026403'	122	041505	044505	MSG51::	.ASCIZ /RECEIVER BUFFER ADDRESS TEST - RECEIVER TIMEOUT/
6031	026410'	042526	020122	052502		
6032	026416'	043106	051105	040440		
6033	026424'	042104	042522	051523		
6034	026432'	052040	051505	020124		
6035	026440'	020055	051040	041505		
6036	026446'	044505	042526	020122		
6037	026454'	044524	042515	052517		
6038	026462'	000124				
6039	026464'	042522	042503	053111	MSG52::	.ASCIZ /RECEIVER BUFFER ADDRESS TEST - BUFFER COMPARISON/
6040	026472'	051105	041040	043125		
6041	026500'	042506	020122	042101		
6042	026506'	051104	051505	020123		
6043	026514'	042524	052123	026440		
6044	026522'	020040	052502	043106		
6045	026530'	051105	041440	046517		
6046	026536'	040520	051522	047511		
6047	026544'	000116				
6048	026546'	042522	042503	053111	MSG53::	.ASCIZ /RECEIVER BUFFER ADDRESS TEST - BYTE COUNT/
6049	026554'	051105	041040	043125		
6050	026562'	042506	020122	042101		

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6051	026570	051104	051505	020123		
6052	026576	042524	052123	026440		
6053	026604	020040	054502	042524		
6054	026612	041440	052517	052116		
6055	026620	000				
6056	026621	122	041505	044505	SMSG54::	.ASCIZ /RECEIVER BUFFER ADDRESS TEST - RECEIVER STATUS/
6057	026626	042526	020122	052502		
6058	026634	043106	051105	040440		
6059	026642	042104	042522	051523		
6060	026650	052040	051505	020124		
6061	026656	020055	051040	041505		
6062	026664	044505	042526	020122		
6063	026672	052123	052101	051525		
6064	026700	000				
6065	026701	122	041505	044505	SMSG55::	.ASCIZ /RECEIVER BUFFER ADDRESS TEST - CRC ERROR/
6066	026706	042526	020122	052502		
6067	026714	043106	051105	040440		
6068	026722	042104	042522	051523		
6069	026730	052040	051505	020124		
6070	026736	020055	041440	041522		
6071	026744	042440	051122	051117		
6072	026752	000				
6073	026753	125	042116	043105	SMSG56::	.ASCIZ /UNDEFINED/
6074	026760	047111	042105	000		
6075	026765	125	042116	043105	SMSG57::	.ASCIZ /UNDEFINED/
6076	026772	047111	042105	000		
6077	026777	103	041522	052040	SMSG60::	.ASCIZ /CRC TEST/
6078	027004	051505	000124			
6079	027010	052522	052116	050040	SMSG61::	.ASCIZ /RUNT PACKET TEST/
6080	027016	041501	042513	020124		
6081	027024	042524	052123	000		
6082	027031	125	042116	043105	SMSG62::	.ASCIZ /UNDEFINED/
6083	027036	047111	042105	000		
6084	027043	117	042526	051522	SMSG63::	.ASCIZ /OVERSIZE PACKET TEST/
6085	027050	055111	020105	040520		
6086	027056	045503	052105	052040		
6087	027064	051505	000124			
6088	027070	047125	042504	044506	SMSG64::	.ASCIZ /UNDEFINED/
6089	027076	042516	000104			
6090	027102	047503	046114	051511	SMSG65::	.ASCIZ /COLLISION TEST/
6091	027110	047511	020116	042524		
6092	027116	052123	000			
6093	027121	125	042116	043105	SMSG66::	.ASCIZ /UNDEFINED/
6094	027126	047111	042105	000		
6095	027133	125	042116	043105	SMSG67::	.ASCIZ /UNDEFINED/
6096	027140	047111	042105	000		
6097	027145	115	046125	044524	SMSG70::	.ASCIZ /MULTICAST TEST/
6098	027152	040503	052123	052040		
6099	027160	051505	000124			
6100	027164	042101	051104	051505	SMSG71::	.ASCIZ /ADDRESS RECOGNITION TEST/
6101	027172	020123	042522	047503		
6102	027200	047107	052111	047511		
6103	027206	020116	042524	052123		
6104	027214	000				
6105	027215	105	052130	051105	SMSG72::	.ASCIZ /EXTERNAL LOOPBACK/
6106	027222	040516	020114	047514		

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6107	027230'	050117	040502	045503		
6108	027236'	000				
6109	027237'	125	042116	043105	MSG73::	.ASCIZ /UNDEFINED/
6110	027244'	047111	042105	000		
6111	027251'	125	042116	043105	MSG74::	.ASCIZ /UNDEFINED/
6112	027256'	047111	042105	000		
6113	027263'	125	042116	043105	MSG75::	.ASCIZ /UNDEFINED/
6114	027270'	047111	042105	000		
6115	027275'	125	042116	043105	MSG76::	.ASCIZ /UNDEFINED/
6116	027302'	047111	042105	000		
6117	027307'	116	053105	051105	MSG77::	.ASCIZ /NEVER GOT STARTED/
6118	027314'	043440	052117	051440		
6119	027322'	040524	052122	042105		
6120	027330'	000				
6121	027332'					.EVEN

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TEST 10: DEUNA ROM DUMP TEST

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.SBTTL TEST 10: DEUNA ROM DUMP TEST

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: THIS TEST WILL VERIFY THAT THE DATA PATH FROM THE T11 PROCESSOR
: TO THE UNIBUS INTERFACE IS INTACT AND ABLE TO TRANSFER DATA RELIABLY.
: THIS DATA PATH IS CRUCIAL FOR FURTHER TESTING BECAUSE IT IS NECESSARY
: FOR LOADING REPAIR-LEVEL DIAGNOSTICS INTO THE WCS.

: THE TEST STRATEGY IS TO TRANSFER KNOWN DATA OVER THE DATA PATH AND TO
: VERIFY THE TRANSFERRED DATA.

: THE DATA SOURCE FOR THE DUMP TEST IS THE ROM MICROCODE RESIDENT ON THE
: DEUNA PORT BOARD. A DUMP OF THE ROM WILL EXERCISE THE DATA PATH NEEDED
: FOR LOADING WCS AND THE ROM CONTENTS CAN BE VERIFIED. THE ROM MICROCODE
: WILL BE CHECKED BY VERIFYING THE CRC BYTES. THE CRC BYTES CHARACTERIZE
: THE DATA CONTENTS OF THE ROM AND ARE BURNED INTO THE ROM AT THE TIME OF
: MANUFACTURE. A FAILURE TO VERIFY THE CRC CALCULATION ON THE DUMPED ROM
: DATA DUMP WILL BE INTERPRETED AS AN ERROR IN THE DATA PATH.

: TEST SEQUENCE:

- 1-WRITE PCSR2 AND PCSR3 WITH THE ADDRESS OF THE PORT CONTROL BLOCK
- 2-WRITE <GET PCBB> PORT COMMAND TO PCSRO
- 3-READ PCSRO AND VERIFY DNI BIT SET
- 4-WRITE PCSRO DNI BIT TO RESET IT
- 5-FILL MEMORY BUFFER WITH A BACKGROUND PATTERN
- 6-WRITE PORT CONTROL BLOCK WITH 'DUMP INTERNAL MEMORY' FUNCTION CODE
- 7-WRITE PORT CONTROL BLOCK WITH UNIBUS DATA BLOCK BASE ADDRESS
- 8-WRITE DATA BLOCK LENGTH TO UNIBUS DATA BLOCK
- 9-WRITE MEMORY BUFFER ADDRESS TO UNIBUS DATA BLOCK
- 10-WRITE INTERNAL DATA BLOCK ADDRESS TO UNIBUS DATA BLOCK
- 11-WRITE <GET CMD> PORT COMMAND TO PCSRO
- 12-READ PCSRO AND VERIFY DNI SET
- 13-CALCULATE CRC ON DUMPED DATA
- 14-REPEAT STEPS 5-13 ON EACH 1K OF ROM
- 15-VERIFY CRC =0

.....

BGNTST

BGNSUB :#1

T10::

T10.1:

TRAP C\$BSUB

: LOAD PCSR2+3; ISSUE GET PORT CONTROL BLOCK PORT COMMAND; AND WAIT FOR 'DNI'

BGNSEG

TRAP C\$BSEG

```

MOV #PCBB,@PCSR2 ;LOAD PCSR2+3 WITH PORT CONTROL BLOCK..
CLR @PCSR3 ;ADDRESS
MOV #GETPCB,@PCSRO ;ISSUE GET PORT CONTROL BLOCK COMMAND
MOV #2*SECOND,METER ;PUT SOME TIME IN THE METER
JSR PC_CHKDNI ;WAIT FOR DNI IO SET
BCC 20$ ;OK

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027332'
027332'
027332'
027332' 104402
027334'
027334' 104404
027336' 012777 000604' 150776
027344' 005077 150774
027350' 012777 000001 150760
027356' 012737 000176 000332'
027364' 004737 017330'
027370' 103022

```

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6178                                     ;ERROR DNI DID NOT SET!
6179 027372' 012737 000776' 000310'      MOV    #SDNI,BITNAM      ;NO, FORMAT ERROR MESSAGE
6180 027400' 012737 001275' 000312'      MOV    #SNSET,BITSTA
6181 027406' 012737 001340' 000314'      MOV    #SAFTER,PWHEN
6182 027414' 012737 001346' 000316'      MOV    #SGTPCB,PCOMND
6183 027422'                                     ERRHRD 030,ROMDMP,MSG1 ;PRINT ERROR MESSAGE
6184 027422' 104456                                     TRAP  C$ERHRD
6185 027424' 000036                                     .WORD 30
6186 027426' 002042'                                     .WORD ROMDMP
6187 027430' 012750'                                     .WORD MSG1
6188 027432'                                     ESCAPE TST      ;CAN NOT CONTINUE TESTING
6189 027432' 104410                                     TRAP  C$ESCAPE
6190 027434' 000310                                     .WORD L10120-
6191 027436'
6192 027436'
6193 027436'
6194 027436' 104405                                     10000$: TRAP  C$ESEG
6  25 027440'                                     BGNSEGE
6196 027440' 104404                                     TRAP  C$BSEG
6197
6198                                     ;WRITE ONE TO CLEAR 'DNI'
6199
6200 027442' 004737 017374'      JSR    PC,CLRDN1      ;GO CLEAR DNI
6201 027446' 103004      BCC    25$            ;OK
6202
6203 027450'      ERRHRD 031,ROMDMP,RACMG7      ;ERROR DNI FAILED TO CLEAR!
6204 027450' 104456                                     ;PRINT ERROR MESSAGE
6205 027452' 000037                                     TRAP  C$ERHRD
6206 027454' 002042'                                     .WORD 31
6207 027456' 012726'                                     .WORD ROMDMP
6208 027460'                                     .WORD RACMG7
6209 027460'
6210 027460'
6211 027460' 104405                                     10001$: TRAP  C$ESEG
6212 027462'      ENDSUB ;#1
6213 027462'
6214 027462' 104403                                     L10121: TRAP  C$ESUB
6215 027464'      BGNSUB ;#2
6216 027464'
6217 027464' 104402                                     T10.2:  TRAP  C$BSUB
6218
6219                                     ;FILL BUFFER WITH PATTERN; FORMAT PCBB AND UDBB; ISSUE GET COMMAND PORT COMMAND
6220                                     ;AND WAIT FOR 'DNI'
6221
6222 027466' 012737 000020 000604'      MOV    #DIM,PCBB      ;LOAD PCB WITH DUMP INTERNAL MEMORY FUNCTION
6223 027474' 012737 000614' 000606'      MOV    #UDBB,PCBB+2  ;LOAD UNIBUS DATA BLOCK BASE ADDRESS...
6224 027502' 005037 000610'      CLR    PCBB+4        ;INTO PCB
6225 027506' 012737 040000 000622'      MOV    #ROMADR,UDBB+6 ;INTERNAL BASE ADDRESS OF ROM
6226 027514' 012705 000010      MOV    #8.,R5        ;DUMP ROM IN 8. 1K CHUNKS
6227 027520' 005004      CLR    R4            ;INITIAL CRC VALUE
6228 027522'
6229 027522'
6230 027522' 104404                                     26$: BGNSEGE
6231 027524' 013701 000324'      MOV    FREMEM,R1     ;GET POINTER TO BUFFER
6232 027530' 012702 002000      MOV    #SIZ1K/2,R2  ;GET 1K WORD COUNT
6233 027534' 012703 000516'      MOV    #PAT1,R3     ;GET A BACKGROUND PATTERN

```

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6234 027540' 011321      27$:  MOV      (R3),(R1)+      ;FILL BUFFER WITH BACKGROUND PATTERN
6235 027542' 005302      DEC      R2
6236 027544' 001375      BNE     27$
6237 027546' 012737 004000 000614'  MOV     #ROMSIZ/8,UDBB      ;1K BYTE COUNT
6238 027554' 013737 000324' 000616'  MOV     FREMEM,UDBB+2      ;AND BUFFER ADDRESS
6239 027562' 005037 000620'      CLR     UDBB+4
6240 027566' 012737 000176 000332'  MOV     #2*SECOND,METER    ;PUT SOME TIME ON THE METER
6241 027574' 012777 000002 150534  MOV     #GETCMD,@PCSR0     ;LOAD 'GET COMMAND' PORT COMMAND
6242 027602' 004737 017330'  JSR     PC,CHKDNI          ;WAIT FOR DNI TO SET
6243 027606' 103022      BCC     30$                ;OK
6244
6245 027610' 012737 000776' 000310'  MOV     #$DNI,BITNAM       ;ERROR, DNI DID NOT SET!
6246 027616' 012737 001275' 000312'  MOV     #$NSET,BITSTA      ;FORMAT ERROR MESSAGE
6247 027624' 012737 001340' 000314'  MOV     #$AFTER,PWHEN
6248 027632' 012737 001355' 000316'  MOV     #$GTCMD,PCOMND
6249 027640'      ERRHRD  032,ROMDMP,MSG1
6250 027640' 104456      TRAP   C$ERHRD
6251 027642' 000040      .WORD  32
6252 027644' 002042'      .WORD  ROMDMP
6253 027646' 012750'      .WORD  MSG1
6254 027650'      ESCAPE  TST
6255 027650' 104410      TRAP   C$ESCAPE
6256 027652' 000072      .WORD  L10120-
6257 027654'
6258 027654' 30$:  ENDSEG
6259 027654'      10000$:
6260 027654' 104405      TRAP   C$ESEG
6261 027656'      BGNSEG
6262 027656' 104404      TRAP   C$BSEG
6263
6264      ;WRITE ONE TO CLEAR 'DNI'
6265      ;
6266 027660' 004737 017374'  JSR     PC,CLRDN1          ;GO CLEAR DNI BIT
6267 027664' 103004      BCC     33$                ;OK
6268
6269      ERRHRD  033,ROMDMP,RACMG7 ;PRINT ERROR MESSAGE
6270 027666' 104456      TRAP   C$ERHRD
6271 027670' 000041      .WORD  33
6272 027672' 002042'      .WORD  ROMDMP
6273 027674' 012726'      .WORD  RACMG7
6274 027676'
6275 027676' 33$:  ENDSEG
6276 027676'      10001$:
6277 027676' 104405      TRAP   C$ESEG
6278
6279      ;CALCULATE CRC ON 1K OF DATA DUMPED FROM ROM
6280      ;
6281 027700' 013700 000324'  MOV     FREMEM,R0          ;GET BUFFER ADDRESS
6282 027704' 012702 004000  MOV     #SIZ1K,R2          ;GET BYTE COUNT
6283 027710' 004737 017042'  JSR     PC,CRC16           ;CALC CRC ON 1K BUFFER
6284 027714' 062737 004000 000622'  ADD     #ROMSIZ/8,UDBB+6   ;POINT TO NEXT 1K OF ROM
6285 027722' 005305      DEC     R5                 ;HAVE WE DONE ALL 8k?
6286 027724' 001276      BNE     26$                ;NO
6287 027726' 005704      TST     R4                 ;YES, IS CRC = 0?
6288 027730' 001404      BEQ     40$                ;YES, OK
6289 027732'      ERRHRD  034,ROMDMP,MSG2 ;PRINT ERROR MESSAGE

```

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6290 027732' 104456
6291 027734' 000042
6292 027736' 002042'
6293 027740' 013012'
6294 027742'
6295 027742'
6296 027742'
6297 027742' 104403
6298 027744'
6299 027744'
6300 027744' 104401
6301
6302
6303

408:
E1.DSUB ;#2

ENDTST

TRAP C\$ERHRD
.WORD 34
.WORD ROMDMP
.WORD MSG2

L10122: TRAP C\$ESUB

L10120: TRAP C\$ETST

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TEST 10: DEUNA ROM DUMP TEST

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.SBTTL TEST 11: WCS LOAD/DUMP TEST

```

:*****
:THIS TEST WILL USE THE LOAD/DUMP PORT COMMAND TO VERIFY THE DATA
:PATHWAY TO/FROM THE WCS. PATTERNS WILL BE USED TO CHECK THE DATA PATHWAY
:FOR ALL SA0 AND SA1 ERRORS.
:BECAUSE THE OPERATIONAL MICROCODE NEEDS THE LOWER 2K OF WCS ONLY THE TOP HALF
:OF WCS WILL BE LOADED WITH A DATA PATTERN THEN DUMPED BACK
:TO MEMORY FOR VERIFICATION. THIS PROCEDURE WILL BE REPEATED FOR ALL PATTERNS.
:TEST SEQUENCE:
: 1-FORMAT UNIBUS DATA BLOCK WITH NUMBER OF WORDS, WCS DESTINATION
:   ADDRESS, AND SOURCE BUFFER ADDRESS.
: 2-FILL SOURCE BUFFER WITH DATA PATTERN
: 3-WRITE PORT CONTROL BLOCK WITH ADDRESS OF UNIBUS DATA BLOCK
:   AND WITH THE 'LOAD INTERNAL MEMORY' FUNCTION CODE
: 4-WRITE PCSR2 AND PCSR3 WITH PORT CONTROL BLOCK ADDRESS
: 5-WRITE PCSRO WITH <GET CMD> PORT COMMAND
: 6-READ PCSRO TO VERIFY DNI SET
: 7-WRITE ONE TO CLEAR DNI
: 8-FILL DESTINATION BUFFER WITH ZEROS
: 9-WRITE PORT CONTROL BLOCK WITH 'DUMP INTERNAL MEMORY'
:   FUNCTION CODE.
:10-WRITE PCSRO WITH <GET CMD> PORT COMMAND
:11-VERIFY DNI SET
:12-WRITE ONE TO CLEAR DNI
:13-COMPARE DESTINATION BUFFER WITH DATA PATTERN
:14-REPEAT STEPS 1-13 FOR ALL DATA PATTERNS
:*****

```

BGNTST

```

:          T11::
BGNSUB   MOV      #PATERN,R4          ;POINT TO LIST OF DATA PATTERNS
:          T11.1:
:          TRAP   C$BSUB
:
:LOAD WCS
:
:          BGNSEG
:          TRAP   C$BSEG
:
:          ;FILL SOURCE BUFFER; FORMAT UDBB AND PCBB; ISSUE GET PORT CONTROL BLOCK PORT
:          ;COMMAND AND WAIT FOR 'DNI'
:
:          MOV      FREMEM,R2          ;GET UNIBUS BUFFER ADDRESS
:          MOV      #WCSSIZ/4,R3      ;GET HALF SIZE OF WCS IN WORDS
10$:     MOV      (R4),(R2)+          ;FILL BUFFER WITH DATA PATTERN
:          DEC      R3
:          BNE     10$
:
:          ;SETUP UNIBUS DATA BLOCK

```

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6360	027774'	012737	010000	000614'	MOV	#WCSSIZ/2,UDBB	:BYTE COUNT		
6361	030002'	013737	000324'	000616'	MOV	FREMEM,UDBB+2	:BUFFER ADDRESS		
6362	030010'	005037	000620'		CLR	UDBB+4			
6363	030014'	012737	010000	000622'	MOV	#WCSADR+<WCSSIZ/2>,UDBB+6	:BASE ADDRESS OF TOP HALF OF WCS		
6364							:SETUP PORT CONTROL BLOCK		
6365	030022'	012737	000021	000604'	MOV	#LIM,PCBB	: 'LOAD INTERNAL MEMORY' FUNCTION		
6366	030030'	012737	000614'	000606'	MOV	#UDBB,PCBB+2	:ADDRESS OF UNIBUS DATA BLOCK		
6367	030036'	005037	000610'		CLR	PCBB+4			
6368	030042'	012777	000604'	150272	MOV	#PCBB,@PCSR2	:LOAD PCSR2+3 WITH PCB ADDRESS		
6369	030050'	005077	150270		CLR	@PCSR3			
6370									
6371	030054'	012777	000001	150254	MOV	#GETPCB,@PCSR0	:ISSUE 'GET PORT CONTROL BLOCK' PORT COMMAND		
6372	030062'	012737	000176	000332'	MOV	#2*SECOND,METER	:PUT SOME TIME ON THE METER		
6373	030070'	004737	017330'		JSR	PC,CHKDNI	:WAIT FOR DNI TO SET		
6374	030074'	103022			BCC	20\$:OK		
6375							:ERROR DNI DID NOT SET!		
6376	030076'	012737	000776'	000310'	MOV	#SDNI,BITNAM	:POINT TO 'DNI' ASCII STRING		
6377	030104'	012737	001275'	000312'	MOV	#NSET,BITSTA	:POINT TO 'NOT SET' ASCII STRING		
6378	030112'	012737	001340'	000314'	MOV	#AFTER,PWHEN	:POINT TO 'AFTER' ASCII STRING		
6379	030120'	012737	001346'	000316'	MOV	#SGTPCB,PCOMND	:POINT TO 'GET PORT CONTROL BLOCK' ASCII STRING		
6380	030126'				ERRHRD	035,DATAID,MSG1	:PRINT ERROR MESSAGE		
6381	030126'	104456					TRAP	C\$ERHRD	
6382	030130'	000043					.WORD	35	
6383	030132'	002067'					.WORD	DATAID	
6384	030134'	012750'					.WORD	MSG1	
6385	030136'				ESCAPE	TST			
6386	030136'	104410					TRAP	C\$ESCAPE	
6387	030140'	000372					.WORD	L10123-	
6388	030142'				20\$:				
6389	030142'				ENDSEG				
6390	030142'						10000\$:		
6391	030142'	104405					TRAP	C\$ESEG	
6392	030144'				BGNSEG				
6393	030144'	104404					TRAP	C\$BSEG	
6394					:				
6395					:WRITE ONE TO CLEAR 'DNI'				
6396					:				
6397	030146'	004737	017374'		JSR	PC,CLRDN1	:GO CLEAR DNI		
6398	030152'	103004			BCC	25\$:OK		
6399							:ERROR DNI FAILED TO CLEAR		
6400	030154'				ERRHRD	036,DATAID,RACMG7	:PRINT ERROR MESSAGE		
6401	030154'	104456					TRAP	C\$ERHRD	
6402	030156'	000044					.WORD	36	
6403	030160'	002067'					.WORD	DATAID	
6404	030162'	012726'					.WORD	RACMG7	
6405	030164'				25\$:				
6406	030164'				ENDSEG				
6407	030164'						10001\$:		
6408	030164'	104405					TRAP	C\$ESEC	
6409	030166'				BGNSEG				
6410	030166'	104404					TRAP	C\$BSEG	
6411					:				
6412					:ISSUE GET COMMAND PORT COMMAND AND WAIT FOR 'DNI'				
6413					:				
6414	030170'	012777	000002	150140	MOV	#GETCMD,@PCSR0	:ISSUE GET COMMAND PORT COMMAND		
6415	030176'	012737	000275	000332'	MOV	#3*SECOND,METER	:PUT SOME TIME ON THE METER		

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6416 030204' 004737 017330'      JSR    PC_CHKDNI      ;GO WAIT FOR DNI
6417 030210' 103022                BCC    30$           ;OK
6418                                ;ERROR DNI DID NOT SET!
6419 030212' 012737 000776' 000310'  MOV    #SDNI,BITNAM
6420 030220' 012737 001275' 000312'  MOV    #SNSET,BITSTA
6421 030226' 012737 001340' 000314'  MOV    #SAFTER,PWHEN
6422 030234' 012737 001355' 000316'  MOV    #SGTCMD,PCOMND
6423 030242'                                ERRHRD 037,DATA LD,MSG1 ;PRINT ERROR MESSAGE
6424 030242' 104456                                TRAP   C$ERHRD
6425 030244' 000045                                .WORD 37
6426 030246' 002067'                                .WORD DATA LD
6427 030250' 012750'                                .WORD MSG1
6428 030252'                                ESCAPE TST
6429 030252' 104410                                TRAP   C$ESCAPE
6430 030254' 000256                                .WORD L10123-
6431 030256' 30$:
6432 030256'     ENDSEG
6433 030256'                                10002$:
6434 030256' 104405                                TRAP   C$ESEG
6435 030260'                                BGNSEG
6436 030260' 104404                                TRAP   C$BSEG
6437
6438 ;WRITE ONE TO CLEAR 'DNI'
6439 ;
6440 030262' 004737 017374'      JSR    PC_CLRDN1      ;GO CLEAR DNI
6441 030266' 103004                BCC    35$           ;OK
6442                                ;ERROR DNI FAILED TO CLEAR
6443                                ;PRINT ERROR MESSAGE
6444 030270' 104456                                ERRHRD 038,DATA LD,RACMG7 ;PRINT ERROR MESSAGE
6445 030272' 000046                                TRAP   C$ERHRD
6446 030274' 002067'                                .WORD 38
6447 030276' 012726'                                .WORD DATA LD
6448 030300'                                .WORD RACMG7
6449 030300' 35$:
6450 030300'     ENDSEG
6451 030300' 104405                                10003$:
6452 030302' 000000'                                TRAP   C$ESEG
6453 030302' 000000'                                ENDSUB ;#1
6454 030302' 104403                                L10124:
6455 030304' 000000'                                TRAP   C$ESUB
6456 030304' 000000'                                BGNSUB ;#2
6457 030304' 104402                                T11 2:
6458                                TRAP   C$BSUB
6459 ;DUMP WCS
6460 ;
6461 030306' 000000'                                BGNSEG
6462 030306' 104404                                TRAP   C$BSEG
6463 ;CLEAR DESTINATION BUFFER; FORMAT PCBB AND UDBB; ISSUE GET COMMAND PORT COMMAND
6464 ;AND WAIT FOR 'DNI'
6465 ;
6466 ;
6467 030310' 013702 000324'      MOV    FREMEM,R2      ;GET ADDRESS OF FREE MEMORY
6468 030314' 012703 004000'      MOV    #WCSSIZ/4,R3  ;SIZE IN WORDS OF BUFFER
6469 030320' 005022                CLR    (R2)+          ;FILL DESTINATION BUFFER WITH ZEROS
6470 030322' 005303                DEC    R3
6471 030324' 001375                BNE   40$

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6472
6473 030326' 013737 000324' 000616'      MOV      FREMEM,UDBB+2
6474 030334' 005037 000620'          CLR      UDBB+4
6475 030340' 012737 000020 000604'      MOV      #DIM.PCBB          ;LOAD 'DUMP INTERNAL MEMORY' FUNCTION
6476 030346' 012777 000002 147762      MOV      #GETCMD,@PCSR0    ;ISSUE GET COMMAND PORT COMMAND
6477 030354' 012737 000275 000332'      MOV      #3*SECOND,METER  ;PUT SOME TIME ON THE METER
6478 030362' 004737 017330'      JSR      PC,CHKDNI         ;WAIT FOR DNI TO SET
6479 030366' 103022          BCC      50$              ;OK
6480 030370' 012737 000776' 000310'      MOV      #SDNI,BITNAM
6481 030376' 012737 001275' 000312'      MOV      #NSET,BITSTA
6482 030404' 012737 001340' 000314'      MOV      #AFTER,PWHEN
6483 030412' 012737 001355' 000316'      MOV      #SGTCMD,PCOMND
6484 030420'          ERRHRD   039,DATA1D,MSG1
6485 030420' 104456          TRAP    C$ERHRD
6486 030422' 000047          .WORD  39
6487 030424' 002067'          .WORD  DATA1D
6488 030426' 012750'          .WORD  MSG1
6489 030430'          ESCAPE TST
6490 030430' 104410          TRAP    C$ESCAPE
6491 030432' 000100          .WORD  L10123-.
6492 030434'          50$:
6493 030434'          ENDSEG
6494 030434'          10000$:
6495 030434' 104405          TRAP    C$ESEG
6496 030436'          BGNSEG
6497 030436' 104404          TRAP    C$BSEG
6498 030440' 004737 017374'      JSR      PC,CLRDN1        ;GO CLEAR DNI
6499 030444' 103004          BCC      60$              ;OK
6500          ERRHRD   040,DATA1D,RACMG7 ;ERROR DNI FAILED TO CLEAR
6501 030446'          ;PRINT ERROR MESSAGE
6502 030446' 104456          TRAP    C$ERHRD
6503 030450' 000050          .WORD  40
6504 030452' 002067'          .WORD  DATA1D
6505 030454' 012726'          .WORD  RACMG7
6506 030456'          60$:
6507 030456'          ENDSEG
6508 030456'          10001$:
6509 030456' 104405          TRAP    C$ESEG
6510          ;
6511          ;COMPARE DUMPED DATA TO WRITTEN PATTERN
6512          ;
6513 030460' 013701 000324'      MOV      FREMEM,R1        ;SOURCE BUFFER ADDRESS
6514 030464' 012703 004000      MOV      #<WCSSIZ/2>/2,R3 ;# OF WORDS TO COMPARE
6515 030470' 021114          70$:  CMP      (R1),(R4)       ;IS WHAT WAS LOADED SAME AS...
6516          ;WHAT WAS DUMPED?
6517 030472' 001404          BEQ      80$              ;YES
6518 030474'          ERRHRD   041,DATA1D,MSG5 ;ERROR DATA COMPARE
6519 030474' 104456          TRAP    C$ERHRD
6520 030476' 000051          .WORD  41
6521 030500' 002067'          .WORD  DATA1D
6522 030502' 013142'          .WORD  MSG5
6523 030504' 005721          80$:  TST      (R1)+          ;POINT TO NEXT LOCATION
6524 030506' 005303          DEC      R3              ;WE DONE ALL WORDS?
6525 030510' 001367          BNE      70$              ;NOT YET
6526 030512'          ENDSUB ;#2
6527 030512'          L10125:

```

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TEST 11: WCS LOAD/DUMP TEST

6528 030512' 104403
6529
6530
6531
6532 030514' 062704 000002
6533 030520' 020427 000530'
6534 030524' 001402
6535 030526' 000137 027752'
6536 030532'
6537 030532'
6538 030532'
6539 030532' 104401

TRAP C\$ESUB

:
:CHECK TO SEE IF ALL PATTERNS HAVE BEEN RUN THROUGH
:

ADD #2,R4
CMP R4,#PAT6
BEQ 90\$
JMP T11.1

:POINT TO NEXT DATA PATTERN
:WE DONE ALL DATA PATTERNS?
:YES END OF TEST
:NO CONTINUE WITH NEW DATA PATTERN

90\$:
ENDTST

L10123:
TRAP C\$ETST

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.SBTTL TEST 12: LOAD AND START FUNCTION TEST

: THIS TEST WILL VERIFY THAT THE LOAD AND START MICROADDRESS PORT COMMAND
: IS OPERATIONAL.

: THE PROCESS IS TO LOAD WCS WITH MICROCODE THAT WHEN STARTED WILL WRITE
: A PATTERN OF DATA TO THE LITE-BYTE FIELD OF PCSR1 REGISTER WHICH CAN BE READ
: FROM THE UNIBUS AND BE VERIFIED.

: NOTE: THIS TEST USES MICROCODE MODULE 'A'

: TEST SEQUENCE:

- 1-FORMAT UNIBUS DATA BLOCK WITH BYTE COUNT, WCS DESTINATION ADDRESS AND UNIBUS SOURCE ADDRESS OF THE MICROCODE.
- 2-WRITE PORT CONTROL BLOCK WITH ADDRESS OF UNIBUS DATA BLOCK AND LOAD INTERNAL MEMORY FUNCTION CODE.
- 3-WRITE PCSR2 AND PCSR3 WITH ADDRESS OF THE PORT CONTROL BLOCK.
- 4-WRITE PCSRO WITH <GET PCB> PORT COMMAND
- 5-READ PCSRO AND VERIFY 'DNI'
- 6-WRITE PCSRO 'DNI' TO CLEAR
- 7-WRITE PCSRO WITH <GET COMMAND> PORT COMMAND
- 8-READ PCSRO AND VERIFY 'DNI'
- 9-WRITE PCSRO 'DNI' TO CLEAR
- 10-WRITE PORT CONTROL BLOCK WITH START ADDRESS OF WCS MICROCODE AND WITH LOAD AND START FUNCTION CODE
- 11-WRITE PCSRO WITH <GET COMMAND> PORT COMMAND
- 12-READ PCSRO AND VERIFY 'DNI' SET
- 13-READ PCSR1 AND VERIFY BITS 13:8 ARE PATTERN WRITTEN BY MICROCODE
- 14-ISSUE RESET TO PCSRO TO RESTART OPERATIONAL MICROCODE
- 15-READ PCSRO AND VERIFY 'DNI' SET
- 16-WRITE PCSRO WITH 'DNI' TO CLEAR

BGNTST

T12::

: LOAD MICROCODE MODULE 'A' INTO THE TOP HALF OF WCS

BGNSEG

TRAP C\$BSEG

: FORMAT THE UNIBUS DATA BLOCK AND THE PORT CONTROL BLOCK FOR THE LOAD
: INTERNAL MEMORY FUNCTION

```

:SETUP UDBB
MOV MICASZ,UDBB ;SIZE OF MICROCODE MODULE TO LOAD
MOV #MICROA,UDBB+2 ;BASE ADDRESS OF MICROCODE MODULE
CLR UDBB+4
MOV #WCSADR+<WCSSIZ/2>,UDBB+6 ;LOAD INTO TOP HALF OF WCS

:SETUP PCB
MOV #LIM,PCBB ;'LOAD INTERNAL MEMORY' FUNCTION

```

030534'
030534'

030534'
030534' 104404

030536' 013737 000000G 000614'
030544' 012737 000000G 000616'
030552' 005037 000620'
030556' 012737 010000 000622'

030564' 012737 000021 000604'

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6596 030572' 012737 000614' 000606'      MOV      #UDBB,PCBB+2      ;SET ADDRESS OF UDBB
6597 030600' 005037 000610'          CLR      PCBB+4
6598 030604' 012777 000604' 147530      MOV      #PCBB,@PCSR2     ;TELL DEUNA WHERE PCBB IS
6599 030612' 005077 147526          CLR      @PCSR3
6600
6601 030616' 012777 000001 147512      MOV      #GETPCB,@PCSR0   ;ISSUE 'GET PCB' PORT COMMAND
6602 030624' 012737 000176 000332'      MOV      #2*SECOND,METER ;SETUP TIMER
6603 030632' 004737 017330'      JSR      PC,CHKDNI        ;WAIT FOR 'DNI' TO SET
6604 030636' 103022          BCC      10$             ;OK
6605
6606 030640' 012737 000776' 000310'      MOV      #$DNI,BITNAM     ;ERROR DNI NOT SET!
6607 030646' 012737 001275' 000312'      MOV      #$NSET,BITSTA    ;SET'JP ERROR MESSAGE
6608 030654' 012737 001340' 000314'      MOV      #$AFTER,PWHEN
6609 030662' 012737 001346' 000316'      MOV      #SGTPCB,PCOMND
6610 030670'          ERRHRD  042,LASFT,MSG1    ;PRINT ERROR MESSAGE
6611 030670' 104456          TRAP    C$ERHRD
6612 030672' 000052          .WORD  42
6613 030674' 002121'          .WORD  LASFT
6614 030676' 012750'          .WORD  MSG1
6615 030700'          ESCAPE  TST              ;NO POINT IN CONTINUING TEST
6616 030700' 104410          TRAP    C$ESCAPE
6617 030702' 000310          .WORD  L10126-.
6618 030704'
6619 030704'          10$:
6620 030704'          ENDSEG
6621 030704' 104405          10000$:
6622 030706'          TRAP    C$ESEG
6623 030706' 104404          TRAP    C$BSEG
6624
6625          ;WRITE ONE TO CLEAR 'DNI'
6626          ;
6627 030710' 004737 017374'      JSR      PC,CLRDN1        ;GO CLEAR 'DNI'
6628 030714' 103006          BCC      20$             ;OK
6629
6630          ERRHRD  043,LASFT,RACMG7 ;PRINT ERROR MESSAGE
6631 030716' 104456          TRAP    C$ERHRD
6632 030720' 000053          .WORD  43
6633 030722' 002121'          .WORD  LASFT
6634 030724' 012726'          .WORD  RACMG7
6635 030726'          ESCAPE  TST              ;DO NOT CONTINUE TEST
6636 030726' 104410          TRAP    C$ESCAPE
6637 030730' 000262          .WORD  L10126-.
6638 030732'
6639 030732'          20$:
6640 030732'          ENDSEG
6641 030732' 104405          10001$:
6642 030734'          TRAP    C$ESEG
6643 030734' 104404          TRAP    C$BSEG
6644
6645          ;NOW THAT THE UNA KNOWS WHERE THE MICROCODE IS, ISSUE THE GET COMMAND PORT
6646          ;COMMAND SO THE MICROCODE CAN GET LOADED INTO WCS
6647          ;
6648 030736' 012777 000002 147372      MOV      #GETCMD,@PCSR0   ;ISSUE <GET COMMAND> PORT COMMAND
6649 030744' 012737 000176 000332'      MOV      #2*SECOND,METER ;SETUP TIMER
6650 030752' 004737 017330'      JSR      PC,CHKDNI        ;WAIT FOR 'DNI'
6651 030756' 103022          BCC      30$             ;OK

```

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```

6652                                     ;ERROR 'DNI' NOT SET!
6653 030760' 012737 000776' 000310'      MOV    #SDNI,BITNAM      ;SETUP ERROR MESSAGE
6654 030766' 012737 001275' 000312'      MOV    #SNSFT,BITSTA
6655 030774' 012737 001340' 000314'      MOV    #SAFTER,PWHEN
6656 031002' 012737 001355' 000316'      MOV    #SGTCMD,PCOMND
6657 031010'                                     ERRHRD 044,LASFT,MSG1      ;PRINT ERROR MESSAGE
6658 031010' 104456                                     TRAP  C$ERHRD
6659 031012' 000054                                     .WORD 44
6660 031014' 002121'                                     .WORD LASFT
6661 031016' 012750'                                     .WORD MSG1
6662 031020'                                     ESCAPE TST
6663 031020' 104410                                     ;LEAVE TEST?
6664 031022' 000170                                     TRAP  C$ESCAPE
6665 031024'                                     .WORD L10126-
6666 031024'
6667 031024'                                     10002$:
6668 031024' 104405                                     TRAP  C$ESEG
6669 031026'                                     TRAP  C$BSEG
6670 031026' 104404
6671                                     ;WRITE ONE TO CLEAR 'DNI'
6672                                     ;
6673                                     ;
6674 031030' 004737 017374'      JSR    PC,CLRDN1      ;GC CLEAR 'DNI'
6675 031034' 103006      BCC    40$           ;OK
6676                                     ;ERROR 'DNI' NOT CLEAR
6677 031036'      ERRHRD 045,LASFT,RACMG7      ;PRINT ERROR MESSAGE
6678 031036' 104456                                     TRAP  C$ERHRD
6679 031040' 000055                                     .WORD 45
6680 031042' 002121'                                     .WORD LASFT
6681 031044' 012726'                                     .WORD RACMG7
6682 031046'                                     ESCAPE TST
6683 031046' 104410                                     ;DO NOT CONTINUE TEST
6684 031050' 000142                                     TRAP  C$ESCAPE
6685 031052'                                     .WORD L10126-
6686 031052'
6687 031052'                                     10003$:
6688 031052' 104405                                     TRAP  C$ESEG
6689                                     ;
6690                                     ;OK, MICROCODE MODULE 'A' IS LOADED INTO WCS. NOW START IT AND CHECK PCSRI.
6691                                     ;
6692                                     ;
6693                                     BGNSEG
6694 031054' 104404                                     TRAP  C$BSEG
6695 031056' 012737 000001 000604'      MOV    #LASM,PCBB      ;LOAD PCBB WITH 'LOAD AND START' FUNCTION CODE
6696 031064' 012737 010000 000606'      MOV    #WCSADR+<WCSsiz/2>,PCBB+2 ;STARTING MICROADDRESS
6697 031072' 012777 000002 147236'      MOV    #GETCMD,@PCSRO  ;ISSUE <GET COMMAND> PORT COMMAND
6698 031100' 012737 000176 000332'      MOV    #2*SECOND,METER ;SETUP TIMER
6699 031106' 004737 017330'      JSR    PC,CHKDNI      ;GO WAIT FOR 'DNI' TO SET
6700 031112' 103020      BCC    50$           ;OK
6701 031114' 012737 000776' 000310'      MOV    #SDNI,BITNAM      ;ERROR 'DNI' NOT SET!
6702 031122' 012737 001275' 000312'      MOV    #SNSFT,BITSTA      ;SETUP ERPOR MESSAGE
6703 031130' 012737 001340' 000314'      MOV    #SAFTER,PWHEN
6704 031136' 012737 001355' 000316'      MOV    #SGTCMD,PCOMND
6705 031144'                                     ERRHRD 046,LASFT,MSG1      ;PRINT ERROR MESSAGE
6706 031144' 104456                                     TRAP  C$ERHRD
6707 031146' 000056                                     .WORD 46

```

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TEST 12: LOAD AND START FUNCTION TEST

```

6708 031150' 002121'
6709 031152' 012750'
6710 031154' 017702 147160
6711 031160' 042702 140370
6712 031164' 012701 012402
6713 031170' 020102
6714 031172' 001404
6715 031174'
6716 031174' 104456
6717 031176' 000057
6718 031200' 002121'
6719 031202' 013212'
6720 031204'
6721 031204'
6722 031204'
6723 031204' 104405
6724
6725
6726
6727
6728 031206' 004737 020104'
6729
6730 031212'
6731 031212'
6732 031212' 104401

50$:  MOV @PCSR1,R2           ;GET PCSR1 CONTENTS
      BIC #^C<SFT!PSTATE>,R2 ;CLEAR ALL BUT SELF TEST AND STATE BITS
      MOV #SFTB0!SFTB2!SFTB4!INTST,R1 ;PATTERN THAT SHOULD BE IN PCSR1
      CMP R1,R2              ;IS PCSR1 PATTERN CORRECT?
      BEQ 60$                ;YES
      ERRHRD 047,LASFT,MSG6  ;NO, PRINT ERROR MESSAGE

60$:  ENDSEG

10004$: TRAP C$ERHRD
        .WORD 47
        .WORD LASFT
        .WORD MSG6

10004$: TRAP C$ESEG

:
:EVERYTHING WORKED JUST FINE, NOW WE HAVE TO GET THE OPERATIONAL MICROCODE
:GOING AGAIN BEFORE WE LEAVE OTHERWISE EVERYTHING WILL BE SCREWED UP.
:
      JSR PC,REUNA           ;RESET DEUNA TO RESTORE OPERATIONAL
                           ;MICROCODE

ENDTST

L10126: TRAP C$ETST

```

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TEST 13: COMPREHENSIVE WCS MEMORY TEST

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.SBTTL TEST 13: COMPREHENSIVE WCS MEMORY TEST
:*****8
:THIS TEST WILL EXHAUSTIVELY TEST THE WCS MEMORY.
:CUSTOM MICROCODE MODULE B, MICROTEST #1 IS USED TO DO THE ACTUAL TESTING.
:MICROTEST #1 RUNS A SERIES OF MICROSUBTESTS TESTS ON THE WCS MEMORY CHECKING
:FOR BOTH ADDRESS AND DATA ERRORS. IF AN ERROR DOES OCCUR THE PORT CONTROL
:BLOCK WILL CONTAIN THE INFORMATION ABOUT THE ERROR.
:PCBB+0: CONTAINS THE MICROSUBTEST THAT FAILED
:PCBB+1: 0 = DATA ERROR, 1 = ADDRESS ERROR
:PCBB+2: CONTAINS THE ADDRESS OF THE LOCATION
:PCBB+4: CONTAINS THE DATA THAT WAS WRITTEN
:PCBB+6: CONTAINS THE DATA THAT WAS READ
:TEST SEQUENCE:
: 1-LOAD MICROMODULE 'B' INTO THE TOP HALF OF WCS IF NOT ALREADY DONE SO
: 2-WAIT FOR THE MICROMONITOR TO BECOME ACTIVE
: 3-CLEAR PCBB LOCATIONS 0-7
: 4-TELL MICROMONITOR TO EXECUTE MICROTEST #1
: 5-WAIT FOR 'DNI'
: 6-IF ERROR PRINT PCBB CONTENTS
: 7-WRITE ONE TO CLEAR 'DNI'
: 8-RESTORE OPERATIONAL MICROCODE
:*****

031214'
031214'

031214'
031214' 104404
031216' 022737
031224' 001004
031226' 122777
031234' 001435
031236' 012737
031244' 004737
031250' 103564
031252' 012737
031260' 004737
031264' 103021
031266' 012737
031274' 012737
031302' 012737
031310' 012737
031316' 104456
031320' 000060
031322' 002165'
031324' 012750'

BGNTST
T13::
:CHECK TO SEE IF MICROCODE MODULE 'B' HAS BEEN LOADED. IF NOT, LOAD IT INTO
:THE TOP HALF OF WCS, START IT AND WAIT FOR THE MICROMONITOR TO SET 'DNI'.
:AFTER 'DNI' SETS WRITE '1' TO CLEAR IT.
:BGNSEG
:TRAP C\$BSEG
:HAS MICROCODE MODULE 'B' BEEN LOADED?
:NO
:YES, IS THE MICROMONITOR ACTIVE?
:GO LOAD MICROCODE MODULE B
:ERROR
:WAIT FOR THE MICROMONITOR
:OK
:PRINT ERROR MESSAGE
:TRAP C\$ERHRD
:WORD 48
:WORD WCSMEM
:WORD MSG1

000102	000326'	CMP	#'B,MICRO	:	HAS MICROCODE MODULE 'B' BEEN LOADED?
		BNE	5\$:	NO
000001	147104	CMPB	#INMON,@PCSR1	:	YES, IS THE MICROMONITOR ACTIVE?
		BEQ	20\$:	YES SKIP LOADING THE MICROMODULE
000102	000326'	MOV	#'B,MICRO	:	GO LOAD MICROCODE MODULE B
020252'		JSR	PC,LODMIC		
		BCS	55\$:	ERROR
000176	000332'	MOV	#2*SECOND,METER	:	WAIT FOR THE MICROMONITOR
017330'		JSR	PC,CHKDNI		
		BCC	20\$:	OK
000776'	000310'	MOV	#\$DNI,BITNAM		
001275'	000312'	MOV	#\$NSET,BITSTA		
001340'	000314'	MOV	#\$AFTER,PWHEN		
001355'	000316'	MOV	#\$GTCMD,PCOMND		
		ERRHRD	048,WCSMEM,MSG1	:	PRINT ERROR MESSAGE

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```

6789 031326* 000535
6790 031330* 004737 017374*
6791 031334* 103005
6792 031336*
6793 031336* 104456
6794 031340* 000061
6795 031342* 002165*
6796 031344* 012726*
6797 031346* 000525
6798 031350*
6799 031350*
6800 031350*
6801 031350* 104405
6802
6803
6804
6805
6806
6807
6808 031352*
6809 031352* 104404
6810 031354* 004737 017776*
6811 031360* 103006
6812 031362*
6813 031362* 104456
6814 031364* 000062
6815 031366* 002165*
6816 031370* 016700*
6817 031372*
6818 031372* 104410
6819 031374* 000234
6820 031376* 012777 000604* 146736 30$:
6821 031404* 005077 146734
6822 031410* 005037 000604*
6823 031414* 005037 000606*
6824 031420* 005037 000610*
6825 031424* 005037 000612*
6826 031430* 012777 000001 146700
6827
6828 031436* 012737 000770 000332*
6829 031444* 004737 017330*
6830 031450* 103017
6831 031452* 004737 020050*
6832 031456* 103005
6833 031460*
6834 031460* 104456
6835 031462* 000063
6836 031464* 002165*
6837 031466* 016454*
6838 031470* 000454
6839 031472* 012702 000003 35$:
6840 031476*
6841 031476* 104456
6842 031500* 000064
6843 031502* 002165*
6844 031504* 013500*

20$: BR 55$
JSR PC,CLRDNI ;CLEAR DNI
BCC 25$
ERRHRD 049,WCSMEM,RACMG7 ;DNI DID NOT CLEAR!

25$: BR 55$
ENDSEG

10000$: TRAP C$ERHRD
TRAP C$ESEG

:WAIT FOR THE MICROMONITOR TO ENTER THE 'INMON' STATE THEN START MICROTTEST #1.
:WAIT FOR 'DNI'. CHECK FOR ILLEGAL INTERRUPTS CHECK THE STATE BITS FOR AN
:ERROR CONDITION. IF ERROR REPORT IT. WRITE '1' TO CLEAR 'DNI' AND RESTORE
:OPERATIONAL MICROCODE.

BGNSEG

TRAP C$BSEG
JSR PC,CHKMON ;WAIT FOR MICROMONITOR
BCC 30$ ;OK
ERRHRD 050,WCSMEM,MSG46 ;PRINT ERROR

TRAP C$ERHRD
WORD 50
WORD WCSMEM
WORD MSG46

ESCAPE TST ;LEAVE TEST

TRAP C$ESCAPE
WORD L10127-.

MOV #PCBB,@PCSR2 ;TELL MICROCODE TEST WHERE PCBB IS
CLR @PCSR3
CLR PCBB+0 ;CLEAR OUT THE PCBB
CLR PCBB+2
CLR PCBB+4
CLR PCBB+6
MOV #1,@PCSR0 ;TELL MICROMONITOR TO EXECUTE...
;MICROTTEST #1
;PUT SOME TIME ON THE METER
;WAIT FOR MICROTTEST TO FINISH
;OK, IT FINISHED
;SEE IF ANY ERROR INTERRUPTS OCCURRED
;NO, OK
ERRHRD 051,WCSMEM,MSG44 ;PRINT ERROR MESSAGE

TRAP C$ERHRD
WORD 51
WORD WCSMEM
WORD MSG44

BR 55$ ;LEAVE
MOV #3,R2 ;MICROTTEST # THAT IS HUNG
ERRHRD 052,WCSMEM,MSG12 ;TELL MICROTTEST HUNG

TRAP C$ERHRD
WORD 52
WORD WCSMEM
WORD MSG12

```


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TEST 14: INTERRUPT VECTOR TEST

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.SBTTL TEST 14: INTERRUPT VECTOR TEST

: THIS TEST WILL VERIFY THAT THE INTERRUPT INTERFACE LOGIC OF THE DEUNA
: IS CAPABLE OF GENERATING AN INTERRUPT VECTOR AND ARBITRATING FOR CONTROL
: OF THE UNIBUS.
: THE DEUNA INTERRUPT ENABLE BIT WILL BE SET AND AN INTERRUPT WILL BE
: CAUSED BY ISSUING A NOP PORT COMMAND. AN INTERRUPT IS EXPECTED AT THE
: CORRECT VECTOR AND AT THE CORRECT PRIORITY.
: TEST SEQUENCE:
: 1-SETUP INTERRUPT VECTOR
: 2-CLEAR INTERRUPT FLAG
: 3-SET INTERRUPT ENABLE IN PCSRO
: 4-ISSUE NOP PORT COMMAND
: 5-WAIT FOR DNI SET IN PCSRO
: 6-VERIFY INTERRUPT FLAG SET
: 7-VERIFY INTERRUPT AT CORRECT PRIORITY
: 8-RELEASE INTERRUPT VECTOR
: 9-CLEAR INTERRUPT ENABLE IN PCSRO
: 10-WRITE DNI TO CLEAR
: *****

BGNTST

T14::

: SETUP INTERRUPT VECTOR, CLEAR INTERRUPT FLAG, SET INTERRUPT ENABLE,
: ISSUE NOP PORT COMMAND AND WAIT FOR 'DNI'

BGNSEG

```

                                TRAP    C$BSEG
SETVEC UNAVEC,#UNASRV,UNAPRI   :SETUP DEUNA INTERRUPT VECTOR
                                MOV      UNAPRI,-(SP)
                                MOV      #UNASRV,-(SP)
                                MOV      UNAVEC,-(SP)
                                MOV      #3,-(SP)
                                TRAP    C$SVEC
                                ADD     #10,SP
CLR    UNAINTE                  :CLEAR UNA INTERRUPTED FLAG
MOV    #IE,@PCSRO               :SET INTERRUPT ENABLE
MOV    #IE!PNOP,@PCSRO         :ISSUE NOP PORT COMMAND
MOV    #2*SECOND,METER        :SETUP TIMER
JSR    PC,CHKDNI               :GO WAIT FOR DNI
BCC    10$                     :OK
MOV    #DNI,BITNAM             :ERROR DNI NOT SET!
MOV    #SNSET,BITSTA          :SETUP ERROR MESSAGE
MOV    #SAFTER,PWHEN
MOV    #SNOP,PCOMND
ERRHRD 055,INTVEC,MSG1        :PRINT ERROR MESSAGE
                                TRAP    C$ERRHRD
                                .WORD   55
                                .WORD   INTVEC
                                .WORD   MSG1
```

```

6936 031756'
6937 031756'
6938 031756'
6939 031756' 104405
6940
6941
6942
6943 031760'
6944 031760' 104404
6945 031762' 012701 000006
6946
6947 031766' 010102
6948 031770' 072227 000005
6949 031774'
6950 031774' 010200
6951 031776' 104441
6952 032000' 000240
6953 032002' 005301
6954 032004' 100370
6955
6956 032006' 005737 000670'
6957 032012' 001006
6958
6959 032014'
6960 032014' 104456
6961 032016' 000070
6962 032020' 002214'
6963 032022' 013300'
6964 032024'
6965 032024' 104410
6966 032026' 000072
6967 032030'
6968 032030' 013701 000272'
6969 032034' 072127 177773
6970 032040' 005301
6971 032042' 072127 000005
6972 032046' 020137 000674'
6973 032052' 001404
6974
6975 032054'
6976 032054' 104456
6977 032056' 000071
6978 032060' 002214'
6979 032062' 013322'
6980 032064'
6981 032064'
6982 032064'
6983 032064' 104405
6984 032066'
6985 032066' 013700 000270'
6986 032072' 104436
6987 032074' 012777 000000 146234
6988 032102' 004737 017374'
6989 032106' 103004
6990 032110'
6991 032110' 104456

10$:
ENDSEG

10000$:
TRAP C$ESEG

:
:VERIFY THAT INTERRUPT OCCURRED AT CORRECT PRIORITY
:
BGNSEG

MOV #6,R1 ;START CPU PRIORITY LOWERING
;FROM PRIORITY 7
;GET INTEGER PRIORITY
;PUT PRIORITY IN CORRECT POSITION
;SET NEW PRIORITY
TRAP C$BSEG

20$:
MOV R1,R2
ASH #5,R2
SETPRI R2
MOV R2,R0
TRAP C$SPRI

NOP ;LET INTERRUPT OCCUR HERE IF PENDING
DEC R1 ;LOWER PRIORITY
BPL 20$ ;IF DONE FROM 6-->0 THEN DONE

TST UNAJNT
BNE 30$ ;UNA SHOULD HAVE INTERRUPTED BY NOW
;OK, GO CHECK THE PRIORITY OF THE INTERRUPT
;ERROR! UNA DID NOT INTERRUPT
;PRINT ERROR MESSAGE
ERRHRD 056,INTVEC,MSG7
TRAP C$ERHRD
.WORD 56
.WORD INTVEC
.WORD MSG7

ESCAPE TST ;LEAVE TEST
TRAP C$ESCAPE
.WORD L10130-

30$:
MOV UNAPRI,R1 ;GET UNA PRIORITY
ASH #-5,R1 ;MAKE IT AN INTEGER
DEC R1 ;THIS IS THE CPU PRIORITY WHEN THE...
ASH #5,R1 ;UNA SHOULD HAVE INTERRUPTED
CMP R1,CPUPRI ;DID UNA INTERRUPT AT CORRECT PRIORITY?
BEQ 40$ ;YES
ERRHRD 057,INTVEC,MSG8 ;PRINT ERROR MESSAGE
;ERROR! UNA INTERRUPT PRIORITY INCORRECT
TRAP C$ERHRD
.WORD 57
.WORD INTVEC
.WORD MSG8

40$:
ENDSEG

10001$:
CLRVEC UNAVEC ;RELEASE INTERRUPT VECTOR
TRAP C$ESEG
MOV UNAVEC,R0
TRAP C$CVEC

MOV #0,@PCSR0 ;CLEAR INTERRUPT ENABLE
JSR PC,CLRDN1 ;CLEAR DNI
BCC 50$
ERRHRD 058,INTVEC,RACMG7 ;ERROR! DNI DID NOT CLEAR
TRAP C$ERHRD

```

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TEST 14: INTERRUPT VECTOR TEST

6992 032112' 000072
6993 032114' 002214'
6994 032116' 012726'
6995 032120'
6996 032120'
6997 032120'
6998 032120' 104401

S0\$:
ENDTST

.WORD 58
.WORD INTVEC
.WORD RACMG7

L10130:
TRAP C\$ETST

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TEST 15: PCSRO INTERRUPT BIT TEST

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7000
7001
7002
7003
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7006
7007
7008
7009
7010
7011
7012
7013
7014
7015
7016
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7018
7019
7020
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7051
7052
7053
7054

```
.SBTTL TEST 15: PCSRO INTERRUPT BIT TEST
:*****
:THIS TEST WILL VERIFY THAT EACH OF THE INTERRUPT BITS IN REGISTER PCSRO
:CAN CAUSE AN INTERRUPT.
:
: EACH OF THE INTERRUPTS OF REGISTER PCSRO IS SET UNDER THE CONTROL OF THE
:T11 AND NOT DIRECTLY BY HARDWARE. THE T11 THEREFORE CAN INITIATE UNIBUS
: INTERRUPTS BY SETTING BITS IN REGISTER PCSRO.
:
: THIS TEST USES MICROMODULE C, MICROTEST #1.
: MICROCODE MODULE C IS LOADED IF NOT ALREADY DONE SO BY A PREVIOUS TEST.
:
: THE DEUNA INTERRUPT VECTOR IS SETUP TO STORE THE CONTENTS OF PCSRO WHEN THE
: INTERRUPT OCCURS. PCBB+0 IS LOADED WITH THE INTERRUPT BIT THAT IS TO BE TESTED
: THEN PCSRO COMMAND BITS ARE LOADED WITH A 1 TO TELL THE T11 TO EXECUTE
: MICROTEST #1. WE WAIT FOR THE INTERRUPT TO OCCUR THEN SEE IF THE CONTENTS
: OF PCSRO AT THE TIME OF THE INTERRUPT CONTAINED THE CORRECT INTERRUPT BIT.
: THE TEST IS REPEATED FOR ALL THE INTERRUPT BITS.
:
: TEST SEQUENCE:
: 1-LOAD MICROMODULE C INTO THE TOP HALF OF WCS IF NOT ALREADY DONE SO
: 2-SETUP DEUNA INTERRUPT VECTOR
: 3-WAIT FOR THE MICROMONITOR TO BECOME ACTIVE
: 4-SET A BIT IN PCBB+0 THAT CORRESPONDS TO THE INTERRUPT BIT TO TEST
: 5-SET INTERRUPT ENABLE
: 6-TELL MICROMONITOR TO EXECUTE MICROTEST #1
: 7-VERIFY INTERRUPT OCCURRED
: 8-VERIFY CORRECT BIT CAUSED INTERRUPT
: 9-WRITE ONE TO CLEAR INTERRUPT BIT
: 10-REPEAT STEPS 3-9 FOR ALL THE INTERRUPT BITS
:*****
```

BGNTST

T15::

```
:CHECK TO SEE IF MODULE 'C' HAS BEEN LOADED. IF NOT LOAD IT INTO
:THE TOP HALF OF WCS, START IT AND WAIT FOR THE MICROMONITOR TO SET 'DNI'.
:AFTER 'DNI' SETS WRITE '1' TO CLEAR IT.
```

BGNSEG

```
TRAP C$BSEG
;HAS MICROCODE MODULE C BEEN LOADED?
5$ :NO
CMP #'C,MICRO
BNE 5$
CMPB #INMON,@PCSR1
;YES, IS THE MICROMONITOR ACTIVE?
BEQ 20$ :YES SKIP LOADING THE MICROMODULE
MOV #'C,MICRO :SETUP TO LOAD MODULE C
JSR PC,LODMIC :GO LOAD MICROMODULE C
BCC 10$ :SUCCESS
ESCAPE TST :ERROR OCCURRED LEAVE
TRAP C$ESCAPE
WORD L10131-.
;PUT SOME TIME ON THE METER
10$: MOV #2*SECOND,METER
JSR PC,CHKDNI :GO WAIT FOR DNI TO SET AFTER THE
```

032122*
032122*

032122* 104404
032124* 022737 000103 000326*
032132* 001004
032134* 122777 000001 146176
032142* 001440
032144* 012737 000103 000326* 5\$:
032152* 004737 020252*
032156* 103002
032160*
032160* 104410
032162* 000444
032164* 012737 000176 000332* 10\$:
032172* 004737 017330*

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```

7055                                     ;LOAD AND START FUNCTION
7056 032176' 103022                       BCC      20$
7057 032200' 012737 000776' 000310'     MOV      #$DNI,BITNAM
7058 032206' 012737 001275' 000312'     MOV      #$NSET,BITSTA
7059 032214' 012737 001340' 000314'     MOV      #$AFTER,PWHEN
7060 032222' 012737 001355' 000316'     MOV      #$GTCMD,PCOMND
7061 032230'                                     ERRHRD  059,INTBIT,MSG1
7062 032230' 104456                                     ;PRINT ERROR MESSAGE
7063 032232' 000073                                     TRAP    C$ERHRD
7064 032234' 002251'                                     .WORD  59
7065 032236' 012750'                                     .WORD  INTBIT
7066 032240'                                     .WORD  MSG1
7067 032240' 104410                                     ESCAPE TST
7068 032242' 000364                                     TRAP    C$ESCAPE
7069 032244' 004737 017374' 20$:         JSR      PC,CLRDN1
7070 032250' 103006                                     .WORD  L10131-
7071 032252'                                     BCC      25$
7072 032252' 104456                                     ERRHRD  060,INTBIT,RACMG7
7073 032254' 000074                                     ;CLEAR DNI
7074 032256' 002251'                                     ;ERROR! DNI DID NOT CLEAR
7075 032260' 012726'                                     TRAP    C$ERHRD
7076 032262'                                     .WORD  60
7077 032262' 104410                                     ESCAPE TST
7078 032264' 000342                                     .WORD  INTBIT
7079 032266'                                     .WORD  RACMG7
7080 032266' 013746 000272' 25$:         SETVEC  UNAVEC,#UNASRV,UNAPRI
7081 032272' 012746 021656'                                     ;SETUP DEUNA INTERRUPT VECTOR
7082 032276' 013746 000270'                                     TRAP    C$ESCAPE
7083 032302' 012746 000003'                                     .WORD  L10131-
7084 032306' 104437                                     MOV      UNAPRI,-(SP)
7085 032310' 062706 000010'                                     MOV      #UNASRV,-(SP)
7086 032314'                                     MOV      UNAVEC,-(SP)
7087 032314'                                     MOV      #3,-(SP)
7088 032314' 104405                                     TRAP    C$SVEC
7089                                     .WORD  #10,SP
7090                                     ENDSEG
7091                                     10000$:
7092                                     TRAP    C$ESEG
7093                                     ;
7094                                     ;THE FOLLOWING LOOP WILL BE EXECUTED 6 TIMES- ONCE FOR EACH BIT 10 THRU 15
7095                                     ;OF PCSRO. IT WAITS FOR THE MICROMONITOR TO BECOME ACTIVE THEN CALLS
7096                                     ;MICROTEST #1 TO SET A BIT IN PCSRO AS DEFINED BY THE PARAMETER IN PCBB+0.
7097                                     ;THIS OPERATION SHOULD CAUSE AN INTERRUPT WHICH WILL BE REFLECTED BY THE
7098                                     ;VARIABLE 'UNAIN'T' WHICH IS SET BY THE UNA INTERRUPT SERVICE ROUTINE. UNAIN'T
7099                                     ;IS LOADED WITH THE VALUE OF PCSRO AT THE TIME OF THE INTERRUPT. THIS WAY
7100                                     ;THE BIT THAT CAUSED THE INTERRUPT CAN BE CHECKED.
7101                                     ;
7102                                     CLR      CSRNUM
7103                                     ;CHECKING PCSRO
7104 032316' 005037 000304'                                     MOV      #$NSET,BITSTA
7105 032322' 012737 001275' 000312'     MOV      #BIT10,R1
7106 032330' 012701 002000                                     ;CHECKING FOR SET BITS
7107 032334' 012737 000012 000306'     MOV      #10.,BITNUM
7108 032342'                                     ;START WITH BIT 10
7109 032342'                                     BGNSEG
7110 032342' 104404                                     TRAP    C$BSEG
7111 032344' 004737 017776'                                     JSR      PC,CHKMON
7112 032350' 103006                                     ;WAIT FOR MICROMONITOR
7113 032352'                                     BCC      30$
7114 032352' 104456                                     ERRHRD  061,INTBIT,MSG46
7115 032354' 000075                                     ;OK
7116 032356' 002251'                                     ;PRINT ERROR
7117                                     TRAP    C$ERHRD
7118                                     .WORD  61
7119                                     .WORD  INTBIT

```

HARDWARE TESTS MACY11 30(1046) 11-JAN-83 10:13 PAGE 149
 CZUAAA.P11 11-JAN-83 09:29 TEST 15: PCSRO INTERRUPT BIT TEST

```

7111 032360' 016700'
7112 032362'          ESCAPE TST          :LEAVE TEST          .WORD MSG46
7113 032362' 104410
7114 032364' 000242          TRAP C$ESCAPE
7115 032366' 005037 000670' 30$: CLR UNAINI          :CLEAR UNA INTERRUPTED FLAG
7116 032372' 010137 000604' MOV R1,PCBB+0          :TELL MICROMONITOR WHICH BIT TO SET
7117 032376' 012737 000077 000332' MOV #1*SECCND,METER :PUT SOME TIME ON THE METER
7118 032404' 012777 000100 145724 MOV #IE,@PCSR0          :SET INTERRUPT ENABLE AND...
7119
7120 032412' 012777 000101 145716 MOV #IE!1,@PCSR0          :CLEAR SELF TEST BITS IN PCSR1
7121 032420' 004737 017276' JSR PC,TIMON          :INVOKE INTERRUPT BIT MICROTST
7122 032424'          SETPRI #PRI04          :TURN ON THE TIMER
7123 032424' 012700 000200          :LOWER PRIORITY TO ALLOW UNA INTERRUPT
7124 032430' 104441          MOV #PRI04,RO
7125 032432' 005737 000670' 35$: TST UNAINI          :HAS INTERRUPT OCCURRED YET?
7126 032436' 001022          BNE 40$          :YES
7127 032440' 005737 000332' TST METER          :NO, HAS METER EXPIRED?
7128 032444' 001372          BNE 35$          :NOT YET
7129 032446' 004737 017314' JSR PC,TIMOFF          :TIMER HAS EXPIRED, SHUT IT OFF
7130
7131 032452' 013703 000306' MOV BITNUM,R3          :ERROR, NO UNA INTERRUPT!
7132 032456' 006303          ASL R3          :GET WHICH BIT
7133 032460' 012737 000356' 000310' MOV #BNAMTO,BITNAM          :MAKE IT A BYTE OFFSET
7134 032466' 060337 000310' ADD R3,BITNAM          :POINT TO TABLE OF BIT MNEMONICS
7135 032472'          ERRHRD 062,INTBIT,MSG9          :INDEX INTO TABLE OF BIT MNEMONICS
7136 032472' 104456          :PRINT ERROR MESSAGE
7137 032474' 000076          TRAP C$ERHRD
7138 032476' 002251'          .WORD 62
7139 032500' 013344'          .WORD INTBIT
7140 032502' 000402          .WORD MSG9
7141 032504' 004737 017314' 40$: BR 45$
7142 032510' 013703 000670' 45$: JSR PC,TIMOFF          :INTERUPT OCCURRED, SHUT OFF THE TIMER
7143 032514' 042703 000377          MOV UNAINI,R3          :GET SAVED PCSRO CONTENTS
7144 032520' 020103          BIC #377,R3          :CLEAR UNWANTED BITS
7145 032522' 001404          CMP R1,R3          :DID CORRECT BIT CAUSE INTERRUPT?
7146
7147 032524'          BEQ 50$          :YES, OK
7148 032524' 104456          ERRHRD 063,INTBIT,RACMG2          :ERROR, INCORRECT BIT CAUSED INTERRUPT!
7149 032526' 000077          :PRINT ERROR MESSAGE
7150 032530' 002251'          TRAP C$ERHRD
7151 032532' 012574'          .WORD 63
7152 032534'          .WORD INTBIT
7153 032534'          .WORD RACMG2
7154 032534'          10001$:
7155 032534' 104405          TRAP C$ESEG
7156
7157          :NOW WRITE '1' TO CLEAR THE BIT THAT CAUSED THE INTERRUPT
7158          :
7159          BGNSEG
7160 032536' 104404          TRAP C$BSEG
7161 032540' 010377 145572          MOV R3,@PCSR0          :WRITE '1' TO CLEAR INTERRUPT BIT
7162 032544' 017704 145566          MOV @PCSR0,R4          :READ IT BACK
7163 032550' 030403          BIT R4,R3          :IS BIT CLEARED?
7164 032552' 001420          BEQ 55$          :YES
7165
7166 032554' 012705 000017          MOV #15.,R5          :ERROR, BIT DID NOT CLEAR!
    
```

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CZUAAA.P11 11-JAN-83 09:29 TEST 15: PCSRO INTERRUPT BIT TEST

```

7167 032560' 006303      52$: ASL R3      ;IS THIS THE BIT WE SET?
7168 032562' 103402      BCS 53$      ;YES
7169 032564' 005305      DEC R5      ;NO
7170 032566' 000774      BR 52$
7171 032570' 006305      53$: ASL R5      ;MAKE IT A BYTE OFFSET
7172 032572' 012737 000356' 000310' MOV #BNAMTO,BITNAM ;GET POINTER TO BIT NAME MNEMONICS
7173 032600' 060537 000310' ADD R5,BITNAM ;INDEX INTO BIT NAME TABLE
7174 032604' ERRHRD 064,INTBIT,MSG33 ;PRINT ERROR MESSAGE
7175 032604' 104456
7176 032606' 000100 TRAP C$ERHRD
7177 032610' 002251' .WORD 64
7178 032612' 015506' .WORD INTBIT
7179 032614' .WORD MSG33
7180 032614'
7181 032614'
7182 032614' 104405
7183 032616' 005237 000306' INC BITNUM
7184 032622' 006301 ASL R1
7185 032624' 103246 BCC 26$
7186
7187 032626' ENDTST
7188 032626'
7189 032626' 104401

```

```

10002$: TRAP C$ESEG
L10131: TRAP C$ETST

```


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.SBTTL TEST 16: TIMER TEST
:*****
:THIS TEST WILL USE THE CUSTOM MICROCODE MODULE 'C' TO CHECK THE OPERATION
:OF THE TIMER.
:THE TIMER IS ACCESSIBLE ONLY TO THE T11 PROCESSOR. THE HOST PROCESSOR
:CAN START THE TIMER ONLY WITH THE ASSISTANCE OF THE T11 PROCESSOR.
:FOR THIS TEST THE MICROCODE WILL BE LOADED ONLY IF IT HAS NOT ALREADY
:BEEN DONE BY A PREVIOUS TEST.
:WHEN THE MICROCODE IS STARTED THE T11 WILL START THE TIMER AND WILL
:SET 'DNI' WHEN THE TIMING INTERVAL HAS EXPIRED. THE INTERVAL IS 10 SECONDS.
:ANY TIME FROM 8 TO 12 SECONDS IS AN ACCEPTABLE RANGE.
:TEST SEQUENCE:
: 1-LOAD MICROCODE MODULE 'C' IF NOT ALREADY DONE SO
: 2-START MICROCODE
: 3-START TIMER
: 4-WAIT FOR 'DNI'
: 5-CHECK TIME INTERVAL
:*****

032630'
032630'

BGNTST
T16::
:CHECK TO SEE IF MODULE 'C' HAS BEEN LOADED. IF NOT LOAD IT INTO
:THE TOP HALF OF WCS, START IT AND WAIT FOR THE MICROMONITOR TO SET 'DNI'.
:AFTER 'DNI' SETS WRITE '1' TO CLEAR IT.

032630'
032630' 104404
032632' 022737
032640' 001004
032642' 122777
032650' 001440
032652' 012737
032660' 004737
032664' 103002
032666'
032666' 104410
032670' 000254
032672' 012737
032700' 004737
032704' 103022
032706' 012737
032714' 012737
032722' 012737
032730' 012737
032736'
032736' 104456
032740' 000101
032742' 002311'

BGNSEG
:HAS MICROCODE MODULE C BEEN LOADED?
:NO
:YES, IS THE MICROMONITOR ACTIVE?
:YES SKIP LOADING THE MICROMODULE
:NO
:GO LOAD MICROCODE MODULE C
:OK
:ERROR OCCURRED LOADING MICROCODE
TRAP C\$BSEG
:WAIT FOR MICROMONITOR TO TAKE OVER
TRAP C\$ESCAPE
WORD L10132-
MOV #2*SECOND,METER
JSR PC,CHKDNI
BCC 20\$
MOV #\$DNI,BITNAM
MOV #\$NSET,BITSTA
MOV #\$AFTER,PWHEN
MOV #\$GTCMD,PCOMND
ERRHRD 065,TIMTST,MSG1
TRAP C\$ERHRD
WORD 65
WORD TIMTST

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 CZUAAA.P11 11-JAN-83 09:29 TEST 16: TIMER TEST

```

7246 032744' 012750'                                     .WORD  MSG1
7247 032746'                                     ESCAPE TST
7248 032746' 104410                                     TRAP  C$ESCAPE
7249 032750' 000174                                     .WORD  L10132-.
7250 032752' 004737 017374' 20$: JSR    PC,CLRDN1          :CLEAR DNI
7251 032756' 103006          BCC    25$
7252 032760'          ERRHRD 066,TIMTST,RACMG7      :ERROR DNI DID NOT CLEAR!
7253 032760' 104456                                     TRAP  C$ERHRD
7254 032762' 000102                                     .WORD  66
7255 032764' 002311'                                     .WORD  TIMTST
7256 032766' 012726'                                     .WORD  RACMG7
7257 032770'                                     ESCAPE TST
7258 032770' 104410                                     TRAP  C$ESCAPE
7259 032772' 000152                                     .WORD  L10132-.
7260 032774'
7261 032774'
7262 032774'
7263 032774' 104405          '0000$: TRAP  C$ESEG
7264
7265          :WAIT FOR THE MICROCODE TO ENTER THE MICROMONITOR, SETUP OUR TIMEOUT TO BE
7266          :12 SECONDS (THIS GIVES A BETTER RESOLUTION THAN 1 SECOND), START THE MICROTEST
7267          :BY LOADING THE COMMAND FIELD OF PCSRO WITH THE MICROTEST # TO EXECUTE.
7268          :CHECK FOR 'DNI' TO BE SET IN LESS THAN 12 SECONDS.
7269          :
7270          BGNSEG
7271 032776' 104404          TRAP  C$BSEG
7272 033000' 004737 017776'          JSR    PC,CHKMON          :WAIT FOR MICROMONITOR
7273 033004' 103006          BCC    30$          :OK
7274 033006'          ERRHRD 068,TIMTST,MSG46      :PRINT ERROR
7275 033006' 104456                                     TRAP  C$ERHRD
7276 033010' 000104                                     .WORD  68
7277 033012' 002311'                                     .WORD  TIMTST
7278 033014' 016700'                                     .WORD  MSG46
7279 033016'          ESCAPE TST          :LEAVE TEST
7280 033016' 104410          TRAP  C$ESCAPE
7281 033020' 000124          .WORD  L10132-.
7282 033022' 012737 001356 000332' 30$: MOV    #750.,METER          :TIMEOUT = 12 SECONDS
7283 033030' 012777 000002 145300      MOV    #2,@PCSRO          :START MICROTEST #2
7284 033036' 004737 017330'          JSR    PC,CHKDNI          :WAIT FOR DNI
7285 033042' 103006          BCC    40$          :OK IT FINISHED IN TIME
7286 033044'          ERRHRD 069,TIMTST,MSG10      :NO TIMER INTERRUPT
7287 033044' 104456                                     TRAP  C$ERHRD
7288 033046' 000105                                     .WORD  69
7289 033050' 002311'                                     .WORD  TIMTST
7290 033052' 013372'                                     .WORD  MSG10
7291 033054'          ESCAPE TST
7292 033054' 104410          TRAP  C$ESCAPE
7293 033056' 000066          .WORD  L10132-.
7294
7295          :OK THE TIMER INTERRUPTED IN LESS THAN 12 SECONDS, SO NOW CHECK TO SEE IF IT
7296          :HAPPENED IN LESS THAN 8.
7297          :
7298 033060' 023727 000332' 000764 40$: CMP    METER,#500.          :DID INTERRUPT OCCUR BEFORE 8 SECS.?
7299 033066' 003416          BLE    60$          :NO, OK
7300 033070' 012700 001161          MOV    #625.,R0
7301 033074' 163700 000332'          SUB    METER,R0          :CALC HOW MUCH TIME
    
```

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7302	033100'	005001						
7303	033102'	152700	000077	50\$:	CLR R1			:CONTAINS SECS
7304	033106'	103402			SUB #63.,R0			:FIND OUT HOW MAY SECS IT TOOK
7305	033110'	005201			BCS 55\$			
7306	033112'	000773			INC R1			
7307	033114'			55\$:	BR 50\$			
7308	033114'	104456			ERRHRD 070,TIMTST,MSG11			:PRINT ERROR MESSAGE
7309	033116'	000106					TRAP	C\$ERHRD
7310	033120'	002311'					.WORD	70
7311	033122'	013414'					.WORD	TIMTST
7312	033124'	004737	017374'	60\$:	JSR PC,CLRDNI			:CLEAR DNI
7313	033130'	103004			BCC 70\$			
7314	033132'				ERRHRD 071,TIMTST,RACMG7			:ERROR DNI DID NOT CLEAR
7315	033132'	104456					TRAP	C\$ERHRD
7316	033134'	000107					.WORD	71
7317	033136'	002311'					.WORD	TIMTST
7318	033140'	012726'					.WORD	RACMG7
7319	033142'			70\$:				
7320	033142'			ENDSEG				
7321	033142'							10001\$:
7322	033142'	104405					TRAP	C\$ESEG
7323	033144'			ENDTST				
7324	033144'							L10132:
7325	033144'	104401					TRAP	C\$ETST

HARDWARE TESTS MACY11 30(1046) 11-JAN-83 10:13 PAGE 154
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```

7326 .SBTTL TEST 17: LINK MEMORY TEST
7327
7328 :*****
7329 :
7330 :THIS TEST WILL EXHAUSTIVELY TEST THE LINK MEMORY.
7331 :
7332 :THE LINK MEMORY OCCUPIES THE 16-32K ADDRESS SPACE OF THE T-11.
7333 :CUSTOM MICROCODE MODULE C MICROTST #3 IS USED TO DO THE ACTUAL TESTING.
7334 :MICROTST #3 RUNS A SERIES OF MICROSUBTESTS TESTS ON THE LINK MEMORY CHECKING
7335 :FOR BOTH ADDRESS AND DATA ERRORS. IF AN ERROR DOES OCCUR THE PORT CONTROL
7336 :BLOCK WILL CONTAIN THE INFORMATION ABOUT THE ERROR.
7337 :
7338 :PCBB+0: CONTAINS THE MICROSUBTEST THAT FAILED
7339 :PCBB+1: 0 = DATA ERROR, 1 = ADDRESS ERROR,
7340 :PCBB+2: CONTAINS THE ADDRESS OF THE LOCATION
7341 :PCBB+4: CONTAINS THE DATA THAT WAS WRITTEN
7342 :PCBB+6: CONTAINS THE DATA THAT WAS READ
7343 :
7344 :MICROSUBTEST #      DESCRIPTION
7345 :
7346 :          1          ACCESS TEST
7347 :          2          ADDRESS SHIFT TEST
7348 :          3          DATA LATCH TEST
7349 :          4          ADDRESS BIT SHIFT #1
7350 :          5          ADDRESS BIT SHIFT #2
7351 :          6          MARCH TEST
7352 :
7353 :TEST SEQUENCE:
7354 : 1-LOAD MICROMODULE 'C' INTO THE TOP HALF OF WCS IF NOT ALREADY DONE SO
7355 : 2-WAIT FOR THE MICROMONITOR TO BECOME ACTIVE
7356 : 3-TELL MICROMONITOR TO EXECUTE MICROTST #3
7357 : 4-VERIFY DNI SET INDICATING TEST COMPLETE
7358 : 5-CHECK STATE FIELD OF PCSR1 FOR ERROR CONDITION
7359 :
7360 :*****

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7361
7362 033146'
7363 033146'
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BGNTST
T17::
:CHECK TO SEE IF MODULE 'C' HAS BEEN LOADED. IF NOT LOAD IT INTO
:THE TOP HALF OF WCS, START IT AND WAIT FOR THE MICROMONITOR TO SET 'DNI'.
:AFTER 'DNI' SETS WRITE '1' TO CLEAR IT.
:
BGNSEG
TRAP CSBSEG
:HAS MICROCODE MODULE 'C' BEEN LOADED?
:NO
:YES, IS THE MICROMONITOR ACTIVE?
:YES SKIP LOADING THE MICROMODULE
:GO LOAD MICROCODE MODULE C
:OK
TRAP C$ESCAPE
:WORD L10133-
:WAIT FOR THE MICROMONITOR

```

```

033146' 104404
033150' 022737 000103 000326'
033156' 001004
033160' 122777 000001 145152
033166' 601440
033170' 012737 000103 000326' 5$:
033176' 004737 020252'
033202' 103002
033204'
033204' 104410
033206' 000404
033210' 012737 000176 000332' 10$:

```

```

CMP #'C,MICRO ;HAS MICROCODE MODULE 'C' BEEN LOADED?
BNE 5$ ;NO
CMPB #INMON,@PCSR1 ;YES, IS THE MICROMONITOR ACTIVE?
BEQ 20$ ;YES SKIP LOADING THE MICROMODULE
MOV #'C,MICRO ;GO LOAD MICROCODE MODULE C
JSR PC,LODMIC
BCC 10$ ;OK
ESCAPE TST
MOV #2*SECOND,METER ;WAIT FOR THE MICROMONITOR

```

HARDWARE TESTS MACY11 30(1046) 11-JAN-83 10:13 PAGE 155
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```

7382 033216' 004737 017330'      JSR    PC,CHKDNI
7383 033222' 103022      BCC    20$      ;OK
7384 033224' 012737 000776' 000310'  MOV    #SDNI,BITNAM
7385 033232' 012737 001275' 000312'  MOV    #SNSET,BITSTA
7386 033240' 012737 001340' 000314'  MOV    #SAFTER,PWHEN
7387 033246' 012737 001355' 000316'  MOV    #SGTCMD,PCOMND
7388 033254'      ERRHRD 072,LNKMEM,MSG1 ;PRINT ERROR MESSAGE
7389 033254' 104456      TRAP  C$ERHRD
7390 033256' 000110      .WORD 72
7391 033260' 002333'      .WORD LNKMEM
7392 033262' 012750'      .WORD MSG1
7393 033264'      ESCAPE TST
7394 033264' 104410      TRAP  C$ESCAPE
7395 033266' 000324      .WORD L10133-.
7396 033270' 004737 017374' 20$: JSR    PC,CLRDN1      ;CLEAR DNI
7397 033274' 103006      BCC    25$
7398 033276'      ERRHRD 073,LNKMEM,RACMG7
7399 033276' 104456      TRAP  C$ERHRD
7400 033300' 000111      .WORD 73
7401 033302' 002333'      .WORD LNKMEM
7402 033304' 012726'      .WORD RACMG7
7403 033306'      ESCAPE TST
7404 033306' 104410      TRAP  C$ESCAPE
7405 033310' 000302      .WORD L10133-.
7406 033312'      25$:
7407 033312'      ENDSEG
7408 033312'      10000$:
7409 033312' 104405      TRAP  C$ESEG
7410
7411      ;
7412      ;WAIT FOR THE MICROMONITOR TO ENTER THE 'IN MONITOR' STATE, CLEAR THE
7413      ;LOCATIONS FOR ERROR INFORMATION, LOAD COMMAND FIELD OF PCSRO WITH A 3
7414      ;CAUSING THE MICROMONITOR TO EXECUTE MICROTEST #3, THIS WILL START THE
7415      ;EXECUTION OF THE MICROSUBTEST SEQUENCE OF MEMORY TESTS. DNI WILL SET
7416      ;WHEN THE TEST IS COMPLETE
7417      ;
7417 033314'      BGNSEG
7418 033314' 104404      TRAP  C$BSEG
7419 033316' 004737 017776'      JSR    PC,CHKMON      ;WAIT FOR MICROMONITOR
7420 033322' 103006      BCC    30$      ;OK
7421 033324'      ERRHRD 074,LNKMEM,MSG46 ;PRINT ERROR
7422 033324' 104456      TRAP  C$ERHRD
7423 033326' 000112      .WORD 74
7424 033330' 002333'      .WORD LNKMEM
7425 033332' 016700'      .WORD MSG46
7426 033334'      ESCAPE TST      ;LEAVE TEST
7427 033334' 104410      TRAP  C$ESCAPE
7428 033336' 000254      .WORD L10133-.
7429 033340' 012777 000604' 144774 30$: MOV    #PCBB,@PCSR2      ;TELL MICROCODE TEST WHERE PCBB IS
7430 033346' 005077 144772      CLR    @PCSR3
7431 033352' 005037 000604'      CLR    PCBB+0      ;CLEAR OUT THE PCBB
7432 033356' 005037 000606'      CLR    PCBB+2
7433 033362' 005037 000610'      CLR    PCBB+4
7434 033366' 005037 000612'      CLR    PCBB+6
7435 033372' 012777 000003 144736      MOV    #3,@PCSR0      ;TELL MICROMONITOR TO EXECUTE...
7436      ;MICROTEST #3
7437 033400' 012737 000770 000332'      MOV    #10*SECOND,METER ;PUT SOME TIME ON THE METER

```

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```

7438 033406' 004737 017330'      JSR      PC,CHKDNI      ;WAIT FOR MICROTEST TO FINISH
7439 033412' 103021      BCC      40$           ;OK, IT FINISHED
7440 033414' 004737 020050'      JSR      PC,CHKINT     ;SEE IF ANY ERROR INTERRUPTS OCCURRED
7441 033420' 103006      BCC      35$           ;NO, OK
7442 033422'           ERRHRD  075,LNKMEN,MSG44 ;PRINT ERROR MESSAGE
7443 033422' 104456      TRAP     C$ERHRD
7444 033424' 000113      .WORD   75
7445 033426' 002333'      .WORD   LNKMEN
7446 033430' 016454'      .WORD   MSG44
7447 033432'           ESCAPE  TST           ;LEAVE TEST
7448 033432' 104410      TRAP     C$ESCAPE
7449 033434' 000156      .WORD   L10133-
7450 033436' 012702 000003      35$:  MOV      #3,R2      ;MICROTEST #
7451 033442'           ERRHRD  076,LNKMEN,MSG12 ;TELL MICROTEST HUNG
7452 033442' 104456      TRAP     C$ERHRD
7453 033444' 000114      .WORD   76
7454 033446' 002333'      .WORD   LNKMEN
7455 033450' 013500'      .WORD   MSG12
7456 033452'           ESCAPE  TST
7457 033452' 104410      TRAP     C$ESCAPE
7458 033454' 000136      .WORD   L10133-
7459
7460           ;MICROTEST IS COMPLETE, NOW CHECK FOR AN ERROR CONDITION
7461
7462 033456' 122777 000003 144654 40$:  CMPB     #INERR,@PCSR1 ;DID AN ERROR OCCUR?
7463 033464' 001027      BNE     47$           ;NO
7464 033466' 122737 000000 000605'  CMPB     #DATERR,PCBB+1 ;YES, WAS IT A DATA ERROR?
7465 033474' 001003      BNE     45$           ;NO
7466 033476' 012702 001444'  MOV      #$DATER,R2   ;YES, POINT TO DATA ERROR STRING
7467 033502' 000406      BR      46$
7468 033504' 122737 000001 000605' 45$:  CMPB     #AIERR,PCBB+1 ;WAS IT AN ADDRESS ERROR
7469 033512' 001014      BNE     47$           ;NO
7470 033514' 012702 001457'  MOV      #$ADRER,R2   ;POINT TO ADDRESS ERROR STRING
7471 033520' 013701 000606' 46$:  MOV      PCEB+2,R1    ;GET FAILING ADDRESS
7472 033524' 013703 000610'  MOV      PCBB+4,R3    ;GET GOOD DATA
7473 033530' 013704 000612'  MOV      PCBB+6,R4    ;GET BAD DATA
7474 033534'           ERRHRD  077,LNKMEN,MSG16 ;PRINT ERROR MESSAGE
7475 033534' 104456      TRAP     C$ERHRD
7476 033536' 000115      .WORD   77
7477 033540' 002333'      .WORD   LNKMEN
7478 033542' 013650'      .WORD   MSG16
7479 033544' 032777 010000 144564 47$:  BIT      #PARERR,@PCSR0 ;DID A PARITY ERROR OCCUR?
7480 033552' 001407      BEQ     50$           ;NO
7481 033554'           ERRHRD  078,LNKMEN,MSG43 ;YES PRINT ERROR
7482 033554' 104456      TRAP     C$ERHRD
7483 033556' 000116      .WORD   78
7484 033560' 002333'      .WORD   LNKMEN
7485 033562' 016432'      .WORD   MSG43
7486 033564' 012777 010000 144544      MOV      #PARERR,@PCSR0 ;CLEAR PARITY ERROR FLAG
7487
7488           ;WRITE ONE TO CLEAR THE DNI BIT
7489
7490           50$:
7491 033572' 004737 017374'      JSR      PC,CLRDN1    ;CLEAR DNI
7492 033576' 103004      BCC     55$
7493 033600'           ERRHRD  079,LNKMEN,PA$MG7
    
```

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7494 033600' 104456
7495 033602' 000117
7496 033604' 002333'
7497 033606' 012726'
7498 033610'
7499 033610'
7500 033610'
7501 033610' 04405
7502 033612'
7503 033612'
7504 033612' 104401
7505

55\$:
ENDSEG

ENDTST

TRAP C\$ERHRD
.WORD 79
.WORD LNKMEM
.WORD RACMG7

10001\$:
TRAP C\$FSEG

L1013\$:
TRAP C\$ETST

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TEST 18: DMA 'TO' ADDRESS TEST

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.SBTTL TEST 18: DMA 'TO' ADDRESS TEST

: THIS TEST WILL VERIFY THAT THE INTERNAL REGISTER 'DMATO' CAN BE READ
: AND WRITTEN. THE T11 WILL BE USED TO WRITE AND READ THIS REGISTER.
: THIS TEST REQUIRES THE USE OF CUSTOM MICROCODE MODULE C MICROTEST #4.
: PCBB+0 WILL BE WRITTEN WITH THE DATA PATTERN TO TEST, THE T11 WILL
: WRITE THIS PATTERN TO THE 'DMATO' REGISTER AND READ IT BACK AND PUT
: THE DATA READ INTO PCBB+2. THE DATA AT PCBB+2 WILL BE VERIFIED.
: TEST SEQUENCE:
: 1-LOAD MICROCODE MODULE 'C' IF NOT ALREADY DONE SO
: 2-LOAD PCBB+0 WITH DATA PATTERN
: 3-START MICROCODE
: 4-WAIT FOR 'DNI'
: 5-VERIFY PCBB+2 FOR CORRECT PATTERN
: 6-REPEAT STEPS 2-6 FOR ALL DATA PATTERNS

BGNTST

T18::

: CHECK TO SEE IF MODULE 'C' HAS BEEN LOADED. IF NOT LOAD IT INTO
: THE TOP HALF OF WCS, START IT AND WAIT FOR THE MICROMONITOR TO SET 'DNI'.
: AFTER 'DNI' SETS WRITE '1' TO CLEAR IT.

BGNSEG

TRAP C\$BSEG
:HAS MICROCODE MODULE 'C' BEEN LOADED?
:NO
:YES, IS THE MICROMONITOR ACTIVE?
:YES SKIP LOADING THE MICROMODULE
:NO
:GO LOAD MICROCODE MODULE C
:OK
TRAP C\$ESCAPE
.WORD L10134-
MOV #2*SECOND,METER :WAIT FOR MICROMONITOR TO TAKE OVER
JSR PC,CHKDNI :OK
BCC 20\$
MOV #SDNI,BITNAM
MOV #SNSET,BITSTA
MOV #SAFTER,PWHEN
MOV #SGTCMD,PCOMND
ERRHRD 080,DMATO,MSG1
TRAP C\$ERHRD
.WORD 80
.WORD DMATO
.WORD MSG1
ESCAPE TST
TRAP C\$ESCAPE
.WORD L10134-
20\$: JSR PC,CLRDN1 ;CLEAR DNI

033614'
033614'

033614' 104404
033616' 022737 000103 000326'
033624' 001004
033626' 122777 000001 144504
033634' 001440
033636' 012737 000103 000326' 5\$:
033644' 004737 020252'
033650' 103002
033652' 104410
033654' 000314
033656' 012737 000176 000332' 10\$:
033664' 004737 017330'
033670' 103022
033672' 012737 000776' 000310'
033700' 012737 001275' 000312'
033706' 012737 001340' 000314'
033714' 012737 001355' 000316'
033722' 104456
033724' 000120
033726' 002363'
033730' 012750'
033732'
033732' 104410
033734' 000234
033736' 0C4737 017374'

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```

7562 033742' 103006          BCC      25$
7563 033744'          ERRHRD  081,DMATO,RACMG7
7564 033744' 104456
7565 033746' 000121          TRAP    C$ERHRD
7566 033750' 002363'          .WORD  81
7567 033752' 012726'          .WORD  DMATO
7568 033754'          .WORD  RACMG7
7569 033754' 104410          ESCAPE  TST
7570 033756' 000212          TRAP    C$ESCAPE
7571 033760'          .WORD  L10134-.
7572 033760'          25$:
7573 033760'          ENDSEG
7574 033760' 104405          10000$:
7575          TRAP    C$ESEG
7576          ;POINT TO LIST OF DATA PATTERNS TO USE, THERE ARE FIVE ENTRIES IN THE LIST
7577          ;SO THE LOOP WILL BE EXECUTED 5 TIMES ONCE FOR EACH DATA PATTERN.
7578          ;
7579 033762' 012701 000516'      MOV     #PATERN,R1      ;GET ADDRESS OF DATA PATTERNS
7580 033766' 012705 000005'      MOV     #5,R5          ;NUMBER OF DATA PATTERNS
7581 033772'          30$:
7582          ;
7583          ;WAIT FOR THE MICROMONITOR TO ENTER THE 'IN MONITOR' STATE, LOAD PCBB+0 WITH
7584          ;A DATA PATTERN, LOAD THE COMMAND FIELD OF PCSRO WITH 4 TO START THE EXECUTION
7585          ;OF MICROTEST #4, DNI SETS WHEN IT IS COMPLETE
7586          ;
7587          BGNSEG
7588 033772' 104404          TRAP    C$BSEG
7589 033774' 004737 017776'      JSR     PC,CHKMON      ;WAIT FOR MICROMONITOR
7590 034000' 103006          BCC     35$           ;OK
7591 034002'          ERRHRD  082,DMATO,MSG46 ;PRINT ERROR
7592 034002' 104456          TRAP    C$ERHRD
7593 034004' 000122          .WORD  82
7594 034006' 002363'          .WORD  DMATO
7595 034010' 016700'          .WORD  MSG46
7596 034012'          ESCAPE  TST          ;LEAVE TEST
7597 034012' 104410          TRAP    C$ESCAPE
7598 034014' 000154          .WORD  L10134-.
7599 034016' 012137 000604'      35$:  MOV     (R1)+,PCBB+0    ;GET A DATA PATTERN
7600 034022' 012777 000004 144306'  MOV     #4,@PCSRO      ;START MICROTEST #4
7601 034030' 012737 000176 000332'  MOV     #2*SECOND,METER ;PUT SOME TIME ON THE METER
7602 034036' 004737 017330'      JSR     PC,CHKDNI      ;WAIT FOR DNI
7603 034042' 103021          BCC     40$           ;OK
7604 034044' 004737 020050'      JSR     PC,CHKINT      ;SEE IF ANY ERROR INTERRUPTS OCCURRED
7605 034050' 103006          BCC     36$           ;NO, OK
7606 034052'          ERRHRD  083,DMATO,MSG44 ;PRINT ERROR MESSAGE
7607 034052' 104456          TRAP    C$ERHRD
7608 034054' 000123          .WORD  83
7609 034056' 002363'          .WORD  DMATO
7610 034060' 016454'          .WORD  MSG44
7611 034062'          ESCAPE  TST          ;LEAVE TEST
7612 034062' 104410          TRAP    C$ESCAPE
7613 034064' 000104          .WORD  L10134-.
7614 034066' 012702 000004      36$:  MOV     #4,R2          ;MICROTEST #
7615 034072'          ERRHRD  084,DMATO,MSG12 ;TELL MICROTEST HUNG
7616 034072' 104456          TRAP    C$ERHRD
7617 034074' 000124          .WORD  84
    
```

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TEST 18: DMA 'TO' ADDRESS TEST

7618	034076'	002363'							
7619	034100'	013500'						.WORD	DMATO
7620	034102'			ESCAPE	TST			.WORD	MSG12
7621	034102'	104410						TRAP	C\$ESCAPE
7622	034104'	000064						.WORD	L10134-
7623									
7624									
7625									
7626									
7627	034106'	013703	000604'						
7628	034112'	042703	000001						
7629	034116'	020337	000606'						
7630	034122'	001407							
7631	034124'	013704	000606'						
7632	034130'								
7633	034130'	104456						TRAP	C\$ERHRD
7634	034132'	000125						.WORD	85
7635	034134'	002363'						.WORD	DMATO
7636	034136'	013524'						.WORD	MSG13
7637	034140'			ENDSEG					
7638	034140'								
7639	034140'	104405						10001\$:	TRAP
7640									C\$ESEG
7641									
7642									
7643	034142'	004737	017374'						
7644	034146'	103006							
7645	034150'								
7646	034150'	104456							
7647	034152'	000126						TRAP	C\$ERHRD
7648	034154'	002363'						.WORD	86
7649	034156'	012726'						.WORD	DMATO
7650	034160'							.WORD	PACMG7
7651	034160'	104410							
7652	034162'	000006						TRAP	C\$ESCAPE
7653	034164'	005305						.WORD	L10134-
7654	034166'	001301							
7655	034170'								
7656	034170'								
7657	034170'	104401						L10134:	TRAP
									C\$ETST

OK NOW CHECK TO SEE IF DATA READ IS SAME AS THE DATA WRITTEN
REMEMBER BIT 0 OF DMATO IS NOT USED

```
40$:  MOV    PCBB+0,R3      ;GET ORIGINAL DATA PATTERN
      BIC    #BIT0,R3     ;STRIP LSB
      CMP    R3,PCBB+2    ;SEE IF DATA WRITTEN = DATA READ
      BEQ    50$          ;YES
      MOV    PCBB+2,R4    ;NO, ERROR
      ERRHRD 085,DMATO,MSG13 ;PRINT ERROR
```

WRITE ONE TO CLEAR DNI BIT

```
50$:  JSR    PC,CLRDNI    ;CLEAR DNI
      BCC    55$
      ERRHRD 086,DMATO,RACMG7;ERROR DNI DID NOT CLEAR
```

```
55$:  DEC    R5          ;ANY MORE DATA PATTERNS?
      BNE    30$        ;YES
```

ENDTST

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TEST 19: DMA 'FROM' ADDRESS REGISTER TEST

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.SBTTL TEST 19: DMA 'FROM' ADDRESS REGISTER TEST

: THIS TEST CHECKS THE OPERATION OF THE REGISTER/COUNTER THAT CONTAINS
: THE ADDRESS OF THE LINK MEMORY WORD TO BE MOVED TO THE HOST DURING
: DMA OPERATIONS. THE REGISTER CAN BE WRITTEN BY THE T11 BUT IT CAN
: NOT BE READ BACK FOR VERIFICATION, THEREFORE IT MUST BE CHECKED
: INDIRECTLY.

: THE METHOD USED IS TO LOAD MICROCODE MODULE C IF IT HAS NOT ALREADY
: BEEN DONE. THE MICROTST #5 LOADS EACH LOCATION OF LINK MEMORY WITH
: ITS ADDRESS THEN IT TAKES THE CONTENTS OF PCBB+0 AND LOADS IT INTO
: THE DMA 'FROM' ADDRESS REGISTER, THE 'TO' REGISTER IS LOADED WITH
: THE ADDRESS OF PCBB+2, THE WORD COUNT IS LOADED FOR A ONE WORD TRANSFER
: AND THE DMA ENGINE IS STARTED. THE HOST VERIFIES PCBB+2 = PCBB+0

: TEST SEQUENCE:
: 1-LOAD MICROCODE MODULE 'C' IF NOT ALREADY DONE SO
: 2-LOAD PCBB+0 WITH ADDRESS OF LINK MEMORY TO 'LOAD
: 3-START MICROCODE
: 4-WAIT FOR 'DNI'
: 5-VERIFY PCBB+2 = PCBB+0
: 6-CHANGE PCBB+0 BY 1K CHUNKS
: 7-REPEAT 2-6

BGNTST

T19::

: CHECK TO SEE IF MODULE 'C' HAS BEEN LOADED. IF NOT LOAD IT INTO
: THE TOP HALF OF WCS, START IT AND WAIT FOR THE MICROMONITOR TO SET 'DNI'.
: AFTER 'DNI' SETS WRITE '1' TO CLEAR IT.

BGNSEG

TRAP C\$B\$SEG
;HAS MICRO MODULE C BEEN LOADED?
;NO
;YES, IS THE MICROMONITOR ACTIVE?
;YES SKIP LOADING THE MICROMODULE
;NO, LOAD MICRO MODULE C
;OK
TRAP C\$ESCAPE
;WORD L10135-
;PUT SOME TIME ON THE METER
;WAIT FOR THE MICROMONITOR
;OK
TRAP C\$ERRHRD

034172'
034172'
034172'
034174'
034202'
034204'
034212'
034214'
034222'
034226'
034230'
034230'
034232'
034234'
034242'
034246'
034250'
034256'
034264'
034272'
034300'
034300' 104456

104404
022737 000103 000326'
001004
122777 000001 144126
012737 000103 000326' 5\$:
004737 020252'
103002
104410
000316
012737 000176 000332' 10\$:
004737 017350'
103022
012737 000776' 000310'
012737 001275' 000312'
012737 001340' 000314'
012737 001355' 000316'

CMP #'C,MICRO
BNE 5\$
CMPB #INMON,@PCSR1
BEQ 20\$
MOV #'C,MICRO
JSR PC,LODMIC
BCC 10\$
ESCAPE TST
MOV #2*SECOND,METER
JSR PC,CHKDNI
BCC 20\$
MOV #SDNI,BITNAM
MOV #S\$SET,BITSTA
MOV #SAFTER,PWHEN
MOV #SGTCMD,PCOMND
ERRHRD 087,DMAFRM,MSG1

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TEST 19: DMA 'FROM' ADDRESS REGISTER TEST

```

7770 034442' 000133          .WORD 91
7771 034444' 002431'        .WORD DMAFRM
7772 034446' 013500'        .WORD MSG12
7773 034450'                ESCAPE TST
7774 034450' 104410          TRAP C$ESCAPE
7775 034452' 000076          .WORD L10135-.
7776
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7781
7782 034454' 013703 000604' 50$: MOV PCBB+0,R3          :GET ORIGINAL 'FROM' ADDRESS
7783 034460' 052703 100004'    BIS #BIT15!BIT2,R3      :MAKE IT ACTUAL LINK MEMORY ADDRESS
7784 034464' 013704 000606'    MOV PCBB+2,R4          :GET WHAT WAS READ FROM LINK MEMORY
7785 034470' 020304          CMP R3,R4             :IS DATA CORRECT?
7786 034472' 001404          BEQ 55$              :YES
7787 034474'                ERRHRD 092,DMAFRM,MSG14      :NO
7788 034474' 104456          TRAP C$ERHRD
7789 034476' 000134          .WORD 92
7790 034500' 002431'        .WORD DMAFRM
7791 034502' 013572'        .WORD MSG14
7792 034504'                55$:
7793 034504'                ENDSEG
7794 034504'                10001$:
7795 034504' 104405          TRAP C$ESEG
7796
7797
7798
7799 034506' 004737 017374'    :WRITE ONE TO CLEAR DNI
7800 034512' 103006          JSR PC,CLRDNI
7801 034514'                BCC 57$
7802 034514' 104456          ERRHRD 093,DMAFRM,RACMG7 :ERROR DNI DID NOT CLEAR!
7803 034516' 000135          TRAP C$ERHRD
7804 034520' 002431'        .WORD 93
7805 034522' 012726'        .WORD DMAFRM
7806 034524'                .WORD RACMG7
7807 034524' 104410          ESCAPE TST
7808 034526' 000022          TRAP C$ESCAPE
7809 034530' 022737 074000 000604' 57$: CMP #74000,PCBB+0      :HAVE WE CHECKED ALL 1K CHUNKS?
7810 034536' 001404          BEQ 60$              :YES
7811 034540' 062737 004000 000604' ADD #4000,PCBB+0      :NO, CHECK NEXT 1K
7812 034546' 000676          BR 30$               :DO AGAIN
7813 034550'                60$:
7814 034550'                ENDTST
7815 034550'                L10135:
7816 034550' 104401          TRAP C$ETST

```

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.SBTTL TEST 20: DMA BLOCK TRANSFER TEST
:*****
:THIS TEST WILL VERIFY THAT THE DMA ENGINE CAN TRANSFER A MAXIMUM SIZE DATA
:BLOCK TO HOST MEMORY.
:THIS TEST USES CUSTOM MICROCODE MODULE C, MICROTEST #6. THE MICROTEST
:FILLS EACH LOCATION OF LINK MEMORY WITH ITS ADDRESS AND THEN SETS
:UP A TRANSFER FROM LINK MEMORY TO THE ADDRESS POINTED TO BY PCBB+0.
:THE TRANSFER SIZE IS 1776 WORDS. AFTER THE MICROTEST FINISHES THE
:BUFFER IS CHECKED TO SEE IF IT CONTAINS THE INCREMENTING ADDRESS PATTERN.
:TEST SEQUENCE:
: 1-LOAD MICROCODE MODULE 'C' IF NOT ALREADY DONE SO
: 2-LOAD PCBB+0 WITH BUFFER ADDRESS
: 3-START MICROCODE
: 4-WAIT FOR DNI
: 5-VERIFY ALL 1776 WORDS STARTING AT BUFFER ADDRESS BASE
:*****

034552'
034552'

BGNTST
T20::
:CHECK TO SEE IF MODULE 'C' HAS BEEN LOADED. IF NOT LOAD IT INTO
:THE TOP HALF OF WCS, START IT AND WAIT FOR THE MICROMONITOR TO SET 'DNI'.
:AFTER 'DNI' SETS WRITE '1' TO CLEAR IT.

034552'
034552' 104404
034554' 022737
034564' 001004
034572' 001435
034574' 012737
034602' 004737
034606' 103530
034610' 012737
034616' 004737
034622' 103021
034624' 012737
034632' 012737
034640' 012737
034646' 012737
034654' 104456
034656' 000136
034660' 002501
034662' 012750
034664' 000501
034666' 004737
034672' 103005
034674' 104456
034676' 000137

BGNSEG
TRAP CSBSEG
:HAS MICRO MODULE C BEEN LOADED?
:NO
:YES, IS THE MICROMONITOR ACTIVE?
:YES SKIP LOADING THE MICROMODULE
:NO, LOAD MICRO MODULE C
:ERROR
:PUT SOME TIME ON THE METER
:WAIT FOR THE MICROMONITOR
:OK
TRAP CSERHRD
.WORD 94
.WORD DMABLK
.WORD MSG1
BR 70\$
JSR PC,CLRDN1 ;GO CLEAR THE DNI BIT
BCC 25\$
ERRHRD 095,DMABLK,RACMG7 ;DNI DID NOT CLEAR!
TRAP CSERHRD
.WORD 95

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```

7873 034700' 002501' .WORD DMABLK
7874 034702' 012726' .WORD RACMG7
7875 034704' 000471' BR 70$
7876 034706' 25$:
7877 034706' ENDSEG
7878 034706'
7879 034706' 104405 10000$: TRAP C$ESEG
7880
7881 ; TELL MICROCODE TO DMA TO A BUFFER IN FREE MEMORY LOCATED ABOVE THIS DIAGNOSTIC
7882 ; BY LOADING PCBB+0 WITH THE ADDRESS TO DMA TO. WAIT FOR THE MICROMONITOR TO
7883 ; ENTER THE 'IN MONITOR' STATE AND LOAD THE COMMAND FIELD OF PCSRO WITH A 6
7884 ; TO START THE EXECUTION OF MICROTEST #6. WAIT FOR DNI TO SET INDICATING IT IS
7885 ; COMPLETE
7886
7887 034710' BGNSEG
7888 034710' 104404 TRAP C$BSEG
7889 034712' 013737 000324' 000604' MOV FREMEM,PCBB+0 ;GET ADDRESS OF FREE MEMORY
7890 034720' 004737 017776' JSR PC,CHKMON ;WAIT FOR MICROMONITOR
7891 034724' 103006 BCC 30$ ;OK
7892 034726' ERRHRD 096,DMABLK,MSG46 ;PRINT ERROR
7893 034726' 104456 TRAP C$ERHRD
7894 034730' 000140 .WORD 96
7895 034732' 002501' .WORD DMABLK
7896 034734' 016700' .WORD MSG46
7897 034736' ESCAPE TST ;LEAVE TEST
7898 034736' 104410 TRAP C$ESCAPE
7899 034740' 000136 .WORD L10136-.
7900 034742' 012777 000006 143366 30$: MOV #6,@PCSRO ;START MICROTEST #6
7901 034750' 012737 000473 000332' MOV #5*SECOND,METER ;PUT SOME TIME ON THE METER
7902 034756' 004737 017330' JSR PC,CHKDNI ;WAIT FOR MICROTEST FINISH
7903 034762' 103020 BCC 40$ ;OK
7904 034764' 004737 020050' JSR PC,CHKINT ;SEE IF ANY ERROR INTERRUPTS OCCURRED
7905 034770' 103006 BCC 35$ ;NO, OK
7906 034772' ERRHRD 097,DMABLK,MSG44 ;PRINT ERROR MESSAGE
7907 034772' 104456 TRAP C$ERHRD
7908 034774' 000141 .WORD 97
7909 034776' 002501' .WORD DMABLK
7910 035000' 016454' .WORD MSG44
7911 035002' ESCAPE TST ;LEAVE TEST
7912 035002' 104410 TRAP C$ESCAPE
7913 035004' 000072 .WORD L10136-.
7914 035006' 012702 000006 35$: MOV #6,R2 ;MICROTEST NEVER FINISHED!
7915 035012' ERRHRD 098,DMABLK,MSG12
7916 035012' 104456 TRAP C$ERHRD
7917 035014' 000142 .WORD 98
7918 035016' 002501' .WORD DMABLK
7919 035020' 013500' .WORD MSG12
7920 035022' 000422 BR 70$
7921
7922 ; OK NOW CHECK THE DATA TRANSFERRED FROM LINK MEMORY TO UNIBUS MEMORY.
7923 ; THE DATA PATTERN IS ACTUALLY THE ADDRESS FROM WHICH THE DATA CAME FROM.
7924 ; THE FIRST LOCATION WILL BE 4 BYTES FROM THE BEGINING OF LINK MEMORY BECAUSE
7925 ; THE DMA FROM ADDRESS REGISTER DOES NOT USE BITS 0 AND 1.
7926
7927 035024' 013702 000604' 40$: MOV PCBB+0,R2 ;GET ADDRESS OF BUFFER
7928 035030' 012746 100004' MOV #100004,-(SP) ;STARTING DATA PATTERN
    
```

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7929	035034	021216		50\$:	CMP	(R2),(SP)		:IS DATA PATTERN CORRECT?		
7930	035036	001405			BEO	60\$:YES		
7931	035040	010601			MOV	SP,R1		:NO GET GOOD DATA		
7932	035042				ERRHRD	099,DMABLK,MSG15		:PRINT ERROR MESSAGE		
7933	035042	104456							TRAP	C\$ERHRD
7934	035044	000143							.WORD	99
7935	035046	002501							.WORD	DMABLK
7936	035050	013620							.WORD	MSG15
7937	035052	021627	103776	60\$:	CMP	(SP),#103776		:DONE ALL DATA?		
7938	035056	002004			BGE	70\$:YES		
7939	035060	0627:6	000002		ADD	#2,(SP)		:NEXT GOOD DATA PATTERN		
7940	035064	005722			TST	(R2)+		:NEXT BUFFER ADDRESS		
7941	035066	000762			BR	50\$:CONTINUE CHECKING		
7942	035070	004737	020104	70\$:	JSR	PC,REUNA		:RESTORE OPERATIONAL MICROCODE		
7943	035074				ENDSEG					
7944	035074								10001\$:	
7945	035074	104405							TRAP	C\$ESEG
7946	035076			ENDTST						
7947	035076								L10136:	
7948	035076	104401							TRAP	C\$ETST

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TEST 21: TRANSMIT DONE TEST

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.SBTTL TEST 21: TRANSMIT DONE TEST

.....
:THE TRANSMIT STATE MACHINE INFORMS THE PORT MODULE PROCESSOR OF A
:'TRANSMIT DONE' CONDITION. IT DOES THIS BY GENERATING AN INTERRUPT WHENEVER
:IT FINISHES TRANSMITTING A DATAGRAM. SINCE THE 'TRANSMIT DONE' INTERRUPT IS
:A NECESSARY CONDITION OF EVERY DATAGRAM TRANSMISSION, THIS TEST WILL USE THE
:INTERRUPT TO INDICATE THAT THE TRANSMIT STATE MACHINE IS FUNCTIONING.
:MICROCODE MODULE D MICROTEST #1 WILL BE USED FOR THIS TEST. IT SETS
:UP THE T-11 FOR AN INTERRUPT, STARTS A DATAGRAM LOOPBACK AND WAITS FOR A
:TRANSMIT INTERRUPT. THE T-11 WILL BE RELEASED FROM THE LOOP IF THE XMIT DONE
:INTERRUPT OCCURS. UPON RELEASE THE DNI BIT WILL BE SET IN PCSRO SIGNALING
:THAT THE TEST IS COMPLETE.
:TEST SEQUENCE:
: 1-LOAD MICROCODE MODULE 'D' IF NOT ALREADY DONE SO
: 2-VERIFY THE MICROMONITOR IN THE 'IN MONITOR' STATE
: 3-SELECT MICROTEST #1
: 4-VERIFY 'DNI' BIT IN PCSRO AFTER A RESONABLE PERIOD OF TIME
: 5-WRITE ONE TO CLEAR DNI
:.....

BGNTST

T21::

:CHECK TO SEE IF MODULE 'D' HAS BEEN LOADED. IF NOT LOAD IT INTO
:THE TOP HALF OF WCS, START IT AND WAIT FOR THE MICROMONITOR TO SET 'DNI'
:AFTER 'DNI' SETS WRITE '1' TO CLEAR IT.

BGNSEG

TRAP C\$BSEG
:HAS MICROCODE MODULE 'D' BEEN LOADED
:NO
:YES, IS THE MICROMONITOR ACTIVE?
:YES SKIP LOADING THE MICROMODULE
:GO LOAD MICRO MODULE 'D'
:OK
TRAP C\$ESCAPE
.WORD L10137-
:WAIT FOR THE MICROMONITOR
:OK
TRAP C\$ERHRD
.WORD 100
.WORD TRNDON
.WORD MSG1

035100'
035100'

035100'
035100' 104404
035102' 022737 000104 000326'
035110' 001004
035112' 122777 000001 143220
035120' 001440
035122' 012737 000104 000326' 5\$:
035130' 004737 020252'
035134' 103002
035136'
035136' 104410
035140' 000246
035142' 012737 000176 000332' 10\$:
035150' 004737 017330'
035154' 103022
035156' 012737 000776' 000310'
035164' 012737 001275' 000312'
035172' 012737 001340' 000314'
035200' 012737 001355' 000316'
035206'
035206' 104456
035210' 000144
035212' 002540'
035214' 012750'

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```

8005 035216'          ESCAPE TST
8006 035216' 104410          TRAP  C$ESCAPE
8007 035220' 000166          .WORD L10137-.
8008 035222' 004737 017374' 20$: JSR    PC,CLRDNI          ;CLEAR DNI BIT
8009 035226' 103006          BCC    25$
8010 035230'          ERRHRD 101,TRNDON,RACMG7 ;DNI DID NOT CLEAR!
8011 035230' 104456          TRAP  C$ERHRD
8012 035232' 000145          .WORD 101
8013 035234' 002540'          .WORD TRNDON
8014 035236' 012726'          .WORD RACMG7
8015 035240'          ESCAPE TST
8016 035240' 104410          TRAP  C$ESCAPE
8017 035242' 000144          .WORD L10137-.
8018 035244'          25$:
8019 035244'          ENDSEG
8020 035244'          10000$:
8021 035244' 104405          TRAP  C$ESEG
8022
8023          ;WAIT FOR THE MICROMONITOR TO ENTER THE 'IN MONITOR' STATE. LOAD THE COMMAND
8024          ;FIELD BITS OF PCSRO WITH A 1 TO START THE EXECUTION OF MICROTEST #1.
8025          ;WAIT ABOUT 1 SECOND FOR IT TO FINISH. IF NO 'DNI' SET PRINT ERROR.
8026          ;
8027 035246'          BGNSEG
8028 035246' 104404          TRAP  C$BSEG
8029 035250' 004737 017776'          JSR    PC,CHKMON          ;WAIT FOR MICROMONITOR
8030 035254' 103006          BCC    30$
8031 035256'          ERRHRD 102,TRNDON,MSG46 ;OK
8032 035256' 104456          ;PRINT ERROR
8033 035260' 000146          TRAP  C$ERHRD
8034 035262' 002440'          .WORD 102
8035 035264' 016700'          .WORD TRNDON
8036 035266'          .WORD MSG46
8037 035266' 104410          ESCAPE TST          ;LEAVE TEST
8038 035270' 000116          TRAP  C$ESCAPE
8039 035272' 012777 000001 143036 30$: MOV    #1,@PCSRO          ;TELL T11 TO EXECUTE FIRST MICROTEST
8040 035300' 012737 000077 000332' MOV    #1*SECOND,METER ;WAIT FOR DNI
8041 035306' 004737 017330' JSR    PC,CHKDNI
8042 035312' 103025          BCC    40$
8043          ;OK-'DNI' SET
8044          ;ERROR 'DNI' DID NOT SET! NO TRANSMIT
8045          ;INTERRUPT!
8045 035314'          ERRHRD 103,TRNDON,MSG36 ;PRINT ERROR MESSAGE
8046 035314' 104456          TRAP  C$ERHRD
8047 035316' 000147          .WORD 103
8048 035320' 002540'          .WORD TRNDON
8049 035322' 016002'          .WORD MSG36
8050 035324' 004737 020050' JSR    PC,CHKINT          ;SEE IF ANY ERROR INTERRUPTS OCCURRED
8051 035330' 103006          BCC    35$
8052 035332'          ERRHRD 103,TRNDON,MSG44 ;NO, OK
8053 035332' 104456          ;PRINT ERROR MESSAGE
8054 035334' 000147          TRAP  C$ERHRD
8055 035336' 002540'          .WORD 103
8056 035340' 016454'          .WORD TRNDON
8057 035342'          .WORD MSG44
8058 035342' 104410          ESCAPE TST          ;LEAVE TEST
8059 035344' 000042          TRAP  C$ESCAPE
8060 035346' 012702 000001 35$: MOV    #1,R2          ;MICROTEST #1 IS HUNG
    
```

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TEST 21: TRANSMIT DONE TEST

8061	035352'		ERRHRD 104,TRNDON,MSG12		:PRINT MESSAGE ABOUT HUNG MICROTST
8062	035352'	104456			TRAP C\$ERHRD
8063	035354'	000150			.WORD 104
8064	035356'	002540'			.WORD TRNDON
8065	035360'	013500'			.WORD MSG12
8066	035362'		ESCAPE TST		
8067	035362'	104410			TRAP C\$ESCAPE
8068	035364'	000022			.WORD L10137-
8069					
8070			:WRITE 1 TO CLEAR DNI BIT		
8071					
8072	035366'	004737 017374'	40\$: JSR PC,CLRDNI		:CLEAR DNI BIT
8073	035372'	103004	BCC 55\$		
8074	035374'		ERRHRD 105,TRNDON,RACMG7		:ERROR DNI DID NOT CLEAR!
8075	035374'	104456			TRAP C\$ERHRD
8076	035376'	000151			.WORD 105
8077	035400'	002540'			.WORD TRNDON
8078	035402'	012726'			.WORD RACMG7
8079	035404'		55\$:		
8080	035404'		ENDSEG		
8081	035404'				10001\$:
8082	035404'	104405			TRAP C\$ESEG
8083	035406'		ENDTST		
8084	035406'				L10137:
8085	035406'	104401			TRAP C\$ETST

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TEST 22: RECEIVER DONE TEST

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035410'
035410'

035410'
035410' 104404
035412' 022737 000104 000326'
035420' 001004
035422' 122777 000001 142710
035430' 001440
035432' 012737 000104 000326' 5\$:
035440' 004737 020252'
035444' 103002
035446'
035446' 104410
035450' 000246
035452' 012737 000176 000332' 10\$:
035460' 004737 017330'
035464' 103022
035466' 012737 000776' 000310'
035474' 012737 001275' 000312'
035502' 012737 001340' 000314'
035510' 012737 001355' 000316'
035516'
035516' 104456
035520' 000152
035522' 002572'

.SBTTL TEST 22: RECEIVER DONE TEST

:THE LINK HARDWARE INCLUDES LOGIC TO TELL THE DEUNA PROCESSOR WHEN
:A LINK MEMORY BUFFER HAS BEEN FILLED AND DATA IS AVAILABLE FOR PROCESSING.
:THE HARDWARE INTERRUPTS THE DEUNA PROCESSOR. BECAUSE THE INTERRUPT HAPPENS
:WHEN A LINK MEMORY BUFFER IS FULL AND THE LINK MEMORY IS FILLED BY THE
:OPERATION OF THE RECEIVE STATE MACHINE, THE INTERRUPT CAN BE USED TO CHECK
:IF THE STATE MACHINE WORKS.
:MICROCODE MODULE D MICROTST #2 WILL BE USED FOR THIS TEST. IT SETS
:UP THE T-11 FOR AN INTERRUPT, STARTS A DATAGRAM LOOPBACK AND WAITS FOR A
:RECEIVER INTERRUPT. THE T-11 WILL BE RELEASED FROM THE LOOP IF THE
:INTERRUPT OCCURS. UPON RELEASE THE DNI BIT WILL BE SET IN PCSRO SIGNALING
:THAT THE TEST IS COMPLETE.
:TEST SEQUENCE:
: 1-LOAD MICROCODE MODULE 'D' IF NOT ALREADY DONE SO
: 2-VERIFY THE MICROMONITOR IN THE 'IN MONITOR' STATE
: 3-SELECT MICROTST #2
: 4-VERIFY 'DNI' BIT IN PCSRO AFTER A REASONABLE PERIOD OF TIME
: 5-WRITE ONE TO CLEAR DNI

BGNTST

T2?::

:CHECK TO SEE IF MODULE 'D' HAS BEEN LOADED. IF NOT LOAD IT INTO
:THE TOP HALF OF WCS, START IT AND WAIT FOR THE MICROMONITOR TO SET 'DNI'
:AFTER 'DNI' SETS WRITE '1' TO CLEAR IT.

BGNSEG

TRAP CSBSEG
:HAS MICROCODE MODULE 'D' BEEN LOADED
:NO
:YES, IS THE MICROMONITOR ACTIVE?
:YES SKIP LOADING THE MICROMODULE
:GO LOAD MICRO MODULE 'D'
:OK
TRAP C\$ESCAPE
.WORD L10140-
:WAIT FOR THE MICROMONITOR
:OK
TRAP C\$ERHRD
.WORD 106
.WORD RCVDON

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CZUAAA.P11 11-JAN-83 09:29 TEST 22: RECEIVER DONE TEST

```

8142 035524' 012750' .WORD MSG1
8143 035526' ESCAPE TST TRAP C$ESCAPE
8144 035526' 104410 .WORD L10140-.
8145 035530' 000166
8146 035532' 004737 017374' 20$: JSR PC,CLRDNI :CLEAR DNI BIT
8147 035536' 103006 BCC 25$
8148 035540' ERRHRD 107,RCVDON,RACMG7 :DNI DID NOT CLEAR!
8149 035540' 104456 TRAP C$ERHRD
8150 035542' 000153 .WORD 107
8151 035544' 002572' .WORD RCVDON
8152 035546' 012726' .WORD RACMG7
8153 035550' ESCAPE TST
8154 035550' 104410 TRAP C$ESCAPE
8155 035552' 000144 .WORD L10140-.
8156 035554' 25$:
8157 035554' ENDSEG
8158 035554'
8159 035554' 104405 10000$: TRAP C$ESEG
8160
8161 ;WAIT FOR THE MICROMONITOR TO ENTER THE 'IN MONITOR' STATE. LOAD THE COMMAND
8162 ;FIELD BITS OF PCSRO WITH A 2 TO START THE EXECUTION OF MICROTEST #2.
8163 ;WAIT ABOUT 1 SECOND FOR IT TO FINISH. IF NO 'DNI' SET PRINT ERROR.
8164 ;
8165 ; BGNSEG
8166 035556' 104404 TRAP C$BSEG
8167 035560' 004737 017776' JSR PC,CHKMON :WAIT FOR MICROMONITOR
8168 035564' 103006 BCC 30$ :OK
8169 035566' ERRHRD 108,RCVDON,MSG46 :PRINT ERROR
8170 035566' 104456 TRAP C$ERHRD
8171 035570' 000154 .WORD 108
8172 035572' 002572' .WORD RCVDON
8173 035574' 016700' .WORD MSG46
8174 035576' ESCAPE TST :LEAVE TEST
8175 035576' 104410 TRAP C$ESCAPE
8176 035600' 000116 .WORD L10140-.
8177 035602' 012777 000002 142526 30$: MOV #2,@PCSRO :TELL T11 TO EXECUTE MICROTEST #2
8178 035610' 012737 000077 000332' MOV #1*SECOND,METER :WAIT FOR DNI
8179 035616' 004737 017330' JSR PC,CHKDNI
8180 035622' 103025 BCC 40$
8181 :OK DNI SET
8182 :ERROR-DNI NOT SET, NO RECEIVER
8183 ERRHRD 109,RCVDON,MSG37 :INTERRUPT
8184 035624' 104456 :PRINT ERROR MESSAGE
8185 035626' 000155 TRAP C$ERHRD
8186 035630' 002572' .WORD 109
8187 035632' 016024' .WORD RCVDON
8188 035634' 004737 020050' JSR PC,CHKINT :SEE IF ANY ERROR INTERRUPTS OCCURRED
8189 035640' 103006 BCC 35$ :NO, OK
8190 035642' ERRHRD 110,RCVDON,MSG44 :PRINT ERROR MESSAGE
8191 035642' 104456 TRAP C$ERHRD
8192 035644' 000156 .WORD 110
8193 035646' 002572' .WORD RCVDON
8194 035650' 016454' .WORD MSG44
8195 035652' ESCAPE TST
8196 035652' 104410 TRAP C$ESCAPE
8197 035654' 000042 .WORD L10140-.

```

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TEST 22: RECEIVER DONE TEST

8198 035656' 012702 000002
8199 035662'
8200 035662' 104456
8201 035664' 000157
8202 035666' 002572'
8203 035670' 013500'
8204 035672'
8205 035672' 104410
8206 035674' 000022
8207
8208
8209
8210 035676' 004737 017374'
8211 035702' 103004
8212 035704'
8213 035704' 104456
8214 035706' 000160
8215 035710' 002572'
8216 035712' 012726'
8217 035714'
8218 035714'
8219 035714'
8220 035714' 104405
8221 035716'
8222 035716'
8223 035716' 104401

35\$: MOV #2,R2
ERRHRD 111,RCVDON,MSG12

:MICROTEST #2 IS HUNG
:PRINT ERROR MESSAGE

TRAP C\$ERHRD
.WORD 111
.WORD RCVDON
.WORD MSG12

ESCAPE TST

TRAP C\$ESCAPE
.WORD L10140-

:WRITE 1 TO CLEAR DNI BIT

40\$: JSR PC,CLRDNI
BCC 55\$
ERRHRD 112,RCVDON,RACMG7

:CLEAR DNI BIT
:ERROR DNI DID NOT CLEAR!

TRAP C\$ERHRD
.WORD 112
.WORD RCVDON
.WORD RACMG7

55\$:
ENDSEG

10001\$:

TRAP C\$ESEG

ENDTST

L10140:

TRAP C\$ETST

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```

8224
8225      .SBTTL TEST 23: DATA BYTE FRAMING TEST
8226
8227      :*****
8228
8229      :THIS TEST WILL CHECK THE LINK MODULE DATA PATH FOR BYTE DATA BOUNDARY
8230      :CONDITIONS.
8231
8232      :THE T-11 PROCESSOR WILL TRANSMIT DATA IN LOOPBACK MODE. THE DATA WILL BE
8233      :ORGANIZED SUCH THAT DATA BOUNDARIES ARE CREATED BETWEEN ADJACENT BYTES
8234      :IN THE DATA STREAM (I.E. 11111111000000011...) THE T-11 PROCESSOR WILL
8235      :VERIFY THE CONDITION OF THE DATA AFTER IT IS LOOPED BACK TO THE RECEIVER
8236      :DATA BUFFER.
8237
8238      :THIS TEST WILL USE MICROCODE MODULE 'D' MICROTTEST #3. TESTING OF THE DATA
8239      :FRAMING WILL BE DONE BY THE T-11 PROCESSOR. THE HOST PROCESSOR, MEANWHILE,
8240      :WILL WAIT FOR A 'DNI' IN REGISTER PCSR0. IF 'DNI' APPEARS, THE HOST PROCESSOR
8241      :WILL CHECK PCSR1 FOR AN ERROR CONDITION. IF AN ERROR CONDITION IS SET,
8242      :ADDITIONAL ERROR INFORMATION WILL BE FOUND IN THE PCBB AS FOLLOWS:
8243
8244      :PCBB+0: RECEIVER STATUS WORD
8245      :PCBB+2: DATA TRANSMITTED
8246      :PCBB+4: DATA RECEIVED
8247      :PCBB+6: WORD OFFSET INTO RECEIVER BUFFER OF BAD DATA
8248
8249      :TEST SEQUENCE:
8250      :   1-LOAD MICROCODE MODULE 'D' IF NOT ALREADY DONE SO
8251      :   2-VERIFY MICROMONITOR TO ENTER THE 'INMON' STATE
8252      :   3-CLEAR OUT THE PCBB+,+2,+4,+6
8253      :   4-SELECT MICROTTEST #3
8254      :   5-VERIFY 'DNI' BIT SET IN PCSR0
8255      :   6-CHECK FOR AN ERROR CONDITION IN PCSR1
8256      :   7-WRITE ONE TO CLEAR THE DNI BIT
8257
8258      :*****

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8259
8260 035720' BGNTST
8261 035720'
8262
8263
8264
8265
8266
8267
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      BGNSEG
      TRAP C$BSEG
      :HAS MICROCODE MODULE 'D' BEEN LOADED
      :NO
      :YES, IS THE MICROMONITOR ACTIVE?
      :YES SKIP LOADING THE MICROMODULE
      :GO LOAD MICRO MODULE 'D'
      :OK
      TRAP C$ESCAPE
      .WORD L10141-
      :WAIT FOR THE MICROMONITOR

```

```

035720' 104404
035722' 022737 000104 000326'
035730' 001004
035732' 122777 000001 142400
035740' 001440
035742' 012737 000104 000326' 5$:
035750' 004737 020252'
035754' 103002
035756'
035756' 104410
035760' 000316
035762' 012737 000176 000332' 10$:

```

```

      CMP #'D,MICRO
      BNE 5$
      CMPB #INMON,@PCSR1
      BEQ 20$
      MOV #'D,MICRO
      JSR PC,LODMIC
      BCC 10$
      ESCAPE TST
      MOV #2*SECOND,METER

```

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```

8280 035770' 004737 017330' JSR PC,CHKDNI
8281 035774' 103022 BCC 20$ ;OK
8282 035776' 012737 000776' 000310' MOV #SDNI,BITNAM ;ERROR 'DNI' BIT NEVER SET
8283 036004' 017737 001275' 000312' MOV #SNSEF,BITSTA
8284 036012' 012737 001340' 000314' MOV #SAFTE,PWHEN
8285 036020' 012737 001355' 000316' MOV #SGTCMD,PLUMIN
8286 036026' ERRHRD 113,DBFRAM,MSG1
8287 036026' 104456 TRAP C$ERHRD
8288 036030' 000161 .WORD 113
8289 036032' 002624' .WORD DBFRAM
8290 036034' 012750' .WORD MSG1
8291 036036' ESCAPE TST
8292 036036' 104410 TRAP C$ESCAPE
8293 036040' 000236 .WORD L10141-.
8294 036042' 004737 017374' 20$: JSR PC,CLRDNI ;CLEAR DNI BIT
8295 036046' 103006 BCC 25$
8296 036050' ERRHRD 114,DBFRAM,RACMG7 ;DNI DID NOT CLEAR.
8297 036050' 104456 TRAP C$ERHRD
8298 036052' 000162 .WORD 114
8299 036054' 002624' .WORD DBFRAM
8300 036056' 012726' .WORD RACMG7
8301 036060' ESCAPE TST
8302 036060' 104410 TRAP C$ESCAPE
8303 036062' 000214 .WORD L10141-.
8304 036064' 25$:
8305 036064' ENDSEG
8306 036064'
8307 036064' 104405 1000): TRAP C$ESEG
8308
8309 ;WAIT FOR MICROMONITOR TO ENTER THE 'INMON' STATE. CLEAR THE PCBB. LOAD THE
8310 ;COMMAND FIELD OF PCSRO WITH A 3 TO START THE EXECUTION OF MICROTEST #3.
8311 ;WAIT FOR THE 'DNI' BIT. IF NO 'DNI' OR IF ANY EXTRANEIOUS INTERRUPTS HAPPEN
8312 ;PRINT ERROR
8313 ;
8314
8315 BGNSEG
8316 036066' 104404 TRAP C$BSEG
8317 036070' 004737 017776' JSR PC,CHKMON ;WAIT FOR MICROMONITOR
8318 036074' 103006 BCC 30$ ;OK
8319 036076' ERRHRD 115,DBFRAM,MSG46 ;PRINT ERROR
8320 036076' 104456 TRAP C$ERHRD
8321 036100' 000163 .WORD 115
8322 036102' 002624' .WORD DBFRAM
8323 036104' 016700' .WORD MSG46
8324 036106' ESCAPE TST ;LEAVE TEST
8325 036106' 104410 TRAP C$ESCAPE
8326 036110' 000166 .WORD L10141-.
8327 036112' 005037 000604' 30$: CLR PCBB ;THIS IS WHERE MICROCODE WILL PUT...
8328 ;RECEIVE BUFFER STATUS WORD
8329 036116' 005037 000606' CLR PCBB+2 ;HERE IS WHERE THE GOOD DATA GOES
8330 036122' 005037 000610' CLR PCBB+4 ;HERE IS WHERE THE BAD DATA GOES
8331 036126' 005037 000612' CLR PCBB+6 ;HERE IS WHERE THE BUFFER OFFSET GOES
8332 036132' 012777 000003 142176 MOV #3,@PCSRO ;TELL T11 TO EXECUTE MICROTEST #3
8333 036140' 012737 000176 000332' MOV #2*SECOND,METER ;WAIT FOR DNI
8334 036146' 004737 017330' JSR PC,CHKDNI
8335 036152' 103021 BCC 40$

```


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```

8336 036154' 004737 020050'      JSR      PC,CHKINT      ;SEE IF ANY ERROR INTERRUPTS OCCURRED
8337 036160' 103006              BCC      35$           ;NO, OK
8338 036162'                    ERRHRD   116,DBFRAM,MSG44 ;PRINT ERROR MESSAGE
8339 036162' 104456              TRAP     C$ERHRD
8340 036164' 000164              .WORD   116
8341 036166' 002624'            .WORD   DBFRAM
8342 036170' 016454'            .WORD   MSG44
8343 036172'                    ESCAPE   TST
8344 036172' 104410              TRAP     C$ESCAPE
8345 036174' 000102              .WORD   L10141-.
8346 036176' 012702 000003      35$:    MOV      #3,R2      ;MICROTEST #3 IS HUNG
8347 036202'                    ERRHRD   117,DBFRAM,MSG12
8348 036202' 104456              TRAP     C$ERHRD
8349 036204' 000165              .WORD   117
8350 036206' 002624'            .WORD   DBFRAM
8351 036210' 013500'            .WORD   MSG12
8352 036212'                    ESCAPE   TST
8353 036212' 104410              TRAP     C$ESCAPE
8354 036214' 000062              .WORD   L10141-.
8355
8356      ;OK WE GOT 'DNI' NOW CHECK PCSR1 TO SEE IF THE T-11 FOUND A BOGUS BYTE IN THE
8357      ;RECEIVED DATA. IF SO, GET THE INFORMATION FROM THE PCBB AND PRINT ERROR
8358
8359 036216' 122777 000003 142114 40$:    CMPB    #INERR,@PCSR1    ;DID AN ERROR OCCUR?
8360 036224' 001014              BNE     50$           ;NO
8361 036226' 013701 000612'      MOV     PCBB+6,R1      ;GET BUFFER OFFSET FOR ERROR REPORT
8362 036232' 013702 000606'      MOV     PCBB+2,R2      ;THIS IS THE GOOD DATA FOR ERROR REPORT
8363 036236' 013703 000610'      MOV     PCBB+4,R3      ;THIS IS THE BAD DATA FOR ERROR REPORT
8364 036242' 013704 000604'      MOV     PCBB,R4        ;THIS IS THE RECEIVER STATUS WORD
8365 036246'                    ERRHRD   118,DBFRAM,MSG17 ;PRINT ERROR MESSAGE
8366 036246' 104456              TRAP     C$ERHRD
8367 036250' 000166              .WORD   118
8368 036252' 002624'            .WORD   DBFRAM
8369 036254' 013742'            .WORD   MSG17
8370
8371      ;WRITE '1' TO CLEAR 'DNI' BIT
8372
8373 036256' 004737 017374'      50$:    JSR      PC,CLRDN1    ;CLEAR DNI BIT
8374 036262' 103004              BCC     55$           ;ERROR DNI DID NOT CLEAR!
8375 036264'                    ERRHRD   119,DBFRAM,RACMG7
8376 036264' 104456              TRAP     C$ERHRD
8377 036266' 000167              .WORD   119
8378 036270' 002624'            .WORD   DBFRAM
8379 036272' 012726'            .WORD   RACMG7
8380 036274'                    55$:
8381 036274'                    ENDSEG
8382 036274'
8383 036274' 104405              10001$: TRAP     C$ESEG
8384 036276'
8385 036276'
8386 036276' 104401              L10141: TRAP     C$ETST

```

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.SBTTL TEST 24: DATA WORD FRAMING TEST

:THIS TEST WILL CHECK THE LINK MODULE DATA PATH FOR WORD DATA BOUNDARY
:CONDITIONS.
:THE T-11 PROCESSOR WILL TRANSMIT DATA IN LOOPBACK MODE. THE DATA WILL BE
:ORGANIZED SUCH THAT DATA BOUNDARIES ARE CREATED BETWEEN ADJACENT WORDS
:IN THE DATA STREAM (I.E. 11111111111111110000000000000011...) THE T-11
:PROCESSOR WILL VERIFY THE CONDITION OF THE DATA AFTER IT IS LOOPED BACK TO
:THE RECEIVER DATA BUFFER.
:THIS TEST WILL USE MICROCODE MODULE 'D' MICROTTEST #4. TESTING OF THE DATA
:FRAMING WILL BE DONE BY THE T-11 PROCESSOR. THE HOST PROCESSOR, MEANWHILE,
:WILL WAIT FOR A 'DNI' IN REGISTER PCSRO. IF 'DNI' APPEARS, THE HOST PROCESSOR
:WILL CHECK PCSR1 FOR AN ERROR CONDITION. IF AN ERROR CONDITION IS SET,
:ADDITIONAL ERROR INFORMATION WILL BE FOUND IN THE PCBB AS FOLLOWS:
:PCBB+0: RECEIVER STATUS WORD
:PCBB+2: DATA TRANSMITTED
:PCBB+4: DATA RECEIVED
:PCBB+6: WORD OFFSET INTO RECEIVER BUFFER OF BAD DATA
:TEST SEQUENCE:
: 1-LOAD MICROCODE MODULE 'D' IF NOT ALREADY DONE SO
: 2-VERIFY MICROMONITOR IS IN THE 'INMON' STATE
: 3-CLEAR OUT THE PCBB+0,+2,+4,+6
: 4-SELECT MICROTTEST #4
: 5-VERIFY 'DNI' BIT SET IN PCSPO
: 6-CHECK FOR AN ERROR CONDITION IN PCSR1
: 7-WRITE ONE TO CLEAR THE DNI BIT

036300*
036300*

BGNTST
T24::
:CHECK TO SEE IF MODULE 'D' HAS BEEN LOADED. IF NOT LOAD IT INTO
:THE TOP HALF OF WCS, START IT AND WAIT FOR THE MICROMONITOR TO SET 'DNI'
:AFTER 'DNI' SETS WRITE '1' TO CLEAR IT.

036300*
036300* 104404
036302* 022737 000104 000326*
036310* 001004
036312* 122777 000001 142020
036320* 001440
036322* 012737 000104 000326* 5\$:
036330* 004737 020252*
036334* 103002
036336*
036336* 104410
036340* 000316
036342* 012737 000176 000332* 10\$:
036350* 004737 017330*

BGNSEG
TRAP C\$BSEG
:HAS MICROCODE MODULE 'D' BEEN LOADED
:NO
:YES, IS THE MICROMONITOR ACTIVE?
:YES SKIP LOADING THE MICROMODULE
:GO LOAD MICRO MODULE 'D'
:OK
TRAP C\$ESCAPE
:WORD L10142-.
:WAIT FOR THE MICROMONITOR
MOV #2*SECOND,METER
JSR PC,CHKDNI

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8443 036354' 103022          BCC      20$          ;OK
8444 036356' 012737 000776' 000310'    MOV      #DNI,BITNAM
8445 036364' 012737 001275' 000312'    MOV      #NSET,BITSTA
8446 036372' 012737 001340' 000314'    MOV      #AFTER,PWHEN
8447 036400' 012737 001355' 000316'    MOV      #SGTCMD,PCOMND
8448 036406'          ERRHRD   120,DWFRAM,MSG1
8449 036406' 104456          TRAP     C$ERHRD
8450 036410' 000170          .WORD   120
8451 036412' 002662'          .WORD   DWFRAM
8452 036414' 012750'          .WORD   MSG1
8453 036416'          ESCAPE   TST
8454 036416' 104410          TRAP     C$ESCAPE
8455 036420' 000236          .WORD   L10142-.
8456 036422' 004737 017374'    20$:    JSR      PC,CLRDNI    ;CLEAR DNI BIT
8457 036426' 103006          BCC      25$
8458 036430'          ERRHRD   121,DWFRAM,RACMG7 ;DNI DID NOT CLEAR!
8459 036430' 104456          TRAP     C$ERHRD
8460 036432' 000171          .WORD   121
8461 036434' 002662'          .WORD   DWFRAM
8462 036436' 012726'          .WORD   RACMG7
8463 036440'          ESCAPE   TST
8464 036440' 104410          TRAP     C$ESCAPE
8465 036442' 000214          .WORD   L10142-.
8466 036444'    25$:
8467 036444'          ENDSEG
8468 036444'          10000$:
8469 036444' 104405          TRAP     C$ESEG
8470
8471          ;
8472          ;WAIT FOR MICROMONITOR TO ENTER THE 'INMON' STATE. CLEAR THE PCBB. LOAD THE
8473          ;COMMAND FIELD OF PCSRO WITH A 4 TO START THE EXECUTION OF MICROTEST #4.
8474          ;WAIT FOR THE 'DNI' BIT. IF NO 'DNI' OR IF ANY EXTRANEIOUS INTERRUPTS HAPPEN
8475          ;PRINT ERROR
8476          ;
8476 036446'          BGNSEG
8477 036446' 104404          TRAP     C$BSEG
8478 036450' 004737 017776'    JSR      PC,CHKMON    ;WAIT FOR MICROMONITOR
8479 036454' 103006          BCC      30$          ;OK
8480 036456'          ERRHRD   122,DWFRAM,MSG46 ;PRINT ERROR
8481 036456' 104456          TRAP     C$ERHRD
8482 036460' 000172          .WORD   122
8483 036462' 002662'          .WORD   DWFRAM
8484 036464' 016700'          .WORD   MSG46
8485 036466'          ESCAPE   TST          ;LEAVE TEST
8486 036466' 104410          TRAP     C$ESCAPE
8487 036470' 000166          .WORD   L10142-.
8488 036472' 005037 000604'    30$:    CLR      PCB
8489          ;THIS IS WHERE MICROCODE WILL PUT...
8490          ;RECEIVE BUFFER STATUS WORD
8491          ;HERE IS WHERE THE GOOD DATA GOES
8492          ;HERE IS WHERE THE BAD DATA GOES
8493          ;HERE IS WHERE THE BUFFER OFFSET GOES
8494          ;TELL T11 TO EXECUTE MICROTEST #4
8495          ;WAIT FOR DNI
8496 036532' 103021          BCC      40$
8497 036534' 004737 020050'    JSR      PC,CHKINT    ;SEE IF ANY ERROR INTERRUPTS OCCURRED
8498 036540' 103006          BCC      35$          ;NO, OK

```

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8499 036542' ERPHRD 123,DWFRAM,MSG44 ;PRINT ERROR MESSAGE
8500 036542' 104456 TRAP C$ERHRD
8501 036544' 000173 .WORD 123
8502 036546' 002662' .WORD DWFRAM
8503 036550' 016454' .WORD MSG44
8504 036552' ESCAPE TST
8505 036552' 104410 TRAP C$ESCAPE
8506 036554' 000102 .WORD L10142-.
8507 036556' 012702 000004 35$: MOV #4,R2 ;MICROTEST #4 IS HUNG
8508 036562' ERRHRD 24,DWFRAM,MSG12
8509 036562' 104456 TRAP C$ERHRD
8510 036564' 000174 .WORD 124
8511 036566' 002662' .WORD DWFRAM
8512 036570' 013500' .WORD MSG12
8513 036572' ESCAPE TST
8514 036572' 104410 TRAP C$ESCAPE
8515 036574' 000062 .WORD L10142-.
8516
8517 ;OK WE GOT 'DNI' NOW CHECK PCSR1 TO SEE IF THE T-11 DETECTED A BOGUS WORD
8518 ;IN THE RECEIVER BUFFER. IF SO, PRINT ERROR.
8519
8520 036576' 122777 000003 141534 40$: CMPB #INERR,@PCSR1 ;DID AN ERROR OCCUR?
8521 036604' 001014 BNE 50$ ;NO
8522 036606' 013701 000612' MOV PCBB+6,R1 ;GET BUFFER OFFSET FOR ERROR REPORT
8523 036612' 013702 000606' MOV PCBB+2,R2 ;THIS IS THE GOOD DATA FOR ERROR REPORT
8524 036616' 013703 000610' MOV PCBB+4,R3 ;THIS IS THE BAD DATA FOR ERROR REPORT
8525 036622' 013704 000604' MOV P 9B,R4 ;THIS IS THE RECEIVER STATUS WORD
8526 036626' ERRHRD 125,DWFRAM,MSG17 ;PRINT ERROR MESSAGE
8527 036626' 104456 TRAP C$ERHRD
8528 036630' 000175 .WORD 125
8529 036632' 002662' .WORD DWFRAM
8530 036634' 013742' .WORD MSG17
8531
8532 ;WRITE '1' TO CLEAR 'DNI' BIT
8533
8534 036636' 004737 017374' 50$: JSR PC,CLRDN1 ;CLEAR DNI BIT
8535 036642' 103004 BCC 55$
8536 036644' ERRHRD 126,DWFRAM,RACMG7 ;ERROR DNI DID NOT CLEAR!
8537 036644' 104456 TRAP C$ERHRD
8538 036646' 000176 .WORD 126
8539 036650' 002662' .WORD DWFRAM
8540 036652' 012726' .WORD RACMG7
8541 036654' 55$:
8542 036654' ENDSEG
8543 036654' 10001$:
8544 036654' 104405 TRAP C$ESEG
8545 036656'
8546 036656' L10142:
8547 036656' 10440i TRAP C$FTST
    
```

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TEST 25: DATA PATH PATTERN TEST

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036660'

036660'
036660' 104404
036662' 022737 000104 000326'
036670' 001004
036672' 122777 000001 141440
036700' 001440
036702' 012737 000104 000326' 5\$:
036710' 004737 020252'
036714' 103002
036716'
036716' 104410
036720' 000332
036722' 012737 000176 000332' 10\$:
036730' 004737 017330'
036734' 103022
036736' 012737 000776' 000310'
036744' 012737 001275' 000312'
036752' 012737 001340' 000314'
036760' 012737 001355' 000316'
036766'
036766' 104456
036770' 000177

.SBTTL TEST 25: DATA PATH PATTERN TEST
:*****
:THIS TEST WILL CHECK THE LINK MODULE DATA PATH FOR ALL 'STUCK AT 0' AND
: 'STUCK AT 1' ERRORS.
:THE T-11 PROCESSOR WILL TRANSMIT DATAGRAMS OF MAXIMUM LENGTH IN LOOPBACK MODE.
:THIS PATTERN LOOPBACK PROCEDURE WILL BE USED FOR ALL PATTEPNS OF UP TO WORD
:WIDTH.
:THIS TEST USES MICROMODULE 'D' MICROTTEST #5 TO DO THE TESTING. THE HOST
:PROCESSOR WILL PASS A DATA PATTERN TO THE T-11 PROCESSOR THROUGH THE PCBB.
:THE T-11 WILL FILL A XMIT BUFFER WITH THE DATA PATTERN AND TRANSMIT THE
:DATAGRAM OVER THE LOOPBACK. THE T-11 PROCESSOR WILL VERIFY THE PATTERN
:IN THE RECEIVER BUFFER. IF THE T-11 FINDS AN ERROR, IT WILL WRITE THE FAILING
:PATTERN TO THE PCBB ALONG WITH THE OFFSET INTO THE RECEIVER BUFFER AT WHICH
:THE PATTERN WAS FOUND. IT WILL INFORM THE HOST OF THE ERROR BY SETTING PCSR1
:TO AN ERROR CONDITION. THE PCBB IS FORMATTED AS FOLLOWS:
:PCBB+0: DATA PATTERN
:PCBB+2: RECEIVER STATUS WORD
:PCBB+4: BAD DATA PATTERN
:PCBB+6: OFFSET INTO RECEIVER BUFFER WHERE BAD DATA WAS FOUND
:*****

BGNTST
T25::
:CHECK TO SEE IF MODULE 'D' HAS BEEN LOADED. IF NOT LOAD IT INTO
:THE TOP HALF OF WCS, START IT AND WAIT FOR THE MICROMONITOR TO SET 'DNI'
:AFTER 'DNI' SETS WRITE '1' TO CLEAR IT.
BGNSEG
TRAP CSBSEG
;HAS MICROCODE MODULE 'D' BEEN LOADED
:NO
;YES, IS THE MICROMONITOR ACTIVE?
;YES SKIP LOADING THE MICROMODULE
;GO LOAD MICRO MODULE 'D'
:OK
TRAP C\$ESCAPE
.WORD L10143-
;WAIT FOR THE MICROMONITOR
:OK
TRAP C\$ERHRD
.WORD 127

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8660 037140' 016454' .WORD MSG44
8661 037142' ESCAPE TST
8662 037142' 104410 TRAP C$ESCAPE
8663 037144' 000106 .WORD L10143-
8664 037146' 012702 0000U5 35$: MOV #5,R2 ;MICROTEST #5 IS HUNG
8665 037152' ERRHRD 131,DPPAT,MSG12
8666 037152' 104456 TRAP C$ERHRD
8667 037154' 000203 .WORD 131
8668 037156' 002720' .WORD DPPAT
8669 037160' 013500' .WORD MSG12
8670 037162' ESCAPE TST
8671 037162' 104410 TRAP C$ESCAPE
8672 037164' 000066 .WORD L10143-
8673
8674 ;OK WE GOT 'DNI' NOW CHECK PCSR1 TO SEE IF AN ERROR HAPPENED, IF SO, PRINT
8675 ;PERTINENT INFORMATION
8676
8677 037166' 122777 000003 141144 40$: CMPB #INERR,@PCSR1 ;DID AN ERROR OCCUR?
8678 037174' 001014 BNE 50$ ;NO
8679 037176' 013701 000612' MOV PCBB+6,R1 ;GET BUFFER OFFSET FOR ERROR REPORT
8680 037202' 013702 000604' MOV PCBB,R2 ;THIS IS THE GOOD DATA FOR ERROR REPORT
8681 037206' 013703 000610' MOV PCBB+4,R3 ;THIS IS THE BAD DATA FOR ERROR REPORT
8682 037212' 013704 000606' MOV PCBB+2,R4 ;THIS IS THE RECEIVER STATUS WORD
8683 037216' ERRHRD 132,DPPAT,MSG17 ;PRINT ERROR MESSAGE
8684 037216' 104456 TRAP C$ERHRD
8685 037220' 000204 .WORD 132
8686 037222' 002720' .WORD DPPAT
8687 037224' 013742' .WORD MSG17
8688
8689 ;WRITE '1' TO CLEAR 'DNI' BIT
8690
8691 037226' 004737 017374' 50$: JSR PC,CLRDNI ;CLEAR DNI BIT
8692 037232' 103004 BCC 55$
8693 037234' ERRHRD 133,DPPAT,RACMG7 ;ERROR DNI DID NOT CLEAR!
8694 037234' 104456 TRAP C$ERHRD
8695 037236' 000205 .WORD 133
8696 037240' 002720' .WORD DPPAT
8697 037242' 012726' .WORD RACMG7
8698 037244' 55$:
8699 037244' ENDSEG
8700 037244' 10001$:
8701 037244' 104405 TRAP C$ESEG
8702 037246' 005305 .WORD 10001$
8703 037250' 001300 DEC R5 ;HAVE WE TESTED WITH ALL DATA PATTERNS?
8704 037252' BNE 27$ ;NOT YET
8705 037252'
8706 037252' 104401 L10143: TRAP C$ETST

```

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TEST 26: STATUS MUX VERIFICATION TEST

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.SBTTL TEST 26: STATUS MUX VERIFICATION TEST

:THE LINK WRITES STATUS IN LINK MEMORY AFTER EACH TRANSMIT ATTEMPT. THE STATUS
:GIVES INFORMATION ABOUT THE ATTEMPTED OPERATION. THE STATUS INFORMATION IS
:WRITTEN INTO THE FIRST TWO LOCATIONS OF THE TRANSMIT BUFFER. THIS INFORMATION
:IS ACCESSIBLE TO THE T-11 BY SIMPLY READING IT FROM LINK MEMORY.

:THIS TEST WILL VERIFY THAT THE STATUS INFORMATION IS WRITTEN INTO THE FIRST
:LOCATION OF THE TRANSMIT BUFFER. THE TEST WILL ALSO CHECK THE SECOND WORD
:OF THE TRANSMIT BUFFER.

:THIS TEST WILL USE MICROMODULE 'D' MICROTEST #6.
:WHEN THE TEST IS STARTED, THE T-11 PROCESSOR WILL SET UP THE LINK FOR LOOPBACK
:OF A DATA PATTERN. A BACKGROUND PATTERN WILL BE WRITTEN INTO THE FIRST WORD
:OF THE TRANSMIT BUFFER. THIS WORD SHOULD BE OVER-WRITTEN BY THE STATUS WHEN
:THE BUFFER IS TRANSMITTED. THE SECOND WORD OF THE TRANSMIT BUFFER CAN NOT BE
:WRITTEN WITH A BACKGROUND BECAUSE IT MUST DESIGNATE THE TRANSMIT BYTE COUNT.

:WHEN THE DATAGRAM HAS BEEN LOOPED BACK, THE T-11 PROCESSOR WILL PASS THE FIRST
:TWO WORDS OF THE TRANSMIT BUFFER TO THE HOST THRU THE PCBB+0 AND PCBB+2.

:PCBB+0: FIRST WORD OF TRANSMIT BUFFER
:PCBB+2: SECOND WORD OF TRANSMIT BUFFER

:THE CORRECT STATUS SHOULD BE:

TRANSMIT STATUS WORD 0 BITS 15,09:00 SHOULD BE ALL 0 AND
BIT 13 SHOULD BE A 1

TRANSMIT STATUS WORD 1 BITS 15:13 SHOULD ALL BE 0

:TEST SEQUENCE:

- 1-LOAD MICROMODULE 'D' IF NOT ALREADY DONE SO
- 2-VERIFY MICROMONITOR IS IN THE 'INMON' STATE
- 3-CLEAR PCBB+0 AND PCBB+2
- 4-SELECT MICROTEST #6
- 5-VERIFY 'DNI' SET
- 6-VERIFY PCBB+0 BITS 15,09:00 = 0 AND BIT 13 = 1
AND PCBB+2 BITS 15:13 = 0
- 7-WRITE ONE TO CLEAR 'DNI'

BGNTST

T26::

:CHECK TO SEE IF MODULE 'D' HAS BEEN LOADED. IF NOT LOAD IT INTO
:THE TOP HALF OF WCS, START IT AND WAIT FOR THE MICROMONITOR TO SET 'DNI'
:AFTER 'DNI' SETS WRITE '1' TO CLEAR IT.

BGNSEG

037254'
037254'

037254'
037254' 104404
037256' 022737
037264' 001004

000104 000326'

CMP #D,MICRO
BNE 58

TRAP C\$BSEG
:HAS MICROCODE MODULE 'D' BEEN LOADED
:NO

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8763 037266' 122777 000001 141044      CMPB  #INMON,@PCSR1      ;YES, IS THE MICROMONITOR ACTIVE?
8764 037274' 001440      BEQ   20$                ;YES SKIP LOADING THE MICROMODULE
8765 037276' 012737 000104 000326' 5$:      MOV   #'D,MICRO        ;GO LOAD MICRO MODULE 'D'
8766 037304' 004737 020252'      JSR   PC,LODMIC
8767 037310' 103002      BCC   10$                ;OK
8768 037312'      ESCAPE TST
8769 037312' 104410      TRAP C$ESCAPE
8770 037314' 000500      .WORD L10144-.
8771 037316' 012737 000176 000332' 10$:     MOV   #2*SECOND,METER ;WAIT FOR THE MICROMONITOR
8772 037324' 004737 017330'      JSR   PC,CHKDNI
8773 037330' 103022      BCC   20$                ;OK
8774 037332' 012737 000776' 000310'     MOV   #DNI,BITNAM
8775 037340' 012737 001275' 000312'     MOV   #NSET,BITSTA
8776 037346' 012737 001340' 000314'     MOV   #AFTER,PWHEN
8777 037354' 012737 001355' 000316'     MOV   #GTCMD,PCOMND
8778 037362'      ERRHRD 134,STAMUX,MSG1
8779 037362' 104456      TRAP C$ERHRD
8780 037364' 000206      .WORD 134
8781 037366' 002756'      .WORD STAMUX
8782 037370' 012750'      .WORD MSG1
8783 037372'      ESCAPE TST
8784 037372' 104410      TRAP C$ESCAPE
8785 037374' 000420      .WORD L10144-.
8786 037376' 004737 017374' 20$:      JSR   PC,CLRDNI        ;CLEAR DNI BIT
8787 037402' 103006      BCC   25$                ;DNI DID NOT CLEAR!
8788 037404'      ERRHRD 135,STAMUX,RACMG7
8789 037404' 104456      TRAP C$ERHRD
8790 037406' 000207      .WORD 135
8791 037410' 002756'      .WORD STAMUX
8792 037412' 012726'      .WORD RACMG7
8793 037414'      ESCAPE TST
8794 037414' 104410      TRAP C$ESCAPE
8795 037416' 000376      .WORD L10144-.
8796 037420' 25$:
8797 037420'      ENDSEG
8798 037420' 10000$:
8799 037420' 104405      TRAP C$ESEG
8800
8801      ;WAIT FOR THE MICROMONITOR TO ENTER THE 'INMON' STATE, THEN CLEAR OUT THE
8802      ;FIRST TWO WORDS OF THE PCBB. EXECUTE MICROTST #6 BY LOADING THE COMMAND
8803      ;FIELD BITS OF PCSRO WITH A 6, WAIT FOR 'DNI'.
8804
8805      BGNSEG
8806 037422' 104404      TRAP C$BSEG
8807 037424' 004737 017776'      JSR   PC,CHKMON        ;WAIT FOR MICROMONITOR
8808 037430' 103006      BCC   30$                ;OK
8809 037432'      ERRHRD 136,STAMUX,MSG46 ;PRINT ERROR
8810 037432' 104456      TRAP C$ERHRD
8811 037434' 000210      .WORD 136
8812 037436' 002756'      .WORD STAMUX
8813 037440' 016700'      .WORD MSG46
8814 037442'      ESCAPE TST
8815 037442' 104410      ;LEAVE TEST
8816 037444' 000350      TRAP C$ESCAPE
8817 037446' 005037 000604' 30$:     CLR   PCBB              .WORD L10144-.
8818
;THIS IS WHERE THE MICROCODE WILL...
;PUT THE CONTENTS OF THE FIRST WORD

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8819
8820 037452' 005037 000606' CLR PCBB+2 ;OF THE TRANSMIT BUFFER
8821 037456' 012777 000006 14C652 MOV #6,@PCSR0 ;HERE IS WHERE THE SECOND WORD GOES
8822 037464' 012737 000176 000332' MOV #2*SECOND,METER ;TELL T11 TO EXECUTE MICROTEST #6
8823 037472' 004737 017330' JSR PC,CHKDNI ;WAIT FOR DNI
8824 037476' 103021 BCC 45$
8825 037500' 004737 020050' JSR PC,CHKINT ;SEE IF ANY ERROR INTERRUPTS OCCURRED
8826 037504' 103006 BCC 35$ ;NO, OK
8827 037506' ERRHRD 137,STAMUX,MSG44 ;PRINT ERROR MESSAGE
8828 037506' 104456
8829 037510' 000211 TRAP C$ERHRD
8830 037512' 002756' .WORD 137
8831 037514' 016454' .WORD STAMUX
8832 037516' ESCAPE TST .WORD MSG44
8833 037516' 104410
8834 037520' 000274 TRAP C$ESCAPE
8835 037522' 012702 000006 35$: MOV #6,R2 ;MICROTEST #6 IS HUNG
8836 037526' ERRHRD 138,STAMUX,MSG12 .WORD L10144-
8837 037526' 104456
8838 037530' 000212 TRAP C$ERHRD
8839 037532' 002756' .WORD 138
8840 037534' 013500' .WORD STAMUX
8841 037536' ESCAPE TST .WORD MSG12
8842 037536' 104410
8843 037540' 000254 TRAP C$ESCAPE
8844 .WORD L10144-
8845 ;OK, NOW PCBB+0 SHOULD CONTAIN THE TRANSMIT STATUS WORD 0 AND PCBB+2 SHOULD
8846 ;CONTAIN TRANSMIT STATUS WORD 1. CHECK THAT PCBB+0 CONTAINS STATUS BITS
8847 ;FOR A GOOD TRANSMIT I.E. BITS 09:00 SHOULD ALL BE 0 BIT 13 SHOULD BE A 1
8848 ;AND BIT 15 SHOULD BE A 0
8849
8850 037542' 005001 45$: CLR R1 ;WE ARE GOING TO CHECK TRANSMIT WORD 0
8851 037544' 012703 000456' MOV #BNAMT2,R3 ;POINT TO A TABLE OF BIT MNEMONICS
8852 037550' 012704 000012 MOV #10,R4 ;FIRST 10 BITS SHOULD BE 0
8853 037554' 012702 000001 MOV #1,R2 ;R2 POINTS TO THE BIT WE ARE TESTING
8854 037560' 030237 000604' 50$: BIT R2,PCBB ;IS THIS BIT A 0?
8855 037564' 001406 BEQ 55$ ;YES
8856 037566' 011337 000310' MOV (R3),BITNAM ;NO, GET POINTER TO BIT NAME ASCII STRING
8857 037572' ERRHRD 139,STAMUX,MSG18 ;PRINT ERROR MESSAGE
8858 037572' 104456
8859 037574' 000213 TRAP C$ERHRD
8860 037576' 002756' .WORD 139
8861 037600' 014034' .WORD STAMUX
8862 037602' 062703 000002 55$: ADD #2,R3 ;POINT TO NEXT BIT MNEMONIC
8863 037606' 006302 ASL R2 ;POINT TO NEXT BIT
8864 037610' 005304 DEC R4 ;HAVE WE DONE ALL 10 BITS?
8865 037612' 001362 BNE 50$ ;NO
8866
8867 037614' 032737 020000 000604' BIT #BIT13,PCBB ;IS BIT 13 A 1?
8868 037622' 001007 BNE 60$ ;YES
8869 037624' 012737 001145' 000310' MOV #BIT13,BITNAM ;NO, POINT TO ASCII STRING
8870 037632' ERRHRD 140,STAMUX,MSG18 ;PRINT ERROR MESSAGE
8871 037632' 104456
8872 037634' 000214 TRAP C$ERHRD
8873 037636' 002756' .WORD 140
8874 037640' 014034' .WORD STAMUX
      .WORD MSG18

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8875
8876 037642' 032737 100000 000604' 60$: BIT #BIT15,PCBB :IS BIT 15 A 0?
8877 037650' 001407 BEQ 65$ :YES
8878 037652' 012737 001127' 000310' MOV #BIT15,BITNAM :NO, POINT TO ASCII STRING
8879 037660' ERRHRD 141,STAMUX,MSG18 :PRINT ERROR MESSAGE
8880 037660' 104456 TRAP C$ERHRD
8881 037662' 000215 .WORD 141
8882 037664' 002756' .WORD STAMUX
8883 037666' 014034' .WORD MSG18
8884
8885 :NOW CHECK TRANSMIT WORD 1 BITS 15,14 AND 13 TO BE ALL 0
8886
8887 037670' 005201 65$: INC R1 :WE ARE CHECKING TRANSMIT WORD 1 NOW
8888 037672' 032737 020000 000606' BIT #BIT13,PCBB+2 :IS BIT 13 A 0?
8889 037700' 001407 BEQ 70$ :YES
8890 037702' 012737 001145' 000310' MOV #BIT13,BITNAM :NO, POINT TO ASCII STRING
8891 037710' ERRHRD 142,STAMUX,MSG18 :PRINT ERROR MESSAGE
8892 037710' 104456 TRAP C$ERHRD
8893 037712' 000216 .WORD 142
8894 037714' 002756' .WORD STAMUX
8895 037716' 014034' .WORD MSG18
8896
8897 037720' 032737 040000 000606' 70$: BIT #BIT14,PCBB+2 :IS BIT 14 A 0?
8898 037726' 001407 BEQ 75$ :YES
8899 037730' 012737 001136' 000310' MOV #BIT14,BITNAM :NO, POINT TO ASCII STRING
8900 037736' ERRHRD 143,STAMUX,MSG18 :PRINT ERROR MESSAGE
8901 037736' 104456 TRAP C$ERHRD
8902 037740' 000217 .WORD 143
8903 037742' 002756' .WORD STAMUX
8904 037744' 014034' .WORD MSG18
8905
8906 037746' 032737 100000 000606' 75$: BIT #BIT15,PCBB+2 :IS BIT 15 A 0?
8907 037754' 001407 BEQ 80$ :YES
8908 037756' 012737 001127' 000310' MOV #BIT15,BITNAM :NO, POINT TO ASCII STRING
8909 037764' ERRHRD 144,STAMUX,MSG18 :PRINT ERROR MESSAGE
8910 037764' 104456 TRAP C$ERHRD
8911 037766' 000220 .WORD 144
8912 037770' 002756' .WORD STAMUX
8913 037772' 014034' .WORD MSG18
8914
8915 :WRITE '1' TO CLEAR 'DNI' BIT
8916
8917 037774' 004737 017374' 80$: JSR PC,CLRDN1 :CLEAR DNI BIT
8918 040000' 103004 BCC 85$
8919 040002' ERRHRD 145,STAMUX,RACMG7 :ERROR DNI DID NOT CLEAR!
8920 040002' 104456 TRAP C$ERHRD
8921 040004' 000221 .WORD 145
8922 040006' 002756' .WORD STAMUX
8923 040010' 012726' .WORD RACMG7
8924 040012'
8925 040012' 85$: ENDSEG
8926 040012'
8927 040012' 104405 10001$: TRAP C$ESEG
8928 040014'
8929 040014' L10144: TRAP C$E1ST
8930 040014' 104401

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TEST 27: LINK BYTE COUNTER TEST

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8979 040016'
8980 040016'
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8986 040016'

.SBTTL TEST 27: LINK BYTE COUNTER TEST

:BYTE COUNTERS ARE INVOLVED BOTH WITH THE LINK TRANSMIT FUNCTION AND THE LINK
:RECEIVE FUNCTION. WHEN THE T-11 PREPARES A TRANSMIT BUFFER FOR TRANSMISS. ON
:OF A DATAGRAM, IT WRITES THE INTENDED BYTE COUNT IN THE SECOND WORD OF THE
:TRANSMIT BUFFER. WHEN TRANSMISSION OF THE TRANSMIT BUFFER BEGINS, THE BYTE
:COUNT VALUE IS USED TO LOAD THE TRANSMIT BYTE COUNTER. THIS COUNTER IS
:DECREMENTED BY THE TRANSMIT STATE MACHINE AS THE DATAGRAM IS TRANSMITTED.
:THE DATAGRAM TRANSMISSION WILL CONTINUE UNTIL THE BYTE COUNTER IS DECREMENTED
:TO ZERO.

:THE RECEIVER ALSO HAS A BYTE COUNTER. THIS COUNTER IS CLEARED AT THE START OF
:A DATAGRAM RECEPTION AND IS INCREMENTED BY THE RECEIVE STATE MACHINE AS THE
:DATAGRAM IS RECEIVED. THE VALUE IN THIS COUNTER IS WRITTEN INTO WORD TWO
:OF THE RECEIVE BUFFER AT THE END OF RECEPTION.

:THIS TEST WILL USE MICROMODULE 'D' MICROTEST #7.
:THIS TEST WILL VERIFY THE BYTE COUNT LOGIC BY LOOPING MESSAGES AND VERIFYING
:THAT THE BYTE COUNT APPEARING IN THE RECEIVE BUFFER CORRESPONDS TO THE BYTE
:COUNT THAT WAS WRITTEN TO THE TRANSMIT BYTE COUNTER. THE TEST WILL ALSO
:VERIFY THAT THE ACTUAL NUMBER OF BYTES TRANSFERRED TO THE RECEIVE BUFFER
:CORRESPONDS TO THE INTENDED BYTE COUNT.

:THE TRANSMIT BYTE COUNT IS PASSED TO THE T-11 VIA THE PCBB+0. AFTER THE
:DATAGRAM LOOPBACK THE RECEIVE BYTE COUNT IS PLACED INTO PCBB+2 BY THE T-11
:PROCESSOR. PCBB+4 IS LOADED BY THE T-11 PROCESSOR WITH THE ACTUAL NUMBER OF
:BYTES THAT WERE TRANSFERRED TO THE RECEIVER BUFFER.

:PCBB+0: TRANSMIT BYTE COUNT
:PCBB+2: RECEIVE BYTE COUNT
:PCBB+4: ACTUAL NUMBER OF BYTES RECEIVED

:TEST SEQUENCE:
: 1-LOAD MICROMODULE 'D' IF NOT ALREADY DONE SO
: 2-WRITE MINIMUM BYTE COUNT IN PCBB+0
: 3-CLEAR PCBB+2 AND PCBB+4
: 4-VERIFY MICROMONITOR IS IN 'INMON' STATE
: 5-SELECT MICROTEST #7
: 6-VERIFY TRANSMIT BYTE COUNT SAME AS RECEIVE BYTE COUNT
: 7-VERIFY ACTUAL NUMBER OF BYTES RECEIVED IS CORRECT
: 8-WRITE '1' TO CLEAR 'DNI'
: 9-ADD 2 TO BYTE COUNT IN PCBB+0
: 10-REPEAT STEPS 3-9 UNTIL PCBB+0 REACHES MAXIMUM BYTE COUNT

BGNTST

127::

:CHECK TO SEE IF MODULE 'D' HAS BEEN LOADED. IF NOT LOAD IT INTO
:THE TOP HALF OF WCS, START IT AND WAIT FOR THE MICROMONITOR TO SET 'DNI'
:AFTER 'DNI' SETS WRITE '1' TO CLEAR IT.

BGNSEG

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9043 040204' 004737 017776'      JSR      PC,CHKMON      ;WAIT FOR MICROMONITOR
9044 040210' 103006      BCC      35$           ;OK
9045 040212'           ERRHRD  148,LNKBYT,MSG46 ;PRINT ERROR
9046 040212' 104456           TRAP      C$ERHRD
9047 040214' 000224           .WORD    148
9048 040216' 003022'           .WORD    LNKBYT
9049 040220' 016700'           .WORD    MSG46
9050 040222'           ESCAPE  TST           ;LEAVE TEST
9051 040222' 104410           TRAP      C$ESCAPE
9052 040224' 000166           .WORD    L10145-.
9053 040226' 012777 000007 140102 35$:  MOV      #7,@PCSR0     ;TELL T11 TO EXECUTE MICROTEST #7
9054 040234' 012737 000176 000332'  MOV      #2*SECOND,METER ;WAIT FOR DNI
9055 040242' 004737 017330'      JSR      PC,CHKDNI
9056 040246' 103021      BCC      40$
9057 040250' 004737 020050'      JSR      PC,CHKINT     ;SEE IF ANY ERROR INTERRUPTS OCCURRED
9058 040254' 103006      BCC      36$           ;NO, OK
9059 040256'           ERRHRD  149,LNKBYT,MSG44 ;PRINT ERROR MESSAGE
9060 040256' 104456           TRAP      C$ERHRD
9061 040260' 000225           .WORD    149
9062 040262' 003022'           .WORD    LNKBYT
9063 040264' 016454'           .WORD    MSG44
9064 040266'           ESCAPE  TST
9065 040266' 104410           TRAP      C$ESCAPE
9066 040270' 000122           .WORD    L10145-.
9067 040272' 012702 000007 36$:  MOV      #7,R2         ;MICROTEST #7 IS HUNG
9068 040276'           ERRHRD  150,LNKBYT,MSG12
9069 040276' 104456           TRAP      C$ERHRD
9070 040300' 000226           .WORD    150
9071 040302' 003022'           .WORD    LNKBYT
9072 040304' 013500'           .WORD    MSG12
9073 040306'           ESCAPE  TST
9074 040306' 104410           TRAP      C$ESCAPE
9075 040310' 000102           .WORD    L10145-.
9076
9077 ;OK, CHECK PCBB+2, WHICH CONTAINS THE BYTE COUNT WRITTEN INTO THE RECEIVER
9078 ;BUFFER BY THE LINK, TO SEE IF IT IS THE SAME AS THE TRANSMIT BYTE COUNT. ALSO
9079 ;CHECK PCBB+4, WHICH IS THE ACTUAL NUMBER OF BYTES RECEIVED IN THE RECEIVER
9080 ;BUFFER, TO SEE IF IT IS THE SAME AS THE RECEIVE BYTE COUNT
9081 ;IF NOT PRINT AN ERROR
9082
9083 040312' 013701 000604' 40$:  MOV      PCBB,R1       ;GET TRANSMIT BYTE COUNT
9084 040316' 010102      MOV      R1,R2
9085 040320' 013703 000606'      MOV      PCBB+2,R3     ;GET RECEIVE BYTE COUNT
9086 040324' 013704 000610'      MOV      PCBB+4,R4     ;GET ACTUAL NUMBER OF BYTES TRANSMITTED
9087 040330' 020203      CMP      R2,R3         ;IS THE RECEIVE BYTE COUNT CORRECT?
9088 040332' 001002      BNE      45$           ;NO, ERROR
9089 040334' 020104      CMP      R1,R4         ;IS THE ACTUAL NUMBER OF BYTES CORRECT?
9090 040336' 001404      BEQ      50$           ;YES
9091 040340'           45$:  ERRHRD  151,LNKBYT,MSG19 ;PRINT ERROR MESSAGE
9092 040340' 104456           TRAP      C$ERHRD
9093 040342' 000227           .WORD    151
9094 040344' 003022'           .WORD    LNKBYT
9095 040346' 014064'           .WORD    MSG19
9096 040350'           50$:
9097 040350'           ENDSEG
9098 040350'

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10001\$:

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CZUAA... TEST 28: ODD BYTE TEST

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040414'
040414'

040414'
040414' 104404
040416' 022737
040424' 001004
040426' 122777
040434' 001440
040436' 012737
040444' 004737
040450' 103002
040452'
040452' 104410
040454' 000340
040456' 012737
040464' 004737
040470' 103022
040472' 012737
040500' 012737
040506' 012737
040514' 012737
040522'
040522' 104456
040524' 000231
040526' 003060
040530' 012750

..BTTL TEST 28. ODD BYTE TEST
:*****
:THIS TEST WILL VERIFY THAT THE LINK CAN TRANSMIT AND RECEIVE DATAGRAMS
:HAVING ONLY ODD BYTE COUNTS.
:THIS TEST IS IDENTICAL TO THE PREVIOUS BYTE COUNTER TEST WITH THE ONLY
:EXCEPTION THAT IT PASSES ONLY ODD BYTE COUNTS TO THE MICROCODE. IT ALSO
:USES MICROMODULE 'D' MICROTEST #7
:TEST SEQUENCE:
:1-LOAD MICROMODULE 'D' IF NOT ALREADY DONE SO
:2-WRITE MINIMUM ODD BYTE COUNT IN PCBB+0
:3-CLEAR PCBB+2 AND PCBB+4
:4-VERIFY MICROMONITOR IS IN 'INMON' STATE
:5-SELECT MICROTEST #7
:6-VERIFY TRANSMIT BYTE COUNT SAME AS RECEIVE BYTE COUNT
:7-VERIFY ACTUAL NUMBER OF BYTES RECEIVED IS CORRECT
:8-WRITE '1' TO CLEAR 'DNI'
:9-ADD 2 TO BYTE COUNT IN PCBB+0
:10-REPEAT STEPS 3-9 UNTIL PCBB+0 REACHES MAXIMUM BYTE COUNT
:*****

BGNTST
T28::
:CHECK TO SEE IF MODULE 'D' HAS BEEN LOADED. IF NOT LOAD IT INTO
:THE TOP HALF OF WCS, START IT AND WAIT FOR THE MICROMONITOR TO SET 'DNI'
:AFTER 'DNI' SETS WRITE '1' TO CLEAR IT.

BGNSEG
TRAP C\$BSEG
:HAS MICROCODE MODULE 'D' BEEN LOADED
:NO
:YES, IS THE MICROMONITOR ACTIVE?
:YES SKIP LOADING THE MICROMODULE
:GO LOAD MICRO MODULE 'D'
:OK
TRAP C\$ESCAPE
:WAIT FOR THE MICROMONITOR
L10146-
:OK
TRAP C\$ERHRD
:ERHRD
153
:ODDBYT
MSG1

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9179 040532'          ESCAPE TST
9180 040532' 104410
9181 040534' 000260          TRAP C$ESCAPE
9182 040536' 004737 017374' 20$: JSR PC,CLRDNI          ;CLEAR DNI BIT      .WORD L10146-.
9183 040542' 103006          BCC 25$
9184 040544'          ERRHRD 154,ODDBYT,RACMG7      ;DNI DID NOT CLEAR!
9185 040544' 104456          TRAP C$ERHRD
9186 040546' 000232          .WORD 154
9187 040550' 003060'          .WORD ODDBYT
9188 040552' 012726'          .WORD RACMG7
9189 040554'          ESCAPE TST
9190 040554' 104410          TRAP C$ESCAPE
9191 040556' 000236          .WORD L10146-.
9192 040560'          25$:
9193 040560'          ENDSEG
9194 040560'          10000$:
9195 040560' 104405          TRAP C$ESEG
9196
9197          ;LOAD PCBB+0 WITH THE MINIMUM ODD BYTE COUNT
9198
9199 040562' 012737 000101 000604' 30$: MOV #MINBYT+1,PCBB      ;BEGIN WITH MINIMUM ODD BYTE COUNT
9200 040570'
9201
9202          ;CLEAR PCBB+2 AND PCBB+4. WAIT FOR THE MICROMONITOR TO ENTER THE 'INMON'
9203          ;STATE. EXECUTE MICROTEST #7 BY LOADING THE COMMAND FIELD OF PCSRO WITH A
9204          ;'7'. WAIT FOR 'DNI'
9205
9206 040570'          BGNSEG
9207 040570' 104404          TRAP C$BSEG
9208 040572' 005037 000606'          CLR PCBB+2          ;THIS IS WHERE MICROCODE WILL PUT...
9209          ;RECEIVER BYTE COUNT
9210 040576' 005037 000610'          CLR PCBB+4          ;HERE IS WHERE MICROCODE WILL PUT...
9211          ;ACTUAL NUMBER OF BYTES RECEIVED
9212 040602' 004737 017776'          JSR PC,CHKMON          ;WAIT FOR MICROMONITOR
9213 040606' 103006          BCC 35$
9214 040610'          ERRHRD 156,ODDBYT,MSG46      ;OK
9215 040610' 104456          ;PRINT ERROR
9216 040612' 000234          TRAP C$ERHRD
9217 040614' 003060'          .WORD 156
9218 040616' 016700'          .WORD ODDBYT
9219 040620'          .WORD MSG46
9220 040620' 104410          ESCAPE TST          ;LEAVE TEST
9221 040622' 000172          TRAP C$ESCAPE
9222 040624' 012777 000007 137504 35$: MOV #7,@PCSRO          .WORD L10146-.
9223 040632' 012737 000176 000332'          MOV #2*SECOND,METER      ;TELL T11 TO EXECUTE MICROTEST #7
9224 040640' 004737 017330'          JSR PC,CHKDNI          ;WAIT FOR DNI
9225 040644' 103021          BCC 40$
9226 040646' 004737 020050'          JSR PC,CHKINT          ;SEE IF ANY ERROR INTERRUPTS OCCURRED
9227 040652' 103006          BCC 36$
9228 040654'          ERRHRD 157,ODDBYT,MSG44      ;NO, OK
9229 040654' 104456          ;PRINT ERROR MESSAGE
9230 040656' 000235          TRAP C$ERHRD
9231 040660' 003060'          .WORD 157
9232 040662' 016454'          .WORD ODDBYT
9233 040664'          .WORD MSG44
9234 040664' 104410          ESCAPE TST          TRAP C$ESCAPE
    
```

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9235 040666' 000126
9236 040670' 012702 000007
9237 040674'
9238 040674' 104456
9239 040676' 000236
9240 040700' 003060'
9241 040702' 013500'
9242 040704'
9243 040704' 104410
9244 040706' 000106
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9252 040710' 0 3701 000604'
9253 040714' 010102
9254 040716' 013703 000606'
9255 040722' 013704 000610'
9256 040726' 020203
9257 040730' 001004
9258 040732' 162704 000001
9259 040736' 020104
9260 040740' 001404
9261 040742'
9262 040742' 104456
9263 040744' 000237
9264 040746' 003060'
9265 040750' 014064'
9266 040752'
9267 040752'
9268 040752'
9269 040752' 104405
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9273 040754' 004737 017374'
9274 040760' 103006
9275 040762'
9276 040762' 104456
9277 040764' 000240
9278 040766' 003060'
9279 040770' 012726'
9280 040772'
9281 040772' 104410
9282 040774' 000020
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9287 040776' 062737 000002 000604'
9288 041004' 023727 000604' 002756
9289 041012' 003666
9290 041014'

36$: MOV #7,R2 ;MICROTEST #7 IS HUNG .WORD L10146-.
ERRHRD 158,ODDBYT,MSG12
TRAP C$ERHRD
. WORD 158
. WORD ODDBYT
. WORD MSG12

ESCAPE TST
TRAP C$ESCAPE
. WORD L10146-.

;CHECK PCBB+2 TO SEE IF IT IS THE SAME AS PCBB+0, THIS WILL VERIFY THE
;THE TRANSMIT AND RECEIVE BYTE COUNTS ARE THE SAME. THEN CHECK PCBB+4
;TO SEE IF IT IS ONE LARGER THAN THE RECEIVE BYTE COUNT. THIS IS BECAUSE
;THE RECEIVE CAN ONLY ADDRESS LINK MEMORY IN WORD BOUNDARIES SO THE ACTUAL
;NUMBER OF BYTES THAT ARE CHANGED IS ONE MORE THAN WHAT WAS TRANSMITTED

40$: MOV PCBB,R1 ;GET TRANSMIT BYTE COUNT
MOV R1,R2
MOV PCBB+2,R3 ;GET RECEIVE BYTE COUNT
MOV PCBB+4,R4 ;GET ACTUAL NUMBER OF BYTES TRANSMITTED
CMP R2,R3 ;IS THE RECEIVE BYTE COUNT CORRECT?
BNE 45$ ;NO, ERROR
SUB #1,R4 ;ACCOUNT FOR THE GARBAGE BYTE
CMP R1,R4 ;IS THE ACTUAL NUMBER OF BYTES CORRECT?
BEQ 50$ ;YES
45$: ERRHRD 159,ODDBYT,MSG19 ;PRINT ERROR MESSAGE
TRAP C$ERHRD
. WORD 159
. WORD ODDBYT
. WORD MSG19

50$:
ENDSEG
10001$: TRAP C$ESEG

;WRITE '1' TO CLEAR 'DNI'
;
JSR PC,CLRDN1 ;CLEAR DNI BIT
BCC 60$
ERRHRD 160,ODDBYT,RACMG7 ;ERROR DNI DID NOT CLEAR!
TRAP C$ERHRD
. WORD 160
. WORD ODDBYT
. WORD RACMG7

ESCAPE TST
TRAP C$ESCAPE
. WORD L10146-.

;REPEAT MICROTEST #7 EACH TIME GIVING IT A LARGER ODD BYTE COUNT. CONTINUE
;UNTIL THE MAXIMUM BYTE COUNT IS REACHED

60$: ADD #2,PCBB ;UP THE BYTE COUNT
CMP PCBB,#MAXBYT ;HAVE WE DONE ALL ODD BYTE COUNTS?
BLE 30$ ;NO, CONTINUE TEST WITH NEW BYTE COUNT

ENDTST

```

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9291 041014'
9292 041014' 104401

L10146: TRAP CSETST

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TEST 29: LINK MAXIMUM BYTE COUNTER TEST

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041016'
041016'

041016'
041016' 104404
041020' 022737
041026' 001004
041030' 122777
041036' 001440
041040' 012737
041046' 004737
041052' 103002
041054'
041054' 104410
041056' 000256
041060' 012737
041066' 004737
041072' 103022
041074' 012737
041102' 012737
041110' 012737
041116' 012737
041124'
041124' 104456

000104 000326'
000001 137302
000104 000326' 5\$:
020252'

000176 000332' 10\$:
017330'
000776' 000310'
001275' 000312'
001340' 000314'
001355' 000316'

.SBTTL TEST 29: LINK MAXIMUM BYTE COUNTER TEST
:*****
:THE RECEIVE BYTE COUNTER IS A 12 BIT BINARY COUNTER THAT COUNTS THE NUMBER OF
:BYTES THAT WERE RECEIVED DURING A DATAGRAM TRANSMISSION. THE BYTE COUNTER IS
:INCREMENTED AS EACH BYTE IS RECEIVED. THE RECEIVE BYTE COUNTER HAS LOGIC THAT
:DISABLES THE COUNTER IF THE MAXIMUM VALUE IS REACHED AND PREVENTS THE COUNTER
:FROM ROLLING OVER TO ZERO.
:THIS TEST WILL CHECK THAT THE COUNTER 'TOPS OUT' AT THE MAXIMUM COUNTER VALUE.
:IT DO THIS MICROMODULE 'D' MICROTEST #9 IS USED. IT WILL TRANSMIT A DATAGRAM
:OF MAXIMUM COUNTER LENGTH OVER THE LOOPBACK. THE LINK CRC HARDWARE WILL BE
:ALLOCATED TO THE TRANSMIT SIDE SO THAT CRC BYTES WILL APPENDED TO THE DATAGRAM.
:THE LENGTH OF THE DATAGRAM WILL THEREFORE EXCEED THE LENGTH OF THE RECEIVE
:BYTE COUNTER. THE RECEIVE COUNTER WILL BE CHECKED TO INSURE THAT THE COUNTER
:HAS REMAINED AT THE MAXIMUM VALUE, IF NOT AN ERROR IS PASSED TO THE HOST.
:TEST SEQUENCE:
: 1-LOAD MICROMODULE 'D' IF NOT ALREADY DONE SO
: 2-VERIFY MICROMONITOR IS IN THE 'INMON' STATE
: 3-SELECT MICROTEST #9
: 4-WAIT FOR 'DNI'
: 5-CHECK FOR AN ERROR IN PCSR1
: 6-WRITE '1' TO CLEAR 'DNI'
:*****

BGNTST
T29::
:CHECK TO SEE IF MODULE 'D' HAS BEEN LOADED. IF NOT LOAD IT INTO
:THE TOP HALF OF WCS, START IT AND WAIT FOR THE MICROMONITOR TO SET 'DNI'.
:AFTER 'DNI' SETS WRITE '1' TO CLEAR IT.
: BGNSEG
TRAP C\$BSEG
;HAS MICROCODE MODULE 'D' BEEN LOADED?
;NO
;YES, IS THE MICROMONITOR ACTIVE?
;YES SKIP LOADING THE MICROMODULE
;GO LOAD MICROCODE MODULE D
;OK
TRAP C\$ESCAPE
;WORD L10147-
;WAIT FOR THE MICROMONITOR
;OK
;PRINT ERROR MESSAGE
TRAP C\$ERHRD

CMP #D,MICRO
BNE 5\$
CMPB #INMON,@PCSR1
BEQ 20\$
MOV #D,MICRO
JSR PC,LODMIC
BCC 10\$
ESCAPE TST

MOV #2*SECOND,METER
JSR PC,CHKDNI
BCC 20\$
MOV #DNI,BITNAM
MOV #SNSI,BITSTA
MOV #AFTER,PWHEN
MOV #SGTCMD,PCOMND
ERRHRD 161,MAXCNT,MSG1

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9349	041126'	000241						.WORD	161
9350	041130'	003122'						.WORD	MAXCNT
9351	041132'	012750'						.WORD	MSG1
9352	041134'			ESCAPE	TST				
9353	041134'	104410						TRAP	C\$ESCAPE
9354	041136'	000176						.WORD	L10147-
9355	041140'	004737	017374'	20\$:	JSR	PC,CLRDN1			;CLEAR DNI
9356	041144'	103006			BCC	25\$			
9357	041146'				ERRHRD	162,MAXCNT,RACMG7			
9358	041146'	104456						TRAP	C\$ERHRD
9359	041150'	000242						.WORD	162
9360	041152'	003122'						.WORD	MAXCNT
9361	041154'	012726'						.WORD	RACMG7
9362	041156'			ESCAPE	TST				
9363	041156'	104410						TRAP	C\$ESCAPE
9364	041160'	000154						.WORD	L10147-
9365	041162'			25\$:					
9366	041162'			ENDSEG					
9367	041162'							10000\$:	
9368	041162'	104405						TRAP	C\$ESEG
9369									
9370									
9371									
9372									
9373									
9374	041164'								
9375	041164'	104404						TRAP	C\$BSEG
9376	041166'	004737	017776'		JSR	PC,CHKMON			;WAIT FOR MICROMONITOR
9377	041172'	103006			BCC	30\$;OK
9378	041174'				ERRHRD	163,MAXCNT,MSG46			;PRINT ERROR
9379	041174'	104456						TRAP	C\$ERHRD
9380	041176'	000243						.WORD	163
9381	041200'	003122'						.WORD	MAXCNT
9382	041202'	016700'						.WORD	MSG46
9383	041204'			ESCAPE	TST				;LEAVE TEST
9384	041204'	104410						TRAP	C\$ESCAPE
9385	041206'	000126						.WORD	L10147-
9386	041210'	012777	000011 137120	30\$:	MOV	#9.,@PCSR0			;TELL MICROMONITOR TO EXECUTE...
9387									;MICROTEST #9
9388	041216'	012737	000176 000332'		MOV	#2*SECOND,METER			;PUT SOME TIME ON THE METER
9389	041224'	004737	017330'		JSR	PC,CHKDNI			;WAIT FOR MICROTEST TO FINISH
9390	041230'	103021			BCC	40\$;OK, IT FINISHED
9391	041232'	004737	020050'		JSR	PC,CHKINT			;SEE IF ANY ERROR INTERRUPTS OCCURRED
9392	041236'	103006			BCC	35\$;NO, OK
9393	041240'				ERRHRD	164,MAXCNT,MSG44			;PRINT ERROR MESSAGE
9394	041240'	104456						TRAP	C\$ERHRD
9395	041242'	000244						.WORD	164
9396	041244'	003122'						.WORD	MAXCNT
9397	041246'	016454'						.WORD	MSG44
9398	041250'			ESCAPE	TST				
9399	041250'	104410						TRAP	C\$ESCAPE
9400	041252'	000062						.WORD	L10147-
9401	041254'	012702	000011	35\$:	MOV	#9.,R2			;MICROTEST #
9402	041260'				ERRHRD	165,MAXCNT,MSG12			;TELL MICROTEST HUNG
9403	041260'	104456						TRAP	C\$ERHRD
9404	041262'	000245						.WORD	165

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9405 041264' 003122' .WORD MAXCNT
9406 041266' 013500' .WORD MSG12
9407 041270' ESCAPE TST
9408 041270' 104410 TRAP C$ESCAPE
9409 041272' 000042 .WORD L10147-
9410
9411 ;MICROTEST IS COMPLETE, NOW CHECK TO SEE IF IT WAS SUCCESSFULL
9412
9413 041274' 122777 000003 137036 40$: CMPB #INERR,@PCSR1 ;DID AN ERROR OCCUR?
9414 041302' 001004 BNE 50$ ;NO
9415 041304' ERRHRD 166,MAXCNT,MSG38 ;PRINT ERROR MESSAGE
9416 041304' 104456 TRAP C$ERHRD
9417 041306' 000246 .WORD 166
9418 041310' 003122' .WORD MAXCNT
9419 041312' 016046' .WORD MSG38
9420
9421 ;WRITE ONE TO CLEAR THE DNI BIT
9422
9423 041314' 004737 017374' 50$: JSR PC,CLRDNI ;CLEAR DNI
9424 041320' 103004 BCC 55$
9425 041322' ERRHRD 167,MAXCNT,RACMG7
9426 041322' 104456 TRAP C$ERHRD
9427 041324' 000247 .WORD 167
9428 041326' 003122' .WORD MAXCNT
9429 041330' 012726' .WORD RACMG7
9430 041332' 55$:
9431 041332' ENDSEG
9432 041332' 10001$:
9433 041332' 104405 TRAP C$ESEG
9434 041334' ENDTST
9435 041334' L10147:
9436 041334' 104401 TRAP C$ETST

```

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041336'
041336'

.SBTTL TEST 30: FIFO TEST
:*****
:THERE ARE TWO FIFO'S USED IN THE DEUNA TO KEEP TRACK OF RECEIVER BUFFERS.
:THE FIRST IS CALLED THE 'RECEIVER BUFFER AVAILABLE FIFO' AND THE SECOND IS
:CALLED THE 'RECEIVER BUFFER DONE FIFO'.
:THE T11 LOADS THE RECEIVER BUFFER AVAILABLE FIFO WITH A LIST OF UNUSED 1K
:BUFFERS IN LINK MEMORY. WHEN THE DEUNA SENSES THAT A PACKET IS COMING IN IT
:PULLS AN AVAILABLE BUFFER ADDRESS FROM THE OUTPUT OF THE RECEIVER BUFFER
:AVAILABLE FIFO AND USES IT TO ADDRESS LINK MEMORY FOR THE STORAGE OF THE
:RECEIVED DATA. AFTER THE DATA HAS BEEN LOADED THE RECEIVER STATE MACHINE
:PUTS THE USED BUFFER ADDRESS INTO THE RECEIVER BUFFER DONE FIFO WHERE AN
:INTERRUPT IS GENERATED TO THE T11 WHEN IT BUBBLES TO THE TOP OF THE FIFO.
:THESE FIFO'S ARE 64 DEEP BY 4 BITS WIDE. THE OPERATIONAL MICROCODE ONLY FILLS
:THE FIFO TO A MAXIMUM OF 16. THE 4 BIT WIDTH REPRESENTS BITS 14-11 OF THE
:LINK MEMORY ADDRESS. THESE BITS ALLOW THE ADDRESSING OF A 1K BUFFER IN LINK
:MEMORY.
:THIS TEST WILL VERIFY THAT THE RECEIVE BUFFER AVAILABLE FIFO AND THE RECEIVER
:BUFFER DONE FIFO OPERATE CORRECTLY. THIS WILL BE DONE BY LOADING THE RECEIVER
:BUFFER AVAILABLE FIFO WITH A 1K BUFFER ADDRESS THEN A PACKET WILL BE
:TRANSMITTED IN LOOPBACK MODE. AFTER THE RECEIVER INTERRUPT OCCURS THE RECEIVER
:BUFFER DONE FIFO IS READ AND THE ADDRESS IS COMPARED WITH WHAT WAS GIVEN
:THE RECEIVER BUFFER AVAILABLE FIFO. THEY SHOULD BE THE SAME IF EVERYTHING IS
:WORKING CORRECTLY. THE OPERATION IS PERFORMED WITH THE TRANSMITTER BUFFER
:SET TO 0 AND WILL BE REPEATED WITH RECEIVER BUFFERS 1-15.
:THIS TEST WILL USE MICROMODULE 'D' MICROTTEST #10.
:PARAMETERS PASSED TO THE MICROCODE WILL BE FORMATTED IN THE PCBB AS FOLLOWS:
:PCBB+0: RECEIVE BUFFER ADDRESS
:PCBB+2: RECEIVE BUFFER COMPLETED ADDRESS
:TEST SEQUENCE:
: 1-LOAD MICROMODULE 'D' IF NOT ALREADY DONE SO
: 2-LOAD PCBB+0 WITH A RECEIVER BUFFER
: 3-CLEAR PCBB+2
: 4-VERIFY MICROMONITOR IS IN THE 'INMON' STATE
: 5-SELECT MICROTTEST #10
: 6-WAIT FOR 'DNI' BIT
: 7-VERIFY PCBB+2 SAME AS PCBB+0
: 8-WRITE '1' TO CLEAR 'DNI'
: 9-REPEAT STEPS 2-8 FOR RECEIVER BUFFERS 1-15
:*****
BGNTST
T30::
:CHECK TO SEE IF MODULE 'D' HAS BEEN LOADED. IF NOT LOAD IT INTO
:THE TOP HALF OF WCS, START IT AND WAIT FOR THE MICROMONITOR TO SET 'DNI'.
:AFTER 'DNI' SETS WRITE '1' TO CLEAR IT.
:

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9493 041336'          BGNSEG
9494 041336' 104404
9495 041340' 022737 000104 000326'      CMP      #'D,MICRO      ;HAS MICROCODE MODULE 'D' BEEN LOADED
9496 041346' 001004          BNE      5$              ;NO
9497 041350' 122777 000001 136762      CMPB     #INMON,@PCSR1 ;YES, IS THE MICROMONITOR ACTIVE?
9498 041356' 001440          BEQ      20$              ;YES SKIP LOADING THE MICROMODULE
9499 041360' 012737 000104 000326' 5$:   MOV      #'D,MICRO      ;GO LOAD MICRO MODULE 'D'
9500 041366' 004737 020252'          JSR      PC,LODMIC
9501 041372' 103002          BCC     10$              ;OK
9502 041374'
9503 04137' 104410
9504 041376' 000326
9505 041400' 012737 000176 000332' 10$:   MOV      #2*SECOND,METER ;WAIT FOR THE MICROMONITOR
9506 041406' 004737 017330'          JSR      PC,CHKDNI
9507 041412' 103022          BCC     20$              ;OK
9508 041414' 012737 000776' 000310'      MOV      #SDNI,BITNAM
9509 041422' 012737 001275' 000312'      MOV      #SNSET,BITSTA
9510 041430' 012737 001340' 000314'      MOV      #SAFTER,PWHEN
9511 041436' 012737 001355' 000316'      MOV      #SGTCMD,PCOMND
9512 041444'          ERRHRD 168,FIFTST,MSG1
9513 041444' 104456
9514 041446' 000250
9515 041450' 003166'
9516 041452' 012750'
9517 041454'          ESCAPE TST
9518 041454' 104410
9519 041456' 000246
9520 041460' 004737 017374' 20$:   JSR      PC,CLRDNI      ;CLEAR DNI BIT
9521 041464' 103006          BCC     25$              ;DNI DID NOT CLEAR!
9522 041466'          ERRHRD 169,FIFTST,RACMG7
9523 041466' 104456
9524 041470' 000251
9525 041472' 003166'
9526 041474' 012726'
9527 041476'          ESCAPE TST
9528 041476' 104410
9529 041500' 000224
9530 041502' 25$:
9531 041502'          ENDSEG
9532 041502'
9533 041502' 104405          10000$: TRAP      C$ESEG
9534
9535          ;LINK MEMORY STARTS AT MICROMEMORY ADDRESS 100000, THE MICROTEST WILL USE
9536          ;100000 AS THE TRANSMIT BUFFER ADDRESS SO THE FIRST RECEIVE BUFFER ADDRESS
9537          ;WE WILL PASS TO THE MICROCODE WILL BE 104000
9538
9539 041504' 012701 104000      MOV      #LINADR+SIZ1K,R1 ;FIRST RECEIVE BUFFER STARTS 1K FROM
9540          ;BASE OF LINK MEMORY
9541 041510' 30$:
9542
9543          ;PUT THE BUFFER ADDRESS WE WANT THE MICROCODE TO LOAD INTO THE AVAILABLE BUFFER
9544          ;INTO PCBB AND CLEAR PCBB+2. WAIT FOR THE MICROMONITOR TO BECOME READY, THEN
9545          ;SELECT MICROTEST #10 BY LOADING THE COMMAND FIELD OF PCSRO WITH A 10. WAIT FOR
9546          ;'DNI'
9547
9548 041510'          BGNSEG

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9549 041510' 104404
9550 041512' 010137 000604' MOV R1,PCBB ;PASS AVAILABLE RECEIVER BUFFER TO DEUNA
9551 041516' 005037 000606' CLR PCBB+2 ;HERE IS WHERE THE MICRO WILL PUT THE
9552 ;'DONE' RECEIVE BUFFER FIFO ADDRESS
9553 041522' 004737 017776' JSR PC,CHKMON ;WAIT FOR MICROMONITOR
9554 041526' 103006 BCC 35$ ;OK
9555 041530' ERRHRD 170,FIFTST,MSG46 ;PRINT ERROR
9556 041530' 104456
9557 041532' 000252 TRAP C$ERHRD
9558 041534' 003166' .WORD 170
9559 041536' 016700' .WORD FIFTST
9560 041540' ESCAPE TST ;LEAVE TEST .WORD MSG46
9561 041540' 104410
9562 041542' 000162 TRAP C$ESCAPE
9563 041544' 012777 000012 136564 35$: MOV #10,@PCSR0 ;TELL T11 TO EXECUTE MICROTEST #10
9564 041552' 012737 000176 000332' MOV #2*SECOND,METER ;WAIT FOR DNI
9565 041560' 004737 017330' JSR PC,CHKDNI
9566 041564' 103021 BCC 45$ ;OK
9567 041566' 004737 020050' JSR PC,CHKINT ;SEE IF ANY ERROR INTERRUPTS OCCURRED
9568 041572' 103006 BCC 40$ ;NO, OK
9569 041574' ERRHRD 171,FIFTST,MSG44 ;PRINT ERROR MESSAGE
9570 041574' 104456 TRAP C$ERHRD
9571 041576' 000253 .WORD 171
9572 041600' 003166' .WORD FIFTST
9573 041602' 016454' .WORD MSG44
9574 041604' ESCAPE TST
9575 041604' 104410 TRAP C$ESCAPE
9576 041606' 000116 .WORD L10150-
9577 041610' 012702 000012 40$: MOV #10,R2 ;MICROTEST #10 IS HUNG
9578 041614' ERRHRD 172,FIFTST,MSG12 ;PRINT ERROR MESSAGE
9579 041614' 104456 TRAP C$ERHRD
9580 041616' 000254 .WORD 172
9581 041620' 003166' .WORD FIFTST
9582 041622' 013500' .WORD MSG12
9583 041624' ESCAPE TST
9584 041624' 104410 TRAP C$ESCAPE
9585 041626' 000076 .WORD L10150-
9586
9587 ;OK, THE MICROCODE HAS FILLED PCBB+2 WITH THE OUTPUT OF THE RECEIVER BUFFER
9588 ;DONE FIFO HOWEVER, THIS VALUE IS NOT IN ADDRESS FORM BECAUSE THERE ARE SOME
9589 ;UNUSED BITS (15,10:00) THAT COULD BE FLOATING SO, STRIP THESE BITS FIRST.
9590 ;THEN SET BIT 15 TO FORCE THE ADDRESS TO BE A LINK MEMORY ADDRESS. COMPARE
9591 ;THE ADDRESS GIVEN TO THE MICROCODE WITH THE ONE JUST GENERATED, THEY SHOULD
9592 ;BE IDENTICAL, IF NOT, PRINT ERROR.
9593
9594 041630' 042737 103777 000606' 45$: BIC #103777,PCBB+2 ;STRIP OFF THE FLOATING BITS
9595 041636' 052737 100000 000606' BIS #100000,PCBB+2 ;MAKE IT A LINK MEMORY ADDRESS
9596 041644' 023737 000604' 000606' CMP PCBB,PCBB+2 ;DID THE DEUNA USE THE BUFFER AVAILABLE
9597 041652' 001404 BEQ 50$ ;YES
9598 041654' ERRHRD 173,FIFTST,MSG35 ;NO, PRINT ERROR MESSAGE
9599 041654' 104456 TRAP C$ERHRD
9600 041656' 000255 .WORD 173
9601 041660' 003166' .WORD FIFTST
9602 041662' 015704' .WORD MSG35
9603 041664' 50$:
9604 041664' ENDSEG

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9605 041664'
9606 041664' 104405
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9610 041666' 004737 017374'
9611 041672' 103006
9612 041674'
9613 041674' 104456
9614 041676' 000256
9615 041700' 003166'
9616 041702' 012726'
9617 041704'
9618 041704' 104410
9619 041706' 000016
9620
9621
9622
9623
9624 041710' 020127 174000
9625 041714' 001403
9626 041716' 062701 004000
9627 041722' 000672
9628 041724'
9629 041724'
9630 041724'
9631 041724' 104401
    
```

```

100C1$: TRAP C$ESEG
:WRITE '1' TO CLEAR 'DNI' BIT
JSR PC,CLRDNI ;GO CLEAR DNI
BCC 60$ ;OK
ERRHRD 174,FIFTST,RACMG7 ;ERROR DNI DID NOT CLEAR!
TRAP C$ERHRD
.WORD 174
.WORD FIFTST
.WORD RACMG7
ESCAPE TST
TRAP C$ESCAPE
.WORD L10150-
:CHECK TO SEE IF WE HAVE TRIED ALL 15 RECEIVER BUFFER ADDRESSES, IF NOT,
:GENERATE A NEW ADDRESS TO PASS TO THE MICROCODE AND RUN THRU THE TEST AGAIN
60$: CMP R1,#LINADR+<SIZ1K*15.> ;HAVE WE TRIED ALL 15 BUFFERS
BEQ 70$ ;YES ALL DONE
ADD #SIZ1K,R1 ;NO POINT TO NEXT RECEIVER BUFFER
BR 30$ ;DO AGAIN
70$:
ENDTST
L10150: TRAP C$ETST
    
```

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TEST 31: RECEIVER LINK MEMORY ADDRESS TEST

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.SBTTL TEST 31: RECEIVER LINK MEMORY ADDRESS TEST

: THIS TEST WILL VERIFY THAT BUFFERS 1-15 OF LINK MEMORY CAN BE ADDRESSED
: CORRECTLY BY THE RECEIVER. THIS WILL BE DONE BY DIRECTING THE MICROCODE TO
: TRANSMIT A DATA PATTERN FROM BUFFER 0 AND TO RECEIVE THE DATA IN BUFFER X
: WHERE X = 1-15. THEN A CHECK WILL BE MADE TO SEE IF THE PATTERN NOT ONLY
: ARRIVED IN THE CORRECT RECEIVER BUFFER BUT THAT THE PATTERN DOES NOT SHOW UP
: ANYWHERE ELSE IN LINK MEMORY EXCEPT WHERE IT W'S SUPPOSE TO.

: THIS TEST WILL USE MICROMODULE 'D' MICROTEST #11. THIS MICROTEST ACCEPTS 2
: PARAMETERS: THE TRANSMIT BUFFER AND THE RECEIVER BUFFER. IT WILL SET UP
: A DATA PATTERN IN THE TRANSMIT BUFFER AND TELL THE LINK TO TRANSMIT, IN
: LOOPBACK MODE, FROM THE TRANSMIT BUFFER GIVEN TO THE RECEIVER BUFFER GIVEN.
: AFTER THE RECEIVER INTERRUPT, THE DATA IS CHECKED IN THE EXPECTED RECEIVER
: BUFFER FOR THE CORRECT DATA PATTERN. THEN ALL OF LINK MEMORY (EXCEPT FOR THE
: TRANSMIT BUFFER) IS CHECKED TO SEE IF THE PATTERN ENDS UP ELSEWHERE. IF
: AN ERROR IS FOUND THE MICROCODE PASSES THE ADDRESS OF LINK MEMORY WHERE THE
: ERROR WAS FOUND, THE DATA THAT WAS FOUND THERE ALONG WITH THE DATA THAT SHOULD
: HAVE BEEN THERE.

: THE PARAMETERS FOR THE MICROCODE ARE FORMATED IN THE PCBB AS FOLLOWS:

: PCBB+0: RECEIVER BUFFER ADDRESS
: PCBB+2: TRANSMIT BUFFER ADDRESS
: PCBB+4: LINK MEMORY ADDRESS (IF ERROR)
: PCBB+6: GOOD DATA PATTERN (IF ERROR)
: PCBB+10: BAD DATA PATTERN (IF ERROR)

: TEST SEQUENCE:
: 1-LOAD MICROMODULE 'D' IF NOT ALREADY DONE SO
: 2-SET PCBB+0 WITH TRANSMIT BUFFER 0 ADDRESS
: 3-SET PCBB+2 WITH RECEIVER BUFFER ADDRESS
: 4-WAIT FOR THE MICROMONITOR TO ENTER THE 'INMON' STATE
: 5-SELECT MICROTEST #11
: 6-WAIT FOR 'DNI'
: 7-CHECK FOR ERROR IN PCSR1 IF SO PRINT ERROR MESSAGE
: 8-WRITE '1' TO CLEAR 'DNI'
: 9-REPEAT STEPS 2-8 WITH RECEIVER BUFFERS 1-15

BGNTST

T31::

: CHECK TO SEE IF MODULE 'D' HAS BEEN LOADED. IF NOT LOAD IT INTO
: THE TOP HALF OF WCS, START IT AND WAIT FOR THE MICROMONITOR TO SET 'DNI'.
: AFTER 'DNI' SETS WRITE '1' TO CLEAR IT.

BGNSEG

041726'
041726'

041726' 104404
041730' 022737 000104 000326'
041736' 001004
041740' 122777 000001 136372
041746' 001440

CMP #D,MICRO ;HAS MICROCODE MODULE 'D' BEEN LOADED
BNE 5\$;NO
CMPB #INMON,@PCSR1 ;YES, IS THE MICROMONITOR ACTIVE?
BEQ 20\$;YES SKIP LOADING THE MICROMODULE

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```

9688 041750' 012737 000104 000326' 5$:  MOV #D,MICRO ;GO LOAD MICRO MODULE 'D'
9689 041756' 004737 020252'  JSR PC,LODMIC
9690 041762' 103002  BCC 10$ ;OK
9691 041764'  ESCAPE TST
9692 041764' 104410  TRAP C$ESCAPE
9693 041766' 000322  .WORD L10151-.
9694 041770' 012737 000176 000332' 10$:  MOV #2*SECOND,METER ;WAIT FOR THE MICROMONITOR
9695 041776' 004737 017330'  JSR PC,CHKDNI
9696 042002' 103022  BCC 20$ ;OK
9697 042004' 012737 000776' 000310'  MOV #SDNI,BITNAM
9698 042012' 012737 001275' 000312'  MOV #SNSET,BITSTA
9699 042020' 012737 001240' 000314'  MOV #SAFTER,PWHEN
9700 042026' 012737 001355' 000316'  MOV #SGTCMD,PCOMND
9701 042034'  ERRHRD 175,RLNKAD,MSG1
9702 042034' 104456  TRAP C$ERHRD
9703 042036' 000257  .WORD 175
9704 042040' 003207'  .WORD RLNKAD
9705 042042' 012750'  .WORD MSG1
9706 042044'  ESCAPE TST
9707 042044' 104410  TRAP C$ESCAPE
9708 042046' 000242  .WORD L10151-.
9709 042050' 004737 017374' 20$:  JSR PC,CLRDN1 ;CLEAR DNI BIT
9710 042054' 103006  BCC 25$
9711 042056'  ERRHRD 176,RLNKAD,RACMG7 ;DNI DID NOT CLEAR!
9712 042056' 104456  TRAP C$ERHRD
9713 042060' 000260  .WORD 176
9714 042062' 003207'  .WORD RLNKAD
9715 042064' 012726'  .WORD RACMG7
9716 042066'  ESCAPE TST
9717 042066' 104410  TRAP C$ESCAPE
9718 042070' 000220  .WORD L10151-.
9719 042072' 25$:
9720 042072'  ENDSEG
9721 042072' 10000$:
9722 042072' 104405  TRAP C$ESEG
9723  ;
9724  ;LOAD PCBB+2 WITH THE ADDRESS OF THE FIRST TRANSMIT BUFFER
9725  ;LOAD PCBB+0 WITH THE FIRST RECEIVER BUFFER ADDRESS
9726  ;
9727 042074' 012701 002756  MOV #MAXBYT,R1 ;TRANSMIT BYTE COUNT
9728 042100' 012737 100000 000606'  MOV #LINADR,PCBB+2 ;SET TRANSMIT BUFFER AT BASE OF LINK
9729  ;MEMORY
9730 042106' 012737 104000 000604'  MOV #LINADR+SIZ1K,PCBB ;FIRST RECEIVE BUFFER STARTS 1K FROM
9731  ;BASE OF LINK MEMORY
9732 042114' 30$:
9733 042114'  BGNSEG
9734 042114' 104404  TRAP C$BSEG
9735  ;
9736  ;WAIT FOR THE MICROMONITOR, THEN SELECT MICROTEST #11 BY LOADING THE COMMAND
9737  ;FIELD OF PCSRO WITH 11 , WAIT FOR 'DNI'
9738  ;
9739 042116' 004737 017776'  JSR PC,CHKMON ;WAIT FOR MICROMONITOR
9740 042122' 103006  BCC 35$ ;OK
9741 042124'  ERRHRD 177,RLNKAD,MSG46 ;PRINT ERROR
9742 042124' 104456  TRAP C$ERHRD
9743 042126' 000261  .WORD 177
    
```

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```

9744 042130' 003207' .WORD RLNKAD
9745 042132' 016700' .WORD MSG46
9746 042134' ESCAPE TST ;LEAVE TEST
9747 042134' 104410 TRAP C$ESCAPE
9748 042136' 000152 .WORD L10151-
9749 042140' 012777 000013 136170 35$: MOV #11,@PCSR0 ;TELL T11 TO EXECUTE MICROTEST #11
9750 042146' 012737 000176 000332' MOV #2*SECOND,METER ;WAIT FOR DNI
9751 042154' 004737 017330' JSR PC,CHKDNI
9752 042160' 103021 BCC 45$ ;OK
9753 042162' 004737 020050' JSR PC,CHKINT ;SEE IF ANY ERROR INTERRUPTS OCCURRED
9754 042166' 103006 BCC 40$ ;NO, OK
9755 042170' ERRHRD 178,RLNKAD,MSG44 ;PRINT ERROR MESSAGE
9756 042170' 104456 TRAP C$ERHRD
9757 042172' 000262 .WORD 178
9758 042174' 003207' .WORD RLNKAD
9759 042176' 016454' .WORD MSG44
9760 042200' ESCAPE TST
9761 042200' 104410 TRAP C$ESCAPE
9762 042202' 000106 .WORD L10151-
9763 042204' 012702 000013 40$: MOV #11,R2 ;MICROTEST #11 IS HUNG
9764 042210' ERRHRD 179,RLNKAD,MSG12 ;PRINT ERROR MESSAGE
9765 042210' 104456 TRAP C$ERHRD
9766 042212' 000263 .WORD 179
9767 042214' 003207' .WORD RLNKAD
9768 042216' 013500' .WORD MSG12
9769 042220' ESCAPE TST
9770 042220' 104410 TRAP C$ESCAPE
9771 042222' 000066 .WORD L10151-
9772
9773 ;IF THE MICROCODE FOUND AN ERROR IT SETS PCSR1 TO THE ERROR STATE SO, CHECK
9774 ;PCSR1 TO SEE IF AN ERROR OCCURRED
9775 ;IF SO PRINT ERROR MESSAGE
9776
9777 042224' 122777 000003 136106 45$: CMPB #INERR,@PCSR1 ;DID AN ERROR OCCUR?
9778 042232' 001004 BNE 50$ ;NO
9779 042234' ERRHRD 180,RLNKAD,MSG42 ;YES, PRINT ERROR MESSAGE
9780 042234' 104456 TRAP C$ERHRD
9781 042236' 000264 .WORD 180
9782 042240' 003207' .WORD RLNKAD
9783 042242' 016262' .WORD MSG42
9784 042244' 50$:
9785 042244' ENDSEG
9786 042244'
9787 042244' 104405 10001$: TRAP C$ESEG
9788
9789 ;WRITE '1' TO CLEAR 'DNI'
9790
9791 042246' 004737 017374' JSR PC,CLRDN1 ;GO CLEAR DNI
9792 042252' 103006 BCC 60$ ;OK
9793 042254' ERRHRD 181,RLNKAD,RACMG7 ;ERROR DNI DID NOT CLEAR!
9794 042254' 104456 TRAP C$ERHRD
9795 042256' 000265 .WORD 181
9796 042260' 003207' .WORD RLNKAD
9797 042262' 012726' .WORD RACMG7
9798 042264' ESCAPE TST
9799 042264' 104410 TRAP C$ESCAPE

```

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TEST 31: RECEIVER LINK MEMORY ADDRESS TEST

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9800 042266' 000022 .WORD L10151-.
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9807 042270' 023727 000604' 174000 60$: CMP PCBB,#LINADR+<SIZ1K*15.> ;HAVE WE TRIED ALL 15 BUFFERS
9808 042276' 001404 BEQ 70$ ;YES ALL DONE
9809 042300' 062737 004000 000604' ADD #SIZ1K,PCBB ;NO POINT TO NEXT RECEIVER BUFFER
9810 042306' 000702 BR 30$ ;DO AGAIN
9811 042310' 70$:
9812 042310' ENDTST
9813 042310'
9814 042310' 104401 L10151: TRAP C$ETST
9815

```

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TEST 32: TRANSMITTER LINK MEMORY ADDRESS TEST

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.SBTTL TEST 32: TRANSMITTER LINK MEMORY ADDRESS TEST

: THIS TEST WILL VERIFY THAT BUFFERS 1-15 CAN BE CORRECTLY ADDRESSED BY THE
: TRANSMITTER. THIS TEST IS IDENTICAL TO THE RECEIVER ADDRESS TEST IN THAT
: IT USES THE SAME MICROCODE (MICROMODULE 'D' MICROTEST #11) EXCEPT IT
: FIXES THE RECEIVER BUFFER AT 0 AND VARIES THE TRANSMIT BUFFER FROM 1-15.

: TEST SEQUENCE:
: 1-LOAD MICROMODULE 'D' IF NOT ALREADY DONE SO
: 2-SET PCBB+0 TO 10000 (BASE OF LINK MEMORY) AS RECEIVER BUFFER ADDRESS
: 3-SET PCBB+2 WITH A TRANSMITTER BUFFER ADDRESS
: 4-WAIT FOR THE MICROMONITOR TO ENTER THE 'INMON' STATE
: 5-SELECT MICROTEST #11
: 6-WAIT FOR 'DNI'
: 7-CHECK PCSR1 FOR AN ERROR CONDITION IF SO PRINT ERROR MESSAGE
: 8-WRITE '1' TO CLEAR 'DNI'
: 9-REPEAT STEPS 2-8 FOR ALL TRANSMIT BUFFERS 1-15

BGNTST

T32::

: CHECK TO SEE IF MODULE 'D' HAS BEEN LOADED. IF NOT LOAD IT INTO
: THE TOP HALF OF WCS, START IT AND WAIT FOR THE MICROMONITOR TO SET 'DNI'.
: AFTER 'DNI' SETS WRITE '1' TO CLEAR IT.

BGNSEG

TRAP C\$BSEG
:HAS MICROCODE MODULE 'D' BEEN LOADED
:NO
:YES, IS THE MICROMONITOR ACTIVE?
:YES SKIP LOADING THE MICROMODULE
:GO LOAD MICRO MODULE 'D'
:OK
TRAP C\$ESCAPE
:WAIT FOR THE MICROMONITOR
L10152-
:OK
TRAP C\$ERHRD
:ERHRD
182
TLNKAD
MSG1
TRAP C\$ESCAPE
:ESCAPE
L10152-
TST

042312'
042312'
042312'
042312' 104404
042314' 022737 000104 000326'
042322' 001004
042324' 122777 000001 136006
042332' 001440
042334' 012737 000104 000326' 5\$:
042342' 004737 020252'
042346' 103002
042350'
042350' 104410
042352' 000322
042354' 012737 000176 000332' 10\$:
042362' 004737 017330'
042366' 103022
042370' 012737 000776' 000310'
042376' 012737 001275' 000312'
042404' C12737 001340' 000314'
042412' 012737 001355' 000316'
042420'
042420' 104456
042422' 000266
042424' 003260'
042426' 012750'
042430'
042430' 104410
042432' 000242

CMP #'D,MICRO
BNE 5\$
CMPB #INMON,@PCSR1
BEQ 20\$
MOV #'D,MICRO
JSR PC,LODMIC
BCC 10\$
ESCAPE TST
MOV #2*SECOND,METER
JSR PC,CHKDNI
BCC 20\$
MOV #DNI,BITNAM
MOV #DSET,BITSTA
MOV #AFTER,PWHEN
MOV #SGTCMD,PCOMND
ERRHRD 182,TLNKAD,MSG1
ESCAPE TST

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TEST 32: TRANSMITTER LINK MEMORY ADDRESS TEST

```

9872 042434' 004737 017374' 20$: JSR PC,CLRDN1 ;CLEAR DNI BIT
9873 042440' 103006 BCC 25$
9874 042442' ERRHRD 183,TLNKAD,RACMG7 ;DNI DID NOT CLEAR!
9875 042442' 104456 TRAP C$ERHRD
9876 042444' 000267 .WORD 183
9877 042446' 003260' .WORD TLNKAD
9878 042450' 012726' .WORD RACMG7
9879 042452' ESCAPE TST
9880 042452' 104410 TRAP C$ESCAPE
9881 042454' 000220 .WORD L10152-.
9882 042456' 25$:
9883 042456' ENDSEG
9884 042456'
9885 042456' 104405 10000$: TRAP C$ESEG
9886
9887 ;
9888 ;LOAD PCBB+0 WITH THE BASE ADDRESS OF LINK MEMORY. THIS IS WHERE THE RECEIVER
9889 ;BUFFER WILL BE FIXED AT. LOAD PCBB+2 WITH THE ADDRESS OF BUFFER #1 (104000)
9890 042460' 012701 002756 MOV #MAXBYT,R1 ;TRANSMIT BYTE COUNT
9891 042464' 012737 100000 000604' MOV #LINADR,PCBB ;SET RECEIVE BUFFER AT BASE OF LINK
9892 ;MEMORY
9893 042472' 012737 104000 000606' MOV #LINADR+SIZ1K,PCBB+2 ;TRANSMIT BUFFER STARTS 1K FROM
9894 ;BASE OF LINK MEMORY
9895 042500' 30$:
9896 ;
9897 ;WAIT FOR THE MICROMONITOR TO ENTER THE 'INMON' STATE. THEN EXECUTE MICROTST
9898 ;#11 BY LOADING PCSRO COMMAND FIELD BITS WITH A 11. WAIT FOR 'DNI'
9899 ;
9900 042500' BGNSEG
9901 042500' 104404 TRAP C$BSEG
9902 042502' 004737 017776' JSR PC,CHKMON ;WAIT FOR MICROMONITOR
9903 042506' 103006 ECC 35$ ;OK
9904 042510' ERRHRD 184,TLNKAD,MSG46 ;PRINT ERROR
9905 042510' 104456 TRAP C$ERHRD
9906 042512' 000270 .WORD 184
9907 042514' 003260' .WORD TLNKAD
9908 042516' 016700' .WORD MSG46
9909 042520' ESCAPE TST ;LEAVE TEST
9910 042520' 104410 TRAP C$ESCAPE
9911 042522' 000152 .WORD L10152-.
9912 042524' 012777 000013 135604 35$: MOV #11,@PCSRO ;TELL T11 TO EXECUTE MICROTST #11
9913 042532' 012737 000176 000332' MOV #2*SECOND,METER ;WAIT FOR DNI
9914 042540' 004737 017330' JSR PC,CHKDNI
9915 042544' 103021 BCC 45$ ;OK
9916 042546' 004737 020050' JSR PC,CHKINT ;SEE IF ANY ERROR INTERRUPTS OCCURRED
9917 042552' 103006 BCC 40$ ;NO, OK
9918 042554' ERRHRD 185,TLNKAD,MSG44 ;PRINT ERROR MESSAGE
9919 042554' 104456 TRAP C$ERHRD
9920 042556' 000271 .WORD 185
9921 042560' 003260' .WORD TLNKAD
9922 042562' 016454' .WORD MSG44
9923 042564' ESCAPE TST
9924 042564' 104410 TRAP C$ESCAPE
9925 042566' 000106 .WORD L10152-.
9926 042570' 012702 000013 40$: MOV #11,R2 ;MICROTST #1' IS HUNG
9927 042574' ERRHRD 186,TLNKAD,MSG12 ;PRINT ERROR MESSAGE

```


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9928 042574' 104456 TRAP C$ERHRD
9929 042576' 000272 .WORD 186
9930 042600' 003260' .WORD TLNKAD
9931 042602' 013500' .WORD MSG12
9932 042604' ESCAPE TST
9933 042604' 104410 TRAP C$ESCAPE
9934 042606' 000066 .WORD L10152-.
9935
9936 ;THE MICROCODE WILL SET PCSR1 TO AN ERROR CONDITION IF IT DETECTED AN ERROR.
9937 ;SO CHECK PCSR1 IF ERROR, PRINT ERROR MESSAGE
9938
9939 042610' 122777 000003 135522 45$: CMPB #INERR,@PCSR1 ;DID AN ERROR OCCUR?
9940 042616' 001004 BNE 50$ ;NO
9941 042620' ERRHRD 187,TLNKAD,MSG42 ;YES, PRINT ERROR MESSAGE
9942 042620' 104456 TRAP C$ERHRD
9943 042622' 000273 .WORD 187
9944 042624' 003260' .WORD TLNKAD
9945 042626' 016262' .WORD MSG42
9946 042630' 50$:
9947 042630' ENDSEG
9948 042630' 10001$:
9949 042630' 104405 TRAP C$ESEG
9950
9951 ;WRITE '1' TO CLEAR 'DNI'
9952
9953 042632' 004737 017374' JSR PC,CLRDNI ;GO CLEAR DNI
9954 042636' 103006 BCC 60$ ;OK
9955 042640' ERRHRD 188,TLNKAD,RACMG7 ;ERKOR DNI DID NOT CLEAR!
9956 042640' 104456 TRAP C$ERHRD
9957 042642' 000274 .WORD 188
9958 042644' 003260' .WORD TLNKAD
9959 042646' 012726' .WORD RACMG7
9960 042650' ESCAPE TST
9961 042650' 104410 TRAP C$ESCAPE
9962 042652' 000022 .WORD L10152-.
9963
9964 ;THIS TEST IS TO BE REPEATED FOR EACH TRANSMIT BUFFER SO CHECK IF THE MICROCODE
9965 ;HAS BEEN PASSED THE LAST TRANSMIT BUFFER, IF NOT, CHANGE PCBB+2 TO POINT
9966 ;TO THE NEXT TRANSMIT BUFFER BY ADDING 1K TO IT
9967
9968 042654' 023727 000606' 174000 60$: CMP PCBB+2,#LINADR+<SIZ1K*15.> ;TRIED BUFFERS 1-15 FOR TRANSMITTER?
9969 042662' 001404 BEQ 70$ ;YES ALL DONE
9970 042664' 062737 004000 000606' ADD #SIZ1K,PCBB+2 ;NO POINT TO NEXT TRANSMIT BUFFER
9971 042672' 000702 BR 30$ ;DO AGAIN
9972 042674' 70$:
9973 042674' ENDTST
9974 042674' L10152:
9975 042674' 104401 TRAP C$ETST
9976

```

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TEST 33: LINK MEMORY ARBITRATION TEST

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.SBTTL TEST 33: LINK MEMORY ARBITRATION TEST

:THE LINK MEMORY CAN BE ACCESSED BY FOUR PROCESSES; THE T-11 PROCESSOR, THE
:DMA ENGINE, THE RECEIVE STATE MACHINE AND THE TRANSMIT STATE MACHINE. THE
:PORT MODULE HAS ARBITRATION CIRCUITRY TO MANAGE LINK MEMORY ACCESSES. THIS
:CIRCUITRY PREVENTS CONFLICTS BETWEEN PROCESSES AND ASSURES THAT HIGHER
:PRIORITY PROCESSES GET PRECEDENCE.

:THIS TEST WILL VERIFY THE ABLILITY OF THE LINK MEMORY ARITRATOR TO HANDLE
:SIMULTANEOUS REQUESTS BY FOUR PROCESSES. EACH OF THESE PROCESSES WILL INVOLVE
:TASKS THAT REQUIRE HEAVY ACCESSES OF LINK MEMORY. DATA WILL BE MOVED INTO OR
:OUT OF LINK MEMORY BY EACH. WHEN THAT TASKS ARE FINISHED THE DATA WILL BE
:VERIFIED.

:THE FOUR PROCESSES ARE:

1-TRANSMIT STATE MACHINE

WILL TRANSMIT A DATAGRAM OF MAXIMUM DATA LENGTH IN LOOPBACK
MODE. THE DATA FIELD WILL CONTAIN A BIT PATTERN STRING OF
TWO 1'S FOLLOWED BY TWO 0'S I.E. 31463 (OCTAL).

2-RECEIVE STATE MACHINE

WILL RECEIVE A DATAGRAM OF MAXIMUM DATA LENGTH OVER THE
LOOPBACK. THE RECEIVE DATA BUFFER WILL BE FILLED WITH 0'S
PRIOR TO THE PECEPTION.

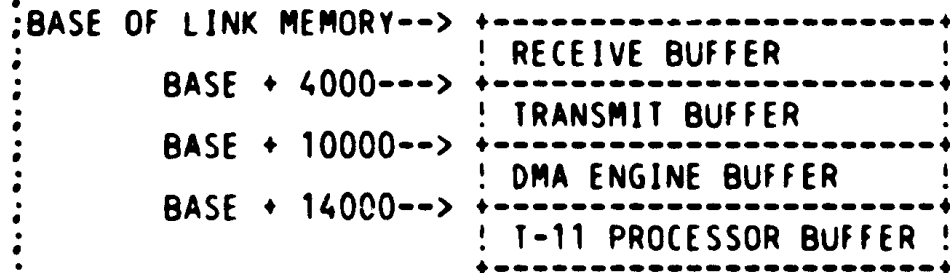
3-T-11 MICROPROCESSOR DMA

A 1K BUFFER IN LINK MEMORY WILL BE FILLED WITH AN ALL 1'S DATA
PATTERN PRIOR TO THE OPERATION THEN ALTERNATING 1'S AND 0'S
DATA PATTERN WILL BE WRITTEN.

4-DMA ENGINE

WILL TRANSFER A 1K BLOCK OF DATA FROM LINK MEMORY TO UNIBUS
MEMORY. THE DATA IN LINK MEMORY WILL A BIT PATTERN STRING
OF FOUR 1'S FOLLOWED BY A STRING OF FOUR 0'S. THE BUFFER
IN UNIBUS MEMORY WILL BE CLEARED PRIOR TO THE OPERATION.

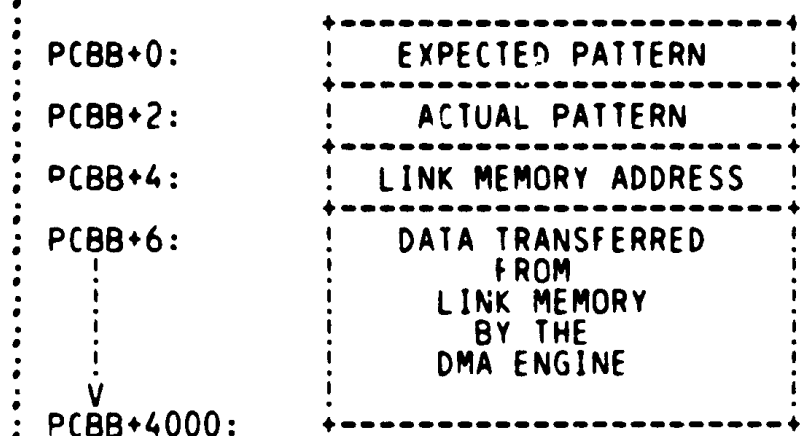
:THE FOUR PROCESSES WILL WORK OUT OF FOUR SEPARATE AREAS OF LINK MEMORY.



:THIS WILL ALLOW THE ARITRATION CIRCUITRY TO BE TESTED AND YET ALLOWS THE DATA

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 10077 042676'
 10078 042676'
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 10084 042676'
 10085 042676' 104404
 10086 042700' 022737 000104 000326'
 10087 042706' 001004
 10088 042710' 122777 000001 135422

:TO BE VERIFIED EASILY AND ASSOCIATED WITH A SINGLE PROCESS.
 :A DATAGRAM WILL BE LOOPED BACK FROM THE TRANSMIT BUFFER TO THE RECEIVE BUFFER.
 :AS THE DATAGRAM IS BEING TRANSFERRED, THE T-11 PROCESSOR WILL FILL IT'S BUFFER
 :AND THE DMA ENGINE WILL TRANSFER IT'S BUFFER FROM LINK MEMORY TO UNIBUS MEMORY.
 :WHEN THE RECEIVE STATE MACHINE IS DONE, THE T-11 WILL VERIFY THE DATA IN THE
 :RECEIVE BUFFER. IF AN ERROR IS FOUND PCSR1 WILL BE SET TO INDICATE AN ERROR.
 :THE HOST WILL WAIT FOR THE MICROCODE TO FINISH AND WHEN DONE, IT WILL VERIFY
 :THE DATA TRANSFERRED BY THE DMA ENGINE TO UNIBUS MEMORY.
 :THE FIRST 3 WORDS OF THE PCB B ARE USED FOR ERROR INFORMATION, THE REST
 :WILL BE THE UNIBUS ADDRESS THAT THE DMA ENGINE WILL TRANSFER TO.



TEST SEQUENCE:
 1-LOAD MICROMODULE 'D' IF NOT ALREADY DONE SO
 2-LOAD PCRS2 AND PCRS3 WITH ADDRESS OF PCB B
 3-CLEAR DMA ENGINE 'TG' BUFFER
 4-WAIT FOR MICROMONITOR TO ENTER 'INMON' STATE
 5-SELECT MICROTEST #8
 6-WAIT FOR 'DNI'
 7-CHECK FOR ERROR IN PCRS1
 8-VERIFY DATA IN DMA 'TO' BUFFER
 9-WRITE '1' TO CLEAR 'DNI'

```

BGNTST                                         T33::
:
:CHECK TO SEE IF MODULE 'D' HAS BEEN LOADED. IF NOT LOAD IT INTO
:THE TOP HALF OF WCS, START IT AND WAIT FOR THE MICROMONITOR TO SET 'DNI'.
:AFTER 'DNI' SETS WRITE '1' TO CLEAR IT.
:
:BGNSEG
                                TRAP    C$BSEG
                                ;HAS MICFOCODE MODULE 'D' BEEN LOADED
                                ;NO
                                ;YES, IS THE MICROMONITOR ACTIVE?
CMP    #'D,MICRO
BNE    5$
CMPB   #INMON,@PCSR1
    
```

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```

10089 042716' 001440          BEQ      20$          ;YES SKIP LOADING THE MICROMODULE
10090 042720' 012737 000104 000326' 5$:  MOV      #'D,MICRO  ;GO LOAD MICRO MODULE 'D'
10091 042726' 004737 020252'          JSR      PC,LODMIC
10092 042732' 103002          BCC      10$          ;OK
10093 042734'          ESCAPE  TST
10094 042734' 104410          TRAP      C$ESCAPE
10095 042736' 000376          .WORD    L10153-.
10096 042740' 012737 000176 000332' 10$:  MOV      #2*SECOND,METER ;WAIT FOR THE MICROMONITOR
10097 042746' 004737 017330'          JSR      PC,CHKDNI
10098 042752' 103022          BCC      20$          ;OK
10099 042754' 012737 000776' 000310'  MOV      #SDNI,BITNAM
10100 042762' 012737 001275' 000312'  MOV      #SNSET,BITSTA
10101 042770' 012737 001340' 000314'  MOV      #SAFTER,PWHEN
10102 042776' 012737 001355' 000316'  MOV      #SGTCMD,PCOMND
10103 043004'          ERRHRD  189,LNKARB,MSG1
10104 043004' 104456          TRAP      C$ERHRD
10105 043006' 000275          .WORD    189
10106 043010' 003334'          .WORD    LNKARB
10107 043012' 012750'          .WORD    MSG1
10108 043014'          ESCAPE  TST
10109 043014' 104410          TRAP      C$ESCAPE
10110 043016' 000316          .WORD    L10153-.
10111 043020' 004737 017374' 20$:  JSR      PC,CLRDNI    ;CLEAR DNI BIT
10112 043024' 103006          BCC      25$
10113 043026'          ERRHRD  190,LNKARB,RACMG7 ;DNI DID NOT CLEAR!
10114 043026' 104456          TRAP      C$ERHRD
10115 043030' 000276          .WORD    190
10116 043032' 003334'          .WORD    LNKARB
10117 043034' 012726'          .WORD    RACMG7
10118 043036'          ESCAPE  TST
10119 043036' 104410          TRAP      C$ESCAPE
10120 043040' 000274          .WORD    L10153-.
10121 043042'          25$:
10122 043042'          ENDSEG
10123 043042'          10000$:
10124 043042' 104405          TRAP      C$ESEG
10125
10126          ;:SETUP PCBB TO BE JUST AFTER LAST LOCATION USED BY THIS DIAGNOSTIC
10127          ;:TELL MICROCODE WHERE PCBB IS BY LOADING PCSR2 AND PCSR3 WITH THE PCBB ADDRESS
10128
10129 043044' 013777 000324' 135270  MOV      FREMEM,@PCSR2 ;TELL MICROCODE WHERE PCBB IS
10130 043052' 005077 135266  CLR      @PCSR3
10131
10132          ;:FIRST CLEAR THE 3 WORDS THAT WILL BE USED BY THE MICROCODE IF AN ERROR OCCURS.
10133          ;:THEN CLEAR OUT THE BUFFER TO BE USED AS THE DMA ENGINE'S 'TO' BUFFER.
10134
10135 043056' 013700 000324'  MOV      FREMEM,R0    ;GET A POINTER TO PCBB
10136 043062' 005020          CLR      (R0)+        ;HERE IS WHERE MICROCODE WILL PUT
10137          ;:EXPECTED DATA PATTERN IF ERROR
10138 043064' 005020          CLR      (R0)+        ;HERE, THE BAD DATA
10139 043066' 005020          CLR      (R0)+        ;AND HERE THE LINK MEMORY ADDRESS
10140
10141 043070' 012701 002000 30$:  MOV      #SIZ1K/2,R1  ;SIZE OF DMA ENGINE 'TO' BUFFER
10142 043074' 005020          CLR      (R0)+        ;CLEAR THE AREA WHERE THE DMA ENGINE...
10143 043076' 005301          DEC      R1           ;WILL LOAD DATA
10144 043100' 001375          BNE     30$

```

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```

10145 043102'
10146 043102'
10147 043102' 104404
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10153 043104' 004737 017776'
10154 043110' 103006
10155 043112'
10156 043112' 104456
10157 043114' 000277
10158 043116' 003334
10159 043120' 016700
10160 043122'
10161 043122' 104410
10162 043124' 000210
10163 043126' 012777 000010 135202 36$:
10164 043134' 012737 000176 000332'
10165 043142' 004737 017330'
10166 043146' 103021
10167 043150' 004737 020050'
10168 043154' 103006
10169 043156'
10170 043156' 104456
10171 043160' 000300
10172 043162' 003334
10173 043164' 016454
10174 043166'
10175 043166' 104410
10176 043170' 000144
10177 043172' 012702 000010 37$:
10178 043176'
10179 043176' 104456
10180 043200' 000301
10181 043202' 003334
10182 043204' 013500
10183 043206'
10184 043206' 104410
10185 043210' 000124
10186
10187
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10189
10190 043212' 122777 000003 135120 40$:
10191 043220' 001013
10192 043222' 013701 000324
10193 043226' 013702 000326
10194 043232' 013703 000330
10195 043236'
10196 043236' 104456
10197 043240' 000302
10198 043242' 003334
10199 043244' 014160
10200 043246' 000410

35$:
BGNSEG
TRAP CSBSEG

:
:WAIT FOR THE MICROMONITOR TO ENTER THE 'INMON' STATE
:SELECT MICROTEST #8 BY LOADING PCSR0 COMMAND FIELD BITS WITH A #8.
:WAIT FOR 'DNI'
:

JSR PC,CHKMON ;WAIT FOR MICROMONITOR
BCC 36$ ;OK
ERRHRD 191,LNKARB,MSG46 ;PRINT ERROR
TRAP CSERHRD
.WORD 191
.WORD LNKARB
.WORD MSG46
ESCAPE TST ;LEAVE TEST
TRAP C$ESCAPE
.WORD L10153-

MOV #8,@PCSR0 ;TELL T11 TO EXECUTE MICROTEST #8
MOV #2*SECOND,METER ;WAIT FOR DNI
JSR PC,CHKDN'
BCC 40$
JSR PC,CHKINT ;SEE IF ANY ERROR INTERRUPTS OCCURRED
BCC 37$ ;NO, OK
ERRHRD 192,LNKARB,MSG44 ;PRINT ERROR MESSAGE
TRAP CSERHRD
.WORD 192
.WORD LNKARB
.WORD MSG44
ESCAPE TST
TRAP C$ESCAPE
.WORD L10153-

MOV #8,R2 ;MICROTEST #8 IS HUNG
ERRHRD 193,LNKARB,MSG12
TRAP CSERHRD
.WORD 193
.WORD LNKARB
.WORD MSG12
ESCAPE TST
TRAP C$ESCAPE
.WORD L10153-

:
:CHECK MICROCODE FINISH STATUS BY CHECKING PCSR1 STATE BITS FOR AN ERROR
:IF ERROR, PRINT ERROR MESSAGE
:
CMPB #INERR,@PCSR1 ;DID AND ERROR OCCUR?
BNE 50$ ;NO
MOV FREMEM,R1 ;YES, GET EXPECTED DATA PATTERN
MOV FREMEM+2,R2 ;GET BAD DATA
MOV FREMEM+4,R3 ;GET LINK MEMORY ADDRESS
ERRHRD 194,LNKARB,MSG20 ;PRINT ERROR MESSAGE
TRAP CSERHRD
.WORD 194
.WORD LNKARB
.WORD MSG20
BR 60$

```

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TEST 33: LINK MEMORY ARBITRATION TEST

```

10201
10202      ;CHECK DATA THAT THE DMA ENGINE TRANSFERRED TO UNIBUS MEMORY
10203
10204 043250' 013701 000324' 50$:   MOV     FREMEM,R1           ;GET POINTER TO DMA ENGINE BUFFER
10205 043254' 062701 000006      ADD     #6,R1
10206 043260' 012704 003774      MOV     #3774,R4           ;NUMBER OF BYTES
10207 043264' 012702 000017      MOV     #17,R2           ;DATA THAT SHOULD BE IN BUFFER
10208 043270' 111103 60$:   MOVB   (R1),R3           ;GET DATA FROM BUFFER
10209 043272' 120211      CMPB   R2,(R1)           ;IS DATA CORRECT?
10210 043274' 001404      BEQ    70$              ;YES
10211 043276'
10212 043276' 104456      ERRHRD 195,LNKARB,MSG21  ;NO, PRINT ERROR
10213 043300' 000303      TRAP   C$ERHRD
10214 043302' 003334'      .WORD 195
10215 043304' 014250'      .WORD LNKARB
10216 043306' 005201      .WORD MSG21
10217 043310' 005304
10218 043312' 001366
10219
10220
10221      ;WRITE '1' TO CLEAR 'DNI'
10222
10223 043314' 004737 017374' 80$:   JSR    PC,CLRDN1         ;CLEAR DNI BIT
10224 043320' 103004      BCC    90$
10225 043322'
10226 043322' 104456      ERRHRD 196,LNKARB,RACMG7 ;ERROR DNI DID NOT CLEAR!
10227 043324' 000304      TRAP   C$ERHRD
10228 043326' 003334'      .WORD 196
10229 043330' 012726'      .WORD LNKARB
10230 043332'      .WORD RACMG7
10231 043332'
10232 043332'
10233 043332' 104405      10001$: TRAP   C$ESEG
10234 043334'
10235 043334'
10236 043334' 104401      L10153: TRAP   C$ETST
  
```

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TEST 34: STATION ADDRESS PATTERN TEST

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.SBTTL TEST 34: STATION ADDRESS PATTERN TEST

*****8

:WITHOUT EITHER THE PROMISCUOUS MODE OR THE MULTICAST MODE ENABLED, THE LINK
:LOGIC WILL RECOGNIZE DATAGRAM ADDRESSES ONLY IF THE ADDRESS IS CONTAINED IN
:THE STATION ADDRESS RAM.

:WHEN A DATAGRAM ARRIVES, THE LINK LOGIC COMPARES THE DATAGRAM DESTINATION
:ADDRESS FIELD TO THE 12 ADDRESSES WRITTEN IN THE STATION ADDRESS RAM. IF THE
:INCOMING ADDRESS MATCHES ONE OF THESE, THEN THE DATAGRAM WILL BE ACCEPTED BY
:THE LINK. THE 'MATCH' BIT IS SET IN THE TRANSMIT BUFFER AND THE RECEIVING
:PROCESS BEGINS.

:THIS TEST WILL VERIFY THAT THE LINK CAN RECOGNIZE A DATAGRAM WHEN THE
:DESTINATION ADDRESS OF THE DATAGRAM MATCHES ONE OF THE ADDRESSES STORED IN
:THE STATION ADDRESS RAM.

:THIS TEST WILL USE MICROMODULE 'E' MICROTEST #1.
:PATTERNS WILL BE USED FOR ADDRESSES IN CHECKING THE STATION ADDRESS LOGIC.
:THE PATTERNS WILL BE SUPPLIED TO THE T-11 THROUGH THE PCBB. THE MICROCODE
:WILL BE RESTARTED FOR EACH DIFFERENT PATTERN TO BE TESTED. UPON START-UP, THE
:T-11 PROCESSOR WILL PICK UP THE CURRENT PATTERN/ADDRESS, LOAD THE SAME PATTERN
:INTO ALL 12 LOCATIONS OF THE STATION ADDRESS RAM, FORMAT THE TRANSMIT BUFFER
:AND LOGIC FOR A LOOPBACK, PRESET PCSR1 TO AN ERROR CONDITION, START THE LINK
:AND WAIT FOR THE MATCH BIT IN THE TRANSMIT BUFFER. IF THE MATCH BIT SETS
:THE PCSR1 ERROR CONDITION IS CLEARED AND THE T-11 WAITS FOR BOTH THE
:TRANSMITTER AND RECEIVER INTERRUPTS BEFORE IT SETS 'DNI' TO INDICATE THE
:TEST WAS SUCCESSFUL

:THE PCBB WILL BE USED TO PASS THE 48 BIT STATION ADDRESS PATTERN:

```
PCBB+0:      +-----+
              | STATION ADDRESS LOW |
              +-----+
PCBB+2:      +-----+
              | STATION ADDRESS MIDDLE |
              +-----+
PCBB+4:      +-----+
              | STATION ADDRESS HIGH |
              +-----+
```

:THE FOLLOWING PATTERNS WILL BE USED:

- ALTERNATING 1'S AND 0'S
- ALTERNATING 0'S AND 1'S
- PAIR OF 0'S FOLLOWED BY PAIR OF 1'S
- FOUR 0'S FOLLOWED BY FOUR 1'S
- EIGHT 0'S FOLLOWED BY EIGHT 1'S
- SIXTEEN 1'S FOLLOWED BY SIXTEEN 0'S FOLLOWED BY 16 1'S
- TWENTYFOUR 1'S FOLLOWED BY TWENTYFOUR 0'S

:TEST SEQUENCE:

- 1-LOAD MICROMODULE 'E' IF NOT ALREADY DONE SO
- 2-LOAD PCBB+0,+2,+4 WITH A PATTERN
- 3-WAIT FOR MICROMONITOR TO ENTER THE 'INMON' STATE
- 4-SELECT MICROTEST #1
- 5-WAIT FOR 'DNI'
- 6-IF NO DNI AND PCSR1 INDICATES ERROR THEN PRINT NO MATCH BIT SET

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043336'
043336'

043336'
043336' 104404
043340' 022737 000105 000326'
043346' 001004
043350' 122777 000001 134762
043356' 001440
043360' 012737 000105 000326' 5\$:
043366' 004737 020252'
043372' 103002
043374'
043374' 104410
043376' 000266
043400' 012737 000176 000332' 10\$:
043406' 004737 017330'
043412' 103022
043414' 012737 000776' 000310'
043422' 012737 001275' 000312'
043430' 012737 001340' 000314'
043436' 012737 001355' 000316'
043444'
043444' 104456
043446' 000305
043450' 003400'
043452' 012750'
043454'
043454' 104410
043456' 000206
043460' 004737 017374'
043464' 103006
043466'
043466' 104456
043470' 000306
043472' 003400'
043474' 012726'
043476'
043476' 104410
043500' 000164
043502'
043502'
043502'
043502' 104405

: 7-IF NO DNI AND PCSR1 INDICATES NO ERROR THEN PRINT MATCH BIT SET
: BUT DATAGRAM WAS NOT RECEIVED.
: 8-WRITE '1' TO CLEAR 'DNI'
: 9-REPEAT STEPS 2-8 FOR ALL SEVEN DATA PATTERNS

:*****

BGNTST

T34::

: CHECK TO SEE IF MODULE 'E' HAS BEEN LOADED. IF NOT LOAD IT INTO
: THE TOP HALF OF WCS, START IT AND WAIT FOR THE MICROMONITOR TO SET 'DNI'.
: AFTER 'DNI' SETS WRITE '1' TO CLEAR IT.

BGNSEG

TRAP C\$BSEG
;HAS MICROCODE MODULE 'E' BEEN LOADED
;NO
;YES, IS THE MICROMONITOR ACTIVE?
;YES SKIP LOADING THE MICROMODULE
;GO LOAD MICRO MODULE 'E'
;OK
TRAP C\$ESCAPE
;WAIT FOR THE MICROMONITOR
;OK
TRAP C\$ERHRD
;CLEAR DNI BIT
;DNI DID NOT CLEAR!
TRAP C\$ERHRD
TRAP C\$ESCAPE
10000\$:
TRAP C\$ESEG

25\$:
ENDSEG

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```

10349      ;SELECT ONE OF THE PATTERNS FROM THE STATION ADDRESS PATTERN TABLE AND LOAD
10350      ;IT INTO THE PCBB
10351      ;
10352      043504' 012701 000532'      MOV      #SPAT1,R1      ;POINT TO STATION ADDRESS PATTERN TABLE
10353      043510' 012705 000007'      MOV      #7,R5         ;# OF ADDRESS PATTERNS
10354      043514' 011137 000604'      27$:    MOV      (R1),PCBB      ;LOAD PCBB WITH AN ADDRESS PATTERN
10355      043520' 016137 000002' 000606'      MOV      2(R1),PCBB+2    ;MIDDLE ORDER
10356      043526' 016137 000004' 000610'      MOV      4(R1),PCBB+4    ;UPPER ORDER
10357      043534'
10358      043534' 104404                                TRAP      C$BSEG
10359
10360      ;WAIT FOR THE MICROMONITOR TO ENTER THE 'INMON' STATE. SELECT MICROTEST #1
10361      ;BY LOADING PCSRO COMMAND FIELD BITS WITH A 1. WAIT FOR DNI.
10362      ;
10363      043536' 004737 017776'      JSR      PC,CHKMON      ;WAIT FOR MICROMONITOR
10364      043542' 103006                                BCC      30$           ;OK
10365      043544'                                ERRHRD   199,STAPAT,MSG46 ;PRINT ERROR
10366      043544' 104456                                TRAP      C$ERHRD
10367      043546' 000307                                .WORD    199
10368      043550' 003400'                                .WORD    STAPAT
10369      043552' 016700'                                .WORD    MSG46
10370      043554'                                ESCAPE   TST           ;LEAVE TEST
10371      043554' 104410                                TRAP      C$ESCAPE
10372      043556' 000106                                .WORD    L10154-.
10373      043560' 012777 000001' 134550' 30$:    MOV      #1,@PCSRO      ;TELL T11 TO EXECUTE MICROTEST #1
10374      043566' 012737 000176' 000332'      MOV      #2*SECOND,METER ;WAIT FOR DNI
10375      043574' 004737 017330'      JSR      PC,CHKDNI
10376      043600' 103015                                BCC      40$
10377
10378      ;DNI DID NOT SET WHICH INDICATES THAT THE RECEIVE INTERRUPT NEVER HAPPENED.
10379      ;THE REASON MIGHT BE BECAUSE THE MATCH BIT NEVER SET IN THE TRANSMIT
10380      ;STATUS REGISTER INDICATING THAT THE STATION ADDRESS COMPARATOR FAILED
10381      ;TO RECOGNIZE THE DATAGRAM DESTINATION ADDRESS. THE MICROCODE CHECKS
10382      ;FOR THE MATCH BIT AND IF IT IS NOT SET IT PUTS AN ERROR STATUS IN PCSR1.
10383      ;IF PCSR1 DOES NOT HAVE AN ERROR STATE THEN THE RECEIVER INTERRUPT FAILED
10384      ;FOR SOME OTHER REASON AND ITS NOT THE STATION ADDRESS RECOGNITION'S FAULT.
10385      ;
10386      043602' 004737 020050'      JSR      PC,CHKINT      ;SEE IF ANY ERROR INTERRUPTS OCCURRED
10387      043606' 103006                                BCC      35$           ;NO
10388      043610'                                ERRHRD   200,STAPAT,MSG44 ;PRINT ERROR MESSAGE
10389      043610' 104456                                TRAP      C$ERHRD
10390      043612' 000310                                .WORD    200
10391      043614' 003400'                                .WORD    STAPAT
10392      043616' 016454'                                .WORD    MSG44
10393      043620'                                ESCAPE   TST           ;LEAVE TST
10394      043620' 104410                                TRAP      C$ESCAPE
10395      043622' 000042                                .WORD    L10154-.
10396      043624' 35$:    ERRHRD   201,STAPAT,MSG25 ;STATION ADDRESS PATTERN WAS NOT
10397      043624' 104456                                TRAP      C$ERHRD
10398      043626' 000311'                                .WORD    201
10399      043630' 003400'                                .WORD    STAPAT
10400      043632' 014626'                                .WORD    MSG25
10401
10402      ;RECOGNIZED! ERROR!
10403
10404      ;WRITE '1' TO CLEAR 'DNI'
    
```

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TEST 34: STATION ADDRESS PATTERN TEST

10405 043634' 004737 017374'
10406 043640' 103004
10407 043642'
10408 043642' 104456
10409 043644' 000312
10410 043646' 003400'
10411 043650' 012726'
10412 043652'
10413 043652'
10414 043652'
10415 043652' 104405
10416
10417
10418
10419 043654' 062701 000006
10420 043660' 005305
10421 043662' 001314
10422
10423 043664'
10424 043664'
10425 043664' 104401

40\$: JSR PC,CLRDNI ;CLEAR DNI BIT
BCC 50\$
ERRHRD 202,STAPAT,RACMG7 ;ERROR DNI DID NOT CLEAR!
TRAP C\$ERHRD
.WORD 202
.WORD STAPAT
.WORD RACMG7
50\$:
ENDSEG
1000\$: TRAP C\$ESEG
:REPEAT THE TEST WITH ALL SEVEN PATTERNS
:
:ADD #6,R1 ;POINT TO NEXT PATTERN
:DEC R5 ;TESTED WITH ALL ADDRESS PATTERNS?
:BNE 27\$;NOT YET
ENDTST
L10154: TRAP C\$ETST

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TEST 35: STATION ADDRESS REJECTION TEST

.SBTTL TEST 35: STATION ADDRESS REJECTION TEST

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:*****
:THIS TEST WILL VERIFY THAT THE STATION ADDRESS DETECTION LOGIC DOES NOT
:RECOGNIZE A DATAGRAM WHEN THE DATAGRAM ADDRESS IS NOT CONTAINED IN THE
:STATION ADDRESS RAM.
:THE MICROCODE WILL FILL THE STATION ADDRESS RAM WITH 0'S. THE DESTINATION
:FIELD OF THE TRANSMIT BUFFER IS FILLED WITH 1'S. A TRANSMISSION IS STARTED
:IN LOOPBACK MODE AND THE T-11 WILL WAIT FOR A RECEIVER INTERRUPT. OF COURSE,
:THE RECEIVER INTERRUPT SHOULD NEVER HAPPEN BECAUSE THE STATION ADDRESS
:LOGIC SHOULD NOT GET A SUCCESSFUL COMPARISON BETWEEN 0'S IN THE DESTINATION
:ADDRESS OF THE INCOMING DATAGRAM AND THE 1'S IN THE STATION ADDRESS RAM.
:THE T-11 WILL BE PUT INTO A LOOP WAITING FOR A RECEIVER INTERRUPT AND THE
:DEUNA TIMER IS STARTED. IF THE LOOP IS BROKEN BY THE RECEIVER INTERRUPT
:AN ERROR WILL BE PRESENTED IN PCSR1 BY THE MICROCODE. IF THE LOOP IS BROKEN
:BY THE TIMER THEN THE TEST WAS SUCCESSFUL.

```

TEST SEQUENCE:

- 1-LOAD MICROMODULE 'E' IF NOT ALREADY DONE SO
- 2-WAIT FOR THE MICROMONITOR TO ENTER THE 'INMON' STATE
- 3-SELECT MICROTEST #2
- 4-WAIT FOR 'DNI'
- 5-CHECK PCSR1 FOR AN ERROR CONDITION
- 6-WRITE '1' TO CLEAR 'DNI'

BGNTST

T35::

```

:CHECK TO SEE IF MODULE 'E' HAS BEEN LOADED. IF NOT LOAD IT INTO
:THE TOP HALF OF WCS, START IT AND WAIT FOR THE MICROMONITOR TO SET 'DNI'.
:AFTER 'DNI' SETS WRITE '1' TO CLEAR IT.

```

BGNSEG

10463	043666'	104404								TRAP	C\$BSEG
10464	043670'	022737	000105	000326'	CMP	#'E,MICRO				:	HAS MICROCODE MODULE 'E' BEEN LOADED
10465	043676'	001004			BNE	5\$:	NO
10466	043700'	122777	000001	134432	CMPB	#INMON,@PCSR1				:	YES, IS THE MICROMONITOR ACTIVE?
10467	043706'	001440			BEQ	20\$:	YES SKIP LOADING THE MICROMODULE
10468	043710'	012737	000105	000326'	MOV	#'E,MICRO	5\$:			:	GO LOAD MICRO MODULE 'E'
10469	043716'	004737	020252'		JSR	PC,LODMIC					
10470	043722'	103002			BCC	10\$:	OK
10471	043724'				ESCAPE	TST					
10472	043724'	104410									
10473	043726'	000256								TRAP	C\$ESCAPE
10474	043730'	012737	000176	000332'	MOV	#2*SECOND,METER	10\$:			:	WAIT FOR THE MICROMONITOR
10475	043736'	004737	017330'		JSR	PC,CHKDNI				:	OK
10476	043742'	103022			BCC	20\$:	OK
10477	043744'	012737	000776'	000310'	MOV	#\$DNI,BITNAM					
10478	043752'	012737	001275'	000312'	MOV	#\$NSET,BITSTA					
10479	043760'	012737	001340'	000314'	MOV	#\$AFTER,PWHEN					
10480	043766'	012737	001355'	000316'	MOV	#\$GTCMD,PCOMND					
10481	043774'				ERRHRD	203,STAREJ,MSG1					

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10482	043774'	104456						TRAP	C\$ERHRD
10483	043776'	000313						.WORD	203
10484	044000'	003444'						.WORD	STAREJ
10485	044002'	012750'						.WORD	MSG1
10486	044004'			ESCAPE	TST				
10487	044004'	104410						TRAP	C\$ESCAPE
10488	044006'	000176						.WORD	L10155-
10489	044010'	004737	017374'	20\$:	JSR	PC,CLRDNI			:CLEAR DNI BIT
10490	044014'	103006			BCC	25\$			
10491	044016'				ERRHRD	204,STAREJ,RACMG7			:DNI DID NOT CLEAR!
10492	044016'	104456						TRAP	C\$ERHRD
10493	044020'	000314						.WORD	204
10494	044022'	003444'						.WORD	STAREJ
10495	044024'	012726'						.WORD	RACMG7
10496	044026'			ESCAPE	TST				
10497	044026'	104410						TRAP	C\$ESCAPE
10498	044030'	000154						.WORD	L10155-
10499	044032'			25\$:					
10500	044032'			ENDSEG					
10501	044032'								10000\$:
10502	044032'	104405						TRAP	C\$ESEG
10503									
10504				:	WAIT FOR THE MICROMONITOR TO ENTER THE 'INMON' STATE, SELECT MICROTEST #2				
10505				:	BY LOADING PLSRO WITH A 2				
10506				:					
10507	044034'			BGNSEG					
10508	044034'	104404						TRAP	C\$BSEG
10509	044036'	004737	017776'		JSR	PC,CHKMON			:WAIT FOR MICROMONITOR
10510	044042'	103006			BCC	30\$:OK
10511	044044'				ERRHRD	205,STAREJ,MSG46			:PRINT ERROR
10512	044044'	104456						TRAP	C\$ERHRD
10513	044046'	000315						.WORD	205
10514	044050'	003444'						.WORD	STAREJ
10515	044052'	016700'						.WORD	MSG46
10516	044054'			ESCAPE	TST				:LEAVE TEST
10517	044054'	104410						TRAP	C\$ESCAPE
10518	044056'	000126						.WORD	L10155-
10519	044060'	012737	000275 000332'	30\$:	MOV	#3*SECOND,METER			:TIMEOUT MUST BE GREATER THAN TIMER
10520									:PERIOD IN THE MICROCODE.
10521	044066'	012777	000002 134242		MOV	#2,@PCSR0			:TELL T11 TO EXECUTE MICROTEST #2
10522	044074'	004737	017330'		JSR	PC,CHKDNI			:WAIT FOR DNI
10523									
10524	044100'	103021			BCC	40\$			
10525	044102'	004737	020050'		JSR	PC,CHKINT			:SEE IF ANY ERROR INTERRUPTS OCCURRED
10526	044106'	103006			BCC	35\$:NO, OK
10527	044110'				ERRHRD	206,STAREJ,MSG44			:PRINT ERROR MESSAGE
10528	044110'	104456						TRAP	C\$ERHRD
10529	044112'	000316						.WORD	206
10530	044114'	003444'						.WORD	STAREJ
10531	044116'	016454'						.WORD	MSG44
10532	044120'			ESCAPE	TST				
10533	044120'	104410						TRAP	C\$ESCAPE
10534	044122'	000062						.WORD	L10155-
10535	044124'	012702	000002	35\$:	MOV	#2,R2			:MICROTEST #2 IS HUNG
10536	044130'				ERRHRD	207,STAREJ,MSG12			
10537	044130'	104456						TRAP	C\$ERHRD

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10538 044132' 000317 .WORD 207
10539 044134' 003444' .WORD STAREJ
10540 044136' 013500' .WORD MSG12
10541 044140' ESCAPE TST
10542 044140' 104410 TRAP C$ESCAPE
10543 044142' 000042 .WORD L10155-.
10544
10545 ;OK 'DNI' SET INDICATING EITHER THE RECEIVER INTERRUPT HAPPENED OR THE TIMER
10546 ;BROKE THE LOOP, CHECK PCSR1 TO FIND OUT WHICH
10547
10548 044144' 122777 000003 134166 40$: CMPB #INERR,@PCSR1 ;DID AN ERROR OCCUR?
10549 044152' 001004 BNE 50$ ;NO
10550 044154' ERRHRD 208,STAREJ ;YES, PRINT ERROR MESSAGE
10551 044154' 104456 TRAP C$ERHRD
10552 044156' 000320 .WORD 208
10553 044160' 003444' .WORD STAREJ
10554 044162' 000000 .WORD 0
10555
10556 ;WRITE '1' TO CLEAR 'DNI'
10557
10558 044164' 004737 017374' 50$: JSR PC,CLRDNI ;CLEAR DNI BIT
10559 044170' 103004 BCC 60$
10560 044172' ERRHRD 209,STAREJ,RACMG7 ;ERROR DNI DID NOT CLEAR!
10561 044172' 104456 TRAP C$ERHRD
10562 044174' 000321 .WORD 209
10563 044176' 003444' .WORD STAREJ
10564 044200' 012726' .WORD RACMG7
10565 044202'
10566 044202' ENDSEG
10567 044202' 10001$:
10568 044202' 104405 TRAP C$ESEG
10569 044204'
10570 044204' L10155:
10571 044204' 104401 TRAP C$ETST

```

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TEST 36: STATION ADDRESS RAM POSITION TEST

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.SBTTL TEST 36: STATION ADDRESS RAM POSITION TEST

:THE STATION ADDRESS RAM CAN HOLD UP TO 12 STATION ADDRESSES. WHEN A DATAGRAM
:IS RECEIVED THE STATION ADDRESS COMPARISON LOGIC DOES A BIT-WISE COMPARISON
:OF ALL 12 RAM STATION ADDRESS WITH THE INCOMING DATAGRAM STATION ADDRESS.
:
:THIS TEST WILL VERIFY THAT THE DEUNA CAN RECOGNIZE A STATION ADDRESS
:REGARDLESS OF THE LOCATION OF THE ADDRESS I: THE STATION ADDRESS RAM.
:
:THIS TEST WILL USE MICROMODULE 'E' MICROTEST #3. THE MICROCODE WILL WRITE
:A STATION ADDRESSES OF ALL 1'S INTO A SINGLE LOCATION OF THE STATION ADDRESS
:RAM. THE OTHER ELEVEN LOCATION WILL BE LOADED WITH 0'S. A DATAGRAM WITH
:AN ALL 1'S DESTINATION ADDRESS WILL BE TRANSMITTED IN LOOPBACK MODE. THE TEST
:WILL VERIFY THAT THE DATAGRAM IS RECEIVED. THE TEST WILL BE REPEATED FOR ALL
:TWELVE LOCATIONS OF THE STATION ADDRESS RAM.
:
:THE MICROTEST WILL BE REPEATED FOR EACH OF THE 12 TEST ITERATIONS. THE PCBB
:WILL BE USED TO PASS TO THE MICROCODE WHICH POSITION IS TO BE LOADED WITH
:1'S. WHEN THE STATION ADDRESS IS LOADED, THE STATION ADDRESSES MUST BE
:ROTATED ORTHOGONALLY, I.E. BIT 0 OF ALL STATION ADDRESSES LOADED TOGETHER,
:THEN BIT 1, THEN BIT 2 ETC. THIS MAKES IT DIFFICULT TO DESCRIBE THE POSITION
:OF ANY SINGLE STATION ADDRESS IN TERMS OF AN OFFSET FROM THE RAM BASE ADDRESS.

:THE PCBB IS FORMATTED AS FOLLOWS:

:PCBB+0: +-----+
 ! RAM ADDRESS POSTION !
 +-----+

:TEST SEQUENCE:

- 1-LOAD MICROMODULE 'E' IF NOT ALREADY DONE SO.
- 2-LOAD PCBB+0 WITH THE RAM POSTION TO BE TESTED
- 3-WAIT FOR THE MICROMONITOR TO ENTER THE 'INMON' STATE
- 4-SELECT MICROTEST #3
- 5-WAIT FOR 'DNI'
- 6-CHECK PCSR1 FOR AN ERROR CONDITION
- 7-WRITE '1' TO CLEAR 'DNI'
- 8-REPEAT STEPS 2-7 FOR ALL 12 RAM POSTIONS

BGNTST

T36::

:CHECK TO SEE IF MODULE 'E' HAS BEEN LOADED. IF NOT LOAD IT INTO
:THE TOP HALF OF WCS, START IT AND WAIT FOR THE MICROMONITOR TO SET 'DNI'.
:AFTER 'DNI' SETS WRITE '1' TO CLEAR IT.

BGNSEG

CMP	#'E,MICRO	TRAP	C\$BSEG
BNE	5\$:	HAS MICROCODE MODULE 'E' BEEN LOADED
CMPB	#INMON,@PCSR1	:	NO
BEQ	20\$:	YES, IS THE MICROMONITOR ACTIVE?
		:	YES SKIP LOADING THE MICROMODULE

044206'
044206'
044206' 104404
044210' 022737 000105 000326'
044216' 001004
044220' 122777 000001 134112
044226' 001440

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```

10628 044230' 012737 000105 000326' 5$:   MOV    #'E,MICRO           ;GO LOAD MICRO MODULE 'E'
10629 044236' 004737 020252'           JSR    PC,LODMIC
10630 044242' 103002           BCC    10$                 ;OK
10631 044244'           ESCAPE TST
10632 044244' 104410           TRAP   C$ESCAPE
10633 044246' 000306           .WORD L10156-
10634 044250' 012737 000176 000332' 10$:   MOV    #2*SECOND,METER   ;WAIT FOR THE MICROMONITOR
10635 044256' 004737 017330'           JSR    PC,CH:DNI
10636 044262' 103022           BCC    20$                 ;OK
10637 044264' 012737 000776' 000310'   MOV    #$DNI,BITNAM
10638 044272' 012737 001275' 000312'   MOV    #$NSET,BITSTA
10639 044300' 012737 001340' 000314'   MOV    #$AFTER,PWHEN
10640 044306' 012737 001355' 000316'   MOV    #$GTCMD,PCOMND
10641 044314'           ERRHRD 210,STAPOS,MSG1
10642 044314' 104456           TRAP   C$ERHRD
10643 044316' 000322           .WORD 210
10644 044320' 003512'           .WORD STAPOS
10645 044322' 012750'           .WORD MSG1
10646 044324'           ESCAPE TST
10647 044324' 104410           TRAP   C$ESCAPE
10648 044326' 000226           .WORD L10156-
10649 044330' 004737 017374' 20$:   JSR    PC,CLRDNI         ;CLEAR DNI BIT
10650 044334' 103006           BCC    25$
10651 044336'           ERRHRD 211,STAPOS,RACMG7 ;DNI DID NOT CLEAR!
10652 044336' 104456           TRAP   C$ERHRD
10653 044340' 000323           .WORD 211
10654 044342' 003512'           .WORD STAPOS
10655 044344' 012725'           .WORD RACMG7
10656 044346'           ESCAPE TST
10657 044346' 104410           TRAP   C$ESCAPE
10658 044350' 000204           .WORD L10156-
10659 044352'           25$:
10660 044352'           :
10661 044352'           :
10662 044352' 104405           :
10663           :
10664           :R1 WILL CONTAIN THE RAM POSTION TO BE TESTED
10665           :
10666 044354' 012701 000001           MOV    #1,R1             ;BEGIN WITH RAM POSTION #1
10667 044360'           30$:
10668           :
10669           :WAIT FOR THE MICROMONITOR TO ENTER THE 'INMON' STATE, LOAD PCBB WITH
10670           :THE RAM POSTION TO BE TESTED, SELECT MICROTEST #3 BY LOADING PCSRO COMMAND
10671           :FIELD WITH A 3, AND WAIT FOR 'DNI' TO SET.
10672           :
10673           :BGNSEG
10674 044360' 104404           TRAP   C$BSEG
10675 044362' 004737 017776'           JSR    PC,CHKMON        ;WAIT FOR MICROMONITOR
10676 044366' 103006           BCC    35$                 ;OK
10677 044370'           ERRHRD 212,STAPOS,MSG46 ;PRINT ERROR
10678 044370' 104456           TRAP   C$ERHRD
10679 044372' 000324           .WORD 212
10680 044374' 003512'           .WORD STAPOS
10681 044376' 016700'           .WORD MSG46
10682 044400'           ESCAPE TST               ;LEAVE TEST
10683 044400' 104410           TRAP   C$ESCAPE

```


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TEST 36: STATION ADDRESS RAM POSITION TEST

10740 044542' 104405
10741 044542' 104405
10742
10743
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10745 044544' 005201
10746 044546' 020127 000014
10747 044552' 103702
10748 044554'
10749 044554'
10750 044554' 104401

: REPEAT TEST FOR ALL 12 RAM POSTIONS
:

INC R1
CMP R1,#12.
BLO 30\$
ENDTST

:DO NEXT RAM POSTION
:HAVE WE DONE ALL 12 RAM POSTIONS?
:NO KEEP GOING

10002\$: TRAP C\$ESEG

L10156: TRAP C\$ETST

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TEST 37: MULTICAST ADDRESS TEST

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.SBTTL TEST 37: MULTICAST ADDRESS TEST

:MULTICAST ADDRESSING PERMITS THE DEUNA TO RESPOND TO MESSAGES AIMED AT
:LOGICALLY RELATED DEVICES ON THE NETWORK. THE MSB OF THE DESTINATION ADDRESS
:OF THESE MESSAGES IS A 1. THIS BIT IS DETECTED BY THE ADDRESS RECOGNITION
:LOGIC.
:THIS TEST WILL VERIFY THAT THE DEUNA CAN RECOGNIZE AND ACCEPT MESSAGES WITH
:THE MULTICAST BIT DESIGNATION.
:THIS TEST WILL USE MICROMODULE 'E' MICROTEST #4.
:THE MICROCODE WILL PREPARE A DATAGRAM WITH THE DESTINATION ADDRESS HAVING
:THE MULTICAST BIT SET. THE DEUNA WILL BE SETUP IN LOOPBACK MODE WITH 'ENABLE
:ALL MULTICAST'. THE DATAGRAM WILL BE TRANSMITTED AND THE T-11 WILL BE PUT IN
:A LOOP WAITING FOR A RECEIVER INTERRUPT. THE TIMER WILL INTERRUPT THE LOOP
:IF THE RECEIVER INTERRUPT DOES NOT OCCUR. IF THIS HAPPENS, PCSR1 WILL INDICATE
:AN ERROR, OTHERWISE WHEN THE RECEIVER INTERRUPT OCCURS IT WILL BREAK THE LOOP
:AND PCSR1 WILL INDICATE A SUCCESSFULL COMPLETION OF THE TEST.
:TEST SEQUENCE:
: 1-LOAD MICROMODULE 'E' IF NOT ALREADY DONE SO
: 2-WAIT FOR MICROMONITOR TO ENTER THE 'INMON' STATE
: 3-SELECT MICROTEST #4
: 4-WAIT FOR 'DNI'
: 5-CHECK PCSR1 FOR AN ERROR CONDITION
: 6-WRITE '1' TO CLEAR 'DNI'

044556'
044556'

BGNTST

T37::

:CHECK TO SEE IF MODULE 'E' HAS BEEN LOADED. IF NOT LOAD IT INTO
:THE TOP HALF OF WCS, START IT AND WAIT FOR THE MICROMONITOR TO SET 'DNI'.
:AFTER 'DNI' SETS WRITE '1' TO CLEAR IT.

BGNSEG

10790	044556'	104404							TRAP	C\$BSEG
10791	044560'	022737	000105	000326'		CMP	#'E,MICRO			:HAS MICROCODE MODULE 'E' BEEN LOADED
10792	044566'	001004				BNE	5\$:NO
10793	044570'	122777	000001	133542		CMPB	#INMON,@PCSR1			:YES, IS THE MICROMONITOR ACTIVE?
10794	044576'	001440				BEQ	20\$:YES SKIP LOADING THE MICROMODULE
10795	044600'	012737	000105	000326'	5\$:	MOV	#'E,MICRO			:GO LOAD MICRO MODULE 'E'
10796	044606'	004737	020252'			JSR	PC,LODMIC			
10797	044612'	103002				BCC	10\$:OK
10798	044614'					ESCAPE	TST			
10799	044614'	104410							TRAP	C\$ESCAPE
10800	044616'	000256							.WORD	L10157-
10801	044620'	012737	000176	000332'	10\$:	MOV	#2*SECOND,METER			:WAIT FOR THE MICROMONITOR
10802	044626'	004737	017330'			JSR	PC,CHKDNI			
10803	044632'	103022				BCC	20\$:OK
10804	044634'	012737	000776'	000310'		MOV	#\$DNI,BITNAM			
10805	044642'	012737	001275'	000312'		MOV	#\$NSET,BITSTA			
10806	044650'	012737	001340'	000314'		MOV	#\$AFTER,PWHEN			

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10807	044656'	012737	001355'	000316'	MOV	#SGTCMD,PCOMND			
10808	044664'				ERRHRD	217,MUCAST,MSG1			
10809	044664'	104456						TRAP	C\$ERHRD
10810	044666'	000331						.WORD	217
10811	044670'	003563'						.WORD	MUCAST
10812	044672'	012750'						.WORD	MSG1
10813	044674'				ESCAPE	TST			
10814	044674'	104410						TRAP	C\$ESCAPE
10815	044676'	000176						.WORD	L10157-
10816	044700'	004737	017374'		20\$:	JSR	PC,CLRDNI		;CLEAR DNI BIT
10817	044704'	103006				BCC	25\$		
10818	044706'					ERRHRD	218,MUCAST,RACMG7		;DNI DID NOT CLEAR!
10819	044706'	104456						TRAP	C\$ERHRD
10820	044710'	000332						.WORD	218
10821	044712'	003563'						.WORD	MUCAST
10822	044714'	012726'						.WORD	RACMG7
10823	044716'				ESCAPE	TST			
10824	044716'	104410						TRAP	C\$ESCAPE
10825	044720'	000154						.WORD	L10157-
10826	044722'				25\$:	ENDSEG			
10827	044722'								
10828	044722'								
10829	044722'	104405						10000\$:	TRAP
10830									C\$ESEG
10831					:	WAIT FOR THE MICROMONITOR TO ENTER THE 'INMON' STATE			
10832					:				
10833	044724'				:	BGNSEG			
10834	044724'	104404						TRAP	C\$BSEG
10835	044726'	004737	017776'		JSR	PC,CHKMON			;WAIT FOR MICROMONITOR
10836	044732'	103006			BCC	30\$;OK
10837	044734'				ERRHRD	219,MUCAST,MSG46			;PRINT ERROR
10838	044734'	104456						TRAP	C\$ERHRD
10839	044736'	000333						.WORD	219
10840	044740'	003563'						.WORD	MUCAST
10841	044742'	016700'						.WORD	MSG46
10842	044744'				ESCAPE	TST			;LEAVE TEST
10843	044744'	104410						TRAP	C\$ESCAPE
10844	044746'	000126						.WORD	L10157-
10845	044750'				30\$:				
10846					:	EXECUTE MICROTEST #4 BY LOADING THE COMMAND FIELD OF PCSRO WITH A 4			
10847					:				
10848					:				
10849	044750'	012777	000004	133360	MOV	#4,@PCSRO			;TELL T11 TO EXECUTE MICROTEST #4
10850									
10851					:	WAIT AT LEAST 3 SECONDS FOR TEST TO FINISH BECAUSE MICROCODE WAITS 2 SECONDS			
10852					:	FOR THE RECEIVER INTERRUPT			
10853					:				
10854	044756'	012737	000275	000332'	MOV	#3*SECOND,METER			;WAIT FOR DNI
10855	044764'	004737	017330'		JSR	PC,CHKDNI			
10856	044770'	103021			BCC	40\$			
10857	044772'	004737	020050'		JSR	PC,CHKINT			;SEE IF ANY ERROR INTERRUPTS OCCURRED
10858	044776'	103006			BCC	35\$;NO, OK
10859	045000'				ERRHRD	220,MUCAST,MSG44			;PRINT ERROR MESSAGE
10860	045000'	104456						TRAP	C\$ERHRD
10861	045002'	000334						.WORD	220
10862	045004'	003563'						.WORD	MUCAST

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```

10863 045006' 016454' .WORD MSG44
10864 045010' ESCAPE TST
10865 045010' 104410 TRAP C$ESCAPE
10866 045012' 000062 .WORD L10157-.
10867 045014' 012702 000004 35$: MOV #4,R2 :MICROTEST #4 IS HUNG
10868 045020' ERRHRD 221,MUCAST,MSG12
10869 045020' 104456 TRAP C$ERHRD
10870 045022' 000335 .WORD 221
10871 045024' 003563' .WORD MUCAST
10872 045026' 013500' .WORD MSG12
10873 045030' ESCAPE TST
10874 045030' 104410 TRAP C$ESCAPE
10875 045032' 000042 .WORD L10157-.
10876
10877 :OK, EITHER THE TIMER BROKE THE LOOP OR THE RECEIVER INTERRUPT DID.
10878 :WHICH WAS IT?
10879
10880 045034' 122777 000003 133276 40$: CMPB #INERR,@PCSR1 :DID AN ERROR OCCUR?
10881 045042' 001004 BNE 50$ :NO
10882 045044' ERRHRD 222,MUCAST :YES, PRINT ERROR MESSAGE
10883 045044' 104456 TRAP C$ERHRD
10884 045046' 000336 .WORD 222
10885 045050' 003563' .WORD MUCAST
10886 045052' 000000 .WORD 0
10887
10888 :WRITE '1' TO CLEAR 'DNI'
10889
10890 045054' 004737 017374' 50$: JSR PC,CLRDNI :CLEAR DNI BIT
10891 045060' 103004 BCC 60$
10892 045062' ERRHRD 223,MUCAST,RACMG7 :ERROR DNI DID NOT CLEAR!
10893 045062' 104456 TRAP C$ERHRD
10894 045064' 000337 .WORD 223
10895 045066' 003563' .WORD MUCAST
10896 045070' 012726' .WORD RACMG7
10897 045072' 60$:
10898 045072' ENDSEG
10899 045072' 10001$:
10900 045072' 104405 TRAP C$ESEG
10901 045074' ENDTST
10902 045074' L10157:
10903 045074' 104401 TRAP C$ETST

```

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045076'

.SBTTL TEST 38: CRC DATA PATTERN TEST

*****8

:THE LINK MODULE HAS HARDWARE TO GUARANTEE THAT DATAGRAMS HAVE NOT BEEN
:CORRUPTED DURING TRANSMISSION AND RECEPTION. THE HARDWARE GENERATES A CRC
:FOR DATAGRAMS TRANSMITTED AND VERIFIES THE CRC FOR DATAGRAMS RECEIVED.
:THE CRC IS A 32 BIT NUMBER GENERATED BY DIVIDING THE DATAGRAM BIT STREAM BY A
:CRC POLYNOMIAL. THE DIVISION RESULTS IN A UNIQUE NUMBER THAT CAN ONLY BE
:REPRODUCED IN CRC CALCULATIONS IF THE BIT STREAM EXACTLY MATCHES THE ORIGINAL.
:THE CRC IS CALCULATED DURING DATAGRAM TRANSMISSION AND IS APPENDED TO THE
:PACKET. THE CRC IS TRANSMITTED AS PART OF THE PACKET. THE CRC IS AGAIN
:CALCULATED WHEN THE DATAGRAM IS RECEIVED AND THE CALCULATED IS COMPARED TO
:THE CRC TRANSMITTED. IF THE DATAGRAM HAS BEEN FAITHFULLY TRANSMITTED, THE
:CRC'S SHOULD MATCH EXACTLY.

:THE DEUNA CALCULATES THE CRC WITH DEDICATED CRC LOGIC. THE LOGIC IS EITHER
:DEDICATED TO THE CALCULATION OF THE OUTGOING DATAGRAM OR THE CALCULATION OF
:THE INCOMING DATAGRAM, BUT NOT BOTH.

:THIS TEST WILL VERIFY THE OPERATION OF THE CRC CALCULATION CIRCUITRY.
:MICROMODULE 'F' MICROTTEST #1 WILL BE USED.
:THE MICROCODE WILL TRANSMIT DATAGRAMS IN LOOPBACK MODE. THE CRC HARDWARE WILL
:BE DEDICATED TO THE TRANSMITTER. WHEN THE DATAGRAM IS RECEIVED THE T-11 WILL
:CALCULATE A CRC ON THE DATA RECEIVED (INCLUDING THE TRANSMITTED CRC).
:THE RESULT OF THIS CALCULATION WILL BE A 32 BIT CONSTANT. THIS CONSTANT IS
:THEN COMPARED TO WHAT WAS EXPECTED AND IF THEY DO NOT MATCH. AN ERROR IS
:PLACED IN PCSR1.

:PATTERNS WILL BE PASSED TO THE MICROCODE THROUGH THE PCBB. THE MICROCODE WILL
:FILL THE TRANSMIT BUFFER WITH THIS PATTERN BEFORE EACH TRANSMISSION TAKES
:PLACE.

:THE PCBB WILL BE FORMATTED AS FOLLOWS:

PCBB+0: +-----+
 ! DATA PATTERN !
 +-----+

:TEST SEQUENCE:

- 1-LOAD MICROMODULE 'F' IF NOT ALREADY DONE SO
- 2-PLACE A DATA PATTERN IN PCBB+0
- 3-WAIT FOR THE MICROMONITOR TO ENTER THE 'INMON' STATE
- 4-SELECT MICROTTEST #1
- 5-WAIT FOR 'DNI'
- 6-CHECK PCSR1 FOR AN ERROR CONDITION
- 7-WRITE '1' TO CLEAR 'DNI'

*****8

BGNTST

T38::

:CHECK TO SEE IF MODULE 'F' HAS BEEN LOADED. IF NOT LOAD IT INTO
:THE TOP HALF OF WCS, START IT AND WAIT FOR THE MICROMONITOR TO SET 'DNI'.
:AFTER 'DNI' SETS WRITE '1' TO CLEAR IT.

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```

10960
10961 045076' ; BGNSEG
10962 045076' 104404
10963 045100' 022737 000106 000326 CMP #'F,MICRO ;HAS MICROCODE MODULE 'F' BEEN LOADED
10964 045106' 001004 BNE 5$ ;NO
10965 045110' 122777 000001 133222 CMPB #INMON,@PCSR1 ;YES, IS THE MICROMONITOR ACTIVE?
10966 045116' 001440 BEQ 20$ ;YES SKIP LOADING THE MICROMODULE
10967 045120' 012737 000106 000326' 5$: MOV #'F,MICRO ;GO LOAD MICRO MODULE 'F'
10968 045126' 004737 020252' JSR PC,LODMIC
10969 045132' 103002 BCC 10$ ;OK
10970 045134' ESCAPE TST
10971 045134' 104410
10972 045136' 000276 TRAP C$ESCAPE
10973 045140' 012737 000176 000332' 10$: MOV #2*SECOND,METER .WORD L10160-.
10974 045146' 004737 017330' JSR PC,CHKDNI ;WAIT FOR THE MICROMONITOR
10975 045152' 103022 BCC 20$ ;OK
10976 045154' 012737 000776' 000310' MOV #SDNI,BITNAM
10977 045162' 012737 001275' 000312' MOV #NSET,BITSTA
10978 045170' 012737 001340' 000314' MOV #AFTER,PWHEN
10979 045176' 012737 001355' 000316' MOV #SGTCMD,PCOMND
10980 045204' ERRHRD 224,CRCDAT,MSG1
10981 045204' 104456 TRAP C$ERHRD
10982 045206' 000340 .WORD 224
10983 045210' 003621' .WORD CRCDAT
10984 045212' 012750' .WORD MSG1
10985 045214' ESCAPE TST
10986 045214' 104410 TRAP C$ESCAPE
10987 045216' 000216 .WORD L10160-.
10988 045220' 004737 017374' 20$: JSR PC,CLRDNI ;CLEAR DNI BIT
10989 045224' 103006 BCC 25$
10990 045226' ERRHRD 225,CRCDAT,RACMG7 ;DNI DID NOT CLEAR!
10991 045226' 104456 TRAP C$ERHRD
10992 045230' 000341 .WORD 225
10993 045232' 003621' .WORD CRCDAT
10994 045234' 012726' .WORD RACMG7
10995 045236' ESCAPE TST
10996 045236' 104410 TRAP C$ESCAPE
10997 045240' 000174 .WORD L10160-.
10998 045242' 25$:
10999 045242' ENDSEG
11000 045242' 10000$:
11001 045242' 104405 TRAP C$ESEG
11002
11003 ;SELECT A DATA PATTERN FROM A TABLE OF PREDEFINED DATA PATTERNS
11004 ;AND LOAD IT INTO PCBB+0 FOR THE MICROCODE
11005
11006 045244' 012701 000516' MOV #PATERN,R1 ;GET ADDRESS OF DATA PATTERN TABLE
11007 045250' 012705 000005' MOV #5,R5 ;# OF DATA PATTERNS
11008 045254' 012137 000604' 27$: MOV (R1)+,PCBB ;LOAD PCBB WITH A DATA PATTERN
11009 045260' BGNSEG
11010 045260' 104404 TRAP C$BSEG
11011
11012 ;WAIT FOR THE MICROMONITOR TO ENTER THE 'INMON' STATE
11013
11014 045262' 004737 017776' JSR PC,CHKMON ;WAIT FOR MICROMONITOR
11015 045266' 103006 BCC 30$ ;OK
    
```

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```

11016 045270'          ERRHRD  227,CRCDAT,MSG46          :PRINT ERROR
11017 045270' 104456          TRAP      C$ERHRD
11018 045272' 000343          .WORD    227
11019 045274' 003621'          .WORD    CRCDAT
11020 045276' 016700'          .WORD    MSG46
11021 045300'          ESCAPE  TST          :LEAVE TEST
11022 045300' 104410          TRAP      C$ESCAPE
11023 045302' 000132          .WORD    L10160-
11024 045304'
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11028
11029 045304' 012777 000001 133024  MOV      #1,@PCSR0          :TELL T11 TO EXECUTE MICROTEST #1
11030 045312' 012737 000176 000332'  MOV      #2*SECOND,METER  :WAIT FOR DNI
11031 045320' 004737 017330'  JSR      PC,CHKDNI
11032 045324' 103021          BCC      40$
11033 045326' 004737 020050'  JSR      PC,CHKINT          :SEE IF ANY ERROR INTERRUPTS OCCURRED
11034 045332' 103006          BCC      35$                :NO, OK
11035 045334'          ERRHRD  228,CRCDAT,MSG44  :PRINT ERROR MESSAGE
11036 045334' 104456          TRAP      C$ERHRD
11037 045336' 000344          .WORD    228
11038 045340' 003621'          .WORD    CRCDAT
11039 045342' 016454'          .WORD    MSG44
11040 045344'          ESCAPE  TST
11041 045344' 104410          TRAP      C$ESCAPE
11042 045346' 000066          .WORD    L10160-
11043 045350' 012702 000005 35$:  MOV      #5,R2          :MICROTEST #1 IS HUNG
11044 045354'          ERRHRD  229,CRCDAT,MSG12
11045 045354' 104456          TRAP      C$ERHRD
11046 045356' 000345          .WORD    229
11047 045360' 003621'          .WORD    CRCDAT
11048 045362' 013500'          .WORD    MSG12
11049 045364'          ESCAPE  TST
11050 045364' 104410          TRAP      C$ESCAPE
11051 045366' 000046          .WORD    L10160-
11052
11053
11054
11055 045370' 122777 000003 132742 40$:  CMPB     #INERR,@PCSR1      :DID AN ERROR OCCUR?
11056 045376' 001004          BNE      50$                :NO
11057 045400'          ERRHRD  230,CRCDAT          :PRINT ERROR MESSAGE
11058 045400' 104456          TRAP      C$ERHRD
11059 045402' 000346          .WORD    230
11060 045404' 003621'          .WORD    CRCDAT
11061 045406' 000000          .WORD    0
11062
11063
11064
11065 045410' 004737 017374' 50$:  JSR      PC,CLRDN1          :CLEAR DNI BIT
11066 045414' 103004          BCC      55$
11067 045416'          ERRHRD  231,CRCDAT,RACMG7  :ERROR DNI DID NOT CLEAR!
11068 045416' 104456          TRAP      C$ERHRD
11069 045420' 000347          .WORD    231
11070 045422' 003621'          .WORD    CRCDAT
11071 045424' 012726'          .WORD    RACMG7

```

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11072
11073 045426'
11074 045426'
11075 045426'
11076 045426' 104405
11077
11078
11079
11080 045430' 005305
11081 045432' 001310
11082 045434'
11083 045434'
11084 045434' 104401

55\$:
ENDSEG

10001\$:
TRAP C\$ESEG

:REPEAT THE TEST EACH TIME WITH A NEW DATA PATTERN

:
DFC R5
BNE 27\$
ENDTST

:HAVE WE TESTED WITH ALL DATA PATTERNS?
:NOT YET

L10160:
TRAP C\$ETST

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045436'
045436'

045436'

104404
022737
001004
122777
001440
012737
004737
103002
104410
000326

000106
000001
000106
020252

000326'
5\$:

.SBTTL TEST 39: CRC ERROR TEST

: THIS TEST WILL VERIFY THAT THE LINK CRC CIRCUITRY CAN DETECT A BAD CRC.

: MICROMODULE 'F' MICROTEST #2 WILL BE USED. THE MICROCODE WILL TRANSMIT
: DATAGRAMS IN LOOPBACK MODE. EACH DATAGRAM WILL HAVE AN ERRONEOUS CRC
: APPENDED TO THE DATA FIELD. THE DEUNA CRC LOGIC WILL BE SETUP SUCH THAT
: THE CRC LOGIC WILL BE DEDICATED TO THE RECEIVER. THIS IS EXPECTED TO CAUSE
: A CRC ERROR.

: THE DATA FIELDS OF EACH DATAGRAM WILL CONSIST OF PATTERNS. THE PATTERNS
: WILL BE PASSED TO THE MICROCODE VIA THE PCBB.

: AFTER THE RECEIVER INTERRUPT THE MICROCODE WILL PASS THE RECEIVER STATUS WORD
: 0 BACK VIA PCBB+2. THE CRC BIT IN THIS WORD IS CHECKED TO SEE IF IT IS SET.

: THE PCBB IS FORMATTED AS FOLLOWS:



: TEST SEQUENCE:

- 1-LOAD MICROMODULE 'F' IF NOT ALREADY DONE SO
- 2-PLACE A DATA PATTERN IN PCBB+0
- 3-WAIT FOR THE MICROCODE TO ENTER THE 'INMON' STATE
- 4-SELECT MICROTEST #2
- 5-WAIT FOR 'DNI'
- 6-VERIFY CRC BIT AND ERROR SUMMARY BIT SET IN PCBB+2
- 7-WRITE '1' TO CLEAR 'DNI'

BGNTST

T39::

: CHECK TO SEE IF MODULE 'F' HAS BEEN LOADED. IF NOT LOAD IT INTO
: THE TOP HALF OF WCS, START IT AND WAIT FOR THE MICROMONITOR TO SET 'DNI'.
: AFTER 'DNI' SETS WRITE '1' TO CLEAR IT.

BGNSEG

```
          TRAP    C$BSEG
CMP      #'F,MICRO      ;HAS MICROCODE MODULE 'F' BEEN LOADED
BNE      5$           ;NO
CMPB    #INMON,@PCSR1  ;YES, IS THE MICROMONITOR ACTIVE?
BEQ      20$          ;YES SKIP LOADING THE MICROMODULE
MOV      #'F,MICRO      ;GO LOAD MICRO MODULE 'F'
JSR     PC,LODMIC
BCC     10$           ;OK
ESCAPE  TST
```

TRAP C\$ESCAPE
.WORD L10161-

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```

11141 045500' 012737 000176 000332' 10$: MOV #2*SECOND,METER ;WAIT FOR THE MICROMONITOR
11142 045506' 004737 017330' JSR PC,CHKDNI
11143 045512' 103022 BCC 20$ ;OK
11144 045514' 012737 000776' 000310' MOV #SDNI,BITNAM
11145 045522' 012737 001275' 000312' MOV #SNSET,BITSTA
11146 045530' 012737 001340' 000314' MOV #SAFTER,PWHEN
11147 045536' 012737 001355' 000316' MOV #SGTCMD,PCOMND
11148 045544' ERRHRD 232,CRCERR,MSG1
11149 045544' 104456 TRAP C$ERHRD
11150 045546' 000350 .WORD 232
11151 045550' 003656' .WORD CRCERR
11152 045552' 012750' .WORD MSG1
11153 045554' ESCAPE TST
11154 045554' 104410 TRAP C$ESCAPE
11155 045556' 000246 .WORD L10161-.
11156 045560' 004737 017374' 20$: JSR PC,CLRDN1 ;CLEAR DNI BIT
11157 045564' 103006 BCC 25$
11158 045566' ERRHRD 233,CRCERR,RACMG7 ;DNI DID NOT CLEAR!
11159 045566' 104456 TRAP C$ERHRD
11160 045570' 000351 .WORD 233
11161 045572' 003656' .WORD CRCERR
11162 045574' 012726' .WORD RACMG7
11163 045576' ESCAPE TST
11164 045576' 104410 TRAP C$ESCAPE
11165 045600' 000224 .WORD L10161-.
11166 045602' 25$:
11167 045602' ENDSEG
11168 045602'
11169 045602' 104405 10000$: TRAP C$ESEG
11170
11171 ;CLEAR PCBB+2, GET A DATA PATTERN FROM A LIST OF PRESELECTED DATA PATTERNS
11172 ;AND PLACE IT IN PCBB+0 FOR THE MICROCODE
11173 ;
11174 045604' 005037 000606' CLR PCBB+2 ;HERE IS WHERE MICROCODE WILL PUT...
11175 ;STATUS WORD
11176 045610' 012701 000516' MOV #PATERN,R1 ;GET ADDRESS OF DATA PATTERN TABLE
11177 045614' 012705 000005' MOV #5,R5 ;# OF DATA PATTERNS
11178 045620' 012137 000604' 27$: MOV (R1)+,PCBB ;LOAD PCBB WITH A DATA PATTERN
11179
11180 ;WAIT FOR THE MICROMONITOR TO BECOME READY
11181 ;
11182 ;BGNSEG
11183 045624' 104404 TRAP C$BSEG
11184 045626' 004737 017776' JSR PC,CHKMON ;WAIT FOR MICROMONITOR
11185 045632' 103006 BCC 30$ ;OK
11186 045634' ERRHRD 234,CRCERR,MSG46 ;PRINT ERROR
11187 045634' 104456 TRAP C$ERHRD
11188 045636' 000352 .WORD 234
11189 045640' 003656' .WORD CRCERR
11190 045642' 016700' .WORD MSG46
11191 045644' ESCAPE TST ;LEAVE TEST
11192 045644' 104410 TRAP C$ESCAPE
11193 045646' 000156 .WORD L10161-.
11194 045650' 30$:
11195 ;
11196 ;EXECUTE MICROTEST #2 BY LOADING THE COMMAND FIELD OF PCSRO WITH A 2
    
```

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```

11197          ;WAIT FOR 'DNI'
11198          ;
11199 045650' 012777 000002 132460      MOV      #2,@PCSR0      ;TELL T11 TO EXECUTE MICROTEST #2
11200 045656' 012737 000176 000332'    MOV      #2*SECOND,METER ;WAIT FOR DNI
11201 045664' 004737 017330'    JSR      PC,CHKDNI
11202 045670' 103021          BCC      40$
11203 045672' 004737 020050'    JSR      PC,CHKINT      ;SEE IF ANY ERROR INTERRUPTS OCCURRED
11204 045676' 103006          BCC      35$            ;NO, OK
11205 045700'          ERRHRD  235,CRCERR,MSG44 ;PRINT ERROR MESSAGE
11206 045700' 104456          TRAP     C$ERHRD
11207 045702' 000353          .WORD   235
11208 045704' 003656'          .WORD   CRCERR
11209 045706' 016454'          .WORD   MSG44
11210 045710'          ESCAPE  TST
11211 045710' 104410          TRAP     C$ESCAPE
11212 045712' 000112          .WORD   L10161-
11213 045714' 012702 000002    35$:  MOV      #2,R2      ;MICROTEST #2 IS HUNG
11214 045720'          ERRHRD  236,CRCERR,MSG12
11215 045720' 104456          TRAP     C$ERHRD
11216 045722' 000354          .WORD   236
11217 045724' 003656'          .WORD   CRCERR
11218 045726' 013500'          .WORD   MSG12
11219 045730'          ESCAPE  TST
11220 045730' 104410          TRAP     C$ESCAPE
11221 045732' 000072          .WORD   L10161-
11222          ;
11223          ;OK, TEST IS COMPLETED, NOW CHECK PCBB+2. PCBB+2 CONTAINS THE RECEIVER STATUS
11224          ;WORD, IT SHOULD HAVE THE CRC ERROR BIT SET AND THE ERROR SUMMARY BIT SET
11225          ;
11226 045734' 013704 000606'    40$:  MOV      PCBB+2,R4      ;THIS IS THE RECEIVER STATUS WORD
11227 045740' 012703 004000'    MOV      #BIT11,R3      ;BIT CRC SHOULD BE SET
11228 045744' 030304          BIT      R3,R4          ;IS CRC BIT SET?
11229 045746' 001004          BNE      45$            ;YES, OK
11230 045750'          ERRHRD  237,CRCERR,MSG27 ;NO, PRINT ERROR MESSAGE
11231 045750' 104456          TRAP     C$ERHRD
11232 045752' 000355          .WORD   237
11233 045754' 003656'          .WORD   CRCERR
11234 045756' 015060'          .WORD   MSG27
11235 045760' 012703 040000'    45$:  MOV      #BIT14,R3      ;ERROR SUMMARY BIT SHOULD BE SET
11236 045764' 030304          BIT      R3,R4          ;IS ERROR SUMMARY BIT SET?
11237 045766' 001004          BNE      50$            ;YES, GOOD
11238 045770'          ERRHRD  238,CRCERR,MSG28 ;NO, PRINT ERROR MESSAGE
11239 045770' 104456          TRAP     C$ERHRD
11240 045772' 000356          .WORD   238
11241 045774' 003656'          .WORD   CRCERR
11242 045776' 015126'          .WORD   MSG28
11243          ;
11244          ;WRITE '1' TO CLEAR 'DNI'
11245          ;
11246 046000' 004737 017374'    50$:  JSR      PC,CLRDN1      ;CLEAR DNI BIT
11247 046004' 103004          BCC      55$
11248 046006'          ERRHRD  239,CRCERR,RACMG7 ;ERROR DNI DID NOT CLEAR!
11249 046006' 104456          TRAP     C$ERHRD
11250 046010' 000357          .WORD   239
11251 046012' 003656'          .WORD   CRCERR
11252 046014' 012726'          .WORD   RACMG7
    
```

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11253 046016'
11254 046016'
11255 046016'
11256 046016' 104405
11257
11258
11259
11260 046020' 005305
11261 046022' 001276
11262
11263 046024'
11264 046024'
11265 046024' 104401

55\$:
ENDSEG
:
:REPEAT TEST WITH ALL DATA PATTERNS
:
DEC R5
BNE 27\$
ENDTST

10001\$: TRAP C\$ESEG
:
:HAVE WE TESTED WITH ALL DATA PATTERNS?
:NOT YET

L10161: TRAP C\$ETST

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TEST 40: CRC PATTERN LENGTH TEST

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.SBTTL TEST 40: CRC PATTERN LENGTH TEST

: THIS TEST WILL VERIFY THAT THE RECEIVE CRC HARDWARE CAN CALCULATE CRC FOR
: DATAGRAMS OF VARYING LENGTHS.

: DATAGRAMS WILL BE TRANSMITTED FOR THE TRANSMIT BUFFER TO THE RECEIVE BUFFER
: IN LOOPBACK MODE. THE TRANSMIT CRC WILL BE DISABLED WHICH WILL ASSIGN THE
: CRC LOGIC TO CALCULATION OF INCOMING DATAGRAMS. THE CRC FOR TRANSMIT DATAGRAMS
: WILL BE CALCULATED BY THE MICROCODE. IT IS EXPECTED THAT THE CRC LOGIC WILL
: VERIFY THE CRC APPENDED TO THE DATAGRAM AS IT IS BEING RECEIVED.

: PATTERNS WILL BE USED TO FILL THE DATAGRAM DATA FIELD. THE PATTERNS WILL BE
: PASSED TO THE MICROCODE THROUGH THE PCBB ALONG WITH THE BYTE COUNT TO BE USED.

: AFTER THE RECEPTION OF THE DATAGRAM THE RECEIVER STATUS WORD WILL BE PASSED
: BACK VIA THE PCBB SO IT CAN BE CHECKED

: THE PCBB IS FORMATTED AS FOLLOWS:



: TEST SEQUENCE:

- 1-LOAD MICROMODULE 'F' IF NOT ALREADY DONE SO
- 2-PLACE A PATTERN IN PCBB+0
- 3-PREPARE A MINIMUM BYTE COUNT
- 4-PLACE BYTE COUNT IN PCBB+4
- 5-WAIT FOR MICROMONITOR TO ENTER 'INMON' STATE
- 6-SELECT MICROTTEST #3
- 7-WAIT FOR 'DNI'
- 8-VERIFY NO ERRORS IN PCBB+4
- 9-WRITE '1' TO CLEAR 'DNI'
- 10-MULTIPLY BYTE COUNT BY 2
- 11-REPEAT STEPS 4-10 UNTIL MAXIMUM BYTE COUNT IS REACHED
- 12-REPEAT STEPS 2-11 WITH ALL DATA PATTERNS

BGNTST

T40::

: CHECK TO SEE IF MODULE 'F' HAS BEEN LOADED. IF NOT LOAD IT INTO
: THE TOP HALF OF WCS, START IT AND WAIT FOR THE MICROMONITOR TO SET 'DNI'.
: AFTER 'DNI' SETS WRITE '1' TO CLEAR IT.

BGNSEG

046026'
046026'

046026' 104404
046030' 022737 000106 000326'
046036' 001004

CMP #1,MICRO
BNE S8

TRAP C\$BSEG
:HAS MICROCODE MODULE 'F' BEEN LOADED
:NO

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 CZUAAA.P11 11-JAN-83 09:29 TEST 40: CRC PATTERN LENGTH TEST

```

11322 046040' 122777 000001 132272      CMPB    #INMON,SPCM1      ;YES, IS THE MICROMONITOR ACTIVE?
11323 046046' 001440      BEQ     20$              ;YES SKIP LOADING THE MICROMODULE
11324 046050' 012737 000106 000326' 5$:    MOV     #'F, MICRO      ;GO LOAD MICRO MODULE 'F'
11325 046056' 004737 020252'      JSR     PC,LODMIC
11326 046062' 103002      BCC    10$              ;OK
11327 046064'      ESCAPE  TST
11328 046064' 104410      TRAP   C$ESCAPE
11329 046066' 000320      .WORD L10162-.
11330 046070' 012737 000176 000332' 10$:   MOV     #2*SECOND,METER ;WAIT FOR THE MICROMONITOR
11331 046076' 004737 017330'      JSR     PC,CHKDNI
11332 046102' 103022      BCC    20$              ;OK
11333 046104' 012737 000776' 000310'      MOV     #SDNI,BITNAM
11334 046112' 012737 001275' 000312'      MOV     #SSET,BITSTA
11335 046120' 012737 001340' 000314'      MOV     #SAFTER,PWHEN
11336 046126' 012737 001355' 000316'      MOV     #SGTCMD,PCOMND
11337 046134'      ERRHRD 240,CRCPAT,MSG1
11338 046134' 104456      TRAP   C$ERHRD
11339 046136' 000360      .WORD 240
11340 046140' 003704'      .WORD CRCPAT
11341 046142' 012750'      .WORD MSG1
11342 046144'      ESCAPE  TST
11343 046144' 104410      TRAP   C$ESCAPE
11344 046146' 000240      .WORD L10162-.
11345 046150' 004737 017374' 20$:   JSR     PC,CLRDN1      ;CLEAR DNI BIT
11346 046154' 103006      BCC    25$
11347 046156'      ERRHRD 241,CRCPAT,RACMG7 ;DNI DID NOT CLEAR!
11348 046156' 104456      TRAP   C$ERHRD
11349 046160' 000361      .WORD 241
11350 046162' 003704'      .WORD CRCPAT
11351 046164' 012726'      .WORD RACMG7
11352 046166'      ESCAPE  TST
11353 046166' 104410      TRAP   C$ESCAPE
11354 046170' 000216      .WORD L10162-.
11355 046172' 25$:
11356 046172'      ENDSEG
11357 046172'      10000$:
11358 046172' 104405      TRAP   C$ESEG
11359
11360      ;POINT TO LIST OF DATA PATTERNS
11361
11362 046174' 012701 000516'      MOV     #PATERN,R1      ;GET ADDRESS OF DATA PATTERN TABLE
11363 046200' 012705 000005      MOV     #5,R5           ;# OF DATA PATTERNS
11364
11365      ;GET A DATA PATTERN FROM THE LIST AND PLACE IT IN PCBB+0
11366
11367 046204' 27$:
11368 046204' 012137 000604'      MOV     (R1)+,PCBB      ;LOAD PCBB WITH A DATA PATTERN
11369 046210'      BGNSEG
11370 046210' 104404      TRAP   C$BSEG
11371
11372      ;GENERATE A MINIMUM BYTE COUNT
11373
11374 046212' 012703 000100      MOV     #BI'6,P3       ;R3 WILL BE BYTE COUNT
11375 046216' 28$:
11376
11377      ;LOAD BYTE COUNT INTO THE PCBB+4
    
```

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11378
11379 046216'          : BGNSEG
11380 046216' 104404          :
11381 046220' 010337 000606' :          MOV      R3,PCBB+2          :LOAD BYTE COUNT INTO PCBB          TRAP      C$BSEG
11382
11383          :
11384          :WAIT FOR THE MICROCODE TO ENTER THE 'INMON' STATE
11385          :EXECUTE MICROTEST #3 BY LOADING THE COMMAND FIELD OF PCSRO WITH A 3
11386          :WAIT FOR 'DNI'
11387 046224' 004737 017176' :          JSR      PC,CHKMON          :WAIT FOR MICROMONITOR
11388 046230' 103006          :          BCC      30$              :OK
11389 046232'          :          ERRHRD 242,CRCPAT,MSG46   :PRINT ERROR
11390 046232' 104456          :
11391 046234' 000362          :
11392 046236' 003704'          :
11393 046240' 016700'          :
11394 046242'          :          ESCAPE  TST              :LEAVE TEST
11395 046242' 104410          :
11396 046244' 000142          :
11397 046246' 012777 000003 132062 30$: :          MOV      #3,@PCSRO          :TELL T11 TO EXECUIE MICROTEST #3
11398 046254' 012737 000176 000332' :          MOV      #2*SECOND,METER   :WAIT FOR DNI
11399 046262' 004737 017330' :          JSR      PC,CHKDNI
11400 046266' 103021          :          BCC      40$
11401 046270' 004737 020050' :          JSR      PC,CHKINT          :SEE IF ANY ERROR INTERRUPTS OCCURRED
11402 046274' 103006          :          BCC      35$              :NO, OK
11403 046276'          :          ERRHRD 243,CRCPAT,MSG44   :PRINT ERROR MESSAGE
11404 046276' 104416          :
11405 046300' 000313          :
11406 046302' 003704'          :
11407 046304' 016454'          :
11408 046306'          :          ESCAPE  TST
11409 046306' 104410          :
11410 046310' 000076          :
11411 046312' 012702 000003 35$: :          MOV      #3,R2              :MICROTEST #3 IS HUNG
11412 046316'          :          ERRHRD 244,CRCPAT,MSG12
11413 046316' 104456          :
11414 046320' 000364          :
11415 046322' 003704'          :
11416 046324' 013500'          :
11417 046326'          :          ESCAPE  TST
11418 046326' 104410          :
11419 046330' 000056          :
11420          :
11421          :CHECK THE RECEIVER STATUS WORD, WHICH THE MICROCODE PLACED IN PCBB+4, FOR ANY
11422          :ERRORS
11423          :
11424 046332' 005737 000610' :          40$:  TST      PCBB+4          :ANYTHING SET IN RECEIVER STATUS WORD 0
11425 046336' 001404          :          BEQ      50$              :NO, OK
11426 046340'          :          ERRHRD 245,CRCPAT,MSG29   :PRINT ERROR MESSAGE
11427 046340' 104456          :
11428 046342' 000365          :
11429 046344' 003704'          :
11430 046346' 015174'          :
11431          :
11432          :WRITE '1' TO CLEAR 'DNI'
11433          :

```

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TEST 40: CRC PATTERN LENGTH TEST

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11434 046350' 004737 017374'      50$:   JSR      PC,CLRDNI      ;CLEAR DNI BIT
11435 046354' 103004                BCC      55$
11436 046356'                        ERRHRD  246,CRCPAT,RACMG7 ;ERROR DNI DID NOT CLEAR!
11437 046356' 104456                TRAP    C$ERHRD
11438 046360' 000366                .WORD  246
11439 046362' 003704'                .WORD  CRCPAT
11440 046364' 012726'                .WORD  RACMG7
11441 046366'
11442 046366'
11443 046366'
11444 046366' 104405                10002$: TRAP    C$ESEG
11445
11446      ;GENERATE A NEW BYTE COUNT BY SHIFTING IT OVER ONE PLACE TO THE LEFT.
11447      ;THIS EFFECTIVELY MULTIPLIES THE NUMBER BY TWO.
11448      ;CHECK TO SEE IF THE NEW NUMBER IS NOT TOO LARGE.
11449      ;REPEAT THE TEST WITH THE NEW BYTE COUNT
11450      ;
11451 046370' 006303                ASL     R3                ;UP THE BYTE COUNT TO NEXT BIT POSITION
11452 046372' 020327 002000        CMP     R3,#BIT10        ;HAVE WE CHECKED ALL BIT POSTIONS IN
11453                                BNE     28$                ;THE BYTE COUNT REGISTER?
11454 046376' 001307                ENDSEG                    ;NOT YET
11455 046400'
11456 046400'
11457 046400' 104405                10001$: TRAP    C$ESEG
11458
11459      ;GET A NEW DATA PATTERN FROM THE LIST OF PRESELECTED DATA PATTERNS
11460      ;REPEAT TEST WITH NEW DATA PATTERN
11461      ;
11462 046402' 005305                DEC     R5                ;HAVE WE TESTED WITH ALL DATA PATTERNS?
11463 046404' 001277                BNE     27$                ;NOT YET
11464
11465 046406'
11466 046406'
11467 046406' 104401                ENDTST                    L10162: TRAP    C$ETST

```


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TEST 41: RECEIVER BUFFER RECOVERY - RUNT TEST

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11523

046410'
046410'

.SBTTL TEST 41: RECEIVER BUFFER RECOVERY - RUNT TEST

: THIS TEST WILL CHECK THE ABILITY OF THE RECEIVE STATE MACHINE TO REJECT A
: DATAGRAM OF LESS THAN 64 BYTES AND TO RECOVER THE RECEIVER BUFFER.

: THIS TEST WILL USE MICROMODULE 'F' MICROTEST #4.
: EACH TRIAL WILL CONSIST OF TWO DATAGRAM TRANSMISSIONS IN LOOPBACK MODE. EACH
: TRANSMISSION WILL LOOPBACK A DATAGRAM FILLED WITH UNIQUE DATA. THE FIRST
: DATAGRAM WILL BE A RUNT OF LESS THAN 64 BYTES. THE SECOND WILL BE A DATAGRAM
: OF LEGAL SIZE.

: EACH TRIAL WILL START WITH THE LINK BUFFER POINTER RESET TO THE FIRST LINK
: BUFFER. THE RUNT WILL BE TRANSMITTED, THEN THE VALID DATAGRAM. IF THE BUFFER
: RECOVERY IS WORKING CORRECTLY, THE SECOND DATAGRAM IS EXPECTED TO BE WRITTEN
: INTO THE SAME LINK MEMORY BUFFER AS WAS THE RUNT.

: THIS TEST WILL BE REPEATED WITH VARIOUS RUNT PACKET SIZES.

: THE BYTE COUNT FOR THE RUNT PACKET TRANSMISSION WILL BE PASSED VIA THE PCBB.
: AFTER THE TWO TRANSMISSIONS, THE MICROCODE WILL PASS BACK THE CONTENTS OF THE
: BUFFER DONE FIFO, AND THE CONTENTS OF THE FIRST DATA WORD OF THE RECEIVER
: BUFFER.

: THE PCBB WILL BE FORMATTED AS FOLLOWS:

PCBB+0:	RUNT BYTE COUNT
PCBB+2:	BUFFER DONE FIFO CONTENTS
PCBB+4:	FIRST DATA WORD OF BUFFER

: TEST SEQUENCE:

- 1-LOAD MICROMODULE 'F' IF NOT ALREADY DONE SO
- 2-PLACE A RUNT BYTE COUNT IN PCBB+0
- 3-CLEAR PCBB+2,+4
- 4-WAIT FOR MICROMONITOR TO ENTER THE 'INMON' STATE
- 5-SELECT MICROTEST #4
- 6-WAIT FOR 'DNI'
- 7-CHECK PCSR1 FOR AN ERROR CONDITION (RECEIVER INTERRUPT OCCURRED ON RUNT PACKET RECEPTION)
- 8-CHECK PCBB+2 FOR CORRECT BUFFER DONE ADDRESS
- 9-CHECK PCBB+4 FOR CORRECT DATA PATTERN
- 10-WRITE '1' TO CLEAR 'DNI'
- 11-REPEAT STEPS 2-10 WITH A NEW RUNT BYTE COUNT

BGNTST

T41::

: CHECK TO SEE IF MODULE 'F' HAS BEEN LOADED. IF NOT LOAD IT INTO
: THE TOP HALF OF WCS, START IT AND WAIT FOR THE MICROMONITOR TO SET 'DNI'.

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11524 ;AFTER 'DNI' SETS WRITE '1' TO CLEAR IT.
11525 ;
11526 046410' BGNSEG
11527 046410' 104404 TRAP C$BSEG
11528 046412' 022737 000106 000326' CMP #'F,MICRO ;HAS MICROCODE MODULE 'F' BEEN LOADED
11529 046420' 001004 BNE 5$ ;NO
11530 046422' 122777 000001 131710 CMPB #INMON,@PCSR1 ;YES, IS THE MICROMONITOR ACTIVE?
11531 046430' 001440 BEQ 20$ ;YES SKIP LOADING THE MICROMODULE
11532 046432' 012737 000106 000326' 5$: MOV #'F,MICRO ;GO LOAD MICRO MODULE 'F'
11533 046440' 004737 020252' JSR PC,LODMIC
11534 046444' 103002 BCC 10$ ;OK
11535 046446' ESCAPE TST
11536 046446' 104410 TRAP C$ESCAPE
11537 046450' 000370 .WORD L10163-.
11538 046452' 012737 000176 000332' 10$: MOV #2*SECOND,METER ;WAIT FOR THE MICROMONITOR
11539 046460' 004737 017330' JSR PC,CHKDNI
11540 046464' 103022 BCC 20$ ;OK
11541 046466' 012737 000776' 000310' MOV #$DNI,BITNAM
11542 046474' 012737 001275' 000312' MOV #$NSET,BITSTA
11543 046502' 012737 001340' 000314' MOV #$AFTER,PWHEN
11544 046510' 012737 001355' 000316' MOV #$GTCMD,PCOMND
11545 046516' ERRHRD 247,RBRRUN,MSG1
11546 046516' 104456 TRAP C$ERHRD
11547 046520' 000367 .WORD 247
11548 046522' 003743' .WORD RBRRUN
11549 046524' 012750' .WORD MSG1
11550 046526' ESCAPE TST
11551 046526' 104410 TRAP C$ESCAPE
11552 046530' 000310 .WORD L10163-.
11553 046532' 004737 017374' 20$: JSR PC,CLRDN1 ;CLEAR DNI BIT
11554 046536' 103006 BCC 25$
11555 046540' ERRHRD 248,RBRRUN,RACMG7 ;DNI DID NOT CLEAR!
11556 046540' 104456 TRAP C$ERHRD
11557 046542' 000370 .WORD 248
11558 046544' 003743' .WORD RBRRUN
11559 046546' 012726' .WORD RACMG7
11560 046550' ESCAPE TST
11561 046550' 104410 TRAP C$ESCAPE
11562 046552' 000266 .WORD L10163-.
11563 046554' 25$:
11564 046554' ENDSEG
11565 046554' 10000$:
11566 046554' 104405 TRAP C$ESEG
11567 ;
11568 ;R1 WILL CONTAIN THE RUNT BYTE COUNT TO BE USED BY THE MICROCODE
11569 ;
11570 046556' 012701 000001 MOV #1,R1 ;BEGIN WITH BYTE COUNT OF 1
11571 046562' 30$:
11572 ;
11573 ;PASS THE BYTE COUNT TO MICROCODE THROUGH PCBB+, CLEAR PCBB+2 AND PCBB+4
11574 ;
11575 046562' BGNSEG
11576 046562' 104404 TRAP C$BSEG
11577 046564' 010137 00J604' MOV R1,PCBB ;PASS BYTE COUNT TO MICROCODE
11578 046570' 005037 000606' CLR PCBB+2 ;HERE IS WHERE THE MICRO WILL PUT THE
11579 ;'DONE' RECEIVE BUFFER FIFO ADDRESS

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TEST 41: RECEIVER BUFFER RECOVERY - RUNT TEST

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11580 046574' 005037 000610'          CLR      PCBB+4          ;HERE IS WHERE THE MICRO WILL PUT THE
11581                                     ;FIRST WORD OF DATA FROM THE RECEIVE BUFFER
11582
11583                                     ;WAIT FOR THE MICROCODE TO ENTER THE 'INMON' STATE, START MICROTEST #4 BY
11584                                     ;LOADING THE COMMAND FIELD OF PCSRO WITH A 4, WAIT FOR 'DNI'
11585
11586 046600' 004737 017776'          JSR      PC,CHKMON          ;WAIT FOR MICROMONITOR
11587 046604' 103006          BCC     35$                ;OK
11588 046606'          ERRHRD 249,RBRRUN,MSG46 ;PRINT ERROR
11589 046606' 104456          TRAP   C$ERHRD
11590 046610' 000371          .WORD 249
11591 046612' 003743'          .WORD RBRRUN
11592 046614' 016700'          .WORD MSG46
11593 046616'          ESCAPE TST          ;LEAVE TEST
11594 046616' 104410          TRAP   C$ESCAPE
11595 046620' 000220          .WORD L10163-.
11596 046622' 012777 000004 131506 35$: MOV     #4,@PCSRO          ;TELL T11 TO EXECUTE MICROTEST #4
11597 046630' 012737 000275 000332' MOV     #3*SECOND,METER ;WAIT FOR DNI
11598 046636' 004737 017330'          JSR     PC,CHKDNI
11599 046642' 103021          BCC     40$                ;OK
11600 046644' 004737 020050'          JSR     PC,CHKINT          ;SEE IF ANY ERROR INTERRUPTS OCCURRED
11601 046650' 103006          BCC     36$                ;NO, OK
11602 046652'          ERRHRD 250,RBRRUN,MSG44 ;PRINT ERROR MESSAGE
11603 046652' 104456          TRAP   C$ERHRD
11604 046654' 000372          .WORD 250
11605 046656' 003743'          .WORD RBRRUN
11606 046660' 016454'          .WORD MSG44
11607 046662'          ESCAPE TST
11608 046662' 104410          TRAP   C$ESCAPE
11609 046664' 000154          .WORD L10163-.
11610 046666' 012702 000004          36$: MOV     #4,R2          ;MICROTEST #4 IS HUNG
11611 046672'          ERRHRD 251,RBRRUN,MSG12 ;PRINT ERROR MESSAGE
11612 046672' 104456          TRAP   C$ERHRD
11613 046674' 000373          .WORD 251
11614 046676' 003743'          .WORD RBRRUN
11615 046700' 013500'          .WORD MSG12
11616 046702'          ESCAPE TST
11617 046702' 104410          TRAP   C$ESCAPE
11618 046704' 000134          .WORD L10163-.
11619
11620                                     ;'DNI' SET INDICATING THE TEST IS FINISHED, NOW CHECK PCSR1 FOR AN ERROR
11621                                     ;CONDITION. THIS CONDITION WILL BE SET IF, AFTER THE MICROCODE TRANSMITTED THE
11622                                     ;RUNT PACKET, A RECEIVER INTERRUPT OCCURRED. THIS SHOULD NOT HAPPEN BECAUSE
11623                                     ;THE RECEIVER STATE MACHINE SHOULD GO THROUGH A BAD PACKET STATE AND NOT
11624                                     ;CAUSE A RECEIVER INTERRUPT
11625
11626 046706' 122777 000003 131424 40$: CMPB   #INERR,@PCSR1          ;DID AN ERROR OCCUR?
11627 046714' 001005          BNE     45$                ;NO
11628 046716'          ERRHRD 252,RBRRUN,MSG30 ;YES, PRINT ERROR MESSAGE
11629 046716' 104456          TRAP   C$ERHRD
11630 046720' 000374          .WORD 252
11631 046722' 003743'          .WORD RBRRUN
11632 046724' 015276'          .WORD MSG30
11633 046726' 000425          BR      55$
11634
11635                                     ;OK, NO RECEIVER INTERRUPT. NOW CHECK PCBB+2, WHICH IS THE CONTENTS OF THE

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11636 ;BUFFER DONE FIFO AFTER THE SECOND PACKET WAS RECEIVED. BITS 14:11 SHOULD
11637 ;ALL BE ZERO INDICATING BUFFER 0 WAS RECOVERED BY THE RECEIVER STATE MACHINE.
11638
11639 046730' 042737 103777 000606' 45$: BIC #103777,PCBB+2 ;STRIP OFF THE FLOATING BITS
11640 046736' 005737 000606' TST PCBB+2 ;DID DEUNA RECOVER RECEIVE BUFFER?
11641 046742' 001407 BEQ 50$ ;YES
11642 046744' 052737 100000 000606' BIS #100000,PCBB+2 ;NO, MAKE IT A LINK MEMORY ADDRESS
11643 046752' ERRHRD 253,RBRRUN,MSG31 ;PRINT ERROR MESSAGE
11644 046752' 104456 TRAP C$ERHRD
11645 046754' 000375 .WORD 253
11646 046756' 003743' .WORD RBRRUN
11647 046760' 015344' .WORD MSG31
11648
11649 ;NOW CHECK THE DATA THAT WAS RECEIVED INTO THE RECIVER BUFFER. IT SHOULD
11650 ;BE AN ALTERNATING 1'S AND 0'S PATTERN.
11651
11652 046762' 022737 052525 000610' 50$: CMP #52525,PCBB+4 ;IS DATA GOOD IN LEGIT RECEIVE BUFFER?
11653 046770' 001404 BEQ 55$ ;YES
11654 046772' ERRHRD 254,RBRRUN,MSG32 ;NO,PRINT ERROR MESSAGE
11655 046772' 104456 TRAP C$ERHRD
11656 046774' 000376 .WORD 254
11657 046776' 003743' .WORD RBRRUN
11658 047000' 015510' .WORD MSG32
11659 047002' 55$:
11660 047002' ENDSEG
11661 047002' 10001$:
11662 047002' 104405 TRAP C$ESEG
11663
11664 ;WRITE '1' TO CLEAR 'DNI'
11665
11666 047004' 004737 017374' JSR PC,CLRDN1 ;GO CLEAR DNI
11667 047010' 103006 BCC 60$ ;OK
11668 047012' ERRHRD 255,RBRRUN,RACMG7 ;ERROR DNI DID NOT CLEAR!
11669 047012' 104456 TRAP C$ERHRD
11670 047014' 000377 .WORD 255
11671 047016' 003743' .WORD RBRRUN
11672 047020' 012726' .WORD RACMG7
11673 047022' ESCAPE TST
11674 047022' 104410 TRAP C$ESCAPE
11675 047024' 000014 .WORD L10163-
11676
11677 ;GENERATE A NEW RUNT PACKET BYTE COUNT. WE WILL JUST SLIDE A BIT THROUGH
11678 ;THE COUNTER, UP TO THE LAST RUNT PACKET SIZE OF 63
11679
11680 047026' 006301 60$: ASL R1 ;MOVE OVER THE ALREADY SET BITS
11681 047030' 005201 INC R1 ;SET LSB
11682 047032' 020127 000100 CMP R1,#MINBYT ;STOP WITH A BYTE COUNT GREATER THAN
11683 ;MINIMUM SIZE
11684 047036' 002651 BLT 30$
11685 047040' ENDTST
11686 047040' L'0163:
11687 047040' 104401 TRAP C$ETST

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047042'
047042'

047042'
047042' 104404
047044' 022737 000106 000326'

.SBTTL TEST 42: HALF-DUPLEX TEST
:*****
:THE LINK INCLUDES A 'HALF DUPLEX' MODE OF OPERATION. THIS MODE CAN BE ENABLED
:OR DISABLED THROUGH THE LINK MODE REGISTER. THE OPERATIONAL MICROCODE NORMALLY
:USES HALF-DUPLEX MODE.
:IN THE HALF-DUPLEX MODE, THE LINK WILL NOT RECEIVE MESSAGES ADDRESSED TO
:ITSELF. INCOMING MESSAGES LOOPED BACK WILL BE IGNORED BY THE RECEIVE STATE
:MACHINE. THE STATE MACHINE WILL NOT ISSUE A 'RECEIVER DONE' INTERRUPT AND THE
:BUFFER CAN BE RECOVERED FOR RECEIVING A LATER DATAGRAM.
:THIS TEST USES MICROMODULE 'F' MICROTEST #5.
:THIS TEST WILL VERIFY THE OPERATION OF HALF-DUPLEX MODE. A DATAGRAM WILL BE
:TRANSMITTED IN LOOPBACK MODE WITH THE HALF-DUPLEX BIT SET. THE MICROCODE
:WILL VERIFY THAT THE RECEIVER INTERRUPT DOES NOT OCCUR. THE MICROCODE WILL
:THEN CLEAR THE HALF-DUPLEX BIT AND LOOP A DATAGRAM AND VERIFY THAT THE
:ORIGINAL BUFFER WAS RECOVERED.
:THIS TEST WILL USE THE PCBB TO PASS INFORMATION. PCBB+0 WILL BE USED TO PASS
:THE CONTENTS OF THE BUFFER DONE FIFO AFTER THE SECOND DATAGRAM IS RECEIVED.
:PCBB+4 WILL BE USED TO PASS THE FIRST WORD OF DATA FROM THE RECEIVER BUFFER
:AFTER THE SECOND DATAGRAM IS TRANSMITTED.
:PCBB+0: !-----!
: ! CONTENTS OF BUFFER DONE FIFO !
:PCBB+2: !-----!
: ! FIRST DATA WORD OF BUFFER DONE!
: !-----!
:THE CONTENTS OF THE BUFFER DONE FIFO SHOULD BE 0 AND THE FIRST DATA WORD
:SHOULD BE AN ALTERNATING 1'S AND 0'S PATTERN.
:TEST SEQUENCE:
: 1-LOAD MICROMODULE 'F' IF NOT ALREADY DONE SO.
: 2-CLEAR PCBB+0 AND PCBB+2
: 3-WAIT FOR THE MICROMONITOR TO ENTER THE 'INMON' STATE
: 4-WAIT FOR 'DNI'
: 5-CHECK PCSR1 FOR AND ERROR CONDITION (THIS SIGNIFIES THAT THE DATAGRAM
: SENT IN HALF-DUPLEX MODE CAUSED AN INTERRUPT)
: 6-VERIFY PCBB+0 IS LINK BUFFER 0
: 7-VERIFY PCBB+2 HAS ALTERNATING 1'S AND 0'S
:*****
BGNTST
T42::
:CHECK TO SEE IF MODULE 'F' HAS BEEN LOADED. IF NOT LOAD IT INTO
:THE TOP HALF OF WCS, START IT AND WAIT FOR THE MICROMONITOR TO SET 'DNI'.
:AFTER 'DNI' SETS WRITE '1' TO CLEAR IT.
BGNSEG
CMP #'F,MICRO ;HAS MICROCODE MODULE 'F' BEEN LOADED TRAP CSBSEG

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11744 047052' 001004      BNE      5$          :NO
11745 047054' 122777 000001 131256      CMPB     #INMON,@PCSR1 :YES, IS THE MICROMONITOR ACTIVE?
11746 047062' 001440      BEQ      20$          :YES SKIP LOADING THE MICROMODULE
11747 047064' 012737 000106 000326' 5$:      MOV      #'F,MICRO    :GO LOAD MICRO MODULE 'F'
11748 047072' 004737 020252'      JSR      PC,LODMIC
11749 047076' 103002      BCC     10$          :OK
11750 047100'      ESCAPE  TST
11751 047100' 104410      TRAP    C$ESCAPE
11752 047102' 000342      .WORD  L10164-.
11753 047104' 012737 000176 000332' 10$:      MOV      #2*SECOND,METER :WAIT FOR THE MICROMONITOR
11754 047112' 004737 017330'      JSR      PC,CHKDNI
11755 047116' 103022      BCC     20$          :OK
11756 047120' 012737 000776' 000310'      MOV      #SDNI,BITNAM
11757 047126' 012737 001275' 000312'      MOV      #SNET,BITSTA
11758 047134' 012737 001340' 000314'      MOV      #SAFTER,PWHEN
11759 047142' 012737 001355' 000316'      MOV      #SGTCMD,PCOMND
11760 047150'      ERRHRD 256,HAFDUP,MSG1
11761 047150' 104456      TRAP    C$ERHRD
11762 047152' 000400      .WORD  256
11763 047154' 004016'      .WORD  HAFDUP
11764 047156' 012750'      .WORD  MSG1
11765 047160'      ESCAPE  TST
11766 047160' 104410      TRAP    C$ESCAPE
11767 047162' 000262      .WORD  L10164-.
11768 047164' 004737 017374' 20$:      JSR      PC,CLRDN1    :CLEAR DNI BIT
11769 047170' 103006      BCC     25$
11770 047172'      ERRHRD 257,HAFDUP,RACMG7 :DNI DID NOT CLEAR!
11771 047172' 104456      TRAP    C$ERHRD
11772 047174' 000401      .WORD  257
11773 047176' 004016'      .WORD  HAFDUP
11774 047200' 012726'      .WORD  RACMG7
11775 047202'      ESCAPE  T
11776 047202' 104410      TRAP    C$ESCAPE
11777 047204' 000240      .WORD  L10164-.
11778 047206' 25$:
11779 047206'      ENDSEG
11780 047206' 10000$:
11781 047206' 104405      TRAP    C$ESEG
11782
11783      :CLEAR PCBB+0 AND PCBB+2 THESE LOCATIONS ARE WHERE THE MICROCODE WILL PLACE
11784      :INFORMATION ABOUT THE TEST SUCCESS
11785      :
11786      BGNSEG
11787 047210' 104404      TRAP    C$BSEG
11788 047212' 005037 000604'      CLR     PCBB+0      :HERE IS WHERE THE MICRO WILL PUT THE
11789      :'DONE' RECEIVE BUFFER FIFO ADDRESS
11790 047216' 005037 000606'      CLR     PCBB+2      :HERE IS WHERE THE MICRO WILL PUT THE
11791      :FIRST WORD OF DATA FROM THE RECEIVE BUFFER
11792
11793      :WAIT FOR THE MICROCODE TO ENTER THE 'INMON' STATE
11794      :THEN EXECUTE MICROTST #5 BY LOADING PCSRO WITH A 5
11795      :WAIT FOR 'DNI'
11796
11797 047222' 004737 017776'      JSR      PC,CHKMON   :WAIT FOR MICROMONITOR
11798 047226' 103006      BCC     35$
11799 047230'      ERRHRD 258,HAFDUP,MSG46 :OK
                                :PRINT ERROR
    
```

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11800 047230' 104456
11801 047232' 000402
11802 047234' 004016'
11803 047236' 016700'
11804 047240'
11805 047240' 104410
11806 047242' 000202
11807 047244' 012777 000005 131064 35$:
11808 047252' 012737 000473 000332'
11809 047260' 004737 017330'
11810 047264' 103021
11811 047266' 004737 020050'
11812 047272' 103006
11813 047274'
11814 047274' 104456
11815 047276' 000403
11816 047300' 004016'
11817 047302' 016454'
11818 047304'
11819 047304' 104410
11820 047306' 000136
11821 047310' 012702 000005
11822 047314'
11823 047314' 104456
11824 047316' 000404
11825 047320' 004016'
11826 047322' 013500'
11827 047324'
11828 047324' 104410
11829 047326' 000116
11830
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11834 047330' 122777 000003 131002 40$:
11835 047336' 001005
11836 047340'
11837 047340' 104456
11838 047342' 000405
11839 047344' 004016'
11840 047346' 016070'
11841 047350' 000425
11842
11843
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11846
11847 047352' 042737 103777 000604' 45$:
11848 047360' 005737 000604'
11849 047364' 001407
11850 047366' 052737 100000 000604'
11851 047374'
11852 047374' 104456
11853 047376' 000406
11854 047400' 004016'
11855 047402' 016112'
    
```

ESCAPE TST ;LEAVE TEST TRAP C\$ERHRD
 .WORD 258
 .WORD HAFDUP
 .WORD MSG46
 TRAP C\$ESCAPE
 .WORD L10164-
 MOV #5,@PCSR0 ;TELL T11 TO EXECUTE MICROTEST #5
 MOV #5*SECOND,METER ;WAIT FOR DNI
 JSR PC,CHKDNI
 BCC 40\$;OK
 JSR PC,CHKINT ;SEE IF ANY ERROR INTERRUPTS OCCURRED
 BCC 36\$;NO, OK
 ERRHRD 259,HAFDUP,MSG44 ;PRINT ERROR MESSAGE
 TRAP C\$ERHRD
 .WORD 259
 .WORD HAFDUP
 .WORD MSG44
 ESCAPE TST TRAP C\$ESCAPE
 .WORD L10164-
 MOV #5,R2 ;MICROTEST #5 IS HUNG
 ERRHRD 260,HAFDUP,MSG12 ;PRINT ERROR MESSAGE
 TRAP C\$ERHRD
 .WORD 260
 .WORD HAFDUP
 .WORD MSG12
 ESCAPE TST TRAP C\$ESCAPE
 .WORD L10164-
 ;AN ERROR IN PCSR1 MEANS THAT THE FIRST DATAGRAM SENT, WHICH IS THE ONE SENT
 ;IN HALF-DUPLEX MODE, CAUSED A RECEIVER INTERRUPT.
 CMPB #INERR,@PCSR1 ;DID AN ERROR OCCUR?
 BNE 45\$;NO
 ERRHRD 261,HAFDUP,MSG39 ;YES, PRINT ERROR MESSAGE
 TRAP C\$ERHRD
 .WORD 261
 .WORD HAFDUP
 .WORD MSG39
 BR 55\$
 ;PCBB+0 CONTAINS THE CONTENTS OF THE BUFFER DONE FIFO AFTER THE SECOND DATAGRAM
 ;WAS RECEIVED IT SHOULD CONTAIN THE BUFFER ADDRESS OF THE FIRST BUFFER IN LINK
 ;MEMORY
 BIC #103777,PCBB ;STRIP OFF THE FLOATING BITS
 TST PCBB ;DID DEUNA RECOVER RECEIVE BUFFER?
 BEQ 50\$;YES
 BIS #100000,PCBB ;MAKE IT A LINK MEMORY ADDRESS
 ERRHRD 262,HAFDUP,MSG40 ;NO, PRINT ERROR MESSAGE
 TRAP C\$ERHRD
 .WORD 262
 .WORD HAFDUP
 .WORD MSG40

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.SBTTL TEST 43: COLLISION TEST

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:*****
:THE RECEIVE STATE MACHINE REACTS TO COLLISIONS ON THE WIRE BY ACTIVATING
:THE RETRY LOGIC. THE RETRY LOGIC WAITS AN INTERVAL OF TIME BEFORE ATTEMPTING
:TO RETRANSMIT THE DATAGRAM. THE INTERVALS ARE NOT UNIFORM BUT ARE OF
:GENERALLY INCREASING PSEUDO-RANDOM DURATION. THE RETRY LOGIC WILL ATTEMPT
:TO RETRANSMIT UP TO 15 ADDITIONAL TIMES BEFORE GIVING UP.
:THIS TEST WILL VERIFY THAT THE RECEIVE STATE MACHINE RESPONDS TO A COLLISION
:AND THAT THE RETRY SEQUENCE IS REPORTED CORRECTLY IN THE TRANSMIT STATUS WORD.
:THIS TEST WILL USE MICROMODULE 'G' MICROTEST #1.
:THE LINK BOARD CONTAINS DIAGNOSTIC LOGIC THAT ALLOWS COLLISIONS TO BE SIMULATED.
:WITH THE FORCE COLLISIONS LOGIC ACTIVATED, THE RETRY HARDWARE CAN BE STEPPED
:THROUGH THE RETRY SEQUENCE. THAT IS, EVERY DATAGRAM LOOPED BACK WILL STEP
:THE RETRY LOGIC THROUGH ONE STEP OF THE RETRY SEQUENCE. THE RETRY SEQUENCE
:CAN BE VERIFIED BY CHECKING THE TRANSMIT BUFFER STATUS WORDS AFTER EACH RETRY
:STEP.
:THE PCBB WILL BE USED TO PASS PARAMETERS BETWEEN THE MICROCODE AND THE HOST
:PROCESSOR. PCBB+0 WILL BE USED TO PASS THE DATA TO BE LOADED INTO THE LINK
:MODE WORD. PCBB+2 WILL BE PASSED BACK BY THE MICROCODE, IT IS THE FIRST WORD
:OF THE TRANSMIT BUFFER (TRANSMIT STATUS WORD 0). PCBB+4 WILL ALSO BE PASSED
:BACK, IT IS TRANSMIT STATUS WORD 1.

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:THE TRANSMIT STATUS WORDS SHOULD SHOW THE FOLLOWING STATUS:

	STATUS BITS			
	WORD 0			WORD 1
LOOPBACK STEP #	ERRS (14)	MORE (12)	ONE (11)	RETRY (10)
1	0	0	1	0
2-15	0	1	0	0
16	1	0	0	1

:THE PCBB IS FORMATTED AS FOLLOWS:

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:PCBB+0:  +-----+
:         | LINK MODE WORD |
:PCBB+2:  +-----+
:         | TRANSMIT STATUS WORD 0 |
:PCBB+4:  +-----+
:         | TRANSMIT STATUS WORD 1 |
:         +-----+

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11994
11995
11996

TEST SEQUENCE:
1-LOAD MICROMODULE 'G' I- NOT ALREADY SONE SO
2-LOAD PCBB+0 WITH PROM'ISCUOUS MODE, INTERNAL LOOPBACK AND FORCE COLLISIONS.
3-WAIT FOR MICROMONITOR TO ENTER THE 'INMON' STATE
4-EXECUTE MICROTEST #1
5-WAIT FOR 'DNI'
6-CHECK FOR 'ONE' BIT IN PCBB+0
7-WRITE '1' TO CLEAR 'DNI'
8-LOAD PCBB+0 WITH PROM'ISCUOUS MODE, INTERNAL LOOPBACK AND FORCE COLLISIONS.
9-WAIT FOR THE MICROMONITOR TO ENTER THE 'INMON' STATE
10-EXECUTE MICROTEST #1
11-WAIT FOR 'DNI'
12-CHECK PCBB+2 FOR 'MORE' BIT
13-WRITE '1' TO CLEAR 'DNI'
14-REPEAT STEPS 8-13 15 TIMES
15-LOAD PCBB+0 WITH PROM'ISCUOUS MODE, INTERNAL LOOPBACK AND FORCE COLLISIONS
16-WAIT FOR THE MICROMONITOR TO ENTER THE 'INMON' STATE
17-EXECUTE MICROTEST #1
18-WAIT FOR 'DNI'
19-CHECK PCBB+4 FOR ERROR SUMMARY BIT IN PCBB+2 AND RE'RY BIT IN PCBB+4
20-WRITE '1' TO CLEAR 'DNI'

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047446'
047446'

BGNTST
T43: :
:CHECK TO SEE IF MODULE 'G' HAS BEEN LOADED. IF NOT LOAD IT INTO
:THE TOP HALF OF WCS, START IT AND WAIT FOR THE MICROMONITOR TO SFT 'DNI'.
:AFTER 'DNI' SETS WRITE '1' TO CLEAR IT.

BGNSEG

047446'
047446'
047450'
047456'
047460'
047466'
047470'
047476'
047502'
047504'
047504'
047506'
047510'
047516'
047522'
047524'
047532'
047540'
047546'
047554'

104404
022737 000107 000326'
001004
122777 000001 130652
001440
012737 000107 000326' 5\$:
004737 020252'
103002
104410
001026
012737 000176 000332' 10\$:
017330'
012737 000776' 000310'
012737 001275' 000312'
012737 001340' 000314'
012737 001355' 000316'
CMP #'G,MICRO ;HAS MICROCODE MODULE 'G' BEEN LOADED
BNE 5\$;NO
CMPB #INMON,@PCSR1 ;YES, IS THE MICROMONITOR ACTIVE?
BEQ 20\$;YES SKIP LOADING THE MICROMODULE
MOV #'G,MICRO ;GO LOAD MICRO MODULE 'G'
JSR PC,LODMIC
BCC 10\$;OK
ESCAPE TST
MOV #2*SECOND,METER ;WAIT FOR THE MICROMONITOR
JSR PC,CHKDNI
BCC 20\$;OK
MOV #SDNI,BITNAM
MOV #NSEI,BITSTA
MOV #SAFTER,PWHEN
MOV #SGTCMD,PCOMND
ERRHRD 265,COLIST,MSG1
TRAP C\$BSEG
TRAP C\$ESCAPE
WORD L10165-

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11997 047554' 104456 TRAP C$ERHRD
11998 047556' 000411 .WORD 265
11999 047560' 004046' .WORD COLTST
12000 047562' 012750' .WORD MSG1
12001 047564' ESCAPE TST
12002 047564' 104410 TRAP C$ESCAPE
12003 047566' 000746 .WORD L10165-.
12004 047570' 004737 017374' 20$: JSR PC,CLRDN1 ;CLEAR DNI BIT
12005 047574' 103006 BCC 25$
12006 047576' ERRHRD 266,COLTST,RACMG7 ;DNI DID NOT CLEAR!
12007 047576' 104456 TRAP C$ERHRD
12008 047600' 000412 .WORD 266
12009 047602' 004046' .WORD COLTST
12010 047604' 012726' .WORD RACMG7
12011 047606' ESCAPE TST
12012 047606' 104410 TRAP C$ESCAPE
12013 047610' 000724 .WORD L10165-.
12014 047612' 25$:
12015 047612' ENDSLG
12016 047612' 10000$:
12017 047612' 104405 TRAP C$ESEG
12018
12019 ;LOAD PCSR2 WITH ADDRESS OF PORT CONTROL BLOCK
12020 ;LOAD REGISTER 5 WITH BITS TO BE LOADED INTO THE LINK MODE REGISTER BY THE
12021 ;MICROCODE
12022
12023 047614' 012777 000604' 130520 MOV #PCBB,@PCSR2 ;TELL DEUNA WHERE PCBB IS
12024 047622' 005077 130516 CLR @PCSR3
12025 047626' 012705 100024 MOV #BIT15!BIT4!BIT2,R5 ;TELL MICROCODE TO LOAD THE FOLLOWING
12026 ;INTO THE LINK MODE REGISTER:
12027 ;PROMISCUOUS MODE, INTERNAL LOOPBACK,
12028 ;AND FORCE COLLISIONS
12029
12030 ;BEGIN FIRST LOOPBACK
12031
12032 ;BGNSEG
12033 047632' 104404 TRAP C$BSEG
12034 047634' 012702 000001 MOV #1,R2 ;START LOOPBACK STEP #1
12035 047640' 010537 000604' MOV R5,PCBB ;LOAD PCBB WITH LINK MODE REGISTER DATA
12036 047644' 005037 000606' CLR PCBB+2 ;MICROCODE WILL PUT XMIT STAT WORD 0
12037 047650' 005037 000610' CLR PCBB+4 ;MICROCODE WILL PUT XMIT STAT WORD 1
12038 047654' 004737 017776' JSR PC,CHKMON ;WAIT FOR MICROMONITOR
12039 047660' 103006 BCC 30$ ;OK
12040 047662' ERRHRD 267,COLTST,MSG46 ;PRINT ERROR
12041 047662' 104456 TRAP C$ERHRD
12042 047664' 000413 .WORD 267
12043 047666' 004046' .WORD COLTST
12044 047670' 016700' .WORD MSG46
12045 047672' ESCAPE TST ;LEAVE TEST
12046 047672' 104410 TRAP C$ESCAPE
12047 047674' 000640 .WORD L10165-.
12048 047676' 012777 000001 130432 30$: MOV #1,@PCSR0 ;EXECUTE MICROTEST #1
12049 047704' 012737 000176 000332' MOV #2*SECOND,METER ;WAIT FOR DNI
12050 047712' 004737 017330' JSR PC,CHKDNI
12051 047716' 103021 BCC 40$
12052 047720' 004737 020050' JSR PC,CHKINT ;SEE IF ANY ERROR INTERRUPTS OCCURRED

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12053 047724' 103006          BCC      35$          :NO, OK
12054 047726'          ERRHRD  268, COLTST,MSG44 :PRINT ERROR MESSAGE
12055 047726' 104456          TRAP    C$ERHRD
12056 047730' 000414          .WORD  268
12057 047732' 004046'          .WORD  COLTST
12058 047734' 016454'          .WORD  MSG44
12059 047736'          ESCAPE  TST
12060 047736' 104410          TRAP    C$ESCAPE
12061 047740' 000574          .WORD  L10165-
12062 047742' 012702 000001    35$:  MOV      #1,R2          :MICROTEST #1 IS HUNG
12063 047746'          ERRHRD  269, COLTST,MSG12
12064 047746' 104456          TRAP    C$ERHRD
12065 047750' 000415          .WORD  269
12066 047752' 004046'          .WORD  COLTST
12067 047754' 013500'          .WORD  MSG12
12068 047756'          ESCAPE  TST
12069 047756' 104410          TRAP    C$ESCAPE
12070 047760' 000554          .WORD  L10165-
12071
12072          ;THE RESULT OF THE FIRST LOOPBACK SHOULD BE TX WORD 0 'ONE' BIT SET AND NO
12073          ;OTHERS
12074
12075 047762' 012703 004000    40$:  MOV      #BIT11,R3          ;'ONE' BIT SHOULD BE SET IN TX WORD 0
12076 047766' 005004          CLR      R4              ;NO BITS SHOULD BE SET IN TX WORD 1
12077 047770' 032737 040000 000606' BIT      #BIT14,PCBB+2    ;IS 'ERROR SUMMARY' SET IN WORD 0?
12078 047776' 001014          BNE      45$            ;YES, ERROR
12079 050000' 032737 010000 000606' BIT      #BIT12,PCBB+2    ;IS 'MORE' BIT SET IN WORD 0?
12080 050006' 001010          BNE      45$            ;YES, ERROR
12081 050010' 032737 004000 000606' BIT      #BIT11,PCBB+2    ;IS 'ONE' BIT SET IN WORD 0?
12082 050016' 001404          BEQ      45$            ;NO, ERROR
12083 050020' 032737 002000 000610' BIT      #BIT10,PCBB+4    ;IS 'RETRY' BIT SET IN WORD 1?
12084 050026' 001404          BEQ      50$            ;NO
12085 050030'          45$:  ERRHRD  270, COLTST,MSG22 :PRINT ERROR MESSAGE
12086 050030' 104456          TRAP    C$ERHRD
12087 050032' 000416          .WORD  270
12088 050034' 004046'          .WORD  COLTST
12089 050036' 014340'          .WORD  MSG22
12090
12091          ;WRITE 'ONE' TO CLEAR 'DNI'
12092
12093
12094 050040' 004737 017374'    50$:  JSR      PC,CLRDN1          ;GO CLEAR DNI
12095 050044' 103004          BCC      55$            ;OK
12096 050046'          ERRHRD  271, COLTST,RACMG7 :PRINT ERROR MESSAGE
12097 050046' 104456          TRAP    C$ERHRD
12098 050050' 000417          .WORD  271
12099 050052' 004046'          .WORD  COLTST
12100 050054' 012726'          .WORD  RACMG7
12101 050056'          55$:
12102 050056'          ENDSEG
12103 050056'
12104 050056' 104405          10001$: TRAP    C$ESEG
12105
12106          ;BEGIN LOOPBACKS 2-15
12107
12108 050060'          BGNSEG

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12109 050060' 104404                                TRAP  C$BSEG
12110 050062' 012702 000002                        MOV  #2,R2      ;START LOOPS 2-15
12111
12112 050066' 010537 000604'                        60$: MOV  R5,PCBB ;LOAD PCBB WITH LINK MODE REGISTER DATA
12113 050072' 005037 000606'                        CLR  PCBB+2    ;MICROCODE WILL PUT XMIT STAT WORD 0
12114 050076' 005037 000610'                        CLR  PCBB+4    ;MICROCODE WILL PUT XMIT STAT WORD 1
12115 050102' 004737 017776'                        JSR  PC,CHKMON ;WAIT FOR MICROMONITOR
12116 050106' 103006                        BCC  70$      ;OK
12117 050110'                                ERRHRD 272,COLTST,MSG46 ;PRINT ERROR
12118 050110' 104456                                TRAP  C$ERHRD
12119 050112' 000420                                .WORD 272
12120 050114' 004046'                                .WORD COLTST
12121 050116' 016700'                                .WORD MSG46
12122 050120'                                ESCAPE TST      ;LEAVE TEST
12123 050120' 104410                                TRAP  C$ESCAPE
12124 050122' 000412                                .WORD L10165-.
12125 050124' 012777 000001 130204 70$: MOV  #1,@PCSR0 ;EXECUTE MICROTEST #1
12126 050132' 012737 000176 000332' MOV  #2*SECOND,METER ;WAIT FOR DNI
12127 050140' 004737 017330' JSR  PC,CHKDNI
12128 050144' 103017                        BCC  80$
12129 050146' 004737 020050' JSR  PC,CHKINT ;SEE IF ANY ERROR INTERRUPTS OCCURRED
12130 050152' 103004                        BCC  75$      ;NO, OK
12131 050154'                                ERRHRD 273,COLTST,MSG44 ;PRINT ERROR MESSAGE
12132 050154' 104456                                TRAP  C$ERHRD
12133 050156' 000421                                .WORD 273
12134 050160' 004046'                                .WORD COLTST
12135 050162' 016454'                                .WORD MSG44
12136 050164' 012702 000001 75$: MOV  #1,R2      ;MICROTEST #1 IS HUNG
12137 050170'                                ERRHRD 274,COLTST,MSG12
12138 050170' 104456                                TRAP  C$ERHRD
12139 050172' 000422                                .WORD 274
12140 050174' 004046'                                .WORD COLTST
12141 050176' 013500'                                .WORD MSG12
12142 050200'                                ESCAPE TST
12143 050200' 104410                                TRAP  C$ESCAPE
12144 050202' 000332                                .WORD L10165-.
12145
12146 ;THE RESULT OF LOOPBACKS 2-15 SHOULD BE THE 'MORE' BIT IN TX 0 AND NO OTHERS
12147 ;
12148 050204' 012703 010000 80$: MOV  #BIT12,R3 ;'MORE' BIT SHOULD BE SET IN TX0
12149 050210' 032737 040000 000606' BIT  #BIT14,PCBB+2 ;IS 'ERROR SUMMARY' SET IN TX 0?
12150 050216' 001014                        BNE  90$      ;YES, ERROR
12151 050220' 032737 010000 000606' BIT  #BIT12,PCBB+2 ;IS 'MORE' BIT SET IN TX 0?
12152 050226' 001410                        BEQ  90$      ;NO, ERROR
12153 050230' 032737 004000 000606' BIT  #BIT11,PCBB+2 ;IS 'ONE' BIT SET IN TX 0?
12154 050236' 001004                        BNE  90$      ;YES, ERROR
12155 050240' 032737 002000 000610' BIT  #BIT10,PCBB+4 ;IS 'RETRY' BIT SET IN TX 1?
12156 050246' 001404                        BEQ  100$
12157 050250' 90$: ERRHRD 275,COLTST,MSG22 ;PRINT ERROR MESSAGE
12158 050250' 104456                                TRAP  C$ERHRD
12159 050252' 000423                                .WORD 275
12160 050254' 004046'                                .WORD COLTST
12161 050256' 014340'                                .WORD MSG22
12162
12163 ;WRITE '1' TO CLEAR 'DNI'
12164

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12165
12166 050260' 004737 017374' 100$: JSR PC,CLRDNI :GO CLEAR DNI
12167 050264' 103004 BCC 105$ :OK
12168 050266' ERRHRD 276,COLTST,RACMG7 :PRINT ERROR MESSAGE
12169 050266' 104456 TRAP C$ERHRD
12170 050270' 000424 .WORD 276
12171 050272' 004046' .WORD COLTST
12172 050274' 012726' .WORD RACMG7
12173 050276' 005202 105$: INC R2 :LOOP COUNT
12174 050300' 022702 000020 CMP #16.,R2 :HAVE WE DONE LOOP STEPS 2-15?
12175 050304' 001270 BNE 60$ :NO CONTINUE
12176 050306' ENDSEG
12177 050306'
12178 050306' 104405 10002$: TRAP C$ESEG
12179
12180 :BEG IN LOOPBACK #16
12181 :
12182 050310' BGNSEG
12183 050310' 104404 TRAP C$BSEG
12184 050312' 010537 000604' MOV R5,PCBB :LOAD PCBB WITH LINK MODE REGISTER DATA
12185 050316' 005037 000606' CLR PCBB+2 :MICROCODE WILL PUT XMIT STAT WORD 0
12186 050322' 005037 000610' CLR PCBB+4 :MICROCODE WILL PUT XMIT STAT WORD 1
12187 050326' 004737 017776' JSR PC,CHKMON :WAIT FOR MICROMONITOR
12188 050332' 103006 BCC 110$ :OK
12189 050334' ERRHRD 277,COLTST,MSG46 :PRINT ERROR
12190 050334' 104456 TRAP C$ERHRD
12191 050336' 000425 .WORD 277
12192 050340' 004046' .WORD COLTST
12193 050342' 016700' .WORD MSG46
12194 050344' ESCAPE TST :LEAVE TEST
12195 050344' 104410 TRAP C$ESCAPE
12196 050346' 000166 .WORD L10165-.
12197 050350' 012777 000001 127760 110$: MOV #1,@PCSR0 :EXECUTE MICROTEST #1
12198 050356' 012737 000176 000332' MOV #2*SECOND,METER :WAIT FOR DNI
12199 050364' 004737 017330' JSR PC,CHKDNI
12200 050370' 103021 BCC 120$
12201 050372' 004737 020050' JSR PC,CHKINT :SEE IF ANY ERROR INTERRUPTS OCCURRED
12202 050376' 103006 BCC 115$ :NO, OK
12203 050400' ERRHRD 278,COLTST,MSG44 :PRINT ERROR MESSAGE
12204 050400' 104456 TRAP C$ERHRD
12205 050402' 000426 .WORD 278
12206 050404' 004046' .WORD COLTST
12207 050406' 016454' .WORD MSG44
12208 050410' ESCAPE TST
12209 050410' 104410 TRAP C$ESCAPE
12210 050412' 000122 .WORD L10165-.
12211 050414' 012702 000001 115$: MOV #1,R2 :MICROTEST #1 IS HUNG
12212 050420' ERRHRD 279,COLTST,MSG12
12213 050420' 104456 TRAP C$ERHRD
12214 050422' 000427 .WORD 279
12215 050424' 004046' .WORD COLTST
12216 050426' 013500' .WORD MSG12
12217 050430' ESCAPE TST
12218 050430' 104410 TRAP C$ESCAPE
12219 050432' 000102 .WORD L10165-.
12220

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12221
12222      ;PCBB+2 HAS TX WORD 0, IT SHOULD HAVE ERROR SUMMARY SET. PCBB+4 HAS TX WORD 1
12223      ;IT SHOULD HAVE RETRY SET
12224
12225 050434' 012703 040000      120$:  MOV      #BIT14,R3      ;'ERROR SUMMARY' SHOULD BE SET IN TX 0
12226 050440' 012704 002000      MOV      #BIT10,R4      ;'RETRY' BIT SHOULD BE SET IN TX 1
12227
12228 050444' 032737 040000 000606' BIT      #BIT14,PCBB+2      ;IS 'ERROR SUMMARY' BIT SET?
12229 050452' 001414      BEQ      130$      ;NO, ERROR
12230 050454' 032737 010000 000606' BIT      #BIT12,PCBB+2      ;IS 'MORE' BIT SET?
12231 050462' 001010      BNE      130$      ;YES, EPROR
12232 050464' 032737 004000 000606' BIT      #BIT11,PCBB+2      ;IS 'ONE' BIT SET?
12233 050472' 001004      BNE      130$      ;YES, ERROR
12234 050474' 032737 002000 000610' BIT      #BIT10,PCBB+4      ;IS 'RETRY' BIT SET?
12235 050502' 001004      BNE      140$      ;YES
12236 050504'      130$:  ERRHRD 280,COLTST,MSG22      ;PRINT ERROR MESSAGE
12237 050504' 104456
12238 050506' 000430
12239 050510' 004046'
12240 050512' 014340'
12241
12242      ;WRITE '1' TO CLEAR 'DNI'
12243
12244 050514' 004737 017374'      140$:  JSR      PC,CLRDN1      ;GO CLEAR DNI BIT
12245 050520' 103004      BCC      150$      ;OK
12246 050522'      ERRHRD 281,COLTST,RACMG7      ;PRINT ERROR MESSAGE
12247 050522' 104456
12248 050524' 000431
12249 050526' 004046'
12250 050530' 012726'
12251 050532'
12252 050532'      150$:  ENDSEG
12253 050532'
12254 050532' 104405      10003$: TRAP      C$ESEG
12255 050534'
12256 050534'
12257 050534' 104401      L10165: TRAP      C$E1ST

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.SBTTL TEST 44: TDR COUNTER TEST
.....
:THE DEUNA HAS A COUNTER DESIGNED TO HELP LOCATE FAULTS IN THE COAXIAL CABLE.
:THE COUNTER IS INITIALIZED WHEN A MESSAGE IS TRANSMITTED AND INCREMENTS AS
:THE DATAGRAM IS TRANSMITTED. COUNTING WILL STOP IF A COLLISION OCCURS OR THE
: CARRIER IS LOST. COUNTING ALSO STOPS IF THE 10 BIT COUNTER REACHES ITS
:MODULUS.
:THIS TEST WILL DETERMINE TAHT THE TDR COUNTER VALUE WILL CHANGE AND THAT THE
: COUNTER IS NOT STUCK.
:BECAUSE THE COUNTER COUNTS DURING TRANSMISSION OF A DATAGRAM AND WILL CONTINUE
: TO COUNT DURING THE TIME THAT THE TRANSMIT STATE MACHINE OPERATES, THE COUNT
: ACCUMULATED IN THE COUNTER DURING TRANSMISSION IS PROPORTIONAL TO THE LENGTH
: OF THE DATAGRAM. THIS TEST WILL USE THIS RELATION TO VERIFY THAT THE COUNTER
: IS NOT STUCK.
:THIS TEST USES MICROMODULE 'G' MICROTEST #2.
:THE TEST WILL SEND DATAGRAMS OVER THE LOOPBACK. THE LENGTH OF THE DATAGRAM
: WILL BE VARIED BY USING A INCREASING BYTE COUNT IN THE TRANSMIT BUFFER.
: AFTER EACH DATAGRAM HAS BEEN LOOPED BACK, THE TRANSMIT BUFFER WORD 1 WILL BE
: PASSED BACK TO THE HOST TO VERIFY THAT IT IS CORRECT. THE CRITERIA FOR
: CORRECTNESS WILL BE: INCREASING BYTE COUNTS SHOULD RESULT IN INCREASING TDR
: VALUES IN TRANSMIT STATUS WORD 1.
:THE PCBB WILL BE FORMATED AS FOLLOWS:
PCBB+0:      +-----+
              |   BYTE COUNT   |
              +-----+
PCBB+2:      +-----+
              | TRANSMIT STATUS WORD 1 |
              +-----+
:TEST SEQUENCE:
: 1-LOAD MICROMODULE 'G' IF NOT ALREADY DONE SO
: 2-LOAD MINIMUM BYTE COUNT INTO PCBB+0
: 3-CLEAR PCBB+2
: 4-WAIT FOR THE MICROMONITOR TO ENTER THE 'INMO' STATE
: 5-SELECT MICROTEST #2
: 6-WAIT FOR 'DNI'
: 7-VERIFY PCBB+2 HAS NON-ZERO VALUE (TDR COUNTER NOT ZERO)
: 8-WRITE '1' TO CLEAR 'DNI'
: 9-INCREASE BYTE COUNT IN PCBB+0 BY 1
:10-WAIT FOR MICROMONITOR TO ENTER 'INMON' STATE
:11-SELECT MICROTEST #2
:12-WAIT FOR 'DNI'
:13-VERIFY VALUE IN PCBB+2 IS GREATER THAN PREVIOUS VALUE IN PCBB+2
: (VERIFY THAT TDR VALUE IS GETTING LARGER WITH LARGER BYTE COUNTS)
:14-WRITE '1' TO CLEAR 'DNI'
:15-REPEAT STEPS 9-14 UNTIL BYTE REACHES MINIMUM SIZE +64
.....
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050536*

BGNTST

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12314 050536'                                     T44::
12315
12316                                     ;CHECK TO SEE IF MODULE 'G' HAS BEEN LOADED. IF NOT LOAD IT INTO
12317                                     ;THE TOP HALF OF WCS, START IT AND WAIT FOR THE MICROMONITOR TO SET 'DNI'.
12318                                     ;AFTER 'DNI' SETS WRITE '1' TO CLEAR IT.
12319
12320 BGNSEG
12321 050536' 104404                                     TRAP C$BSEG
12322 050540' 022737 000107 000326'                 CMP #'G,MICRO ;HAS MICROCODE MODULE 'G' BEEN LOADED
12323 050546' 001004                                     BNE 5$ ;NO
12324 050550' 122777 000001 127562                 CMPB #INMON,@PCSR1 ;YES, IS THE MICROMONITOR ACTIVE?
12325 050556' 001440                                     BEQ 20$ ;YES SKIP LOADING THE MICROMODULE
12326 050560' 012737 000107 000326' 5$:           MOV #'G,MICRO ;GO LOAD MICRO MODULE 'G'
12327 050566' 004737 020252'                         JSR PC,LODMIC
12328 050572' 103002                                     BCC 10$ ;OK
12329 050574'
12330 050574' 104410                                     TRAP C$ESCAPE
12331 050576' 000460                                     .WORD L10166-.
12332 050600' 012737 000176 000332' 10$:           MOV #2*SECOND,METER ;WAIT FOR THE MICROMONITOR
12333 050606' 004737 017330'                         JSR PC,CHKDNI
12334 050612' 103022                                     BCC 20$ ;OK
12335 050614' 012737 000776' 000310'                 MOV #SDNI,BITNAM
12336 050622' 012737 001275' 000312'                 MOV #SNSET,BITSTA
12337 050630' 012737 001340' 000314'                 MOV #SAFTER,PWHEN
12338 050636' 012737 001355' 000316'                 MOV #SGTCMD,PCOMND
12339 050644' ERRHRD 282,TDRCNT,MSG1
12340 050644' 104456                                     TRAP C$ERRHRD
12341 050646' 000432                                     .WORD 282
12342 050650' 004074'                                     .WORD TDRCNT
12343 050652' 012750'                                     .WORD MSG1
12344 050654'
12345 050654' 104410                                     TRAP C$ESCAPE
12346 050656' 000400                                     .WORD L10166-.
12347 050660' 004737 017374' 20$:                 JSR PC,CLRDN1 ;CLEAR DNI BIT
12348 050664' 103006                                     BCC 25$
12349 050666' ERRHRD 283,TDRCNT,RACMG7 ;DNI DID NOT CLEAR!
12350 050666' 104456                                     TRAP C$ERRHRD
12351 050670' 000433                                     .WORD 283
12352 050672' 004074'                                     .WORD TDRCNT
12353 050674' 012726'                                     .WORD RACMG7
12354 050676'
12355 050676' 104410                                     TRAP C$ESCAPE
12356 050700' 000356                                     .WORD L10166-.
12357 050702'
12358 050702'
12359 050702'
12360 050702' 104405                                     10000$: TRAP C$ESEG
12361
12362                                     ;LOAD MINIMUM BYTE COUNT INTO PCBB+0, CLEAR PCBB+2, WAIT FOR MICROMONITOR
12363                                     ;EXECUTE MICROTST #2 BY LOADING PCSR0 WITH A 2
12364                                     ;WAIT FOR 'DNI'
12365
12366 BGNSEG
12367 050704' 104404                                     TRAP C$BSEG
12368 050706' 012737 000100 000604'                 MOV #MINBYT,PCBB ;BEGIN WITH MINIMUM BYTE COUNT
12369 050714' 005037 000606'                 CLR PCBB+2 ;THIS IS WHERE MICROCODE WILL PUT...

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12370
12371 050720' 004737 017776'      JSR      PC,CHKMON      ;THE TDR COUNTER VALUE
12372 050724' 103006      BCC      35$           ;WAIT FOR MICROMONITOR
12373 050726'           ERRHRD  284,TDRCNT,MSG46 ;OK
12374 050726' 104456           ;PRINT ERROR
12375 050730' 000434           TRAP     C$ERHRD
12376 050732' 004074'       .WORD   284
12377 050734' 016700'       .WORD   TDRCNT
12378 050736'           .WORD   MSG46
12379 050736' 104410      ESCAPE  TST           ;LEAVE TEST
12380 050740' 000316           TRAP     C$ESCAPE
12381 050742' 012777 000002 127366 35$: MOV     #2,@PCSR0      .WORD   L10166-
12382 050750' 012737 000176 000332' MOV     #2*SECOND,METER ;TELL T11 TO EXECUTE MICROTEST #2
12383 050756' 004737 017330' JSR     PC,CHKDNI      ;WAIT FOR DNI
12384 050762' 103021      BCC     40$
12385 050764' 004737 020050' JSR     PC,CHKINT      ;SEE IF ANY ERROR INTERRUPTS OCCURRED
12386 050770' 103006      BCC     36$           ;NO, OK
12387 050772'           ERRHRD  285,TDRCNT,MSG44 ;PRINT ERROR MESSAGE
12388 050772' 104456           TRAP     C$ERHRD
12389 050774' 000435       .WORD   285
12390 050776' 004074'       .WORD   TDRCNT
12391 051000' 016454'       .WORD   MSG44
12392 051002'           ESCAPE  TST
12393 051002' 104410           TRAP     C$ESCAPE
12394 051004' 000252       .WORD   L10166-
12395 051006' 012702 000002 36$: MOV     #2,R2           ;MICROTEST #2 IS HUNG
12396 051012'           ERRHRD  286,TDRCNT,MSG12
12397 051012' 104456           TRAP     C$ERHRD
12398 051014' 000436       .WORD   286
12399 051016' 004074'       .WORD   TDRCNT
12400 051020' 013500'       .WORD   MSG12
12401 051022'           ESCAPE  TST
12402 051022' 104410           TRAP     C$ESCAPE
12403 051024' 000232       .WORD   L10166-
12404
12405
12406      ;CHECK THAT THE TRANSMIT STATUS WORD 1 HAS A NON-ZERO TDR VALUE IN IT
12407
12408 051026' 013701 000606' 40$: MOV     PCBB+2,R1      ;GET MINIMUM TDR VALUE
12409 051032' 001004      BNE     45$           ;SHOULD BE A NON-ZERO VALUE
12410 051034'           ERRHRD  287,TDRCNT,MSG23 ;ERROR TDR COUNTER NOT COUNTING
12411 051034' 104456           TRAP     C$ERHRD
12412 051036' 000437       .WORD   287
12413 051040' 004074'       .WORD   TDRCNT
12414 051042' 014454'       .WORD   MSG23
12415
12416      ;WRITE '1' TO CLEAR 'DNI'
12417
12418 051044' 004737 017374' 45$: JSR     PC,CLRDN1      ;GO CLEAR DNI
12419 051050' 103004      BCC     46$           ;OK
12420 051052'           ERRHRD  288,TDRCNT,RACMG7 ;ERROR OCCURRED PRINT ERROR MESSAGE
12421 051052' 104456           TRAP     C$ERHRD
12422 051054' 000440       .WORD   288
12423 051056' 004074'       .WORD   TDRCNT
12424 051060' 012726'       .WORD   RACMG7
12425 051062'           46$:

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12426 051062'          ENDSEG
12427 051062'
12428 051062' 10440'          10001$: TRAP C$ESEG
12429
12430          : INCREASE THE BYTE COUNT
12431
12432 051064' 005237 000604' 47$: INC PCBB          ; INCREASE BYTE COUNT BY 1
12433 051070'          BGNSEG
12434 051070' 104404          TRAP C$BSEG
12435
12436          : WAIT FOR THE MICROMONITOR, EXECUTE MICROTEST #2, AND WAIT FOR 'DNI'
12437
12438 051072' 004737 017776'          JSR PC,CHKMON          ; WAIT FOR MICROMONITOR
12439 051076' 103006          BCC 50$          ; OK
12440 051100'          ERRHRD 289,TDRCNT,MSG46          ; PRINT ERROR
12441 051100' 104456          TRAP C$ERRHRD
12442 051102' 000441          .WORD 289
12443 051104' 004074'          .WORD TDRCNT
12444 051106' 016700'          .WORD MSG46
12445 051110'          ESCAPE TST          ; LEAVE TEST
12446 051110' 104410          TRAP C$ESCAPE
12447 051112' 000144          .WORD L10166-
12448 051114' 012777 000002 127214 50$: MOV #2,@PCSR0          ; TELL T11 TO EXECUTE MICROTEST #2
12449 051122' 012737 000176 000332' MOV #2=SECOND,METER          ; WAIT FOR DNI
12450 051130' 004737 017330' JSR PC,CHKDNI
12451 051134' 103021          BCC 55$
12452 051136' 004737 020050' JSR PC,CHKINT          ; SEE IF ANY ERROR INTERRUPTS OCCURRED
12453 051142' 103006          BCC 51$          ; NO OK
12454 051144'          ERRHRD 290,TDRCNT,MSG44          ; PRINT ERROR MESSAGE
12455 051144' 104456          TRAP C$ERRHRD
12456 051146' 000442          .WORD 290
12457 051150' 004074'          .WORD TDRCNT
12458 051152' 016454'          .WORD MSG44
12459 051154'          ESCAPE TST          ; LEAVE TEST
12460 051154' 104410          TRAP C$ESCAPE
12461 051156' 000100          .WORD L10166-
12462 051160' 012702 000002 51$: MOV #2,R2          ; MICROTEST #2 IS HUNG
12463 051164'          ERRHRD 291,TDRCNT,MSG12
12464 051164' 104456          TRAP C$ERRHRD
12465 051166' 000443          .WORD 291
12466 051170' 004074'          .WORD TDRCNT
12467 051172' 013500'          .WORD MSG12
12468 051174'          ESCAPE TST          ; LEAVE TEST
12469 051174' 104410          TRAP C$ESCAPE
12470 051176' 000060          .WORD L10166-
12471
12472          : VERIFY THAT THE TRANSMIT STATUS WORD 1 TDR VALUE IS LARGER THAN THE PREVIOUS
12473          : VALUE
12474
12475 051200' 013702 000606' 55$: MOV PCBB+2,R2          ; GET NEW TDR VALUE
12476 051204' 020201          CMP R2,R1          ; IS TDR GETTING LARGER?
12477 051206' 101006          BHI 60$          ; YES
12478 051210'          ERRHRD 292,TDRCNT,MSG23          ; NO ERROR
12479 051210' 104456          TRAP C$ERRHRD
12480 051212' 000444          .WORD 292
12481 051214' 004074'          .WORD TDRCNT
    
```

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```

12482 051216' 014454'
12483 051220'          ESCAPE TST          .WORD  MSG23
12484 051220' 104410
12485 051222' 000034          TRAP  C$ESCAPE
12486                                     .WORD  L10166-
12487                                     :WRITE '1' TO CLEAR 'DNI'
12488                                     :
12489 051224' 004737 017374' 60$: JSR PC,CLRDNI          :GC CLEAR DNI
12490 051230' 103004          BCC 65$          :OK
12491 051232'          ERRHRD 293,'DRCNT,RACMG7 :ERROR OCCURRED PRINT ERROR MESSAGE
12492 051232' 104456          TRAP  C$ERRHRD
12493 051234' 000445          .WORD  293
12494 051236' 004074'          .WORD  TDRCNT
12495 051240' 012726'          .WORD  RACMG7
12496 051242'          65$:
12497 051242'          ENDSEG
12498 051242'
12499 051242' 104405          10002$:
12500                                     TRAP  C$ESEG
12501                                     :CONTINUE THE LOOP UNTIL THE BYTE COUNT IS TOO LARGE FOR THE COUNTER MODULUS
12502                                     :
12503 051244' 010201          MOV R2,R1          :HOLD NEW TDR VALUE FOR LATER COMPARE
12504 051246' 023727 000606' 000164 CMP PCBB+2,#116. :HAVE WE DONE ENOUGH PACKETS?
12505 051254' 103703          BLO 47$          :NO KEEP GOING
12506 051256'          ENDTST
12507 051256'          L10166:
12508 051256' 104401          TRAP  C$ETST

```

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.SBTTL TEST 45: RETRY LOGIC TEST
.....
:THE RETRY LOGIC IS ACTIVATED WHENEVER A COLLISION IS ENCOUNTERED DURING A
:TRANSMISSION ATTEMPT. THE LINK STOPS TRANSMISSION AND WAITS FOR A PERIOD OF
:TIME BEFORE ATTEMPTING TO RETRANSMIT.
:
:THE WAIT TIME IS AN INTEGRAL NUMBER OF 'SLOT TIMES'. THE NUMBER COMES FROM
:A RANDOM NUMBER GENERATOR. THE NUMBER OF SLOT TIMES IS NOT EXACTLY RANDOM
:SINCE THE RETRY LOGIC WAITS A GENERALLY INCREASING NUMBER OF SLOT TIMES BEFORE
:TRYING TO RETRANSMIT. THIS TEST WILL VERIFY THAT THE RETRY LOGIC IS CAPABLE OF
:GENERATING VARIABILITY IN THE DURATION OF THE RETRY WAIT TIMES.
:
:THIS TEST WILL USE MICROMODULE 'G' MICROTEST #3
:THE LINK MODULE HAS A DIAGNOSTIC MAINTENANCE FACILITY MAKING IT POSSIBLE TO
:SINGLE STEP THE RETRY LOGIC THROUGH THE MAXIMUM SIXTEEN RETRY STEPS. THIS
:FEATURE WILL ALSO MAKE IT POSSIBLE TO MEASURE THE RETRY WAIT INTERVAL.
:
:THE MICROCODE WILL SET THE COLLISION BIT IN THE LINK MODE REGISTER AND
:AND TRANSMIT A DATAGRAM IN LOOPBACK MODE. THE T-11 WILL COUNT WHILE WAITING
:FOR THE TRANSMIT STATE MACHINE TO INTERRUPT. THE ACCUMULATED COUNT SHOULD
:PROVIDE A MEASURE OF TIME TAKEN FOR THE TRANSMISSION ATTEMPT TO OCCUR. SINCE
:THE COLLISION BIT IS SET, THIS INTERVAL WILL INCLUDE THE RETRY WAIT INTERVAL.
:THE ACCUMULATE' COUNT WILL BE WRITTEN BY THE MICROCODE TO THE PCBB.
:
:THE MICROTEST WILL BE EXECUTED 16 TIMES. AFTER EACH EXECUTION, THE COUNT WILL
:BE READ FROM THE PCBB AND STORED IN A TABLE. THE TABLE WILL BE SCANNED TO
:VERIFY THAT THEY ARE NOT ALL THE SAME.
:
:THE PCBB IS FORMATTED AS FOLLOWS:
:
:PCBB+0:      +-----+
:              |   BYTE COUNT   |
:              +-----+
:PCBB+2:      +-----+
:              | TRANSMIT WAIT COUNT |
:              +-----+
:
:TEST SEQUENCE:
:  1-LOAD MICROMODULE 'G' IF NOT ALREADY DONE SO
:  2-PLACE A MINIMUM BYTE COUNT IN PCBB+0
:  3-CLEAR PCBB+2
:  4-WAIT FOR MICROMONITOR TO ENTER THE 'INMON' STATE
:  5-SELECT MICROTEST #3
:  6-WAIT FOR 'DNI'
:  7-WRITE '1' TO CLEAR 'DNI'
:  8-READ PCBB+2 AND PUT COUNT IN TABLE
:  9-REPEAT STEPS 2-8 15 TIMES
: 10-VERIFY NO 10 CONSECUTIVE ENTRIES IN THE TABLE ARE THE SAME.
.....
BGNTST
T45::
:
:CHECK TO SEE IF MODULE 'G' HAS BEEN LOADED. IF NOT LOAD IT INTO
:THE TOP HALF OF WCS, START IT AND WAIT FOR THE MICROMONITOR TO SET 'DNI'.

```

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```

12565 ;AFTER 'DNI' SETS WRITE '1' TO CLEAR IT.
12566 ;
12567 051260' BGNSEG
12568 051260' 104404 TRAP C$BSEG
12569 051262' 022737 000107 000326' CMP #'G,MICRO ;HAS MICROCODE MODULE 'G' BEEN LOADED
12570 051270' 001004 BNE 5$ ;NO
12571 051272' 122777 000001 127040 CMPB #INMON,@PCSR1 ;YES, IS THE MICROMONITOR ACTIVE?
12572 051300' 001440 BEQ 20$ ;YES SKIP LOADING THE MICROMODULE
12573 051302' 012737 000107 000326' 5$: MOV #'G,MICRO ;GO LOAD MICRO MODULE 'G'
12574 051310' 004737 020252' JSR PC,L0DMIC
12575 051314' 103002 BCC 10$ ;OK
12576 051316' ESCAPE TST
12577 051316' 104410 TRAP C$ESCAPE
12578 051320' 000352 .WORD L10167-.
12579 051322' 012777 000176 000332' 10$: MOV #2*SECOND,METER ;WAIT FOR THE MICROMONITOR
12580 051330' 004737 017330' JSR PC,CHKDNI
12581 051334' 103022 BCC 20$ ;OK
12582 051336' 012737 000776' 000310' MOV #SDNI,BITNAM
12583 051344' 012737 001275' 000312' MOV #SSET,BITSTA
12584 051352' 012737 001340' 000314' MOV #SAFTER,PWHEN
12585 051360' 012737 001355' 000316' MOV #SGTCMD,PCOMND
12586 051366' ERRHRD 294,RETLOG,MSG1
12587 051366' 104456 TRAP C$ERHRD
12588 051370' 000446 .WORD 294
12589 051372' 004124' .WORD RETLOG
12590 051374' 012750' .WORD MSG1
12591 051376' ESCAPE TST
12592 051376' 104410 TRAP C$ESCAPE
12593 051400' 000272 .WORD L10167-.
12594 051402' 004737 017374' 20$: JSR PC,CLRDNI ;CLEAR DNI BIT
12595 051406' 103006 BCC 25$
12596 051410' ERRHRD 295,RETLOG,RACMG7 ;DNI DID NOT CLEAR!
12597 051410' 104456 TRAP C$ERHRD
12598 051412' 000447 .WORD 295
12599 051414' 004124' .WORD RETLOG
12600 051416' 012726' .WORD RACMG7
12601 051420' ESCAPE TST
12602 051420' 104410 TRAP C$ESCAPE
12603 051422' 000250 .WORD L10167-.
12604 051424' 25$:
12605 051424' ENDSEG
12606 051424'
12607 051424' 104405 10000$: TRAP C$ESEG
12608 ;
12609 ;WRITE A BYTE COUNT TO PCBB+0, CLEAR PCBB+2, GET POINTER TO TOP OF TABLE
12610 ;INITIALIZE A COUNTER TO 16
12611 ;
12612 051426' 012737 000100 000604' MOV #MINBYT,PCBB ;BEGIN WITH MINIMUM BYTE COUNT
12613 051434' 005037 000606' CLR PCBB+2 ;THIS IS WHERE MICROCODE WILL PUT...
12614 ;THE RETRY WAIT INTERVAL
12615 051440' 012702 000624' MOV #CNTTAB,R2 ;GET POINTER TO WAIT INTERVAL STORAGE AREA
12616 051444' 012701 000020 MOV #16.,R1 ;NUMBER OF VALUES TO STORE
12617 ;
12618 ;WAIT FOR THE MICROCODE TO ENTER THE MICROMONITOR. EXECUTE MICROTST #3 BY
12619 ;LOADING THE COMMAND FIELD OF PCSRO WITH A 3. WAIT FOR 'DNI'
12620 ;

```

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```

12621 051450'          BGNSEG
12622 051450' 104404
12623 051452' 004737 017776'      JSR    PC,CHKMON      ;WAIT FOR MICROMONITOR
12624 051456' 103006          BCC    35$           ;OK
12625 051460'          ERRHRD 296,RETLOG,MSG46 ;PRINT ERROR
12626 051460' 104456          TRAP   C$ERHRD
12627 051462' 000450          .WORD 296
12628 051464' 004124'          .WORD RETLOG
12629 051466' 016700'          .WORD MSG46
12630 051470'          ESCAPE TST      ;LEAVE TEST
12631 051470' 104410          TRAP   C$ESCAPE
12632 051472' 000200          .WORD L10167-.
12633 051474' 012777 000003 126634 35$:  MOV    #3,@PCSR0      ;TELL T11 TO EXECUTE MICROTEST #3
12634 051502' 012737 000770 000332'  MOV    #10*SECOND,METER ;WAIT FOR DNI
12635 051510' 004737 017330'      JSR    PC,CHKDNI
12636 051514' 103021          BCC    40$
12637 051516' 004737 020050'      JSR    PC,CHKINT      ;SEE IF ANY ERROR INTERRUPTS OCCURRED
12638 051522' 103006          BCC    36$           ;NO, OK
12639 051524'          ERRHRD 297,RETLOG,MSG44 ;PRINT ERROR MESSAGE
12640 051524' 104456          TRAP   C$ERHRD
12641 051526' 000451          .WORD 297
12642 051530' 004124'          .WORD RETLOG
12643 051532' 016454'          .WORD MSG44
12644 051534'          ESCAPE TST
12645 051534' 104410          TRAP   C$ESCAPE
12646 051536' 000134          .WORD L10167-.
12647 051540' 012702 000003 36$:  MOV    #3,R2          ;MICROTEST #3 IS HUNG
12648 051544'          ERRHRD 298,RETLOG,MSG12
12649 051544' 104456          TRAP   C$ERHRD
12650 051546' 000452          .WORD 298
12651 051550' 004124'          .WORD RETLOG
12652 051552' 013500'          .WORD MSG12
12653 051554'          ESCAPE TST
12654 051554' 104410          TRAP   C$ESCAPE
12655 051556' 000114          .WORD L10167-.
12656          ;
12657          ;WRITE '1' TO CLEAR 'DNI'
12658          ;
12659 051560' 004737 017374' 40$:  JSR    PC,CLRDN1      ;GO CLEAR DNI
12660 051564' 103006          BCC    45$           ;OK
12661 051566'          ERRHRD 299,RETLOG,RACMG7 ;ERROR
12662 051566' 104456          TRAP   C$ERHRD
12663 051570' 000453          .WORD 299
12664 051572' 004124'          .WORD RETLOG
12665 051574' 012726'          .WORD RALMG7
12666 051576'          ESCAPE TST      ;LEAVE
12667 051576' 104410          TRAP   C$ESCAPE
12668 051600' 000072          .WORD L10167-.
12669          ;
12670          ;STORE THE VALUE FORM PCBB+2 IN THE TABLE AND BUMP THE POINTER TO NEXT ENTRY
12671          ;REPEAT THE TEST UNTIL ALL 16 ENTRIES ARE OBTAINED
12672          ;
12673 051602' 013722 000606' 45$:  MOV    PCBB+2,(R2)+    ;STORE COUNTER VALUE IN THE TABLE
12674 051606' 00530'          DEC    R1            ;HAVE WE STORED 16 VALUES?
12675 051610' 00133'          BNE    35$           ;NOT YET
12676          ;

```

HARDWARE TESTS MACY11 30(1046)
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 TEST 45: RETRY LOGIC TEST

```

12677 ;TREAT THE TABLE AS 7 GROUPS OF 10 ENTRIES. VERIFY THAT ALL 10 ENTRIES OF EACH
12678 ;GROUP ARE NOT THE SAME.
12679 .
12680 051612' 012701 000624' 50$: MOV #CNTTAB,R1 ;POINT TO TOP OF TABLE
12681 051616' 010102 MOV R1,R2 ;GET POINTER TO FIRST ELEMENT IN THIS
12682 ;GFOUP
12683 051620' 010204 MOV R2,R4 ;GET POINTER TO SECOND ELEMENT IN THIS
12684 ;GROUP
12685 051622' 062704 000002 ADD #2,R4
12686 051626' 012703 000007 MOV #7,R3 ;NUMBER OF GROUPS OF 10 TO COMPARE
12687 051632' 022224 55$: CMP (R2)+,(R4)+ ;IS THIS PAIR THE SAME?
12688 051634' 001010 BNE 60$ ;NO, START A NEW GROUP TO CHECK
12689 051636' 005303 DEC R3 ;HAVE WE CHECKED ALL 10 VALUES IN THIS
12690 ;GROUP?
12691 051640' 001374 BNE 55$ ;NOT YET
12692 051642' ERRHRD 300,RETLOG,MSG24 ;YES, PRINT ERROR MESSAGE AND DUMP TABLE
12693 051642' 104456 TRAP C$ERHRD
12694 051644' 000454 .WORD 300
12695 051646' 004124' .WORD RETLOG
12696 051650' 014476' .WORD MSG24
12697 051652' ESCAPE TST
12698 051652' 104410 TRAP C$ESCAPE
12699 051654' 000016 .WORD L10167-
12700 051656' 062701 000002 60$: ADD #2,R1 ;POINT TO NEXT GROUP OF 10 ELEMENTS
12701 051662' 020127 000642' CMP R1,#CNTTAB+16 ;CHECKED ALL GROUPS OF 10?
12702 051666' 001353 BNE 50$ ;NOT YET
12703 051670' ENDSEG
12704 051670'
12705 051670' 104405 10001$: TRAP C$ESEG
12706 051672' ENDTST
12707 051672'
12708 051672' 104401 L10167: TRAP C$ETST
    
```


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.SBTTL TEST 46: PRINT DEVICE PARAMETERS TEST

```

*****
THIS TEST PRINTS THE DEFAULT PHYSICAL ADDRESS, THE MICROCODE
REVISION AND THE SWITCH PACK SETTINGS.

TEST SEQUENCE:
1. READ DEFAULT PHYSICAL ADDRESS
2. READ MICROCODE REVISION
3. READ SWITCH PACK SETTINGS
4. PRINT

NOTE: THIS TEST IS ONLY EXECUTED ONCE FOR EACH UNIT REGARDLESS OF THE PASS #
*****

```

BGNTST

```

T46::
TST FRSTIM ; RUN THIS TEST ?
BNE 5$ ; YES
EXIT TST ; NO, EXIT
TRAP C$EXIT
.WORD L10170-.

5$: JSR PC,REUNA ; GO RESET UNA
BCC 20$ ; OK
ESCAPE TST ; ABORT TEST
TRAP C$ESCAPE
.WORD L10170-.

20$: MOV #PCBB,@PCSR2 ; LOAD PCSR2 WITH PORT CONTROL BLOCK ADR
CLR @PCSR3 ; LOAD PCSR3 WITH 0
MOV #GETPCB,@PCSR0 ; ISSUE GET PCBB PORT COMMAND
MOV #1*SECOND,METER ; PUT SOME TIME ON THE METER
JSR PC,CHKDNI ; DNI?
BCC 40$ ; YES
; ERROR DNI FAILED TO SET!
; FORMAT ERROR MESSAGE

MOV #SDNI,BITNAM
MOV #SNSET,BITSTA
MOV #SAFTER,PWHEN
MOV #SGTPCB,PCOMND
ERRHRD 301,PRTPAR,MSG1
TRAP C$ERHRD
.WORD 301
.WORD PRTPAR
.WORD MSG1

ESCAPE TST ; AND ABORT TEST
TRAP C$ESCAPE
.WORD L10170-.

40$: JSR PC,CLRDN1 ; WRITE ONE TO CLEAR DNI!
; ERROR ?
BCC 50$ ; NO

```

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```

12765                                     ;ERROR DNI FAILED TO CLEAR!
12766 052026* ERRHRD 302,PRTPAR,RACMG7
12767 052026* 104456
12768 052030* 000456
12769 052032* 004154*
12770 052034* 012726*
12771 052036*
12772 052036* 104410
12773 052040* 000706
12774
12775 ;:READ DEFAULT PHYSICAL ADDRESS
12776
12777 052042* 012737 000002 000604* 50$: MOV #RDPA,PCBB ;LOAD PCBB WITH READ DEFAULT PHY ADR
12778 052050* 012737 000176 000332* MOV #2*SECOND,METER ;PUT SOME TIME ON THE METER
12779 052056* 012777 000002 126252 MOV #GETCMD,@PCSR0 ;ISSUE GET_CMD PORT COMMAND
12780 052064* 004737 017330* JSR PC,CHKDNI ;DNI ?
12781 052070* 103022 BCC 60$ ;YES
12782 ;ERROR DNI FAILED TO SET!
12783 052072* 012737 000776* 000310* MOV #SDNI,BITNAM
12784 052100* 012737 001275* 000312* MOV #SNSET,BITSTA
12785 052106* 012737 001340* 000314* MOV #SAFTER,PWHEN
12786 052114* 012737 001355* 000316* MOV #SGTCMD,PCOMND
12787 052122* ERRHRD 303,PRTPAR,MSG1 ;PRINT ERROR MESSAGE
12788 052122* 104456
12789 052124* 000457
12790 052126* 004154*
12791 052130* 012750*
12792 052132*
12793 052132* 104410
12794 052134* 000612
12795
12796 052136* 004737 017374* 60$: JSR PC,CLRDNI ;WRITE ONE TO CLEAR DNI
12797
12798 052142* 103006 BCC 70$ ;ERROR ?
12799 ;NO
12800 ERRHRD 304,PRTPAR,RACMG7 ;ERROR DNI FAILED TO CLEAR!
12801 052144* 104456 ;PRINT ERROR MESSAGE
12802 052146* 000460
12803 052150* 004154*
12804 052152* 012726*
12805 052154*
12806 052154* 104410
12807 052156* 000570
12808
12809 ;:MOVE DEFAULT PHYSICAL ADDRESS FROM PCBB -> DPA
12810
12811 052160* 013737 000606* 052750* 70$: MOV PCBB+2,DPA
12812 052166* 013737 000610* 052752* MOV PCBB+4,DPA+2
12813 052174* 013737 000612* 052754* MOV PCBB+6,DPA+4
12814
12815 ;:LOAD ASCII MESSAGE (DEFADR)
12816
12817 052202* 004737 017130* JSR PC,HEXDPA ;CONVERT TO ASCII HEX
12818
12819 ;:READ MICROCODE REVISION
12820

```

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```

12821 052206' 012737 000016 000604' 100$: MOV #RPS,PCBB ;LOAD PCBB WITH READ PORT STATUS FUNCTION
12822 052214' 012737 000176 000332' MOV #2*SECOND,METER ;PUT SOME TIME ON THE METER
12823 052222' 012777 000002 126106 MOV #GETCMD,@PCSRO ;ISSUE GET_CMD PORT COMMAND
12824 052230' 004737 017330' JSR PC,CHKDNI ;DNI?
12825 052234' 103022 BCC 110$ ;YES
12826 ;ERROR DNI FAILED TO SET
12827 052236' 012737 000776' 000310' MOV #SDNI,BITNAM
12828 052244' 012737 001275' 000312' MOV #NSET,BITSTA
12829 052252' 012737 001340' 000314' MOV #SAFTER,PWHEN
12830 052260' 012737 001355' 000316' MOV #SGTCMD,PCOMND
12831 052266' ERRHRD 305,PRTPAR,MSG1 ;PRINT ERROR MESSAGE
12832 052266' 104456 TRAP C$ERHRD
12833 052270' 000461 .WORD 305
12834 052272' 004154' .WORD PRTPAR
12835 052274' 012750' .WORD MSG1
12836 052276' ESCAPE TST ;AND ABORT TEST
12837 052276' 104410 TRAP C$ESCAPE
12838 052300' 000446 .WORD L10170-.
12839
12840 052302' 004737 017374' 110$: JSR PC,CLRDN1 ;WRITE ONE TO CLEAR DNI
12841 ;ERROR?
12842 052306' 103006 BCC 120$ ;NO
12843 ;ERROR DNI FAILED TO SET
12844 052310' ERRHRD 306,PRTPAR,RACMG7 ;PRINT ERROR MESSAGE
12845 052310' 104456 TRAP C$ERHRD
12846 052312' 000462 .WORD 306
12847 052314' 004154' .WORD PRTPAR
12848 052316' 012726' .WORD RACMG7
12849 052320' ESCAPE TST ;AND ABORT TEST
12850 052320' 104410 TRAP C$ESCAPE
12851 052322' 000424 .WORD L10170-.
12852
12853 ;MOVE MICROCODE REVISION FROM PCBB -> RREV
12854
12855 052324' 013737 000606' 052756' 120$: MOV PCBB+2,RREV
12856 052332' 042737 177700 052756' BIC #177700,RREV ;MASK RREV
12857
12858 ;READ SWITCH PACK
12859
12860 052340' 012737 000020 000604' 130$: MOV #DIM,PCBB ;LOAD DUMP INTERNAL MEMORY FUNCTION
12861 052346' 012737 000614' 000606' MOV #UDBB,PCBB+2 ;GIVE THE UNIBUS DATA BLOCK BASE ADR
12862 052354' 005037 000610' CLR PCBB+4
12863 052360' 012737 000002 000614' MOV #2,UDBB ;SETUP TO LOAD 2 BYTES
12864 052366' 012737 052760' 000616' MOV #SWPACK,UDBB+2 ;LOAD ADDRESS
12865 052374' 005037 000620' CLR UDBB+4
12866 052400' 013737 000334' 000622' MOV SWADDR,UDBB+6 ;LOAD INTERNAL ADDRESS
12867 052406' 012737 000176 000332' MOV #2*SECOND,METER ;PUT SOME TIME ON THE METER
12868 052414' 012777 000002 125714 MOV #GETCMD,@PCSRO ;ISSUE GET COMMAND PORT COMMAND
12869 052422' 004737 017330' JSR PC,CHKDNI ;DNI?
12870 052426' 103022 BCC 140$ ;YES
12871 ;ERROR DNI FAILED TO SET
12872 052430' 012737 000776' 000310' MOV #SDNI,BITNAM
12873 052436' 012737 001275' 000312' MOV #NSET,BITSTA
12874 052444' 012737 001340' 000314' MOV #SAFTER,PWHEN
12875 052452' 012737 001355' 000316' MOV #SGTCMD,PCOMND
12876 052460' ERRHRD 307,PRTPAR,MSG1

```

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 CZUAAA.P11 11-JAN-83 09:29 TEST 46: PRINT DEVICE PARAMETERS TEST

```

12877 052460' 104456
12878 052462' 000463
12879 052464' 004154'
12880 052466' 012750'
12881 052470'          ESCAPE TST          ; AND ABORT TEST
12882 052470' 104410
12883 052472' 000254
12884
12885 052474' 004737 017374'      140$: JSR      PC,CLRDNI          ; WRITE ONE TO CLEAR DNI
12886
12887 052500' 103006
12888 052502'          BCC      150$          ; ERROR ?
12889 052502' 104456          ERRHRD 308,PRTPAR,RACMG7      ; NO
12890 052504' 000464
12891 052506' 004154'
12892 052510' 012726'
12893 052512'          ESCAPE TST          ; AND ABORT TEST
12894 052512' 104410
12895 052514' 000232
12896
12897          ; GET SWITCH PACK INFO READY TO PRINT
12898
12899 052516' 013704 052760'      150$: MOV      SWPACK,R4          ; SWITCH PACK -> R4
12900 052522' 042704 167777          BIC      #167777,R4          ; MASK BIT 12
12901 052526' 012700 000013          MOV      #11.,R0          ; SHIFT BIT FOR INDEX
12902 052532' 006204
12903 052534' 005300
12904 052536' 001375
12905 052540' 062704 053100'          ADD      #LPTBL,R4          ; INDEX INTO LOOP TABLE
12906 052544' 011437 052762'          MOV      (R4),LPMSG          ; LOAD INTO LOOP MESSAGE
12907 052550' 013704 052760'      170$: MOV      SWPACK,R4          ; SWITCH PACK -> R4
12908 052554' 042704 171777          BIC      #171777,R4          ; MASK BITS 10 AND 11
12909 052560' 012700 000011          MOV      #9.,R0          ; SHIFT BITS FOR INDEX
12910 052564' 006204
12911 052566' 005300
12912 052570' 001375
12913 052572' 062704 053104'          ADD      #BITBL,R4          ; INDEX INTO BOOT TABLE
12914 052576' 011437 052764'          MOV      (R4),BTMSG          ; LOAD INTO BOOT MESSAGE
12915
12916          ; PRINT
12917
12918 052602'          PRINTB #FORM28,#DEFHDR      ; PRINT DEFAULT PHYSICAL ADDRESS
12919 052602' 012746 052770'          MOV      #DEFHDR,-(SP)
12920 052606' 012746 006331'          MOV      #FORM28,-(SP)
12921 052612' 012746 000002          MOV      #2,-(SP)
12922 052616' 010600          MOV      SP,R0
12923 052620' 104414          TRAP    C$PNTB
12924 052622' 062706 000006          ADD      #6,SP
12925 052626'          PRINTB #FORM64,RREV          ; PRINT MICROCODE REV
12926 052626' 013746 052756'          MOV      RREV,-(SP)
12927 052632' 012746 011066'          MOV      #FORM64,-(SP)
12928 052636' 012746 000002          MOV      #2,-(SP)
12929 052642' 010600          MOV      SP,R0
12930 052644' 104414          TRAP    C$PNTB
12931 052646' 062706 000006          ADD      #6,SP
12932 052652'          PRINTB #FORM28,#SWHDR      ; PRINT SWITCH PACK HEADER

```

HARDWARE TESTS MACY11 30(1046)
CZUAAA.P11 11-JAN-83 09:29

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TEST 46: PRINT DEVICE PARAMETERS TEST

12933	052652'	012746	053114'
12934	052656'	012746	006331'
12935	052662'	012746	000002
12936	052666'	010600	
12937	052670'	104414	
12938	052672'	062706	000006
12939	052676'		
12940	052676'	013746	052762'
12941	052702'	012746	006331'
12942	052706'	012746	000002
12943	052712'	010600	
12944	052714'	104414	
12945	052716'	062706	000006
12946	052722'		
12947	052722'	013746	052764'
12948	052726'	012746	006331'
12949	052732'	012746	000002
12950	052736'	010600	
12951	052740'	104414	
12952	052742'	062706	000006
12953			
12954	052746'		
12955	052746'		
12956	052746'		
12957	052746'	104401	

250\$:

PRINTB #FORM28,LPMSG

; PRINT LOOPBACK MESSAGE

PRINTB #FORM28,BTMSG

; PRINT BOOT MESSAGE

ENDTST

L10170:

MOV	#SWHDR,-(SP)
MOV	#FORM28,-(SP)
MOV	#2,-(SP)
MOV	SP,R0
TRAP	C\$PNTB
ADD	#6,SP
MOV	LPMSG,-(SP)
MOV	#FORM28,-(SP)
MOV	#2,-(SP)
MOV	SP,R0
TRAP	C\$PNTB
ADD	#6,SP
MOV	BTMSG,-(SP)
MOV	#FORM28,-(SP)
MOV	#2,-(SP)
MOV	SP,R0
TRAP	C\$PNTB
ADD	#6,SP
TRAP	C\$ETST

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CZUAAA.P11 11-JAN-83 09:29 TEST 46: PRINT DEVICE PARAMETERS TEST

```

12958 ;LOCAL STORAGE FOR TEST 41
12959 052750' 000000 DPA:: .WORD 0 ; DEFAULT PHYSICAL ADDRESS (15:0)
12960 052752' 000000 .WORD 0 ; DEFAULT PHYSICAL ADDRESS (31:16)
12961 052754' 000000 .WORD 0 ; DEFAULT PHYSICAL ADDRESS (47:32)
12962 ;
12963 052756' 000000 RREV:: .WORD 0 ; MICROCODE REVISION
12964 ;
12965 052760' 000000 SWPACK:: .WORD 0 ; SWITCH PACK CONTENTS
12966 052762' 000000 LPMSG:: .WORD 0 ; LOOPBACK MESSAGE ADDRESS
12967 052764' 000000 BTMSG:: .WORD 0 ; BOOT MESSAGE ADDRESS
12968 ;
12969 052766' 000 HEXDAT:: .BYTE 0 ; HEX DATA FOR CONVERSION
12970 052767' 000 HEXVAL:: .BYTE 0 ; ASCII HEX VALUE
12971 ;
12972 052770' 005015 052105 042510 DEFHDR:: .ASCII <15><12>/ETHERNET DEFAULT ADDRESS (HEX): /
12973 052776' 047122 052105 042040
12974 053004' 043105 052501 052114
12975 053012' 040440 042104 042522
12976 053020' 051523 024040 042510
12977 053026' 024530 020072 040
12978 053033' 040 DEFADR:: .ASCII / /
12979 053035' 055 .ASCII /- /
12980 053036' 020040 .ASCII / /
12981 053040' 055 .ASCII /- /
12982 053041' 040 040 .ASCII / /
12983 053043' 055 .ASCII /- /
12984 053044' 020040 .ASCII / /
12985 053046' 055 .ASCII /- /
12986 053047' 040 040 .ASCII / /
12987 053051' 055 .ASCII /- /
12988 053052' 020040 .ASCII / /
12989 053054' 005015 000 .ASCIIZ <15><12>
12990 ;
12991 053057' 060 HEXTBL:: .ASCII /0 /
12992 053060' 061 .ASCII /1 /
12993 053061' 062 .ASCII /2 /
12994 053062' 063 .ASCII /3 /
12995 053063' 064 .ASCII /4 /
12996 053064' 065 .ASCII /5 /
12997 053065' 066 .ASCII /6 /
12998 053066' 067 .ASCII /7 /
12999 053067' 070 .ASCII /8 /
13000 053070' 071 .ASCII /9 /
13001 053071' 101 .ASCII /A /
13002 053072' 102 .ASCII /B /
13003 053073' 103 .ASCII /C /
13004 053074' 104 .ASCII /D /
13005 053075' 105 .ASCII /E /
13006 053076' 106 .ASCII /F /
13007 053100' .EVEN
13008 ;
13009 ;
13010 ;LOOP MESSAGE TABLE
13011 053100' 053146' LPTBL:: .WORD LPMSG0
13012 053102' 053205' .WORD LPMSG1
13013 ;BOOT MESSAGE TABLE

```

HARDWARE TESTS MACV11 30(1046) 11-JAN-83 10:13 PAGE 269
CZUAAA.P11 11-JAN-83 09:29 TEST 46: PRINT DEVICE PARAMETERS TEST

13014	053104*	053243*			BTBTL::	.WORD	BTMSG0
13015	053106*	053301*				.WORD	BTMSG1
13016	053110*	053334*				.WORD	BTMSG2
13017	053112*	053400*				.WORD	BTMSG3
13018					:ASCII MESSAGES		
13019	053114*	005015	053523	052111	SWHDR::	.ASCII	<15><12>/SWITCH PACK SET FOR :/
13020	053122*	044103	050040	041501			
13021	053130*	020113	042523	020124			
13022	053136*	047506	020122	072			
13023	053143*	015	000012			.ASCII	<15><12>
13024	053146*	020040	020040	051440	LPMSG0::	.ASCII	/ SELF TEST LOOP DISABLED/
13025	053154*	046105	020106	042524			
13026	053162*	052123	046040	047517			
13027	053170*	020120	044504	040523			
13028	053176*	046102	042105				
13029	053202*	005015	000			.ASCII	<15><12>
13030	053205*	040	020040	020040	LPMSG1::	.ASCII	/ SELF TEST LOOP ENABLED/
13031	053212*	042523	043114	052040			
13032	053220*	051505	020124	047514			
13033	053226*	050117	042440	040516			
13034	053234*	046102	042105				
13035	053240*	005015	000			.ASCII	<15><12>
13036	053243*	040	020040	020040	BTMSG0::	.ASCII	/ NO REMOTE BOOT ENABLED/
13037	053250*	047516	051040	046505			
13038	053256*	052117	020105	047502			
13039	053264*	052117	042440	040516			
13040	053272*	046102	042105				
13041	053276*	005015	000			.ASCII	<15><12>
13042	053301*	040	020040	020040	BTMSG1::	.ASCII	/ REMOTE BOOT ENABLED/
13043	053306*	042522	047515	042524			
13044	053314*	041040	047517	020124			
13045	053322*	047105	041101	042514			
13046	053330*	104					
13047	053331*	015	000012			.ASCII	<15><12>
13048	053334*	020040	020040	051040	BTMSG2::	.ASCII	/ REMOTE BOOT ENABLED WITH ROM/
13049	053342*	046505	052117	020105			
13050	053350*	047502	052117	042440			
13051	053356*	040516	046102	042105			
13052	053364*	053440	052111	020110			
13053	053372*	047522	115				
13054	053375*	015	000012			.ASCII	<15><12>
13055	053400*	020040	020040	051040	BTMSG3::	.ASCII	/ REMOTE BOOT AND POWER UP BOOT BOTH ENABLED/
13056	053406*	046505	052117	020105			
13057	053414*	047502	052117	040440			
13058	053422*	042116	050040	053517			
13059	053430*	051105	052440	020120			
13060	053436*	047502	052117	041040			
13061	053444*	052117	020110	047105			
13062	053452*	041101	042514	104			
13063	053457*	015	000012			.ASCII	<15><12>
13064						.EVEN	

PARAMETER CODING MACY11 30(1046)
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TEST 46: PRINT DEVICE PARAMETERS TEST

13065
13066
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13082
13083
13084
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13108

.TITLE PARAMETER CODING

.SBTTL HARDWARE PARAMETER CODING SECTION

..*+
: THE HARDWARE PARAMETER CODING SECTION CONTAINS MACROS
: THAT ARE USED BY THE SUPERVISOR TO BUILD P-TABLES. THE
: MACROS ARE NOT EXECUTED AS MACHINE INSTRUCTIONS BUT ARE
: INTERPRETED BY THE SUPERVISOR AS DATA STRUCTURES. THE
: MACROS ALLOW THE SUPERVISOR TO ESTABLISH COMMUNICATIONS
: WITH THE OPERATOR.
:--

BGNHRD

053462'
053462' 000010
053464'
053464'
053464' 000021
053466' 053504'
053470' 160000
053472' 177776
053474'
053474' 001021
053476' 053537'
053500' 000000
053502' 000776
053504'

GPRMA ASKCSR,0,0,160000,177776,NO

.WORD L10171-L\$HARD/2
L\$HARD: :
:FIRST P-TABLE QUESTION

.WORD T\$CODE
.WORD ASKCSR
.WORD T\$LOLIM
.WORD T\$HILIM
.WORD T\$CODE
.WORD ASKVEC
.WORD T\$LOLIM
.WORD T\$HILIM

GPRMA ASKVEC,2,0,0,776,NO

:SECOND P-TABLE QUESTION

ENDHRD

.EVEN
L10171:

ASKCSR: .ASCIZ /WHAT IS THE PCSRO ADDRESS?/

ASKVEC: .ASCIZ /WHAT IS THE VECTOR ADDRESS?/

.EVEN

PARAMETER CODING
CZUAAA.P1: 11-JAN-83 09:29 MACY11 30(1046) 11-JAN-83 10:13 PAGE 271
HARDWARE PARAMETER CODING SECTION

13109 U53574* 000100
13110
13111
13112 000000*
13113 000000*
13114 000000*
13115 000000*
13116 000000*
13117 000000*
13118 000000*
13119 000000*
13120 000000*
13121 000001

\$PATCH: .BLKW 100

.CSECT MICRA
.CSECT MICRB
.CSECT MICRC
.CSECT MICRD
.CSECT MICRE
.CSECT MICRF
.CSECT MICRG
.CSECT NOMORE
ENDMOD

.END

PARAMETER CODING MACY11 30(1046) 11-JAN-83 10:13 PAGE 275
CZUAAA.P11 1-JAN-83 09:29 CROSS REFERENCE TABLE -- USER SYMBOLS

C\$CLOS= 000035	1389#													
C\$CLP1= 000006	1389#	4948	5051	5197	5247									
C\$CVEC= 000036	1389#	4951	5007	5054	5161	5200	5208	5250	5262	6986				
C\$DCLN= 000044	1389#	4758	4956	5059	5205	5255								
C\$DODU= 000051	1389#	4954	5057	5203	5253									
C\$DRPI= 000024	1389#													
C\$DU = 000053	1389#	4824												
C\$EDIT= 000003	1389#	1447												
C\$ERDF= 000055	1389#	4943	5046	5192	5242									
C\$ERHR= 000056	1389#	4977	4994	5080	5097	5114	5131	5148	5303	5332	5354	5385	5409	
	5434	5453	5501	5541	5557	5610	5632	5657	5674	5711	5728	5750	5763	
	6184	6204	6250	6270	6290	6381	6401	6424	6444	6485	6502	6519	6611	
	6631	6658	6678	6706	6716	6785	6793	6813	6834	6841	6861	6868	6932	
	6960	6976	6991	7062	7072	7108	7136	7148	7175	7243	7253	7275	7287	
	7308	7315	7389	7399	7422	7443	7452	7475	7482	7494	7554	7564	7592	
	7607	7616	7633	7646	7713	7723	7746	7760	7769	7788	7802	7863	7871	
	7893	7907	7916	7933	8001	8011	8032	8046	8053	8062	8075	8139	8149	
	8170	8184	8191	8200	8213	8287	8297	8320	8339	8348	8366	8376	8449	
	8459	8481	8500	8509	8527	8537	8602	8612	8643	8657	8666	8684	8694	
	8779	8789	8810	8828	8837	8858	8871	8880	8892	8901	8910	8920	9006	
	9016	9046	9060	9069	9092	9106	9175	9185	9215	9229	9238	9262	9276	
	9348	9358	9379	9394	9403	9416	9426	9513	9523	9556	9570	9579	9599	
	9613	9702	9712	9742	9756	9765	9780	9794	9865	9875	9905	9919	9928	
	9942	9956	10104	10114	10156	10170	10179	10196	10212	10226	10327	10337	10366	
	10389	10397	10408	10482	10492	10512	10528	10537	10551	10561	10642	10652	10678	
	10693	10702	10715	10731	10809	10819	10838	10860	10869	10883	10893	10981	10991	
	11017	11036	11045	11058	11068	11149	11159	11187	11206	11215	11231	11239	11249	
	11338	11348	11390	11404	11413	11427	11437	11546	11556	11589	11603	11612	11629	
	11644	11655	11669	11761	11771	11800	11814	11823	11837	11852	11863	11877	11997	
	12007	12041	12055	12064	12086	12097	12118	12132	12138	12158	12169	12190	12204	
	12213	12237	12247	12340	12350	12374	12388	12397	12411	12421	12441	12455	12464	
	12479	12492	12587	12597	12626	12640	12649	12662	12693	12754	12767	12788	12801	
	12832	12845	12877	12889										
C\$ERRO= 000060	1389#													
C\$ERSF= 000054	1389#													
C\$ERSO= 000057	1389#													
C\$ESCA= 000010	1389#	5308	5662	5716	5733	5755	6189	6255	6386	6429	6490	6616	6636	
	6663	6683	6818	6935	7051	7067	7077	7113	7233	7248	7258	7280	7292	
	7379	7394	7404	7427	7448	7457	7544	7559	7569	7597	7612	7621	7651	
	7703	7718	7728	7751	7765	7774	7807	7898	7912	7991	8006	8016	8037	
	8058	8067	8129	8144	8154	8175	8196	8205	8277	8292	8302	8325	8344	
	8353	8439	8454	8464	8486	8505	8514	8592	8607	8617	8648	8662	8671	
	8769	8784	8794	8815	8833	8842	8996	9011	9021	9051	9065	9074	9111	
	9165	9180	9190	9220	9234	9243	9281	9338	9353	9363	9384	9399	9408	
	9503	9518	9528	9561	9575	9584	9618	9692	9707	9717	9747	9761	9770	
	9799	9855	9870	9880	9910	9924	9933	9961	10094	10109	10119	10161	10175	
	10184	10317	10332	10342	10371	10394	10472	10487	10497	10517	10533	10542	10632	
	10647	10657	10683	10698	10707	10736	10799	10814	10824	10843	10865	10874	10971	
	10986	10996	11022	11041	11050	11139	11154	11164	11192	11211	11220	11328	11343	
	11353	11395	11409	11418	11536	11551	11561	11594	11608	11617	11674	11751	11766	
	11776	11805	11819	11828	11987	12002	12012	12046	12060	12069	12123	12143	12195	
	12209	12218	12330	12345	12355	12379	12393	12402	12446	12460	12469	12484	12577	
	12592	12602	12631	12645	12654	12667	12698	12738	12759	12772	12793	12806	12837	
	12850	12882	12894											
C\$ESEG= 000005	1389#	4984	5001	5087	5104	5121	5138	5155	5259	5339	5361	5392	5416	
	5441	5460	5508	5548	5564	5681	6194	6211	6260	6277	6391	6408	6434	

PARAMETER COD.NG MACY11 30(1046) 11-JAN-83 10:13 PAGE 280
CZUAAA.P11 11-JAN-83 09:29 CROSS REFERENCE TABLE -- USER SYMBOLS

6940	6965	6984	6997	6999	7035	7051	7067	7077	7089	7113	7156	7183
7188	7190	7217	7233	7248	7258	7264	7280	7292	7323	7324	7326	7363
7379	7394	7404	7410	7427	7448	7457	7502	7503	7505	7528	7544	7559
7569	7575	7597	7612	7621	7640	7651	7656	7658	7687	7703	7718	7728
7734	7751	7765	7774	7796	7807	7815	7817	7840	7880	7898	7912	7946
7947	7949	7975	7991	8006	8016	8022	8037	8058	8067	8083	8084	8086
8113	8129	8144	8151	8160	8175	8196	8205	8221	8222	8224	8261	8277
8292	8302	8308	8325	8344	8353	8384	8385	8387	8423	8439	8454	8464
8470	8486	8505	8514	8545	8546	8548	8576	8592	8607	8617	8623	8648
8662	8671	8702	8705	8707	8753	8769	8784	8794	8800	8815	8833	8842
8928	8929	8931	8980	8996	9011	9021	9027	9051	9065	9074	9100	9111
9121	9123	9149	9165	9180	9190	9196	9220	9234	9243	9270	9281	9291
9293	9322	9338	9353	9363	9369	9384	9399	9408	9434	9435	9437	9487
9503	9518	9528	9534	9561	9575	9584	9607	9618	9630	9632	9676	9692
9707	9717	9723	9747	9761	9770	9788	9799	9813	9815	9839	9855	9870
9880	9886	9910	9924	9933	9950	9961	9974	9976	10078	10094	10109	10119
10125	10161	10175	10184	10234	10235	10237	10301	10317	10332	10342	10348	10371
10394	10416	10424	10426	10456	10472	10487	10497	10503	10517	10533	10542	10569
10570	10572	10616	10632	10647	10657	10663	10683	10698	10707	10723	10736	10742
10749	10751	10783	10799	10814	10824	10830	10843	10865	10874	10901	10902	10904
10955	10971	10986	10996	11002	11022	11041	11050	11077	11083	11085	11123	11139
11154	11164	11170	11192	11211	11220	11257	11264	11266	11312	11328	11343	11353
11359	11395	11409	11418	11458	11466	11468	11520	11536	11551	11561	11567	11594
11608	11617	11663	11674	11686	11688	11735	11751	11766	11776	11782	11805	11819
11828	11871	11883	11885	11971	11987	12002	12012	12018	12046	12060	12069	12105
12123	12143	12179	12195	12209	12218	12255	12256	12258	12314	12330	12345	12355
12361	12379	12393	12402	12429	12446	12460	12469	12484	12500	12507	12509	12561
12577	12592	12602	12608	12631	12645	12654	12667	12698	12706	12707	12709	12728
12732	12738	12759	12772	12793	12806	12837	12850	12882	12894	12956	12958	13096
13121												
1389#	13081	13094										
1389#	1555	1563										
1389#	4677	4761										
1389#	4646	4800	4820	4831	12732							
1389#	1393	13121										
1389#	2888	2897	2901	2912	2916	2933	2937	2945	2949	2957	2961	2973
2977	2992	2996	3008	3012	3020	3024	3041	3045	3067	3071	3079	3083
3091	3095	3104	3108	3116	3120	3141	3145	3154	3158	3174	3178	3188
3192	3203	3207	3231	3235	3259	3263	3273	3277	3302	3306	3329	3333
3356	3360	3391	3395	3403	3407	3441	3445	3489	3493	3508	3512	3528
3532	3548	3552	3576	3580	3595	3599	3636	3640	3663	3667	3676	3680
3696	3700	3723	3727	3735	3739	3747	3751	3759	3763	3771	3775	3798
3802	3818	3822	3860	3864	3872	3876	3911	3915	3935	3939	3947	
1389#	4661	4668										
1389#												
1389#	4644	4649										
1389#	4968	4983	4986	5000	5071	5086	5089	5103	5106	5120	5123	5137
5140	5154	5235	5258	5322	5338	5348	5360	5374	5391	5405	5415	5424
5440	5443	5459	5493	5507	5535	5547	5551	5563	5666	5680	6171	6193
6196	6210	6230	6259	6262	6276	6348	6390	6393	6407	6410	6433	6436
6450	6462	6494	6497	6508	6583	6620	6623	6640	6643	6667	6670	6687
6693	6722	6769	6800	6809	6874	6912	6938	6944	6982	7042	7087	7104
7154	7160	7181	7224	7262	7271	7321	7370	7408	7418	7500	7535	7573
7588	7638	7694	7732	7742	7794	7847	7878	7888	7944	7982	8020	8028
8081	8120	8158	8166	8219	8268	8306	8316	8382	8430	8468	8477	8543
8583	8621	8635	8700	8760	8798	8806	8926	8987	9025	9038	9098	9156

F\$HARD= 000004
 F\$HW = 000013
 F\$INIT= 000006
 F\$JMP = 000050
 F\$MOD = 000000
 F\$MSG = 000011

 F\$PROT= 000021
 F\$PWR = 000017
 F\$RPT = 000012
 F\$SEG = 000003

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IE = 000100 G		1660#	4202	6921	6922	7118	7120							
IER = 020000 G		1641#												
INERR = 000003		1673#	3446	6846	7462	8359	8520	8677	9413	9777	9939	10190	10548	10712
		10880	11055	11626	11834									
INITH = 177777 G		1661#	3987											
INITL = 177777 G		1662#	3988											
INMON = 000001		1671#	4388	6772	7045	7227	7373	7538	7697	7850	7985	8123	8271	8433
		8586	8763	8990	9159	9332	9497	9686	9849	10088	10311	10466	10626	10793
		10965	11133	11322	11530	11745	11981	12324	12571					
INTBIT 002251RG	002	2105#	7064	7074	7110	7138	7150	7177						
INTE = 000100 G		1687#	5603	5607										
INTR = 000200 G		1686#	5326											
INTST = 000002		1672#	6712											
INTVEC 002214RG	002	2100#	6934	6962	6978	6993								
LOADR = 020000 G		1657#	1658											
IOSIZ = 020000 G		1653#	1658											
ISR = 000100 G		1634#												
IXE = 004000 G		1639#												
ISAU = 000041		1389#	4829#	4836#										
ISAUTO= 000041		1389#	4771#	4775#										
ISCLN = 000041		1389#	4783#	4800	4815#									
ISDU = 000041		1389#	4818#	4825#										
ISHRD = 000041		13081#	13096#											
ISINIT= 000041		1389#	4677#	4763#										
ISMOD = 000041		1389#	1393#	13121#										
ISMSG = 000041		1389#	2888#	2899#	2901#	2914#	2916#	2935#	2937#	2947#	2949#	2959#	2961#	2975#
		2977#	2994#	2996#	3010#	3012#	3022#	3024#	3043#	3045#	3069#	3071#	3081#	3083#
		3093#	3095#	3106#	3108#	3118#	3120#	3143#	3145#	3156#	3158#	3176#	3178#	3190#
		3192#	3205#	3207#	3233#	3235#	3261#	3263#	3275#	3277#	3304#	3306#	3331#	3333#
		3358#	3360#	3393#	3395#	3405#	3407#	3443#	3445#	3491#	3493#	3510#	3512#	3530#
		532#	3550#	3552#	3578#	3580#	3597#	3599#	3638#	3640#	3665#	3667#	3678#	3680#
		3698#	3700#	3725#	3727#	3737#	3739#	3749#	3751#	3761#	3763#	3773#	3775#	3800#
		3802#	3820#	3822#	3862#	3864#	3874#	3876#	3913#	3915#	3937#	3939#	3949#	
ISPROT= 000040		1389#	4661#											
ISPTAB= 000041		1389#												
ISPR = 000041		1389#												
ISRPT = 000041		1389#	4644#	4651#										
ISSFG = 000041		1389#	4923	4932	4962	4968#	4985#	4986#	5002#	5026	5035	5065	5071#	5088#
		5089#	5105#	5106#	5122#	5123#	5139#	5140#	5156#	5178	5226	5235#	5260#	5285
		5290	5316	5322#	5340#	5348#	5362#	5374#	5393#	5403#	5417#	5424#	5442#	5443#
		5461#	5487	5493#	5509#	5531	5535#	5549#	5551#	5565#	5594	5596	5619	5641
		5666#	5682#	5707	6163	6165	6171#	6195#	6196#	6212#	6216	6230#	6261#	6262#
		6278#	6339	6342	6348#	6392#	6393#	6409#	6410#	6435#	6436#	6452#	6456	6462#
		6496#	6497#	6510#	6578	6583#	6622#	6623#	6642#	6643#	6669#	6670#	6689#	6693#
		6724#	6762	6769#	6802#	6809#	6876#	6906	6912#	6940#	6944#	6984#	7035	7042#
		7089#	7104#	7156#	7160#	7183#	7217	7224#	7264#	7271#	7323#	7363	7370#	7410#
		7418#	7502#	7528	7535#	7575#	7588#	7640#	7687	7694#	7734#	7742#	7796#	7840
		7847#	7880#	7888#	7946#	7975	7982#	8022#	8028#	8083#	8113	8120#	8160#	8166#
		8221#	8261	8268#	8308#	8316#	8384#	8423	8430#	8470#	8477#	8545#	8576	8583#
		8623#	8635#	8702#	8753	8760#	8800#	8806#	8928#	8980	8987#	9027#	9038#	9100#
		9149	9156#	9196#	9207#	9270#	9322	9329#	9369#	9375#	9434#	9487	9494#	9534#
		9549#	9607#	9676	9683#	9723#	9734#	9788#	9839	9846#	9880#	9901#	9950#	10078
		10085#	10125#	10147#	10234#	10301	10308#	10348#	10358#	10416#	10456	10463#	10503#	10508#
		10569#	10616	10623#	10663#	10674#	10723#	10727#	10742#	10783	10790#	10830#	10834#	10901#
		10955	10962#	11002#	11010#	11077#	11123	11130#	11170#	11183#	11257#	11312	11319#	11359#
		11370#	11380#	11458#	11520	11527#	11567#	11576#	11663#	11735	11742#	11782#	11787#	11871#

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LPMSG1	053205RG	002	13012	13030#		
LPTBL	053100RG	002	12905	13011#		
LSACP	000110RG	002	1477#			
LSAPT	000036RG	002	1435#			
LSAU	021640RG	002	1462	4829#		
LSAUT	000070RG	002	1461#			
LSAUTO	021470RG	002	1478	4771#		
LSCCP	000106RG	002	1475#			
LSCLEA	021472RG	002	1476	4783#		
LSCO	000032RG	002	1431#			
LSDEPO	000011RG	002	1413#			
LSDESC	000704RG	002	1468	1936#		
LSDESP	000076RG	002	1457#			
LSDEVP	000060RG	002	1453#			
LSDISP	000124RG	002	1438	1497#		
LSDLY	000116RG	002	1483#			
LSDTP	000040RG	002	1437#			
LSDTYP	000034RG	002	1433#			
LSDU	021632RG	002	1464	4818#		
LSDUT	000072RG	002	1463#			
LSDVTY	000676RG	002	1454	1928#		
LSEF	000052RG	002	1448#			
LSENV1	000044RG	002	1441#			
LSETP	000102RG	002	1471#			
LSEXP1	000046RG	002	1443#			
LSEXP4	000064RG	002	1457#			
LSEXP5	000066RG	002	1459#			
LSHARD	053464RG	002	1420	13081	13082#	
LSHIME	000120RG	002	1485#			
LSHPCP	000016RG	002	1419#			
LSHPTP	000022RG	002	1423#			
LSHW	000262RG	002	1424	1555	1556#	
LSICP	000104RG	002	1473#			
LSINIT	021124RG	002	1474	4677#		
LSLADP	000026RG	002	1427#			
LSLAST=	***** GX		1428			
LSLOAD	000100RG	002	1469#			
LSLUN	000074RG	002	1465#			
LSMREV	000050RG	002	1445#			
LSNAME	000000RG	002	1402#			
LSPRIO	000042RG	002	1439#			
LSPROT	021116RG	002	1480	4661#		
LSPRT	000112RG	002	1479#			
LSREPP	000062RG	002	1455#			
LSREV	000010RG	002	1411#			
LSRPT	021110RG	002	1456	4644#		
LSSPC	000056RG	002	1451#			
LSPLP	000020RG	002	1421#			
LSPTP	000024RG	002	1425#			
LSSTA	000030RG	002	1429#			
LSTEST	000114RG	002	1481#			
LSTML	000014RG	002	1417#			
LSUNIT	000012RG	002	1415#	4737		
L10000	000266R	002	1555	1563#		
L10001	012572R	002	2897#			
L10002	012630R	002	2912#			

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L10003	012702R	002	2933#	
L10004	012724R	002	2945#	
L10005	012746R	002	2957#	
L10006	013010R	002	2973#	
L10007	013054R	002	2992#	
L10010	013116R	002	3008#	
L10011	013140R	002	3020#	
L10012	013210R	002	3041#	
L10013	013276R	002	3067#	
L10014	013320R	002	3079#	
L10015	013342R	002	3091#	
L10016	013370R	002	3104#	
L10017	013412R	002	3116#	
L10020	013476R	002	3141#	
L10021	013522R	002	3154#	
L10022	013570R	002	3174#	
L10023	013616R	002	3188#	
L10024	013646R	002	3203#	
L10025	013740R	002	3231#	
L10026	014032R	002	3259#	
L10027	014062R	002	3273#	
L10030	014156R	002	3302#	
L10031	014246R	002	3329#	
L10032	014336R	002	3356#	
L10033	014452R	002	3391#	
L10034	014474R	002	3403#	
L10035	014624R	002	3441#	
L10036	015012R	002	3489#	
L10037	015056R	002	3508#	
L10040	015124R	002	3528#	
L10041	015172R	002	3548#	
L10042	015274R	002	3576#	
L10043	015342R	002	3595#	
L10044	015506R	002	3636#	
L10045	015604R	002	3663#	
L10046	015632R	002	3676#	
L10047	015702R	002	3696#	
L10050	016000R	002	3723#	
L10051	016022R	002	3735#	
L10052	016044R	002	3747#	
L10053	016066R	002	3759#	
L10054	016110R	002	3771#	
L10055	016206R	002	3798#	
L10056	016260R	002	3818#	
L10057	016430R	002	3860#	
L10060	016452R	002	3872#	
L10061	016614R	002	3911#	
L10062	016676R	002	3935#	
L10063	016720R	002	3947#	
L10064	021114R	002	4647	4649#
L10066	021466R	002	4761#	
L10067	021470R	002	4773#	
L10070	021630R	002	4801	4813#
L10071	021636R	002	4821	4823#
L10072	021644R	002	4832	4834#
L10073	021654R	002	4855#	

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RSETER	001717RG	002	2064#	5305	5334	5356	5387	5411	5436	5455							
RSIDP	= 000022 G		1759#														
RTM	= 000014 G		1753#														
RUN	= 000003 G		1727#														
RXI	= 020000 G		1680#	4318	4320	4321											
SECOND	= 000077 G		1645#	4386	4459	4579	4607	4788	5296	5647	5718	6175	6240	6372	6415		
			6477	6602	6649	6697	6777	6828	6923	7053	7117	7235	7381	7437	7546		
			7601	7705	7754	7855	7901	7993	8040	8131	8178	8279	8333	8441	8494		
			8594	8651	8771	8822	8998	9054	9167	9223	9340	9388	9505	9564	9694		
			9750	9857	9913	10096	10164	10319	10374	10474	10519	10634	10687	10801	10854		
			10973	11030	11141	11200	11330	11398	11538	11597	11753	11808	11989	12049	12126		
			12198	12332	12382	12449	12579	12634	12744	12778	12822	12867					
			1678#	4304	4306	4307											
SERI	= 1C0000 G		1646#														
SET	= 000001 G		1719#	5737	5740	6711											
SFT	= 037400 G		1713#	6712													
SFTB0	= 000400 G		1714#														
SFTB1	= 001000 G		1715#	6712													
SFTB2	= 002000 G		1716#														
SFTB3	= 004000 G		1717#	6712													
SFTB4	= 010000 G		1718#														
SFTB5	= 020000 G		1648#	1649	6232	6282	9539	9624	9626	9730	9807	9809	9893	9968	9970		
SI21K	= 004000 G		10141														
SI22K	= 010000 G		1649#	1650													
SI24K	= 020000 G		1650#	1651	1652	1653											
SI28K	= 040000 G		1651#	1654	1655												
SLFT	= 000003 G		1702#	5719													
SLFTST	002021RG	002	2077#	5713	5730	5752	5765										
SMSG00	024404RG	002	5778	5843#													
SMSG01	024432RG	002	5779	5847#													
SMSG02	024457RG	002	5780	5851#													
SMSG03	024470RG	002	5781	5853#													
SMSG04	024525RG	002	5782	5858#													
SMSG05	024552RG	002	5783	5862#													
SMSG06	024613RG	002	5784	5868#													
SMSG07	024644RG	002	5785	5873#													
SMSG10	024660RG	002	5786	5875#													
SMSG11	024706RG	002	5787	5879#													
SMSG12	024721RG	002	5788	5881#													
SMSG13	024734RG	002	5789	5883#													
SMSG14	024746RG	002	5790	5885#													
SMSG15	024760RG	002	5791	5887#													
SMSG16	024772RG	002	5792	5889#													
SMSG17	025004RG	002	5793	5891#													
SMSG20	025016RG	002	5794	5893#													
SMSG21	025037RG	002	5795	5896#													
SMSG22	025051RG	002	5796	5898#													
SMSG23	025063RG	002	5797	5900#													
SMSG24	025075RG	002	5798	5902#													
SMSG25	025107RG	002	5799	5904#													
SMSG26	025121RG	002	5800	5906#													
SMSG27	025133RG	002	5801	5908#													
SMSG30	025145RG	002	5802	5910#													
SMSG31	025220RG	002	5803	5918#													
SMSG32	025270RG	002	5804	5925#													
SMSG33	025341RG	002	5805	5932#													

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7293	7308	7309	7310	7311	7315	7316	7317	7318	7322	7325	7370	7379
7380	7389	7390	7391	7392	7394	7395	7399	7400	7401	7402	7404	7405
7409	7418	7422	7423	7424	7425	7427	7428	7443	7444	7445	7446	7448
7449	7452	7453	7454	7455	7457	7458	7475	7476	7477	7478	7482	7483
7484	7485	7494	7495	7496	7497	7501	7504	7535	7544	7545	7554	7555
7556	7557	7559	7560	7564	7565	7566	7567	7569	7570	7574	7588	7592
7593	7594	7595	7597	7598	7607	7608	7609	7610	7612	7613	7616	7617
7618	7619	7621	7622	7633	7634	7635	7636	7639	7646	7647	7648	7649
7651	7652	7657	7694	7703	7704	7713	7714	7715	7716	7718	7719	7723
7724	7725	7726	7728	7729	7733	7742	7746	7747	7748	7749	7751	7752
7760	7761	7762	7763	7765	7766	7769	7770	7771	7772	7774	7775	7788
7789	7790	7791	7795	7802	7803	7804	7805	7807	7808	7816	7847	7863
7864	7865	7866	7871	7872	7873	7874	7879	7888	7893	7894	7895	7896
7898	7899	7907	7908	7909	7910	7912	7913	7916	7917	7918	7919	7933
7934	7935	7936	7945	7948	7982	7991	7992	8001	8002	8003	8004	8006
8007	8011	8012	8013	8014	8016	8017	8021	8028	8032	8033	8034	8035
8037	8038	8046	8047	8048	8049	8053	8054	8055	8056	8058	8059	8062
8063	8064	8065	8067	8068	8075	8076	8077	8078	8082	8085	8120	8129
8130	8139	8140	8141	8142	8144	8145	8149	8150	8151	8152	8154	8155
8159	8166	8170	8171	8172	8173	8175	8176	8184	8185	8186	8187	8191
8192	8193	8194	8196	8197	8200	8201	8202	8203	8205	8206	8213	8214
8215	8216	8220	8223	8268	8277	8278	8287	8288	8289	8290	8292	8293
8297	8298	8299	8300	8302	8303	8307	8316	8320	8321	8322	8323	8325
8326	8339	8340	8341	8342	8344	8345	8348	8349	8350	8351	8353	8354
8366	8367	8368	8369	8376	8377	8378	8379	8383	8386	8430	8439	8440
8449	8450	8451	8452	8454	8455	8459	8460	8461	8462	8464	8465	8469
8477	8481	8482	8483	8484	8486	8487	8500	8501	8502	8503	8505	8506
8509	8510	8511	8512	8514	8515	8527	8528	8529	8530	8537	8538	8539
8540	8544	8547	8583	8592	8593	8602	8603	8604	8605	8607	8608	8612
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CROSS REFERENCE TABLE -- USER SYMBOLS

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			12934	12935	12936	12937	12938	12940	12941	12942	12943	12944	12945	12947	12948
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			6456												
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SWPACK	052760RG	002	12864	12899	12907	12965#									
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TIMON	017276RG	002	4199#	4240	4387	7121									

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CROSS REFERENCE TABLE -- USER SYMBOLS

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T\$LOI I= 000000
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 T&SEGL = 177777

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CROSS REFERENCE TABLE -- USER SYMBOLS

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11396	11409#	11410	11418#	11419	11443#	11456#	11466#	11536#	11537	11551#	11552	11561#
11562	11565#	11594#	11595	11608#	11609	11617#	11618	11661#	11674#	11675	11686#	11751#
11752	11766#	11767	11776#	11777	11780#	11805#	11806	11819#	11820	11828#	11829	11869#
11883#	11987#	11988	12002#	12003	12012#	12013	12016#	12046#	12047	12060#	12061	12069#
12070	12103#	12123#	12124	12143#	12144	12177#	12195#	12196	12209#	12210	12218#	12219
12253#	12256#	12330#	12331	12345#	12346	12355#	12356	12359#	12379#	12380	12393#	12394
12402#	12403	12427#	12446#	12447	12460#	12461	12469#	12470	12484#	12485	12498#	12507#
12577#	12578	12592#	12593	12602#	12603	12606#	12631#	12632	12645#	12646	12654#	12655
12667#	12668	12698#	12699	12704#	12707#	12732#	12733	12738#	12739	12759#	12760	12772#
12773	12793#	12794	12806#	12807	12837#	12838	12850#	12851	12882#	12883	12894#	12895
12956#	13084#	13089#	13094#	13121#								
1389#	4923#	4932	4962	5026#	5035	5065	5178#	5226#	5285#	5290	5316	5487#
5531#	5594#	5596	5619	5641	5707#	6163#	6165	6216	6339#	6342	6456	6578#
6762#	6906#	7035#	7217#	7363#	7528#	7687#	7840#	7975#	8113#	8261#	8423#	8576#
8753#	8980#	9149#	9322#	9487#	9676#	9839#	10078#	10301#	10456#	10616#	10783#	10955#
11123#	11312#	11520#	11735#	11971#	12314#	12561#	12728#					
1389#	2894	2898	2909	2913	2922	2930	2934	2942	2946	2954	2958	2970
2974	2982	2989	2993	3005	3009	3017	3021	3029	3038	3042	3050	3057

TEST= 000056

TESTM= 177777

3064	3068	3076	3080	3088	3092	3101	3105	3113	3117	3125	3131	3138
3142	3151	3155	3164	3171	3175	3185	3189	3200	3204	3213	3220	3228
3232	3241	3248	3256	3260	3270	3274	3283	3291	3299	3303	3311	3318
3326	3330	3338	3345	3353	3357	3367	3377	3387	3392	3400	3404	3412
3418	3428	3435	3442	3452	3459	3465	3472	3479	34	3490	3498	3505
3509	3517	3525	3529	3537	3545	3549	3559	3566	351	3577	3585	3592
3596	3604	3611	3618	3625	3633	3637	3645	3653	3660	3664	3673	3677
3686	3693	3697	3706	3712	3720	3724	3732	3736	3744	3748	3756	3760
3768	3772	3780	3787	3795	3799	3807	3815	3819	3828	3835	3842	3849
3857	3861	3869	3873	3883	3891	3899	3907	3912	3920	3926	3932	3936
3944	3948	4201	4215	4244	4357	4391	4475	4485	4507	4573	4588	4601
4616	4629	4650	4683	4688	4693	4698	4703	4709	4717	4731	4741	4758
4762	4774	4795	4800	4814	4824	4835	4929	4933	4943	4948	4951	4954
4956	4960	4963	4968	4977	4984	4986	4994	5001	5004	5007	5010	5032
5036	5046	5051	5054	5057	5059	5063	5066	5071	5080	5087	5089	5097
5104	5106	5114	5121	5123	5131	5138	5140	5148	5155	5158	5161	5164
5184	5192	5197	5200	5203	5205	5208	5212	5232	5235	5242	5247	5250
5253	5255	5259	5262	5265	5291	5303	5308	5313	5317	5322	5332	5339
5348	5354	5361	5374	5385	5392	5403	5409	5416	5424	5434	5441	5443
5453	5460	5463	5466	5493	5501	5508	5513	5535	5541	5548	5551	5557
5564	5567	5597	5610	5617	5620	5632	5639	5642	5657	5662	5666	5674
5681	5684	5687	5711	5716	5728	5733	5750	5755	5763	5770	6166	6171
6184	6189	6194	6196	6204	6211	6214	6217	6230	6250	6255	6260	6262
6270	6277	6290	6297	6300	6343	6348	6381	6386	6391	6393	6401	6408
6410	6424	6429	6434	6436	6444	6451	6454	6457	6462	6485	6490	6495
6497	6502	6509	6519	6528	6539	6583	6611	6616	6621	6623	6631	6636
6641	6643	6658	6663	6668	6670	6678	6683	6688	6693	6706	6716	6723
6732	6769	6785	6793	6801	6809	6813	6818	6834	6841	6861	6868	6875
6878	6912	6918	6932	6939	6944	6951	6960	6965	6976	6983	6986	6991
6998	7042	7051	7062	7067	7072	7077	7084	7088	7104	7108	7113	7124
7136	7148	7155	7160	7175	7182	7189	7224	7233	7243	7248	7253	7258
7263	7271	7275	7280	7287	7292	7308	7315	7322	7325	7370	7379	7389
7394	7399	7404	7409	7418	7422	7427	7443	7448	7452	7457	7475	7482
7494	7501	7504	7535	7544	7554	7559	7564	7569	7574	7588	7592	7597
7607	7612	7616	7621	7633	7639	7646	7651	7657	7694	7703	7713	7718
7723	7728	7733	7742	7746	7751	7760	7765	7769	7774	7788	7795	7802
7807	7816	7847	7863	7871	7879	7888	7893	7898	7907	7912	7916	7933
7945	7948	7982	7991	8001	8006	8011	8016	8021	8028	8032	8037	8046
8053	8058	8062	8067	8075	8082	8085	8120	8129	8139	8144	8149	8154
8159	8166	8170	8175	8184	8191	8196	8200	8205	8213	8220	8223	8268
8277	8287	8292	8297	8302	8307	8316	8320	8325	8339	8344	8348	8353
8366	8376	8383	8386	8430	8439	8449	8454	8459	8464	8469	8477	8481
8486	8500	8505	8509	8514	8527	8537	8544	8547	8583	8592	8602	8607
8612	8617	8622	8635	8643	8648	8657	8662	8666	8671	8684	8694	8701
8706	8760	8769	8779	8784	8789	8794	8799	8806	8810	8815	8828	8833
8837	8842	8858	8871	8880	8892	8901	8910	8920	8927	8930	8987	8996
9006	9011	9016	9021	9026	9038	9046	9051	9060	9065	9069	9074	9092
9099	9106	9111	9122	9156	9165	9175	9180	9185	9190	9195	9207	9215
9220	9229	9234	9238	9243	9262	9269	9276	9281	9292	9329	9338	9348
9353	9358	9363	9368	9375	9379	9384	9394	9399	9403	9408	9416	9426
9433	9436	9494	9503	9513	9518	9523	9528	9533	9549	9556	9561	9570
9575	9579	9584	9599	9606	9613	9618	9631	9683	9692	9702	9707	9712
9717	9722	9734	9742	9747	9756	9761	9765	9770	9780	9787	9794	9799
9814	9846	9855	9865	9870	9875	9880	9885	9901	9905	9910	9919	9924
9928	9933	9942	9949	9956	9961	9975	10085	10094	10104	10109	10114	10119
10124	10147	10156	10161	10170	10175	10179	10184	10196	10212	10226	10233	10236

PARAMETER CODING
(ZUAAA.P11 11-JAN-83

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P
C

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10408	10415	10425	10463	10472	10482	10487	10492	10497	10502	10508	10512	10517	
10528	10533	10537	10542	10551	10561	10568	10571	10623	10632	10642	10647	10652	
10657	10662	10674	10678	10683	10693	10698	10702	10707	10715	10722	10727	10731	
10736	10741	10750	10790	10799	10809	10814	10819	10824	10829	10834	10838	10843	
10860	10865	10869	10874	10883	10893	10900	10903	10962	10971	10981	10986	10991	
10996	11001	11010	11017	11022	11036	11041	11045	11050	11058	11068	11076	11084	
11130	11139	11149	11154	11159	11164	11169	11183	11187	11192	11206	11211	11215	
11220	11231	11239	11249	11256	11265	11319	11328	11338	11343	11348	11353	11358	
11370	11380	11390	11395	11404	11409	11413	11418	11427	11437	11444	11457	11467	
11527	11536	11546	11551	11556	11561	11566	11576	11589	11594	11603	11608	11612	
11617	11629	11644	11655	11662	11669	11674	11687	11742	11751	11761	11766	11771	
11776	11781	11787	11800	11805	11814	11819	11823	11828	11837	11852	11863	11870	
11877	11884	11978	11987	11997	12002	12007	12012	12017	12033	12041	12046	12055	
12060	12064	12069	12086	12097	12104	12109	12118	12123	12132	12138	12143	12158	
12169	12178	12183	12190	12195	12204	12209	12213	12218	12237	12247	12254	12257	
12321	12330	12340	12345	12350	12355	12360	12367	12374	12379	12388	12393	12397	
12402	12411	12421	12428	12434	12441	12446	12455	12460	12464	12469	12479	12484	
12492	12499	12508	12568	12577	12587	12592	12597	12602	12607	12622	12626	12631	
12640	12645	12649	12654	12662	12667	12693	12698	12705	12708	12732	12738	12754	
12759	12767	12772	12788	12793	12801	12806	12832	12837	12845	12850	12877	12882	
12889	12894	12923	12930	12937	12944	12951	12957						
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	6763#	6907#	7036#	7218#	7364#	7529#	7668#	7841#	7976#	8114#	9262#	8424#	8577#
	8754#	8981#	9150#	9323#	9488#	9677#	9840#	10079#	10302#	10457#	10617#	10784#	10956#
	11124#	11313#	11521#	11736#	11972#	12315#	12562#	12729#					
TSSAU = 010072	4829#	4831	4834										
TSSAUT= 010067	4771#	4773											
TSSCLE= 010070	4783#	4800	4813										
TSSDU = 010071	4818#	4820	4823										
TSSHAR= 010171	13081#	13095											
TSSHW = 010000	1555#	1563											
TSSINI= 010066	4677#	4761											
TSSMSG= 010063	2888#	2897	2901#	2912	2916#	2933	2937#	2945	2949#	2957	2961#	2973	2977#
	2992	2996#	3008	3012#	3020	3024#	3041	3045#	3067	3071#	3079	3083#	3091
	3095#	3104	3108#	3116	3120#	3141	3145#	3154	3158#	3174	3178#	3188	3192#
	3203	3207#	3231	3235#	3259	3263#	3273	3277#	3302	3306#	3329	3333#	3356
	3360#	3391	3395#	3403	3407#	344	3445#	3489	3493#	3508	3512#	3528	3532#
	3548	3552#	3576	3580#	3595	3599#	3636	3640#	3663	3667#	3676	3680#	3696
	3700#	3723	3727#	3735	3739#	3747	3751#	3759	3763#	3771	3775#	3798	3802#
	3818	3822#	3860	3864#	3872	3876#	3911	3915#	3935	3939#	3947		
TSSPRO= 010065	4661#												
TSSRPT= 010064	4644#	4646	4649										
TSSSEG= 010001	4968#	4983#	4986#	5000#	5071#	5086#	5089#	5103#	5106#	5120#	5123#	5137#	5140#
	5154#	5235#	5258#	5322#	5338#	5348#	5360#	5374#	5391#	5403#	5415#	5424#	5440#
	5443#	5459#	5493#	5507#	5535#	5547#	5551#	5563#	5666#	5680#	6171#	6193#	6196#
	6210#	6230#	6259#	6262#	6276#	6348#	6390#	6393#	6407#	6410#	6433#	6436#	6450#
	6462#	6494#	6497#	6508#	6583#	6620#	6623#	6640#	6643#	6667#	6670#	6687#	6693#
	6722#	6769#	6800#	6809#	6874#	6912#	6938#	6944#	6982#	7042#	7087#	7104#	7154#
	7160#	7181#	7224#	7262#	7271#	7321#	7370#	7408#	7418#	7500#	7535#	7573#	7588#
	7638#	7694#	7732#	7742#	7794#	7847#	7878#	7888#	7944#	7982#	8020#	8028#	8081#
	8120#	8158#	8166#	8219#	8268#	8306#	8316#	8382#	8430#	8468#	8477#	8543#	8583#
	8621#	8635#	8700#	8760#	8798#	8806#	8926#	8987#	9025#	9038#	9098#	9156#	9194#
	9207#	9268#	9329#	9367#	9375#	9432#	9494#	9532#	9549#	9605#	9683#	9721#	9734#
	9786#	9846#	9884#	9901#	9948#	10085#	10123#	10147#	10232#	10308#	10346#	10358#	10414#
	10463#	10501#	10508#	10567#	10623#	10661#	10674#	10721#	10727#	10740#	10790#	10828#	10834#

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10899#	10962#	11000#	11010#	11075#	11130#	11168#	11183#	11255#	11319#	11357#	11370#	11380#
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12103#	12109#	12177#	12183#	12253#	12321#	12359#	12367#	12427#	12434#	12498#	12568#	12606#
12622#	12704#											
4852#	4855	4871#	4875	4897#	4903							
4933#	4959	4963#	5003	5036#	5062	5066#	5157	5291#	5312	5317#	5462	5597#
5616	5620#	5638	5642#	5683	6166#	6213	6217#	6296	6343#	6453	6457#	6527
4924#	5009	5027#	5163	5179#	5211	5227#	5264	5286#	5308	5465	5488#	5512
5532#	5566	5595#	5662	5686	5708#	5716	5733	5755	5769	6164#	6189	6255
6299	6340#	6386	6429	6490	6538	6579#	6616	6636	6663	6683	6731	6763#
6818	6877	6907#	6965	6997	7036#	7051	7067	7077	7113	7188	7218#	7233
7248	7258	7280	7292	7324	7364#	7379	7394	7404	7427	7448	7457	7503
7529#	7544	7559	7569	7597	7612	7621	7651	7656	7688#	7703	7718	7728
7751	7765	7774	7807	7815	7841#	7898	7912	7947	7976#	7991	8006	8016
8037	8058	8067	8084	8114#	8129	8144	8154	8175	8196	8205	8222	8262#
8277	8292	8302	8325	8344	8353	8385	8424#	8439	8454	8464	8486	8505
8514	8546	8577#	8592	8607	8617	8648	8662	8671	8705	8754#	8769	8784
8794	8815	8833	8842	8929	8981#	8996	9011	9021	9051	9065	9074	9111
9121	9150#	9165	9180	9190	9220	9234	9243	9281	9291	9323#	9338	9353
9363	9384	9399	9408	9435	9488#	9503	9518	9528	9561	9575	9584	9618
9630	9677#	9692	9707	9717	9747	9761	9770	9799	9813	9840#	9855	9870
9880	9910	9924	9933	9961	9974	10079#	10094	10109	10119	10161	10175	10184
10235	10302#	10317	10332	10342	10371	10394	10424	10457#	10472	10487	10497	10517
10533	10542	10570	10617#	10632	10647	10657	10683	10698	10707	10736	10749	10784#
10799	10814	10824	10843	10865	10874	10902	10956#	10971	10986	10996	11022	11041
11050	11083	11124#	11139	11154	11164	11192	11211	11220	11264	11313#	11328	11343
11353	11395	11409	11418	11466	11521#	11536	11551	11561	11594	11608	11617	11674
11686	11736#	11751	11766	11776	11805	11819	11828	11883	11972#	11987	12002	12012
12046	12060	12069	12123	12143	12195	12209	12218	12256	12315#	12330	12345	12355
12379	12393	12402	12446	12460	12469	12484	12507	12562#	12577	12592	12602	12631
12645	12654	12667	12698	12707	12729#	12732	12738	12759	12772	12793	12806	12837
12850	12882	12894	12956									

T1	021710RG	002	1498	
T1.1	021736R	002	4932#	
T1.2	022014R	002	4962#	
T10	027332RG	002	1507	6163#
T10.1	027332R	002	6165#	
T10.2	027464R	002	6216#	
T11	027746RG	002	1508	6339#
T11.1	027752R	002	6342#	6535
T11.2	030304R	002	6456#	
T12	030534RG	002	1509	6578#
T13	031214RG	002	1510	6762#
T14	031632RG	002	1511	6906#
T15	032122RG	002	1512	7035#
T16	032630RG	002	1513	7217#
T17	033146RG	002	1514	7363#
T18	033614RG	002	1515	7528#
T19	034172RG	002	1516	7687#
T2	022122RG	002	1499	5026#
T2.1	022150R	002	5035#	
T2.2	022230R	002	5065#	
T20	034552RG	002	1517	7840#
T21	035100RG	002	1518	7975#
T22	035410RG	002	1519	8113#
T23	035720RG	002	1520	8261#

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 CZUAAA.P11 11-JAN-83 09:29 CROSS REFERENCE TABLE -- USER SYMBOLS

WSIDP = 000023 G		1760#														
WTM = 000015 G		1754#														
XPWR = 100000 G		1705#	5447													
X\$ALWA= 000000		1389#														
X\$FALS= 000040		1389#														
X\$OFFS= 000400		1389#														
X\$TRUE= 000020		1389#														
\$ADRER 001457RG	002	2030#	6854	7470												
\$AFTER 001340RG	002	2012#	4465	5287	5399	5654	5725	6181	6247	6378	6421	6482	6608	6655		
		6703	6782	6929	7059	7240	7386	7551	7710	7860	7998	8136	8284	8446		
		8599	8775	9003	9172	9345	9510	9699	9862	10101	10324	10479	10639	10806		
		10978	11146	11335	11543	11758	11994	12337	12584	12751	12785	12829	12874			
\$BEFOR 001331RG	002	2010#														
\$BIT0 001267RG	002	1811	1846	2001#												
\$BIT1 001261RG	002	1812	1847	1999#												
\$BIT10 001172RG	002	1856	1981#													
\$BIT11 001163RG	002	1857	1979#													
\$BIT12 001154RG	002	1858	1977#													
\$BIT13 001145RG	002	1859	1975#	8869	8890											
\$BIT14 001136RG	002	1860	1973#	8899												
\$BIT15 001127RG	002	1861	1971#	8878	8908											
\$BIT2 001253RG	002	1813	1848	1997#												
\$BIT3 001245RG	002	1814	1849	1995#												
\$BIT4 001237RG	002	1815	1832	1850	1993#											
\$BIT5 001231RG	002	1833	1851	1991#												
\$BIT6 001223RG	002	1834	1852	1989#												
\$BIT7 001215RG	002	1853	1987#													
\$BIT8 001207RG	002	1854	1985#													
\$BIT9 001201RG	002	1820	1855	1983#												
\$CLR 001323RG	002	2008#														
\$DATER 001444RG	002	2028#	6850	7466												
\$DNI 000776RG	002	1822	1951#	4337	4463	5300	5652	5723	6179	6245	6376	6419	6480	6606		
		6653	6701	6780	6927	7057	7238	7384	7549	7708	7853	7996	8134	8282		
		8444	8597	8774	9001	9170	9343	9508	9697	9860	10099	10322	10477	10637		
		10804	10976	11144	11333	11541	11756	11992	12335	12582	12749	12783	12827	12872		
\$FAT1 001017RG	002	1819	1955#	4351												
\$GT CMD 001355RG	002	2015#	6248	6422	6483	6656	6704	6783	7060	7241	7387	7552	7711	7861		
		7999	8137	8285	8447	8600	8777	9004	9173	9346	9511	9700	9863	10102		
		10325	10480	10640	10807	10979	11147	11336	11544	11759	11995	12338	12585	12786		
		12830	12875													
\$GT PCB 001346RG	002	2013#	6182	6379	6609	12752										
\$ICAB 001074RG	002	1842	1965#	5432												
\$INTE 001041RG	002	1817	1959#													
\$INTR 001030RG	002	1818	1957#	5329												
\$NCLR 001311RG	002	2006#	4972	5075	5345	5398	5431	5450								
\$NOP 001403RG	002	2020#	5655	5930												
\$NSE1 001275RG	002	2003#	4464	5301	5330	5653	5724	6180	6246	6377	6420	6481	6607	6654		
		6702	6781	6928	7058	7099	7239	7385	7550	7709	7859	7997	8135	8283		
		8445	8598	8775	9002	9171	9344	9509	9698	9861	10100	10323	10478	10638		
		10805	10977	11145	11334	11542	11757	11993	12336	12583	12750	12784	12828	12873		
\$PARER 001500RG	002	2033#	6856													
\$PATCH 053574RG	002	13109#														
\$FCEI 000745RG	002	1825	1945#	4316												
\$PCTO 001105RG	002	1835	1967#													
\$PDNDM 001415RG	002	2023#														
\$RCEI 001006RG	002	1821	1953#	4344												

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\$RESET	001436RG	002	2027#																							
\$RMTC	001116RG	002	1831	1969#																						
\$RSET	001052RG	002	1816	1961#																						
\$RXI	000756RG	002	1824	1947#	4323																					
\$SERI	000734RG	002	1826	1943#	4309																					
\$SET	001305RG	002	2005#																							
\$SLFT	001371RG	002	2018#	5726																						
\$STOP	001431RG	002	2026#																							
\$STRT	001407RG	002	2021#																							
\$TXI	000766RG	002	1823	1949#	4330																					
\$XPWR	001063RG	002	1843	1963#	5451																					
.	= 000000R	012	1903#	4647	4801	4810#	4821	4832	5309	5665	5717	5734	5756	6121#	6190											
			6256	6387	6430	6491	6617	6637	6664	6684	6819	6966	7052	7068	7078											
			7114	7234	7249	7259	7281	7293	7380	7395	7405	7428	7449	7458	7545											
			7560	7570	7598	7613	7622	7652	7704	7719	7729	7752	7766	7775	7808											
			7899	7913	7992	8007	8017	8038	8059	8068	8130	8145	8155	8176	8197											
			8206	8278	8293	8303	8326	8345	8354	8440	8455	8465	8487	8506	8515											
			8593	8608	8618	8649	8663	8672	8770	8785	8795	8816	8834	8843	8997											
			9012	9022	9052	9066	9075	9112	9166	9181	9191	9221	9235	9244	9282											
			9339	9354	9364	9385	9400	9409	9504	9519	9529	9562	9576	9585	9619											
			9693	9708	9718	9748	9762	9771	9800	9856	9871	9881	9911	9925	9934											
			9962	10095	10110	10120	10162	10176	10185	10318	10333	10343	10372	10395	10473											
			10488	10498	10518	10534	10543	10633	10648	10658	10684	10699	10708	10737	10800											
			10815	10825	10844	10866	10875	10972	10987	10997	11023	11042	11051	11140	11155											
			11165	11193	11212	11221	11329	11344	11354	11396	11410	11419	11537	11552	11562											
			11595	11609	11618	11675	11752	11767	11777	11806	11820	11829	11988	12003	12013											
			12047	12061	12070	12124	12144	12196	12210	12219	12331	12346	12356	12380	12394											
			12403	12447	12461	12470	12485	12578	12593	12603	12632	12646	12655	12668	12699											
			12733	12739	12760	12773	12794	12807	12838	12851	12883	12895	13007#	13108#	13109#											

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BCOMPL	1#	1389#	4684	4689	4711										
BERROR	1#	1389#													
BGNAU	1#	1389#	4828												
BGNAUT	1#	1389#	4770												
BGNCLN	1#	1389#	4782												
BGNDU	1#	1389#	4817												
BGNHRD	1#	1389#	13080												
BGNHW	1#	1389#	1554												
BGNINI	1#	1389#	4676												
BGNMOD	1#	1389#	1392												
BGNMSG	1#	1389#	2887	2900	2915	2936	2948	2960	2976	2995	3011	3023	3044	3070	3082
	3094	3107	3119	3144	3157	3177	3191	3206	3234	3262	3276	3305	3332	3359	3394
	3406	3444	3492	3511	3531	3551	3579	3598	3639	3666	3679	3699	3726	3738	3750
	3762	3774	3801	3821	3863	3875	3914	3938							
BGNPRO	1#	1389#	4660												
BGNPTA	1#	1389#													
BGNRPT	1#	1389#	4643												
BGNSEG	1#	1389#	4967	4985	5070	5088	5105	5122	5139	5234	5321	5347	5373	5402	5423
	5442	5492	5534	5550	5665	6170	6195	6229	6261	6347	6392	6409	6435	6461	6496
	6582	6622	6642	6669	6692	6768	6808	6911	6943	7041	7103	7159	7223	7270	7369
	7417	7534	7587	7693	7741	7846	7887	7981	8027	8119	8165	8267	8315	8429	8476
	8582	8634	8759	8805	8986	9037	9155	9206	9328	9374	9493	9548	9682	9733	9845
	9900	10084	10146	10307	10357	10462	10507	10622	10673	10726	10789	10833	10961	11009	11129
	11182	11318	11369	11379	11526	11575	11741	11786	11977	12032	12108	12182	12320	12366	12433
	12567	12621													
BGNSET	1#	1389#													
BGNSFT	1#	1389#													
BGNSRV	1#	1389#	4851	4870	4996										
BGNSUB	1#	1389#	4931	4961	5034	5064	5289	5315	5595	5618	5640	6164	6215	6341	6455
BGNSW	1#	1389#													
BGNTST	1#	1389#	4922	5025	5177	5225	5284	5486	5530	5593	5706	6162	5338	6577	6761
	6905	7034	7216	7362	7527	7686	7839	7974	8112	8260	8422	8575	8752	8979	9148
	9321	9486	9675	9838	10077	10300	10455	10615	10782	10954	11122	11311	11519	11734	11970
	12313	12560	12727												
BNCOMP	1#	1389#	4694	4699	4743										
BNERRO	1#	1389#													
BREAK	1#	1389#	4243	4390											
BRESET	1#	1389#													
CKLOOP	1#	1389#	4947	5050	5196	5246									
CLOCK	1#	1389#	4707												
CLOSE	1#	1389#													
CLRVEC	1#	1389#	4949	5005	5052	5159	5198	5206	5248	5260	6984				
COMMEN	1#	1389#													
DELAY	1#	1389#													
DESCRI	1#	1389#	1935												
DEVTYP	1#	1389#	1927												
DISPAT	1#	1389#	1495												
DISPLA	1#	1389#													
DOCLN	1#	1389#	4757	4955	5058	5204	5254								
DODU	1#	1389#	4952	5055	5201	5251									
DORPT	1#	1389#													
ENDAU	1#	1389#	4833												
ENDAUT	1#	1389#	4772												
ENDCLN	1#	1389#	4812												
ENDCOM	1#	1389#													
ENDDU	1#	1389#	4822												

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ENDHRD	1#	1389#	13093																	
ENDPW	1#	1389#	1562																	
END.NI	1#	1389#	4760																	
ENDMOD	1#	1389#	13120																	
ENDMSG	1#	1389#	2890	2911	2932	2944	2956	2972	2991	3007	3019	3040	3066	3078	3090					
		3103	3115	3140	3153	3173	3187	3202	3230	3258	3272	3301	3328	3355	3390	3402				
		3440	3488	3507	3527	3547	3575	3594	3635	3662	3675	3695	3722	3734	3746	3758				
		3770	3797	3817	3859	3871	3910	3934	3946											
ENDPRO	1#	1389#	4667																	
ENDPTA	1#	1389#																		
ENDRPT	1#	1389#	4648																	
ENDSEG	1#	1389#	4982	4999	5085	5102	5119	5136	5153	5257	5337	5359	5390	5414	5439					
		5458	5506	5546	5562	5679	6109	6209	6258	6275	6389	6406	6432	6449	6493	6507				
		6619	6639	6666	6686	6721	6799	6873	6937	6981	7086	7153	7180	7261	7320	7334				
		7499	7572	7637	7731	7793	7877	7943	8019	8080	8157	8218	8305	8381	8467	8517				
		8620	8699	8797	8925	9024	9097	9193	9267	9366	9431	9531	9604	9720	9785	9855				
		9947	10122	10231	10345	10413	10500	10566	10660	10720	10739	10827	10898	10999	11074	11167				
		11254	11356	11442	11455	11564	11660	11779	11868	12015	12102	12176	12252	12358	12426	12497				
		12605	12703																	
ENDSET	1#	1389#																		
ENDSFT	1#	1389#																		
ENDSRV	1#	1389#	4854	4874	4902															
ENDSUB	1#	1389#	4958	5002	5061	5156	5311	5461	5615	5637	5682	6212	6295	6452	6526					
ENDSW	1#	1389#																		
ENDTST	1#	1389#	5008	5162	5210	5263	5464	5511	5565	5685	5768	6298	6537	6730	6876					
		6996	7187	7323	7502	7655	7814	7946	8083	8221	8384	8545	8704	8928	9120	9290				
		9434	9629	9812	9973	10234	10423	10569	10748	10901	1082	11263	11465	11685	11882	12255				
		12506	12706	12955																
EQUALS	1#	1389#	1575																	
ERRDF	1#	1389#	4942	5045	5191	5241														
ERRHRD	1#	1389#	4976	4993	5079	5096	5113	5130	5147	5302	5331	5353	5384	5408	5433					
		5452	5500	5540	5556	5609	5631	5656	5673	5710	5727	5749	5762	6183	6203	6249				
		6269	6289	6380	6400	6423	6443	6484	6501	6518	6610	6630	6657	6677	6705	6715				
		6784	6792	6812	6833	6840	6860	6867	6931	6959	6975	6990	7061	7071	7107	7135				
		7147	7174	7242	7252	7274	7286	7307	7314	7388	7398	7421	7442	7451	7474	7481				
		7493	7553	7563	7591	7606	7615	7632	7645	771	7722	7745	7759	7768	7787	7801				
		7862	7870	7892	7906	7915	7932	8000	8010	8071	8045	8052	8061	8074	8138	8148				
		8169	8183	8190	8199	8212	8286	8296	8319	87	8347	8365	8375	8448	8458	8480				
		8499	8508	8526	8536	8601	8611	8642	8656	8755	8683	8673	8778	8788	8809	8827				
		8836	8857	8870	8879	8891	8900	8909	8919	9015	9015	9045	9059	9068	9091	9105				
		9174	9184	9214	9228	9237	9261	9275	9347	9357	9378	9393	9402	9415	9425	9512				
		9522	9555	9569	9578	9598	9612	9701	9711	9741	9755	9764	9779	9793	9864	9874				
		9904	9918	9927	9941	9955	10103	10113	10155	10169	10178	10195	10211	10225	10326	10336				
		10365	10388	10396	10407	10481	10491	10511	10527	10536	10550	10560	10641	10651	10677	10692				
		10701	10714	10730	10808	10818	10837	10859	10868	10882	10892	10980	10990	11016	11035	11044				
		11057	11067	11148	11158	11186	11205	11214	11230	11238	11248	11337	11347	11389	11403	11412				
		11426	11436	11545	11555	11588	11602	11611	11628	11643	11654	11668	11760	11770	11799	11813				
		11822	11836	11851	11862	11876	11996	12006	12040	12054	12063	12085	12096	12117	12131	12137				
		12157	12168	12189	12203	12212	12236	12246	12339	12349	12373	12387	12396	12410	12420	12440				
		12454	12463	12478	12491	12586	12596	12625	12639	12648	12661	12692	12753	12766	12787	12800				
		12831	12844	12876	12888															
ERROR	1#	1389#																		
ERRSF	1#	1389#																		
ERRSOF	1#	1389#																		
ERRIBL	1#	1389#																		
ESCAPE	1#	1389#	5307	5661	5715	5732	5754	6188	6254	6385	6428	6489	6615	6635	6662					

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6682	6817	6964	7050	7066	7076	7112	7232	7247	7257	7279	7291	7378	7393	7403	
7426	7447	7456	7543	7558	7568	7596	7611	7620	7650	7702	7717	7727	7750	7764	
7773	7806	7897	7911	7990	8005	8015	8036	8057	8066	8128	8143	8153	8174	8195	
8204	8276	8291	8301	8324	8343	8352	8438	8453	8463	8485	8504	8513	8591	8606	
8616	8647	8661	8670	8768	8783	8793	8814	8832	8841	8995	9010	9020	9050	9064	
9073	9110	9164	9179	9189	9219	9233	9242	9280	9337	9352	9362	9383	9398	9407	
9502	9517	9527	9560	9574	9583	9617	9691	9706	9716	9746	9760	9769	9798	9854	
9869	9879	9909	9923	9932	9960	10093	10108	10118	10160	10174	10183	10316	10331	10341	
10370	10393	10471	10486	10496	10516	10532	10541	10631	10646	10656	10622	10697	10706	10735	
10798	10813	10823	10842	10864	10873	10970	10985	10995	11021	11040	11040	11138	11153	11163	
11191	11210	11219	11327	11342	11352	11394	11408	11417	11535	11550	11560	11593	11607	11616	
11673	11750	11765	11775	11804	11818	11827	11986	12001	12011	12045	12059	12068	12122	12142	
12194	12208	12217	12329	12344	12354	12378	12392	12401	12445	12459	12468	12483	12576	12591	
12601	12630	12644	12653	12666	12697	12737	12758	12771	12792	12805	12836	12849	12881	12893	
EXIT	1#	1389#	4645	4799	4819	4830	12731								
FEQUAL	1#	1389#													
GETBYT	1#	1389#													
GETPRI	1#	1389#													
GETWOR	1#	1389#													
GMANIA	1#	1389#													
GMANID	1#	1389#													
GMANIL	1#	1389#													
GPHARD	1#	1389#	4739												
GPRMA	1#	1389#	13083	13088											
GPRMD	1#	1389#													
GPRML	1#	1389#													
HEADER	1#	1389#	1401												
LOOP	1#	1389#													
IOSETU	1#	1389#													
IOSTAR	1#	1389#													
KT11	1#	1389#													
LASTAD	1#	1389#													
MANUAL	1#	1389#													
MEMORY	1#	1389#	4702												
MSBYTE	1#	1389#	1402#	1409	1410										
MSCHEC	1#	1389#	4646#	4800#	4820#	4831#	12732#								
MSCNTO	1#	1389#	13084#	13059#											
MSCOUN	1#	1389#	2890#	2903#	2918#	2925#	2939#	2951#	2963#	2979#	2985#	2998#	3014#	3020#	3032#
	3047#	3053#	3060#	3073#	3085#	3097#	3110#	3122#	3128#	3134#	3147#	3160#	3167#	3180#	3194#
	3209#	3216#	3223#	3237#	3244#	3251#	3265#	3279#	3286#	3294#	3308#	3314#	3321#	3335#	3341#
	3348#	3363#	3371#	3381#	3397#	3409#	3415#	3424#	3431#	3449#	3456#	3462#	3468#	3475#	3482#
	3495#	3501#	3514#	3520#	3534#	3540#	3554#	3562#	3569#	3582#	3588#	3601#	3607#	3614#	3621#
	3628#	3648#	3648#	3656#	3669#	3682#	3689#	3702#	3709#	3715#	3729#	3741#	3753#	3765#	3777#
	3783#	3799#	3804#	3810#	3824#	3831#	3838#	3845#	3852#	3866#	3880#	3888#	3896#	3904#	3917#
	3923#	3929#	3941#	4353#	4468#	4482#	4503#	4569#	4584#	4597#	4612#	4625#	4714#	4792#	12919#
MSDATA	1#	1389#	12926#	12933#	12940#	12947#									
	1435	1437	1439	1441#	1443	1445	1448	1451	1453	1455	1457	1459	1461	1463	1465
	1467	1469	1471	1473	1475	1477	1479	1481	1483	1485	1928#	1936#			
MSDECR	1#	1389#	1563#	2897#	2912#	2933#	2945#	2957#	2973#	2992#	3008#	3020#	3041#	3067#	3079#
	3091#	3104#	3116#	3141#	3154#	3174#	3188#	3203#	3231#	3259#	3273#	3302#	3329#	3356#	3391#
	3403#	3441#	3489#	3508#	3528#	3548#	3576#	3595#	3636#	3663#	3676#	3696#	3723#	3735#	3747#
	3759#	3771#	3798#	3818#	3860#	3872#	3911#	3935#	3947#	4649#	4668#	4761#	4773#	4813#	4823#
	4834#	4855#	4875#	4903#	4959#	4983#	5000#	5003#	5009#	5062#	5086#	5103#	5120#	5137#	5154#
	5157#	5163#	5211#	5258#	5264#	5312#	5338#	5360#	5391#	5415#	5440#	5459#	5462#	5465#	5507#
	5512#	5547#	5563#	5566#	5616#	5638#	5680#	5683#	5686#	5769#	6193#	6210#	6213#	6259#	6276#

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	6296#	6299#	6390#	6407#	6433#	6450#	6453#	6494#	6508#	6527#	6538#	6620#	6640#	6667#	6687#
	6722#	6731#	6800#	6874#	6877#	6938#	6982#	6997#	7087#	7154#	7181#	7188#	7262#	7321#	7324#
	7408#	7500#	7503#	7573#	7638#	7656#	7732#	7794#	7815#	7878#	7944#	7947#	8020#	8081#	8084#
	8158#	8219#	8222#	8306#	8382#	8385#	8468#	8543#	8546#	8621#	8700#	8705#	8798#	8926#	8929#
	9025#	9098#	9121#	9194#	9268#	9291#	9367#	9432#	9435#	9532#	9605#	9630#	9721#	9786#	9813#
	9884#	9948#	9974#	10123#	10232#	10235#	10346#	10414#	10424#	10501#	10567#	10570#	10661#	10721#	10740#
	10749#	10828#	10899#	10902#	11000#	11075#	11083#	11168#	11255#	11264#	11357#	11443#	11456#	11466#	11565#
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PARAMETER CODING MACY11 30(1046) 11-JAN-83 10:13 PAGE 314
 CZUAAA.P11 11-JAN-83 09:29 CROSS REFERENCE TABLE -- MACRO NAMES

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PARAMETER CODING MACY11 30(1046)
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CROSS REFERENCE TABLE -- MACRO NAMES

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F C

M O P P

F F P R R S S S X X X

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PARAMETER CODING MACY11 30(1046) 11-JAN-83 10:13 PAGE 317
CZUAAA.P11 11-JAN-83 09:29 CROSS REFERENCE TABLE -- MACRO NAMES

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PARAMETER CODING
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CROSS REFERENCE TABLE -- MACRO NAMES

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PARAMETER CODING
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	4835	4925#	4929	4932#	4933	4943	4948#	4950#	4951	4953#	4954	4956#	4959#	4960	4962#
	4963	4968#	4977	4983#	4984	4986#	4994	5000#	5001	5003#	5004	5006#	5007	5009#	5010
	5028#	5032	5035#	5036	5046	5051#	5053#	5054	5056#	5057	5059#	5062#	5063	5065#	5066
	5071#	5080	5086#	5087	5089#	5097	5103#	5104	5106#	5114	5120#	5121	5123#	5131	5137#
	5138	5140#	5148	5154#	5155	5157#	5158	5160#	5161	5163#	5164	5180#	5184	5192	5197#
	5199#	5200	5202#	5203	5205#	5207#	5208	5211#	5212	5228#	5232	5235#	5242	5247#	5249#
	5250	5252#	5253	5255#	5258#	5259	5261#	5262	5264#	5265	5290#	5291	5303	5308#	5312#
	5313	5316#	5317	5322#	5332	5338#	5339	5348#	5354	5360#	5361	5374#	5385	5391#	5392
	5403#	5409	5415#	5416	5424#	5434	5440#	5441	5443#	5453	5459#	5460	5462#	5463	5465#
	5466	5493#	5501	5507#	5508	5512#	5513	5535#	5541	5547#	5548	5551#	5557	5563#	5564
	5566#	5567	5596#	5597	5610	5616#	5617	5619#	5620	5632	5638#	5639	5641#	5642	5657
	5662#	5666#	5674	5680#	5681	5683#	5684	5686#	5687	5711	5716#	5728	5733#	5750	5755#
	5763	5769#	5770	6165#	6166	6171#	6184	6189#	6193#	6194	6196#	6204	6210#	6211	6213#
	6214	6216#	6217	6230#	6250	6255#	6259#	6260	6262#	6270	6276#	6277	6290	6296#	6297

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 CZUAAA.P11 11-JAN-83 09:29 CROSS REFERENCE TABLE -- MACRO NAMES

6299#	6300	6342#	6343	6348#	6381	6386#	6390#	6391	6393#	6401	6407#	6408	6410#	6424
6429#	6433#	6434	6436#	6444	6450#	6451	6453#	6454	6456#	6457	6462#	6485	6490#	6494#
6495	6497#	6502	6508#	6509	6519	6527#	6528	6538#	6539	6583#	6611	6616#	6620#	6621
6623#	6631	6636#	6640#	6641	6643#	6658	6663#	6667#	6668	6670#	6678	6683#	6687#	6688
6693#	6706	6716	6722#	6723	6731#	6732	6769#	6785	6793	6800#	6801	6809#	6813	6818#
6834	6841	6861	6868	6874#	6875	6877#	6878	6912#	6914#	6918	6932	6938#	6939	6944#
6950#	6951	6960	6965#	6976	6982#	6983	6985#	6986	6991	6997#	6998	7042#	7051#	7062
7067#	7072	7077#	7080#	7084	7087#	7088	7104#	7108	7113#	7123#	7124	7136	7148	7154#
7155	7160#	7175	7181#	7182	7138#	7189	7224#	7233#	7243	7248#	7253	7258#	7262#	7263
7271#	7275	7280#	7287	7292#	7308	7315	7321#	7322	7324#	7325	7370#	7379#	7389	7394#
7399	7404#	7408#	7409	7418#	7422	7427#	7443	7448#	7452	7457#	7475	7482	7494	7500#
7501	7503#	7504	7535#	7544#	7554	7559#	7564	7569#	7573#	7574	7588#	7592	7597#	7607
7612#	7616	7621#	7633	7638#	7659	7646	7651#	7656#	7657	7694#	7703#	7713	7718#	7723
7728#	7732#	7733	7742#	7746	7751#	7760	7765#	7769	7774#	7738	7794#	7795	7802	7807#
7815#	7816	7847#	7863	7871	7878#	7879	7888#	7893	7898#	7907	7912#	7916	7933	7944#
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8268#	8277#	8287	8292#	8297	8302#	8306#	8307	8316#	8320	8325#	8339	8344#	8348	8353#
8366	8376	8382#	8383	8385#	8386	8430#	8439#	8449	8454#	8459	8464#	8468#	8469	8477#
8481	8486#	8500	8505#	8509	8514	8527	8537	8543#	8544	8546#	8547	8583#	8592#	8602
8607#	8612	8617#	8621#	8622	8635#	8643	8648#	8657	8662#	8666	8671#	8684	8694	8700#
8701	8705#	8706	8760#	8769#	8779	8784#	8789	8794#	8798#	8799	8806#	8810	8815#	8828
8833#	8837	8842#	8858	8871	8880	8892	8901	8910	8920	8926#	8927	8929#	8930	8987#
8996#	9006	9011#	9016	9021#	9025#	9026	9038#	9046	9051#	9060	9065#	9069	9074#	9092
9098#	9099	9106	9111#	9121#	9122	9156#	9165#	9175	9180#	9185	9190#	9194#	9195	9207#
9215	9220#	9229	9234#	9238	9243#	9262	9268#	9269	9276	9281#	9291#	9292	9329#	9338#
9348	9353#	9358	9363#	9367#	9368	9375#	9379	9384#	9394	9399#	9407	9408#	9416	9426
9432#	9433	9435#	9436	9494#	9503#	9513	9518#	9523	9528#	9532#	9537	9549#	9556	9561#
9570	9575#	9579	9584#	9599	9605#	9606	9613	9618#	9630#	9631	9683#	9692#	9702	9707#
9712	9717#	9721#	9722	9734#	9742	9747#	9756	9761#	9765	9770#	9780	9786#	9787	9794
9799#	9813#	9814	9846#	9855#	9865	9870#	9875	9880#	9884#	9885	9901#	9905	9910#	9919
9924#	9928	9933#	9942	9948#	9949	9956	9961#	9974#	9975	10085#	10094#	10104	10109#	10114
10119#	10123#	10124	10147#	10156	10161#	10170	10175#	10179	10184#	10196	10212	10226	10232#	10233
10235#	10236	10308#	10317#	10327	10332#	10337	10342#	10346#	10347	10358#	10366	10371#	10389	10394#
10397	10408	10414#	10415	10424#	10425	10463#	10472#	10482	10487#	10492	10497#	10501#	10502	10508#
10512	10517#	10528	10533#	10537	10542#	10551	10561	10567#	10568	10570#	10571	10623#	10632#	10642
10647#	10652	10657#	10661#	10662	10674#	10678	10683#	10693	10698#	10702	10707#	10715	10721#	10722
10727#	10731	10736#	10740#	10741	10749#	10750	10790#	10799#	10809	10814#	10819	10824#	10828#	10829
10834#	10838	10843#	10860	10865#	10869	10874#	10883	10893	10899#	10900	10902#	10903	10962#	10971#
10981	10986#	10991	10996#	11000#	11001	11010#	11017	11022#	11036	11041#	11045	11050#	11058	11068
11075#	11076	11087#	11084	11130#	11139#	11147	11154#	11159	11164#	11168#	11169	11183#	11187	11192#
11206	11211#	11215	11220#	11231	11239	11249	11255#	11256	11264#	11265	11319#	11328#	11338	11343#
11348	11353#	11357#	11358	11370#	11380#	11390	11395#	11404	11409#	11413	11418#	11427	11437	11443#
11444	11456#	11457	11466#	11467	11527#	11536#	11546	11551#	11556	11561#	11565#	11566	11576#	11589
11594#	11603	11608#	11612	11617#	11629	11644	11655	11661#	11662	11669	11674#	11686#	11687	11742#
11751#	11761	11766#	11771	11776#	11780#	11781	11787#	11800	11805#	11814	11819#	11823	11828#	11837
11852	11863	11869#	11870	11877	11883#	11884	11978#	11987#	11997	12002#	12007	12012#	12016#	12017
12033#	12041	12046#	12055	12060#	12064	12069#	12086	12097	12103#	12104	12109#	12118	12123#	12132
12138	12143#	12158	12169	12177#	12178	12183#	12190	12195#	12204	12209#	12213	12218#	12237	12247
12253#	12254	12256#	12257	12321#	12330#	12340	12345#	12350	12355#	12359#	12360	12367#	12374	12379#
12388	12393#	12397	12402#	12411	12421	12427#	12428	12434#	12441	12446#	12455	12460#	12464	12469#
12479	12484#	12492	12498#	12499	12507#	12508	12568#	12577#	12587	12592#	12597	12602#	12606#	12607
12622#	12626	12631#	12640	12645#	12649	12654#	12662	12667#	12693	12698#	12704#	12705	12707#	12708
12732#	12738#	12754	12759#	12767	12772#	12788	12793#	12801	12806#	12832	12837#	12845	12850#	12877
12882#	12889	12894#	12919#	12923	12926#	12930	12933#	12937	12940#	12944	12947#	12951	12956#	12957

PARAMETER CODING MACY11 30(1046) 11-JAN-83 10:13 PAGE 325
C'UAAA.P11 11-JAN-83 09:29 CROSS REFERENCE TABLE -- MACRO NAMES

Table with 16 columns and multiple rows of macro names (e.g., MSLAB 1#, 2982#, 3080#, etc.)

PARAMETER CODING MACY11 30(1046) 11-JAN-83 10:13 PAGE 326
 (ZUAAA.P11 11-JAN-83 09:29 CROSS REFERENCE TABLE -- MACRO NAMES

11045#	11050#	11058#	11068#	11076#	11084#	11130#	11139#	11149#	11154#	11159#	11164#	11169#	11183#	11187#
11192#	11206#	11211#	11215#	11220#	11231#	11239#	11249#	11256#	11265#	11319#	11328#	11338#	11343#	11348#
11353#	11358#	11370#	11380#	11390#	11395#	11404#	11409#	11413#	11418#	11427#	11437#	11444#	11457#	11467#
11527#	11536#	11546#	11551#	11556#	11561#	11566#	11576#	11589#	11594#	11603#	11608#	11612#	11617#	11629#
11644#	11655#	11662#	11669#	11674#	11687#	11742#	11751#	11761#	11766#	11771#	11776#	11781#	11787#	11800#
11805#	11814#	11819#	11823#	11828#	11837#	11852#	11862#	11870#	11877#	11884#	11978#	11987#	11997#	12002#
12007#	12012#	12017#	12033#	12041#	12046#	12055#	12060#	12064#	12069#	12086#	12097#	12104#	12109#	12118#
12123#	12132#	12138#	12143#	12158#	12169#	12178#	12183#	12190#	12195#	12204#	12209#	12213#	12218#	12237#
12247#	12254#	12257#	12321#	12330#	12340#	12345#	12350#	12355#	12360#	12367#	12374#	12379#	12388#	12393#
12397#	12402#	12411#	12421#	12428#	12434#	12441#	12446#	12455#	12460#	12464#	12469#	12479#	12484#	12492#
12499#	12508#	12568#	12577#	12587#	12592#	12597#	12602#	12607#	12622#	12626#	12631#	12640#	12645#	12649#
12654#	12652#	12667#	12693#	12692	12705#	12708#	12732#	12738#	12754#	12759#	12767#	12772#	12788#	12793#
12801#	12806#	12832#	12837#	12842	12850#	12877#	12882#	12889#	12894#	12923#	12930#	12937#	12944#	12951#
12957#														

MSSTL

1#	1389#	2894#	2898#	2909#	2913#	2922#	2930#	2934#	2942#	2946#	2954#	2958#	2970#	2974#
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3080#	3088#	3092#	3101#	3105#	3113#	3117#	3125#	3131#	3138#	3142#	3151#	3155#	3164#	3171#
3175#	3185#	3189#	3200#	3204#	3213#	3220#	3228#	3232#	3241#	3248#	3256#	3260#	3270#	3274#
3283#	3291#	3299#	3303#	3311#	3318#	3326#	3330#	3338#	3345#	3353#	3357#	3367#	3377#	3387#
3392#	3400#	3404#	3412#	3418#	3428#	3435#	3442#	3452#	3459#	3465#	3472#	3479#	3486#	3490#
3498#	3505#	3509#	3517#	3525#	3529#	3537#	3545#	3549#	3559#	3566#	3573#	3577#	3585#	3592#
3596#	3604#	3611#	3618#	3625#	3633#	3637#	3645#	3653#	3660#	3664#	3673#	3677#	3686#	3693#
3697#	3706#	3712#	3720#	3724#	3732#	3736#	3744#	3748#	3756#	3760#	3768#	3772#	3780#	3787#
3795#	3799#	3807#	3815#	3819#	3828#	3835#	3842#	3849#	3857#	3861#	3869#	3873#	3883#	3891#
3899#	3907#	3912#	3920#	3926#	3932#	3936#	3944#	3948#	4201#	4215#	4244#	4357#	4391#	4475#
4485#	4507#	4573#	4588#	4601#	4616#	4629#	4650#	4683#	4688#	4693#	4698#	4703#	4709#	4717#
4731#	4741#	4758#	4762#	4774#	4795#	4800#	4814#	4824#	4835#	4929#	4933#	4943#	4948#	4951#
4954#	4956#	4960#	4963#	4968#	4977#	4984#	4986#	4994#	5001#	5004#	5007#	5010#	5032#	5036#
5046#	5051#	5054#	5057#	5059#	5063#	5066#	5071#	5080#	5087#	5089#	5097#	5104#	5106#	5114#
5121#	5123#	5131#	5138#	5140#	5148#	5155#	5158#	5161#	5164#	5184#	5192#	5197#	5200#	5203#
5205#	5208#	5212#	5232#	5235#	5242#	5247#	5250#	5253#	5255#	5259#	5262#	5265#	5291#	5303#
5308#	5313#	5317#	5322#	5332#	5339#	5348#	5354#	5361#	5374#	5385#	5392#	5403#	5409#	5416#
5424#	5434#	5441#	5443#	5453#	5460#	5463#	5466#	5493#	5501#	5508#	5513#	5535#	5541#	5548#
5551#	5557#	5564#	5567#	5597#	5610#	5617#	5620#	5632#	5639#	5642#	5657#	5662#	5666#	5674#
5681#	5684#	5687#	5711#	5716#	5728#	5733#	5750#	5755#	5763#	5770#	6166#	6171#	6184#	6189#
6194#	6196#	6204#	6211#	6214#	6217#	6230#	6250#	6255#	6260#	6262#	6270#	6277#	6290#	6297#
6300#	6343#	6348#	6381#	6386#	6391#	6393#	6401#	6408#	6410#	6424#	6429#	6434#	6436#	6444#
6451#	6454#	6457#	6462#	6485#	6490#	6495#	6497#	6502#	6509#	6519#	6528#	6539#	6583#	6611#
6616#	6621#	6623#	6631#	6636#	6641#	6643#	6658#	6663#	6668#	6670#	6678#	6683#	6688#	6693#
6706#	6716#	6723#	6732#	6769#	6785#	6793#	6801#	6809#	6813#	6818#	6834#	6841#	6861#	6868#
6875#	6878#	6912#	6918#	6932#	6939#	6944#	6951#	6960#	6965#	6976#	6983#	6986#	6991#	6998#
7042#	7051#	7062#	7067#	7072#	7077#	7084#	7088#	7104#	7108#	7113#	7124#	7136#	7148#	7155#
7160#	7175#	7182#	7189#	7224#	7233#	7243#	7248#	7253#	7258#	7263#	7271#	7275#	7280#	7287#
7292#	7308#	7315#	7322#	7325#	7370#	7379#	7389#	7394#	7399#	7404#	7409#	7418#	7422#	7427#
7443#	7448#	7452#	7457#	7475#	7482#	7494#	7501#	7504#	7535#	7544#	7554#	7559#	7564#	7569#
7574#	7588#	7592#	7597#	7607#	7612#	7616#	7621#	7633#	7639#	7646#	7651#	7657#	7694#	7703#
7713#	7718#	7723#	7728#	7733#	7742#	7746#	7751#	7760#	7765#	7769#	7774#	7788#	7795#	7802#
7807#	7816#	7847#	7863#	7871#	7879#	7888#	7893#	7898#	7907#	7912#	7916#	7933#	7945#	7948#
7982#	7991#	8001#	8006#	8011#	8016#	8021#	8028#	8032#	8037#	8046#	8053#	8058#	8062#	8067#
8075#	8082#	8085#	8120#	8129#	8139#	8144#	8149#	8154#	8159#	8166#	8170#	8175#	8184#	8191#
8196#	8200#	8205#	8213#	8220#	8223#	8228#	8277#	8287#	8292#	8297#	8302#	8307#	8316#	8320#
8325#	8339#	8344#	8348#	8353#	8366#	8376#	8383#	8386#	8430#	8439#	8449#	8454#	8459#	8464#
8469#	8477#	8481#	8486#	8500#	8505#	8509#	8514#	8527#	8537#	8544#	8547#	8583#	8592#	8602#
8607#	8612#	8617#	8622#	8635#	8643#	8648#	8657#	8662#	8666#	8671#	8684#	8694#	8701#	8706#
8760#	8769#	8773#	8784#	8789#	8794#	8799#	8806#	8810#	8815#	8828#	8833#	8837#	8842#	8858#
8871#	8880#	8892#	8901#	8910#	8920#	8927#	8930#	8987#	8996#	9006#	9011#	9016#	9021#	9026#

PARAMETER CODING MACY11 30(1046) 11-JAN-83 10:13 PAGE 327
CZUAAA.P11 11-JAN-83 09:29 CROSS REFERENCE TABLE -- MACRO NAMES

Table with 15 columns of macro names, ranging from 9038# to 12957#.

MSWORD 1# 1389# 1441# 1450 1496# 1498 1499 1500 1501 1502 1503 1504 1505 1506 1507
1508 1509 1510 1511 1512 1513 1514 1515 1516 1517 1518 1519 1520 1521 1522
1523 1524 1525 1526 1527 1528 1529 1530 1531 1532 1533 1534 1535 1536 1537
1538 1539 1540 1541 1542 1543 4646# 4800# 4820# 4831# 4943# 4944 4945 4946 4977#
4978 4979 4980 4994# 4995 4996 4997 5046# 5047 5048 5049 5080# 5081 5082 5083
5097# 5098 5099 5100 5114# 5115 5116 5117 5131# 5132 5133 5134 5148# 5149 5150
5151 5192# 5193 5194 5195 5242# 5243 5244 5245 5303# 5304 5305 5306 5332# 5333
5334 5335 5354# 5355 5356 5357 5385# 5386 5387 5388 5409# 5410 5411 5412 5434#
5435 5436 5437 5453# 5454 5455 5456 5501# 5502 5503 5504 5541# 5542 5543 5544
5557# 5558 5559 5550 5610# 5611 5612 5613 5632# 5633 5634 5635 5657# 5658 5659
5660 5674# 5675 5676 5677 5711# 5712 5713 5714 5728# 5729 5730 5731 5750# 5751
5752 5753 5763# 5764 5765 5766 6184# 6185 6186 6187 6204# 6205 6206 6207 6250#
6251 6252 6253 6270# 6271 6272 6273 6290# 6291 6292 6293 6381# 6382 6383 6384
6401# 6402 6403 6404 6424# 6425 6426 6427 6444# 6445 6446 6447 6485# 6486 6487
6488 6502# 6503 6504 6505 6519# 6520 6521 6522 6611# 6612 6613 6614 6631# 6632
6633 6634 6658# 6659 6660 6661 6678# 6679 6680 6681 6706# 6707 6708 6709 6716#
6717 6718 6719 6785# 6786 6787 6788 6793# 6794 6795 6796 6813# 6814 6815 6816
6834# 6835 6836 6837 6841# 6842 6843 6844 6861# 6862 6863 6864 6868# 6869 6870
6871 6932# 6933 6934 6935 6960# 6961 6962 6963 6976# 6977 6978 6979 6991# 6992
6993 6994 7062# 7063 7064 7065 7072# 7073 7074 7075 7108# 7109 7110 7111 7136#
7137 7138 7139 7148# 7149 7150 7151 7175# 7176 7177 7178 7243# 7244 7245 7246
7253# 7254 7255 7256 7275# 7276 7277 7278 7287# 7288 7289 7290 7308# 7309 7310
7311 7315# 7316 7317 7318 7389# 7390 7391 7392 7399# 7400 7401 7402 7422# 7423
7424 7425 7443# 7444 7445 7446 7452# 7453 7454 7455 7475# 7476 7477 7478 7482#
7483 7484 7485 7494# 7495 7496 7497 7554# 7555 7556 7557 7564# 7565 7566 7567
7592# 7593 7594 7595 7607# 7608 7609 7610 7616# 7617 7618 7619 7633# 7634 7635
7636 7646# 7647 7648 7649 7713# 7714 7715 7716 7723# 7724 7725 7726 7746# 7747
7748 7749 7760# 7761 7762 7763 7769# 7770 7771 7772 7788# 7789 7790 7791 7802#

CROSS REFERENCE TABLE -- MACRO NAMES

7803	7804	7805	7863#	7864	7865	7866	7871#	7872	7873	7874	7893#	7894	7895	7896
7907#	7908	7909	7910	7916#	7917	7918	7919	7933#	7934	7935	7936	8001#	8002	8003
8004	8011#	8012	8013	8014	8032#	8033	8034	8035	8046#	8047	8048	8049	8053#	8054
8055	8056	8062#	8063	8064	8065	8075#	8076	8077	8078	8139#	8140	8141	8142	8149#
8150	8151	8152	8170#	8171	8172	8173	8184#	8185	8186	8187	8191#	8192	8193	8194
8200#	8201	8202	8203	8213#	8214	8215	8216	8287#	8288	8289	8290	8297#	8298	8299
8300	8320#	8321	8322	8323	8339#	8340	8341	8342	8348#	8349	8350	8351	8366#	8367
8368	8369	8376#	8377	8378	8379	8449#	8450	8451	8452	8459#	8460	8461	8462	8481#
8482	8483	8484	8500#	8501	8502	8503	8509#	8510	8511	8512	8527#	8528	8529	8530
8537#	8538	8539	8540	8602#	8603	8604	8605	8612#	8613	8614	8615	8643#	8644	8645
8646	8657#	8658	8659	8660	8666#	8667	8668	8669	8684#	8685	8686	8687	8694#	8695
8696	8697	8779#	8780	8781	8782	8789#	8790	8791	8792	8810#	8811	8812	8813	8828#
8829	8830	8831	8837#	8838	8839	8840	8858#	8859	8860	8861	8871#	8872	8873	8874
8880#	8881	8882	8883	8892#	8893	8894	8895	8901#	8902	8903	8904	8910#	8911	8912
8913	8920#	8921	8922	8923	9006#	9007	9008	9009	9016#	9017	9018	9019	9046#	9047
9048	9049	9060#	9061	9062	9063	9069#	9070	9071	9072	9092#	9093	9094	9095	9106#
9107	9108	9109	9175#	9176	9177	9178	9185#	9186	9187	9188	9215#	9216	9217	9218
9229#	9230	9231	9232	9238#	9239	9240	9241	9262#	9263	9264	9265	9276#	9277	9278
9279	9348#	9349	9350	9351	9358#	9359	9360	9361	9379#	9380	9381	9382	9394#	9395
9396	9397	9403#	9404	9405	9406	9416#	9417	9418	9419	9426#	9427	9428	9429	9513#
9514	9515	9516	9523#	9524	9525	9526	9556#	9557	9558	9559	9570#	9571	9572	9573
9579#	9580	9581	9582	9599#	9600	9601	9602	9613#	9614	9615	9616	9702#	9703	9704
9705	9712#	9713	9714	9715	9742#	9743	9744	9745	9756#	9757	9758	9759	9765#	9766
9767	9768	9780#	9781	9782	9783	9794#	9795	9796	9797	9865#	9866	9867	9868	9875#
9876	9877	9878	9905#	9906	9907	9908	9919#	9920	9921	9922	9928#	9929	9930	9931
9942#	9943	9944	9945	9956#	9957	9958	9959	10104#	10105	10106	10107	10114#	10115	10116
10117	10156#	10157	10158	10159	10170#	10171	10172	10173	10179#	10180	10181	10182	10196#	10197
10198	10199	10212#	10213	10214	10215	10226#	10227	10228	10229	10327#	10328	10329	10330	10337#
10338	10339	10340	10366#	10367	10368	10369	10389#	10390	10391	10392	10397#	10398	10399	10400
10408#	10409	10410	10411	10482#	10483	10484	10485	10492#	10493	10494	10495	10512#	10513	10514
10515	10528#	10529	10530	10531	10537#	10538	10539	10540	10551#	10552	10553	10554	10561#	10562
10563	10564	10642#	10643	10644	10645	10652#	10653	10654	10655	10678#	10679	10680	10681	10693#
10694	10695	10696	10702#	10703	10704	10705	10715#	10716	10717	10718	10731#	10732	10733	10734
10809#	10810	10811	10812	10819#	10820	10821	10822	10838#	10839	10840	10841	10860#	10861	10862
10863	10869#	10870	10871	10872	10883#	10884	10885	10886	10893#	10894	10895	10896	10981#	10982
10983	10984	10991#	10992	10993	10994	11017#	11018	11019	11020	11036#	11037	11038	11039	11045#
11046	11047	11048	11058#	11059	11060	11061	11068#	11069	11070	11071	11149#	11150	11151	11152
11159#	11160	11161	11162	11187#	11188	11189	11190	11206#	11207	11208	11209	11215#	11216	11217
11218	11231#	11232	11233	11234	11239#	11240	11241	11242	11249#	11250	11251	11252	11338#	11339
11340	11341	11348#	11349	11350	11351	11390#	11391	11392	11393	11404#	11405	11406	11407	11413#
11414	11415	11416	11427#	11428	11429	11430	11437#	11438	11439	11440	11546#	11547	11548	11549
11556#	11557	11558	11559	11589#	11590	11591	11592	11603#	11604	11605	11606	11612#	11613	11614
11615	11629#	11630	11631	11632	11644#	11645	11646	11647	11655#	11656	11657	11658	11669#	11670
11671	11672	11761#	11762	11763	11764	11771#	11772	11773	11774	11800#	11801	11802	11803	11814#
11815	11816	11817	11823#	11824	11825	11826	11837#	11838	11839	11840	11852#	11853	11854	11855
11863#	11864	11865	11866	11877#	11878	11879	11880	11997#	11998	11999	12000	12007#	12008	12009
12010	12041#	12042	12043	12055#	12056	12057	12058	12064#	12065	12066	12067	12086#	12087	12088
12088	12089	12097#	12098	12099	12100	12118#	12119	12120	12121	12132#	12133	12134	12135	12138#
12139	12140	12141	12158#	12159	12160	12161	12169#	12170	12171	12172	12190#	12191	12192	12193
12204#	12205	12206	12207	12213#	12214	12215	12216	12237#	12238	12239	12240	12247#	12248	12249
12250	12340#	12341	12342	12343	12350#	12351	12352	12353	12374#	12375	12376	12377	12388#	12389
12390	12391	12397#	12398	12399	12400	12411#	12412	12413	12414	12421#	12422	12423	12424	12441#
12442	12443	12444	12455#	12456	12457	12458	12464#	12465	12466	12467	12479#	12480	12481	12482
12492#	12493	12494	12495	12587#	12588	12589	12590	12597#	12598	12599	12600	12626#	12627	12628
12629	12640#	12641	12642	12643	12649#	12650	12651	12652	12662#	12663	12664	12665	12693#	12694
12695	12696	12732#	12754#	12755	12756	12757	12767#	12768	12769	12770	12788#	12789	12790	12791

PARAMETER CODING MACY11 30(1046) 11-JAN-83 10:13 PAGE 329
 CZUAAA.P11 11-JAN-83 09:29 CROSS REFERENCE TABLE -- MACRO NAMES

	12801#	12802	12803	12804	12832#	12833	12834	12835	12845#	12846	12847	12848	12877#	12878	12879
	12880	12889#	12890	12891	12892	13084#	13089#								
M\$XFER	1#	1389#													
OPEN	1#	1389#													
POINTE	1#	1389#	1399												
PRINTB	1#	1389#	2889	2902	2917	2924	2938	2950	2962	2978	2984	2997	3013	3025	3031
	3046	3052	3059	3072	3084	3096	3109	3121	3127	3133	3146	3159	3166	3179	3193
	3208	3215	3222	3236	3243	3250	3264	3278	3285	3293	3307	3313	3320	3334	3340
	3347	3362	3370	3380	3396	3408	3414	3423	3430	3448	3455	3461	3467	3474	3481
	3494	3500	3513	3519	3533	3539	3553	3561	3568	3581	3587	3600	3606	3613	3620
	3627	3641	3647	3655	3668	3681	3688	3701	3708	3714	3728	3740	3752	3764	3776
	3782	3789	3803	3809	3823	3830	3837	3844	3851	3865	3879	3887	3895	3903	3916
	3922	3928	3940	12918	12925	12932	12939	12946							
PRINTF	1#	1389#	4352	4467	4481	4502	4568	4583	4596	4611	4624	4713	4791		
PRINTS	1#	1389#													
PRINTX	1#	1389#													
READBU	1#	1389#													
READEF	1#	1389#	4681	4686	4691	4696									
RFLAGS	1#	1389#													
SETPRI	1#	1389#	4199	4213	6949	7122									
SETVEC	1#	1389#	4726	4924	5027	5179	5227	6913	7079						
SLASH	1#	1389#													
STARS	1#	1389#													
SVC	1#	1389#													
XFER	1#	1389#	4646#	4800#	4820#	4831#	12732#								
XFERF	1#	1389#													
XFERT	1#	1389#													

. ABS.	000000	000
	000000	001
UNAREP	053774	002
MICRA	000000	003
MICRB	000000	004
MICRC	000000	005
MICRD	000000	006
MICRE	000000	007
MICRF	000000	010
MICRG	000000	011
NOMORE	000000	012

ERRORS DETECTED: 0
 % DEFAULT GLOBALS GENERATED: 15

CZUAAA.OBJ,CZUAAA.LST/CRF/SOL/NL:TOC=SVC34R.P11,CZUAAA.P11
 RUN-TIME: 77 91 11 SECONDS
 RUN-TIME RATIO: 361/181=1.9
 CORE USED: 33K (66 PAGES)

MICROA - MICROCODE MODULE A
 MICROA.MAC 10-JAN-83 17:52

MACY11 30(1046) 11-JAN-83 09:50 PAGE 1

```

1
2
3      .TITLE MICROA - MICROCODE MODULE A
4      000000'
5      .CSECT MICRA
6      .SBTTL REGISTER DEFINITIONS USED BY THE T11
7
8      021000 IPCSR0 = 21000 ;INTERNAL PCSRO ADDRESS
9      021002 DMACSR = 21002 ;DMA ENGINE CONTROL STATUS REGISTER
10     021004 DMATO = 21004 ;DMA ENGINE TO ADDRESS REGISTER #0
11     021006 DMAT1 = 21006 ;DMA ENGINE TO ADDRESS REGISTER #1
12     021010 MDMA0 = 21010 ;MICROCPU DMA TO ADDRESS REGISTER #0
13     021012 MDMA1 = 21012 ;MICROCPU DMA TO ADDRESS REGISTER #1
14     021014 MDMARO = 21014 ;MICROCPU DMA DATA REGISTER - AUTO INCREMENT R/O
15     021016 MDMAR1 = 21016 ;MICROCPU DMA DATA REGISTER - AUTO DECREMENT R/O
16     021020 IPCSR1 = 21020 ;INTERNAL PCSR1 ADDRESS
17     021022 DMAF = 21022 ;DMA ENGINE FROM ADDRESS REGISTER
18     021024 DMAWC = 21024 ;DMA ENGINE WORD COUNT REGISTER
19     021026 MDMAW0 = 21026 ;MICROCPU DMA DATA REGISTER - AUTO INCREMENT W/O
20     021030 LTAC = 21030 ;LINK TRANSMIT ADDRESS CONTROL REGISTER
21     021032 LFRBUF = 21032 ;LINK RECEIVE BUFFER ADDRESS FIFO
22     021034 CLRIF = 21034 ;CLEAR FIFO
23     021036 MDMAW1 = 21036 ;MICROCPU DMA DATA REGISTER - AUTO DECREMENT W/O
24     021040 PCSRSW = 21040 ;SWITCH PACK REGISTER
25     021042 MDMSR = 21042 ;MICROCPU DMA STATUS REGISTER
26     021044 LRBUF = 21044 ;LINK RECEIVE BUFFER COMPLETED
27     021060 PHYAD0 = 21060 ;PHYSICAL ADDRESS ROM BYTE 0
28     021062 PHYAD1 = 21062 ;PHYSICAL ADDRESS ROM BYTE 1
29     021064 PHYAD2 = 21064 ;PHYSICAL ADDRESS ROM BYTE 2
30     021066 PHYAD3 = 21066 ;PHYSICAL ADDRESS ROM BYTE 3
31     021070 PHYAD4 = 21070 ;PHYSICAL ADDRESS ROM BYTE 4
32     021072 PHYAD5 = 21072 ;PHYSICAL ADDRESS ROM BYTE 5
33
34     .SBTTL OTHER DEFINITIONS USED BY THE MICROCODE
35
36     100000 BIT15 = 100000
37     040000 BIT14 = 40000
38     020000 BIT13 = 20000
39     010000 BIT12 = 10000
40     004000 BIT11 = 4000
41     002000 BIT10 = 2000
42     001000 BIT9 = 1000
43     000400 BIT8 = 400
44     000200 BIT7 = 200
45     000100 BIT6 = 100
46     000040 BIT5 = 40
47     000020 BIT4 = 20
48     000010 BIT3 = 10
49     000004 BIT2 = 4
50     000002 BIT1 = 2
51     000001 BIT0 = 1
52
53     012400 LASFTP = BIT8:BIT10:BIT12 ;LOAD AND START FUNCTION TEST PATTERN
54     000340 PRI07 = 340
55     000300 PRI06 = 300
56     000240 PRI05 = 240
    
```

MICROA - MICROCODE MODULE A
 MICROA.MAC 10-JAN-83 17:52

MACY11 30(1046) 11-JAN-83 09:50 PAGE 2
 OTHER DEFINITIONS USED BY THE MICROCODE

57	000200	PRI04 =	200	
58	000140	PRI03 =	140	
59	000100	PRI02 =	100	
60	000040	PRI01 =	40	
61	000000	PRI00 =	0	
62		.		
63		:PCSR0 - PORT CONTROL STATUS REGISTER 0		
64		.		
65	100000	SERI =	BIT15	
66	040000	PCEI =	BIT14	
67	020000	RXI =	BIT13	
68	010000	TXI =	BIT12	
69	004000	DNI =	BIT11	
70	002000	RCEI =	BIT10	
71	000400	FATI =	BIT8	
72		.		
73	000134	:SANVEC=	134	:VECTOR ADDRESS FOR THE SANITY TIMER
74	000150	:NXMVEC=	150	:VECTOR ADDRESS FOR THE NON-EXISTANT MEMORY TIMOUT
75	000064	:CSRVEC=	64	:VECTOR ADDRESS FOR CSR WRITE INTERRUPT
76	000114	:DMAVEC=	114	:VECTOR ADDRESS FOR DMA DONE INTERRUPT
77	000140	:PARVEC=	140	:VECTOR ADDRESS FOR LINK MEMORY PARITY ERROR
78	000150	:NXMVEC=	150	:VECTOR ADDRESS FOR NON-EXISTANT UNIBUS ADDRESS
79	001000	:STACK=	1000	:STACK LOCATION
80	000001	:INMON=	1	:IN MICROMONITOR STATE
81	000002	:INTST=	2	:IN A TEST STATE
82	000003	:INERR=	3	:IN ERROR STATE
83	000001	:CSRFLG=	BIT0	:CSR WRITE INTERRUPT OCCURED
84	000002	:ERRFLG=	BIT1	:UNEXPECTED ERROR OCCURED
85	000004	:PARFLG=	BIT2	:PARITY ERROR OCCURED
86	000010	:NXMFLG=	BIT3	:NON-EXISTANT MEMORY ERROR OCCURRED
87	020000	:SIZ4K=	20000	:4K WORDS
88	040000	:SIZ8K=	SIZ4K*2	:8K WORDS
89	020000	:WCSSIZ=	SIZ4K	:SIZE OF WRITEABLE CONTROL STORE
90	020000	:IOSIZ=	SIZ4K	:SIZE OF I/O PAGE
91	040000	:ROMSIZ=	SIZ8K	:SIZE OF ROM
92	077774	:LINSIZ=	SIZ8K*2-4	:SIZE OF LINK MEMORY
93	000000	:WCSADR=	0	:BASE ADDRESS OF WCS
94	020000	:IOADR=	WCSADR+WCSSIZ	:BASE ADDRESS OF I/O PAGE
95	040000	:ROMADR=	IOADR+IOSIZ	:BASE ADDRESS OF ROM
96	100000	:LINADR=	ROMADR+ROMSIZ	:BASE ADDRESS OF LINK MEMORY
97	000000	:DATERR=	0	:FLAG INDICATING DATA ERROR OCCURRED
98	000001	:ADRERR=	1	:FLAG INDICATING ADDRESS ERROR OCCURRED
99	000002	:PARERR=	2	:FLAG INDICATING PARITY ERROR OCCURRED
100	177774	:MODREG=	LINADR+LINSIZ	:LINK MODE REGISTER
101	177774	:ADDREG=	MODREG	:LINK STATION ADDRESS RAM REGISTER
102	177776	:CMDREG=	MODREG+2	:LINK COMMAND REGISTER
103				

MICROA - MICROCODE MODULE A
MICROA.MAC 10-JAN-83 17:52

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OTHER DEFINITIONS USED BY THE MICROCODE

104
105
106
107
108
109
110
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112
113
114
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122

.SBTTL MICROCODE MODULE A

: THIS MODULE CONTAINS MICROCODE THAT IS USED FOR THE LOAD AND START
: FUNCTION TEST

000000*	106427	000340		MICROA::MTPS	#PRI07		:DISABLE INTERRUPTS
000004*	012737	012402	021020	MOV	#LASFTP!INTST,@#IPCSR1		:SET TEST PATTERN AND IN TEST STATE BITS
000012*	012737	004000	021000	MOV	#DNI,@#IPCSRO		:SET DONE BIT
000020*	000777			BR	.		:HANG HERE UNTIL HOST RESETS US
000022*	000024			MICASZ::MICASZ-MICROA+2			:SIZE OF MICROCODE MODULE A
	000001			.END			

MICROA - MICROCODE MODULE A
MICROA.MAC 10-JAN-83 17:52

MACY11 30(1046) 11-JAN-83 09:50 PAGE 5
CROSS REFERENCE TABLE -- USER SYMBOLS

ADDREG=	177774	101#		
ADRERR=	000001	98#		
BIT0 =	000001	51#	83	
BIT1 =	000002	50#	84	
BIT10 =	002000	41#	53	70
BIT11 =	004000	40#	69	
BIT12 =	010000	39#	53	68
BIT13 =	020000	38#	67	
BIT14 =	040000	37#	66	
BIT15 =	100000	36#	65	
BIT2 =	000004	49#	85	
BIT3 =	000010	48#	86	
BIT4 =	000020	47#		
BIT5 =	000040	46#		
BIT6 =	000100	45#		
BIT7 =	000200	44#		
BIT8 =	000400	43#	53	71
BIT9 =	001000	42#		
CLIF =	021034	22#		
MODREG=	177776	102#		
CSRFLG=	000001	83#		
CSRVEC=	000064	75#		
DATERR=	000000	97#		
DMACSR=	021002	9#		
DMAF =	021022	17#		
DMATO =	021004	10#		
DMAT1 =	021006	11#		
DMAVEC=	000114	76#		
DMAWC =	021024	18#		
DNI =	004000	69#	118	
ERRFLG=	000002	84#		
FATI =	000400	71#		
INERR =	000003	82#		
INMON =	000001	80#		
INTST =	000002	81#	117	
IOADR =	020000	94#	95	
IOSIZ =	020000	90#	95	
IPCSR0=	021000	8#	118*	
IPCSR1=	021020	16#	117*	
LASFTP=	012400	53#	117	
LFRBUF=	021032	21#		
LINADR=	100000	96#	100	
LINSIZ=	077774	92#	100	
LRBUF =	021044	26#		
LTAC =	021030	20#		
MDMAR0=	021014	14#		
MDMAR1=	021016	15#		
MDMAW0=	021026	19#		
MDMAW1=	021036	23#		
MDMA0 =	021010	12#		
MDMA1 =	021012	13#		
MDMSR =	021042	25#		
MICASZ	000027RG	002	121#	
MICROA	000000RG	002	116#	121
MODREG=	177774	100#	101	102
NXMFLG=	000010	86#		

MICROA - MICROCODE MODULE A
MICROA.MAC 10-JAN-83 17:52

MACY11 30(1046) 11-JAN-83 09:50 PAGE 6
CROSS REFERENCE TABLE -- USER SYMBOLS

NXMVEC=	000150	74#	78#		
PARERR=	000002	99#			
PARFLG=	000004	85#			
PARVEC=	000140	77#			
PCEI =	040000	60#			
PCSRSW=	021040	24#			
PHYAD0=	021060	27#			
PHYAD1=	021062	28#			
PHYAD2=	021064	29#			
PHYAD3=	021066	30#			
PHYAD4=	021070	31#			
PHYAD5=	021072	32#			
PRI00 =	000000	61#			
PRI01 =	000040	60#			
PRI02 =	000100	59#			
PRI03 =	000140	58#			
PRI04 =	000200	57#			
PRI05 =	000240	56#			
PRI06 =	000300	55#			
PRI07 =	000340	54#	116*		
RCEI =	002000	70#			
ROMADR=	040000	95#	96		
ROMSIZ=	040000	91#	96		
RXI =	020000	67#			
SANVEC=	000134	73#			
SERI =	100000	65#			
SIZ4K =	020000	87#	88	89	90
SIZ8K =	040000	88#	91	92	
STACK =	001000	79#			
TXI =	010000	68#			
WCSADR=	000000	93#	94		
WCSSIZ=	020000	89#	94		
. =	000024R	002	119		

. ABS. 000000 000
000000 001
MICRA 000024 002

ERRORS DETECTED: 0
DEFAULT GLOBALS GENERATED: 0

MICROA.OBJ,MICROA.LST/CRF/DOC/NL:TOC/SOL=MICROA.MAC
RUN-TIME: .5 .2 .2 SECONDS
RUN-TIME RATIO: 59/1=55.7
CORE USED: 8K (16 PAGES)

DOCUMENT PAGES: 5

MICROB - MICROCODE MODULE B
 MICROB.MAC 10-JAN-83 17:52

MACV11 30(1046) 11-JAN-83 09:50 PAGE 1

```

1
2
3      .TITLE  MICROB - MICROCODE MODULE B
4      000000' .CSECT  MICRB
5
6      .SBTTL  REGISTER DEFINITIONS USED BY THE T11
7
8      021000  IPCSR0 =      21000  :INTERNAL PCSR0 ADDRESS
9      021002  DMACSR =      21002  :DMA ENGINE CONTROL STATUS REGISTER
10     021004  DMAT0  =      21004  :DMA ENGINE TO ADDRESS REGISTER #0
11     021006  DMAT1  =      21006  :DMA ENGINE TO ADDRESS REGISTER #1
12     021010  MDMA0  =      21010  :MICROCPU DMA TO ADDRESS REGISTER #0
13     021012  MDMA1  =      21012  :MICROCPU DMA TO ADDRESS REGISTER #1
14     021014  MDMAR0 =      21014  :MICROCPU DMA DATA REGISTER - AUTO INCREMENT R/O
15     021016  MDMAR1 =      21016  :MICROCPU DMA DATA REGISTER - AUTO DECREMENT R/O
16     021020  IPCSR1 =      21020  :INTERNAL PCSR1 ADDRESS
17     021022  DMAF   =      21022  :DMA ENGINE FROM ADDRESS REGISTER
18     021024  DMAWC  =      21024  :DMA ENGINE WORD COUNT REGISTER
19     021026  MDMAW0 =      21026  :MICROCPU DMA DATA REGISTER - AUTO INCREMENT W/O
20     021030  LTA    =      21030  :LINK TRANSMIT ADDRESS COUNTER REGISTER
21     021032  LFRBUF =      21032  :LINK RECIEVE BUFFER ADDRESS FIFO
22     021034  CLRIF  =      21034  :CLEAR FIFO
23     021036  MDMAW1 =      21036  :MICROCPU DMA DATA REGISTER - AUTO DECREMENT W/O
24     021040  PCSRSW =      21040  :SWITCH PACK REGISTER
25     021042  MDMSR  =      21042  :MICROCPU DMA STATUS REGISTER
26     021044  LRBUF  =      21044  :LINK RECIEVE BUFFER COMPLETED
27     021060  PHYAD0 =      21060  :PHYSICAL ADDRESS ROM BYTE 0
28     021062  PHYAD1 =      21062  :PHYSICAL ADDRESS ROM BYTE 1
29     021064  PHYAD2 =      21064  :PHYSICAL ADDRESS ROM BYTE 2
30     021066  PHYAD3 =      21066  :PHYSICAL ADDRESS ROM BYTE 3
31     021070  PHYAD4 =      21070  :PHYSICAL ADDRESS ROM BYTE 4
32     021072  PHYAD5 =      21072  :PHYSICAL ADDRESS ROM BYTE 5
33
34     .SBTTL  OTHER DEFINITIONS USED BY THE MICROCODE
35
36     100000  BIT15 =      100000
37     040000  BIT14 =      40000
38     020000  BIT13 =      20000
39     010000  BIT12 =      10000
40     004000  BIT11 =      4000
41     002000  BIT10 =      2000
42     001000  BIT9  =      1000
43     000400  BIT8  =      400
44     000200  BIT7  =      200
45     000100  BIT6  =      100
46     000040  BIT5  =      40
47     000020  BIT4  =      20
48     000010  BIT3  =      10
49     000004  BIT2  =      4
50     000002  BIT1  =      2
51     000001  BIT0  =      1
52
53     012400  LASFTP =      BIT8!BIT10!BIT12 ;LOAD AND START FUNCTION TEST PATTERN
54     000340  PRI07  =      340
55     000300  PRI06  =      300
56     000240  PRI05  =      240

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MICROB - MICROCODE MODULE B
 MICROB.MAC 10-JAN-83 17:52

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 OTHER DEFINITIONS USED BY THE MICROCODE

57	000200	PRI04 =	200	
58	000140	PRI03 =	140	
59	000100	PRI02 =	100	
60	000040	PRI01 =	40	
61	000000	PRI00 =	0	
62		.		
63		:PCSR0 - PORT CONTROL STATUS REGISTER 0		
64		.		
65	100000	SERI =	BIT15	
66	040000	PCEI =	BIT14	
67	020000	RXI =	BIT13	
68	C10000	TXI =	BIT12	
69	004000	DNI =	BIT11	
70	002000	RCEI =	BIT10	
71	000400	FATI =	BIT8	
72		.		
73	000134	SANVEC=	134	:VECTOR ADDRESS FOR THE SANITY TIMER
74	000064	CSRVEC=	64	:VECTOR ADDRESS FOR CSR WRITE INTERRUPT
75	000114	DMAVEC=	114	:VECTOR ADDRESS FOR DMA DONE INTERRUPT
76	000140	PARVEC=	140	:VECTOR ADDRESS FOR LINK MEMORY PARITY ERROR
77	001000	STACK=	1000	:STACK LOCATION
78	000001	INMON=	1	:IN MICROMONITOR STATE
79	000002	INTST=	2	:IN A TEST STATE
80	000003	INERR=	3	:IN ERROR STATE
81	000001	CSRFLG=	BIT0	:CSR WRITE INTERRUPT OCCURED
82	000002	ERRFLG=	BIT1	:UNEXPECTED ERROR OCCURED
83	000004	PARFLG=	BIT2	:PARITY ERROR OCCURED
84	000010	NXMFLG=	BIT3	:NON-EXISTANT MEMORY ERROR OCCURRED
85	000020	NPRFLG=	BIT4	:NPR TIMEOUT OCCURRED
86	100000	NPRERR=	BIT15	:PCSR0 FLAG INDICATING NPR ERROR OCCURRED
87	040000	NXMERR=	BIT14	:PCSR0 FLAG INDICATING NON-EXISTANT MEMORY ERROR OCCURRED
88	020000	UNIERR=	BIT13	:PCSR0 FLAG INDICATING UNEXPECTED INTERRUPT OCCURRED
89	010000	PARERR=	BIT12	:PCSR0 FLAG INDICATING LINK MEMORY PARITY ERROR OCCURRED
90	020000	SIZ4K=	20000	:4K WORDS
91	040000	SIZ8K=	SIZ4K*2	:8K WORDS
92	020000	WCSSIZ=	SIZ4K	:SIZE OF WRITEABLE CONTROL STORE
93	020000	IOSIZ=	SIZ4K	:SIZE OF I/O PAGE
94	040000	ROMSIZ=	SIZ8K	:SIZE OF ROM
95	077774	LINSIZ=	SIZ8K*2-4	:SIZE OF LINK MEMORY
96	000000	WCSADR=	0	:BASE ADDRESS OF WCS
97	020000	IOADR=	WCSADR+WCSSIZ	:BASE ADDRESS OF I/O PAGE
98	040000	ROMADR=	IOADR+IOSIZ	:BASE ADDRESS OF ROM
99	100000	LINADR=	ROMADR+ROMSIZ	:BASE ADDRESS OF LINK MEMORY
100	000000	DATERR=	0	:FLAG INDICATING DATA ERROR OCCURRED
101	000001	ADRERR=	1	:FLAG INDICATING ADDRESS ERROR OCCURRED
102	177774	MODREG=	LINADR+LINSIZ	:LINK MODE REGISTER
103	177774	ADDREG=	MODREG	:LINK STATION ADDRESS RAM REGISTER
104	177776	CMDREG=	MODREG+2	:LINK COMMAND REGISTER
105				

MICROB - MICROCODE MODULE B
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OTHER DEFINITIONS USED BY THE MICROCODE

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.SBTTL MICROCODE MODULE B

.....
: THIS MODULE CONTAINS A MICROMONITOR AND MICROCODE FOR:
: 1-WCS MEMORY TEST
:

MICROB::MTPS #PRI07 ;DISABLE INTERRUPTS
MOV #0,@#CMDREG ;TURN OFF THE LINK
MOV #STACK,SP ;SETUP STACK
MOV #INMON,PCSR1 ;TELL HOST WE ARE IN MICROMONITOR
MOV PCSR1,@#IPCSR1
MOV #DNI,@#IPCSRO ;TELL HOST THE LOAD AND START FINISHED
MOV PC,R0 ;GET ADDRESS OF UNEXPECTED ERROR...
ADD #ERRINT-.,R0 ;HANDLER
CLR R1 ;FILL ALL UNUSED VECTORS WITH TRAP...
10\$: MOV R0,(R1)+ ;HANDLER
MOV #PRI07,(R1)+
CMP R1,#1000
BLT 10\$

MOV PC,R0 ;SETUP PARITY TRAP VECTOR
ADD #PARINT-.,R0
MOV R0,@#PARVEC
MOV #PRI07,@#PARVEC+2

MOV PC,R0 ;SETUP DMA INTERRUPT VECTOR
ADD #DMAINT-.,R0
MOV R0,@#DMAVEC
MOV #PRI07,@#DMAVEC+2

MOV PC,R0 ;SETUP CSR WRITE VECTOR
ADD #CSRWRT-.,R0
MOV R0,@#CSRVEC
MOV #PRI04,@#CSRVEC+2

MOV PC,R0 ;SETUP SANITY TIMER VECTOR
ADD #TIMINT-.,R0
MOV R0,@#SANVEC
MOV #PRI05,@#SANVEC+2

MOV @#PCSRW,R0 ;GET SWITCH PACK BITS
BIS #176000,R0 ;MAP THEM INTO HOST I/O PAGE
ASL R0 ;SHIFT OVER TO POSITION CORRECTLY
ASL R0
ASL R0
ADD #4,R0 ;PCSR2 IS PCSR0+4
MOV R0,IPCSR2 ;SAVE PCSR2 ADDRESS
MOV #3,IPCSR2+2 ;HIGH ORDER BITS 17:16
CLR FLG2 ;INITIALIZE FLAG WORD
15\$: MTPS #PRI00 ;ALLOW INTERRUPTS

MICROB - MICROCODE MODULE B MACY11 30(1046) 11-JAN-83 09:50 PAGE 4
 MICROB.MAC 10-JAN-83 17:52 MICROCODE MODULE B

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162
163 000230' 005 7 000274      20$:  TST      FLG2      ;WAIT FOR A COMMAND FROM HOST
164 000234' 001775
165
166 000236' 106427 000340      MTPS     #PRI07    ;RAISE CPU PRIORITY TO SERVICE COMMAND
167 000242' 032767 000001 000260  BIT     #CSRFLG,FLG2 ;DID HOST GIVE US A COMMAND?
168 000250' 001001      BNE     30$      ;YES
169 000252' 000777      BR      .        ;NO, ERROR SO JUST SIT HERE...
170
171
172 000254' 013700 021000      30$:  MOV     @#IPCSRO,R0 ;GET WHAT HOST WROTE TO PCSRO
173 000260' 042700 177760      BIC     @177760,R0 ;STRIP ALL BUT COMMAND BITS
174 000264' 001004      BNE     35$      ;WAS IT THE CLEAR FUNCTION?
175 000266' 012737 000001 021020  MOV     #INMON,@#IPCSR1 ;YES, CLEAR SELF TEST BITS
176 000274' 000432      BR      50$
177 000276' 022700 000017      35$:  CMP     #17,R0    ;RETURN TO OPERATIONAL MICROCODE?
178 000302' 001432      BEQ     60$      ;YES
179 000304' 162700 000001      SUB     #1,R0    ;WILL FORM A TABLE INDEX WITH THIS
180 000310' 010701      MOV     PC,R1    ;GET ADDRESS OF OUR COMMAND TABLE
181 000312' 062701 000004      ADD     #TBLB-.,R1
182 000316' 006300      ASL     R0      ;MAKE COMMAND A BYTE OFFSET
183 000320' 060001      ADD     R0,R1   ;USE IT TO INDEX INTO COMMAND TABLE
184 000322' 061101      ADD     (R1),R1 ;R1 NOW HAS COMMAND ROUTINE ADDRESS
185 000324' 004711      ISR     PC,(R1) ;EXECUTE AS COMMANDED FROM HOST
186 000326' 103404      S        40$    ;ERROR OCCURRED
187 000330' 112767 000001 000176  MOVVB  #INMON,PCSR1 ;INDICATE TO HOST WE ARE BACK IN...
188 000336' 000403      BR      45$    ;MICROMONITR
189 000340' 112767 000003 000166  40$:  MOVVB  #INERR,PCSR1 ;INDICATE TO HOST ERROR OCCURRED
190 000346' 016737 000162 021020  45$:  MOV     PCSR1,@#IPCSR1
191 000354' 012737 004000 021000  MOV     #DNI,@#IPCSRO ;TELL HOST THIS MICROTEST FINISHED
192 000362' 005067 000142      50$:  CLR     FLG2    ;RESET FLAG WORD
193 000366' 000716      BR      15$    ;GO WAIT FOR ANOTHER COMMAND
194
195 000370' 005000      60$:  CLR     R0      ;FAKE SUCCESSFULL SELF TEST DONE
196 000372' 000137 040006      JMP     @#40006 ;START OPERATIONAL MICROCODE
197
198 000376' 052767 000001 000124  CSRWRT: BIS     #CSRFLG,FLG2 ;INDICATE A CSR WRITE INTERRUPT OCCURED
199 000404' 000002      RTI
200
201 000406' 052767 000002 000114  ERRINT: BIS     #ERRFLG,FLG2 ;INDICATE A UNEXPECTED INTERRUPT OCCURED
202 000414' 012737 020000 021000  MOV     #UNIERR,@#IPCSRO ;TELL HOST AN UNFXPECTED INTERRUPT
203
204 000422' 000777      BR      .        ;HAPPENED
205
206 000424' 005267 000102      TIMINT: INC     SANTIM ;JUST SIT HERE AND SPIN WHEELS
207 000430' 000002      RTI ;COUNT TICKS AS THEY OCCUR
208
209 000432' 013767 021002 000106  DMAINT: MOV     @#DMACSR,DMDONE ;GET DMA STATUS
210 000440' 032767 040000 000100  BIT     #BIT14,DMDONE ;DID A NON-EXISTANT MEMORY INTERRUPT OCCUR?
211 000446' 001404      BEQ     10$    ;NO
212 000450' 012737 040000 021000  MOV     #NXMERR,@#IPCSRO ;YES, TELL HOST A NON-EXISTANT MEMORY
213
214 000456' 000407      BR      20$    ;LOCATION WAS ADDRESSED
215 000460' 032767 100000 000060  10$:  BIT     #BIT15,DMDONE ;DID A NPR TIMEOUT OCCUR?
216 000466' 001407      BEQ     30$    ;NO
217 000470' 012737 100000 021000  MOV     #NPRERR,@#IPCSRO ;TELL HOST NPR TIMEOUT HAPPENED
    
```


MICROB - MICROCODE MODULE B
MICROB.MAC 10-JAN-83 17:52

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MODULE B, MICROTTEST #1

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293 000750' 010700      MOV      PC,R0      ;GET BASE ADDRESS OF THIS MICROTTEST
294 000752' 062700 177026  ADD      #MICROB-.,R0
295 000756' 016701 001330  MOV      MICBSZ,R1  ;GET ITS SIZE
296 000762' 012702 001000  MOV      #1000,R2  ;RELOCATE TO JUST ABOVE STACK
297 000766' 112022      35$:  MOVB    (R0)+,(R2)+ ;RELOCATE THIS MICROTTEST
298 000770' 005301      DEC      R1
299 000772' 001375      BNE     35$
300 000774' 010700      MOV      PC,R0
301 000776' 062700 177610  ADD      #5$-.,R0
302 001002' 162700 007000  SUB      #WCSSIZ/2-1000,R0
303 001006' 000110      JMP     (R0)
304 001010' 000241      40$:  CLC
305 001012' 106746      45$:  MFPS    -(SP)
306 001014' 005767 000072  TST     BOTH
307 001020' 001412      BEQ     55$
308 001022' 010700      MOV      PC,R0
309 001024' 062700 176754  ADD      #MICROB-.,R0
310 001030' 016701 001256  MOV      MICBSZ,R1
311 001034' 012702 010000  MOV      #WCSSIZ/2,R2
312 001040' 112022      50$:  MOVB    (R0)+,(R2)+
313 001042' 005301      DEC      R1
314 001044' 001375      BNE     50$
315 001046' 106426      55$:  MTPS    (SP)+
316 001050' 112767 000001 177457  MOVB    #1,PCSR1+1 ;RESTORE RESULT STATUS
317 001056' 016737 177452 021020  MOV     PCSR1,@#IPCSR ;TELL HOST WHICH TEST THIS WAS
318 001064' 000207      RTS     PC          ;RETURN TO MICROMONITOR
319
320 001066' 000026      B1STBL: MICB1A-.
321 001070' 000162      MICB1B-.
322 001072' 000320      MICB1C-.
323 001074' 000432      MICB1D-.
324 001076' 000670      MICB1E-.
325 001100' 001002      MICB1F-.
326 001102' 000000      .WORD  0
327
328 001104' 000000      LOWLIM: .WORD  0
329 001106' 000000      HILIM:  .WORD  0
330 001110' 000      SUBNUM:  .BYTE  0
331 001111' 000      ERRYP:  .BYTE  0
332 001112' 000000      BOTH:   .WORD  0
333
;END OF LIST
;LOW LIMIT OF MEMORY TESTS
;HIGH LIMIT OF MEMORY TESTS
;CURRENT SUBTEST # BEING EXECUTED
;TYPE OF ERROR, DATA =0, ADDRESS-1
;FLAG INDICATING WHEN BOTH HALVES OF
;WCS HAVE BEEN TESTED

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MICROB - MICROCODE MODULE B
MICROB.MAC 10-JAN-83 17:52

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MODULE B, MICROTEST #1, MICROSUBTEST A

.SBTTL MODULE B, MICROTEST #1, MICROSUBTEST A

: THIS IS AN ACCESS TEST OF WCS MEMORY. IT WRITES ONES
: IN MEMORY BETWEEN LOWLIM AND HILIM AND VERIFIES SAME. IT THEN WRITES
: ZEROS AND VERIFIES SAME. IT ALSO CHECKS FOR BOGUS PARITY ERRORS.

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344 001114' 010046          MICB1A: MOV      R0,-(SP)          :SAVE R0
345 001116' 112767 000001 177764      MOVB     #1,SUBNUM        :TELL WE ARE IN SUBTEST A
346 001124' 106427 000300          MTPS     #PRI06          :ALLOW PARITY ERRORS
347 001130' 012703 177777          MOV      #177777,R3      :GOOD DATA = ALL ONES
348 001134' 016700 177744          5$:     MOV      LOWLIM,R0  :GET BASE ADDRESS OF MEMORY
349 001140' 010320          10$:    MOV      R3,(R0)+    :WRITE ALL OVER MEMORY
350 001142' 020067 177740          CMP      R0,HILIM
351 001146' 103774          BLO     10$
352
353 001150' 016701 177730          MOV      LOWLIM,R1      :POINT BACK TO BASE
354 001154' 011104          12$:    MOV      (R1),R4      :READ DATA
355 001156' 032767 000004 177344      BIT      #PARFLG,FLG2    :DID A PARITY ERROR OCCUR?
356 001164' 001407          BEQ     15$             :NO
357 001166' 020304          CMP     R3,R4           :YES, WAS DATA READ GOOD?
358 001170' 001007          BNE    20$             :NO, DATA ERROR
359 001172' 112767 010000 177711      MOVB     #PARERR,ERRTYP  :TELL MICROMONITOR TYPE OF ERROR
360 001200' 000261          SEC
361 001202' 000421          BR     40$             :TELL MICROMONITOR ERROR OCCURRED
362
363 001204' 020304          15$:    CMP     R3,R4           :WAS DATA GOOD?
364 001206' 001405          BEQ     30$             :YES
365 001210' 112767 000000 177673      20$:    MOVB     #DATERR,ERRTYP :TELL MICROMONITOR TYPE OF ERROR
366 001216' 000261          SEC
367 001220' 000412          BR     40$             :TELL MICROMONITOR ERROR OCCURRED
368
369 001222' 062701 000002          30$:    ADD     #2,R1           :POINT TO NEXT MEMORY ADDRESS
370 001226' 020167 177654          CMP     R1,HILIM        :DONE ALL MEMORY?
371 001232' 001350          BNE    12$             :NOT YET
372 001234' 005703          TST     R3              :DONE BOTH DATA TYPES?
373 001236' 001402          BEQ     35$             :YES
374 001240' 005103          COM     R3              :NO, WRITE ONES NOW
375 001242' 000734          BR     5$
376
377 001244' 000241          35$:    CLC
378 001246' 012600          40$:    MOV     (SP)+,R0      :INDICATE SUCCESS
379 001250' 000207          RTS     PC              :RESTORE R0
380

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MICROB - MICROCODE MODULE B
MICROB.MAC 10-JAN-83 17:52

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MODULE B, MICROTEST #1, MICROSUBTEST C

.SBTTL MODULE B, MICROTEST #1, MICROSUBTEST C

:DATA LATC TEST

:AT THE FIRST LOCATION OF EACH BANK A '1' IS SHIFTED THROUGH EACH
:BIT POSITION AND CHECKED FROM LSB TO MSB.
:THEN IN THE SAME LOCATION A '0' IS SHIFTED THROUGH EACH BIT POSITION
:AND CHECKED.

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001412' 010046
001414' 112767 000003 177466
001422' 016701 177456
001426' 012700 000001
001432' 010002
001434' 010011
001436' 011104
001440' 020004
001442' 001406
001444' 112767 000000 177437
001452' 010003
001454' 000261
001456' 000421
001460' 005702
001462' 001406
001464' 006300
001466' 103362
001470' 005002
001472' 012700 177776
001476' 000756
001500' 000261
001502' 006100
001504' 103753
001506' 062701 001000
001512' 020167 177370
001516' 103743
001520' 000241
001522' 012600
001524' 000207

MICB1C: MOV R0, -(SP)
MOV #3, SUBNUM
MOV LOWLIM, R1
1\$: MOV #1, R0
MOV R0, R2
2\$: MOV R0, (R1)
MOV (R1), R4
3\$: CMP R0, R4
BEQ 4\$
MOVB #DATERR, ERRYP
MOV R0, R3
SEC
BR 6\$
4\$: TST R2
BEQ 5\$
ASL R0
BCC 2\$
CLR R2
MOV #177776, R0
BR 2\$
5\$: SEC
ROL R0
BCS 2\$
ADD #1000, R1
CMP R1, HILIM
BLO 1\$
CLC
6\$: MOV (SP)+, R0
RTS PC

:SAVE R0
:TELL WEA ARE IN SUBTEST 'C'
:GET BASE ADDRESS OF MEMORY
:DATA = 1 IN LEAST SIGNIFICANT BIT
:INDICATE WE ARE SHIFTING A '1'
:WRITE LOCATION WITH GOOD DATA
:READ DATA FROM SAME LOCATION
:IS DATA THE SAME AS WRITTEN?
:YES, OK
:ERROR IS DATA ERROR
:GOOD DATA
:INDICATE THIS SUBTEST FAILED
:LEAVE THIS SUBTEST
:ARE WE SHIFTING A 1 OR A 0?
:ZERO
:SHIFT THE ONE OVER
:IF THE '1' HAS NOT BEEN SHIFTED...
:THRU THE 16 POSITIONS CONTINUE WITH 1
:ELSE START SHIFTING A '0'
:START WITH LSB = 0 ALL OTHERS 1'S
:CONTINUE WITH SHIFTING A 0
:MOVE '0' OVER ONE BIT POSITION
:HAS '0' BEEN IN ALL POSITIONS?
:NO CONTINUE WITH SHIFTING A 0
:CONTINUE TEST AT NEXT BOUNDARY
:DONE ALL OF MEMORY?
:NO
:INDICATE THIS SUBTEST SUCCESSFUL
:RESTORE R0

MICROB - MICROCODE MODULE B
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MODULE B, MICROTEST #1, MICROSUBTEST D

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495 001526' 010046
496 001530' 112767 000004 177352
497 001536' 012767 000377 177004
498 001544' 005003
499 001546' 016700 177332
500 001552' 016720 176772
501 001556' 020067 177324
502 001562' 103773
503 001564' 016700 176760
504
505 001570' 005002
506 001572' 050302
507 001574' 020267 177304
508 001600' 103450
509 001602' 020267 177300
510 001606' 103061
511 001610' 000312
512 001612' 005001
513 001614' 050301
514 001616' 020167 177262
515 001622' 103426
516 001624' 020167 177256
517 001630' 103033
518 001632' 011104
519 001634' 020102
520 001636' 001414
521 001640' 020004
522 001642' 001416
523
524 001644' 112767 000001 177237
525 001652' 010003
526 001654' 000261
527 001656' 000441
528 001660' 020067 176664
529 001664' 001405
530 001666' 000403
531
532 001670' 000300
533 001672' 020004
534 001674' 001363
535 001676' 000300

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.SBTL MODULE B, MICROTEST #1, MICROSUBTEST D
:*****
:ADDRESS BIT SHIF1 #1
:THIS TEST CHECKS FOR DUAL ADDRESS PROBLEMS BY FIRST WRITING A BACKGROUND
:PATTERN THROUGHOUT MEMORY.
:THEN STARTING AT THE LOWEST LOCATION IN A BANK IT WRITES THE COMPLEMENT
:OF THE BACKGROUND PATTERN. THEN READS THE MEMORY FOR CORRECT CONTENTS
:IT THEN SHIFTS A '1' THROUGH THE ADDRESS POINTER AND REPEATS THE ABOVE.
:IT DOES THIS FOR EACH BANK.
:*****
MICB1D: MOV R0,-(SP) ;SAVE R0
MOV #4,SUBNUM ;TELL WE ARE IN SUBTEST 4
MOV #377,BAKPAT ;LOAD BAKPAT CONSTANT
1$: CLR R3 ;CONTAINS BANK ADDRESS
2$: MOV LOWLIM,R0 ;WRITE LINK MEMORY WITH BACKGROUND...
3$: MOV BAKPAT,(R0)+ ;PATTERN
CMP R0,HILIM
BLO 3$
MOV BAKPAT,R0 ;R0 CONTAINS GOOD DATA
4$: CLR R2 ;R2 WILL BE OUR 'WRITTEN TO' ADDRESS
6$: BIS R3,R2 ;INDEX INTO THIS BANK
CMP R2,LOWLIM ;IS RESULT LESS THAN MEM BASE?
BLO 16$ ;YES, DON'T USE THIS ADDRESS
CMP R2,HILIM ;IS RESULT LARGER THAN MEM TOP?
BHS 20$ ;YES, DON'T USE THIS ADDRESS EITHER
SWAB (R2) ;WRITE COMPLEMENT OF PATTERN
CLR R1 ;R1 WILL BE OUR 'READ FROM' ADDRESS
7$: BIS R3,R1 ;INDEX INTO THIS BANK
CMP R1,LOWLIM ;IS RESULT LESS THAN MEM BASE?
BLO 12$ ;YES, DON'T USE THIS ADDRESS
CMP R1,HILIM ;IS RESULT LARGER THAN MEM TOP?
BHS 15$ ;YES, DON'T USE THIS ADDRESS EITHER
MOV (R1),R4 ;READ DATA
CMP R1,R2 ;IS 'READ FROM' AND 'WRITTEN TO' SAME?
BEQ 10$ ;YES, GO CHECK DATA
CMP R0,R4 ;NO, DATA READ SHOULD BE SAME AS BAKPAT
BEQ 12$ ;IF SO CONTINUE WITH NEW INDEX
8$: MOV #ADRERR,ERRTYP ;INDICATE ADDRESS ERROR
MOV R0,R3 ;GET GOOD DATA
SEC ;INDICATE THIS SUBTEST FAILED
BR 25$ ;LEAVE THIS SUBTEST
CMP R0,BAKPAT ;DOES R0 CONTAIN SWAPPED DATA?
BEQ 12$ ;NO
BR 11$ ;YES
10$: SWAB R0 ;MAKE GOOD DATA LIKE SWAPPED BAKPAT
CMP R0,R4 ;IS DATA READ SAME AS DATA WRITTEN?
BNE 8$ ;NO, ERROR
11$: SWAB R0 ;RESTORE GOOD DATA TO BAKPAT

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MICROB - MICROCODE MODUL B
 MICROB.MAC 10-JAN-83 17:52

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 MODULE B, MICROTEST #1, MICROSUBTEST E

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.SBTTL MODULE B, MICROTEST #1, MICROSUBTEST E

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:*****
:ADDRESS BIT SHIFT #2
:THIS TEST CHECKS FOR DUAL ADDRESSING PROBLEMS BY WRITING EACH LOCATION
:OF MEMORY WITH ITS ADDRESS AND THEN VERIFYING EACH LOCATION.
:IT THEN DOES THE SAME THING BUT USING THE COMPLEMENT OF THE ADDRESS
:AS DATA.
:*****
    
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MICB1E: MOV      R0,-(SP)          ;SAVE R0
        MOVB     #5,SUBNUM        ;TELL WHICH SUBTEST WE ARE IN
        CLR      R3              ;FLAG INDICATING ADDRESS IS COMPLEMENTED
        MOV      LOWLIM,R1        ;GET STARTING ADDRESS OF MEMORY
1$:     MOV      R1,R0            ;GET ADDRESS TO WORK WITH
        TST      R3              ;SHOULD WE COMPLEMENT THE DATA TO STOP ?
        BEQ      2$              ;NO STORE AS IS
        COM      R0              ;COMPLEMENT DATA
        MOV      R0,(R1)+         ;WRITE DATA
        CMP      R1,HILIM        ;IS NEW ADDRESS LARGER THAN MEM?
        BLO     1$              ;NO, KEEP FILLING MEMORY
        MOV      -(R1),R4        ;READ DATA STORED
        CMP      R0,R4          ;IS DATA READ SAME AS WRITTEN?
        BEQ      4$              ;YES
        MOVB     #ADRERR,ERRTYP   ;INDICATE ADDRESS ERROR
        MOV      R0,R3          ;GET GOOD DATA
        SEC      SEC            ;INDICATE FAILURE
        BR       10$            ;LEAVE THIS SUBTEST
        MOV      R1,R0          ;CALC GOOD DATA FOR NEXT LOCATION
        SUB      #2,R0
        TST      R3
        BEQ      5$
        COM      R0
        CMP      R1,LOWLIM      ;HAVE WE CHECKED ALL LOCATIONS
        BHI     3$              ;NOT YET
        COM      R3              ;HAVE WE DONE IT COMPLEMENTED?
        BNE     1$              ;NO, REPEAT WITH COMPLEMENT
        CLC
        MOV      (SP)+,R0        ;INDICATE SUCCESS
        RTS      PC             ;RESTORE R0
    
```

MICROB - MICROCODE MODULE B
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MODULE B, MICROTEST #1, MICROSUBTEST F

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.SBTTL MODULE B, MICROTEST #1, MICROSUBTEST F

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:MARCH TEST

:THIS TEST WRITES A BACKGROUND PATTERN IN ALL OF MEMORY

:1-READ EVERY LOCATION FOR CORRECT DATA, SWAPS BYTES AT EACH LOCATION
:AND PROCEED IN MAX TO MIN DIRECTION
:2-READ EVERY LOCATION FOR SWAPPED DATA, WRITES ORIGINAL PATTERN IN EACH
:LOCATION AND PROCEEDS IN MIN TO MAX DIRECTION
:3-REPEAT 1 GOING IN MIN TO MAX DIRECTION
:4-REPEAT 2 GOING IN MAX TO MIN DIRECTION

.....

002102	010046			MICB1F:	MOV	R0,-(SP)	:SAVE R0
002104	112767	000006	176776		MOVB	#6,SUBNUM	:TELL WHICH SUBTEST WE ARE IN
002112	005003				CLR	R3	:ADDRESS DIRECTION FLAG 0 = MIN.->MAX
002114	016701	176764		10\$:	MOV	LOWLIM,R1	:FILL MEMORY WITH BACKGROUND PATTERN
002120	012700	000377			MOV	#377,R0	:BACKGROUND PATTERN=LOW BYTE ALL 1'S
002124	010021			12\$:	MOV	R0,(R1)+	
002126	020167	176754			CMP	R1,HILIM	
002132	103774				BLO	12\$	
002134	014104			20\$:	MOV	-(R1),R4	:STARTING FROM THE TOP, READ DATA
002136	020004				CMP	R0,R4	:R0 = GOOD DATA, R4 = DATA READ
002140	001406				BEQ	30\$:IF SAME OK
002142	112767	000000	176741		MOVB	#DATERR,ERRTYP	:INDICATE DATA ERROR
002150	010003				MOV	R0,R3	:GET GOOD DATA
002152	000261				S-C		:INDICATE FAILURE
002154	000454				B	200\$:LEAVE THIS SUBTEST
002156	000300			30\$:	SWAB	R0	:NEW GOOD DATA PATTERN
002160	010011				MOV	R0,(R1)	:STORE AT SAME PLACE
002162	011104				MOV	(R1),R4	:READ IT BACK
002164	020400				CMP	R4,R0	:R0=GOOD DATA, R4=DATA READ
002166	001406				BEQ	40\$:IF SAME OK
002170	112767	000000	176713		MOVB	#DATERR,ERRTYP	:INDICATE DATA ERROR
002176	010003				MOV	R0,R3	:GET GOOD DATA
002200	000261				SEC		:FAILURE
002202	000441				BR	200\$:LEAVE THIS SUBTEST
002204	000300			40\$:	SWAB	R0	:SWITCH GOOD DATA AGAIN
002206	001 7				BNE	90\$:IF ORIGINAL PATTERN THEN WE ARE...
							:READING THE MEMORY TO CONTAIN A...
							:BACKGROUND OF LOW BYTE = ALL 1'S...
							:AND WRITING IT BACK SWAPPED AND...
							:VERIFING SWAPPED DATA
002210	005703			50\$:	TST	R3	
002212	001027				BNE	100\$	
002214	062701	000002		60\$:	ADD	#2,R1	:WE ARE GOING MIN->MAX SO ADJUST POINTER
002220	020167	176662			CMP	R1,HILIM	:WE AT MAX?
002224	103012				BHS	80\$:YES
002226	011104			70\$:	MOV	(R1),R4	:READ DATA
002230	020004				CMP	R0,R4	:R0=GOOD DATA, R4 DATA READ
002232	001751				BEQ	30\$:OK IF SAME

MICROB - MICROCODE MODULE B
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MODULE B, MICROTEST #1, MICROSUBTEST F

662	002234'	112767	000000	176647	MOVB	#DATERR,ERRTYP	:INDICATE DATA ERROR
663	002242'	010003			MOV	R0,R3	:GET GOOD DATA
664	002244'	000261			SEC		:INDICATE FAILURE
665	002246'	000417			BR	200\$:LEAVE THIS SUBTEST
666	002250'	000742			BR	30\$	
667	002252'	000300		80\$:	SWAB	R0	:SWITCH GOOD DATA
668	002254'	001727			BEQ	20\$:IF LOW BYTE = ALL 0'S GO IN MAX->MIN
669	002256'	005103			COM	R3	:ELSE GO IN MIN->MAX DIRECTION
670	002260'	016701	176620		MOV	LOWLIM,R1	:SET ADDRESS POINTER TO MIN
671	002264'	000760			BR	70\$	
672							
673	002266'	005703		90\$:	TST	R3	:ARE WE GOING MIN->MAX?
674	002270'	001347			BNE	50\$:YES
675							
676	002272'	020167	176606	100\$:	CMP	R1,LOWLIM	:NO, CHECK TO SEE IF WE ARE AT MIN
677	002276'	101316			BHI	20\$:NOT YET
678	002300'	000300			SWAB	R0	:WE ARE AT MIN SO SWITCH GOOD DATA
679	002302'	001751			BEQ	70\$:IF LOW BYTE = ALL 0'S ELSE WE ARE DONE
680	002304'	000241			CLC		:INDICATE SUCCESS
681	002306'	012600		200\$:	MOV	(SP)+,R0	:RESTORE R0
682	002310'	000207			RTS	PC	
683							
684	002312'	002314			MICBSZ::MICBSZ-MICROB+2		:SIZE OF MICROCODE MODULE B
685		000001			.END		

MICROB - MICROCODE MODULE B
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CROSS REFERENCE TABLE -- USER SYMBOLS

ADDREG= 177774		103#																
ADRERR= 000001		101#	414	524	589													
BAKPAT 000557R	002	235#	497*	500	503	528	555*											
BIT0 = 000001		51#	81															
BIT1 = 000002		50#	82															
BIT1C = 002000		41#	53	70														
BIT11 = 004000		40#	69															
BIT12 = 010000		39#	53	68	89													
BIT13 = 020000		38#	67	88														
BIT14 = 040000		37#	66	87	210													
BIT15 = 100000		36#	65	86	215	218												
BIT2 = 000004		49#	83															
BIT3 = 000010		48#	84															
BIT4 = 000020		47#	85															
BIT5 = 000C40		46#																
BIT6 = 00C100		45#																
BIT7 = 000200		44#																
BIT8 = 000400		43#	53	71														
BIT9 = 001000		42#																
BOTH 001112R	002	258*	288	290*	306	332#												
B1STBL 001066R	002	263	320#															
CLRFIF= 021034		22#																
CMDREG= 177776		104#	119*															
CSRFLG= 000001		81#	167	198														
CSRVEC= 000064		74#	144*	145*														
CSWRIT 000376R	002	143	198#															
DATERR= 000000		100#	365	420	459	635	644	662										
DMACSR= 021002		9#	209	218*														
DMAF = 021022		17#																
DMAMNT 000432R	002	138	209#															
DMATO = 021004		10#																
DMAT1 = 021006		11#																
DMAVEC= 000114		75#	139*	140*														
DMAWC = 021024		18#																
DMDON. 000546R	002	209*	210	215	234#													
DNI = 004000		69#	123	191														
ERRFLG= 000002		82#	201															
ERRINT 000406R	002	125	201#															
ERRTYP 0C1111R	002	331#	359*	365*	414*	420*	459*	524*	589*	635*	644*	662*						
FATI = 000400		71#																
FLG2 000530R	002	160*	163	167	192*	198*	201*	222*	229#	265*	355	516	553	583				
HILIM 0C1106R	002	260*	292*	329#	350	370	425	429	475	501	509	516	553	583				
		629	657															
INERR = 000003		80#	189															
INMON = 000001		78#	121	175	187													
INTST = 000002		79#	256															
LOADR = 020000		97#	98															
IOSIZ = 020000		93#	98															
IPCSRO= 021000		8#	123*	172	191*	202*	212*	217*	223*									
IPCSR1= 021020		16#	122*	175*	190*	257*	317*											
IPCSR2 000536R	002	158*	159*	232#	274	275												
LASFTP= 012400		53#																
LFRBUF= 021032		21#																
LINADR= 100000		99#	102															
LINSIZ= 077774		95#	102															
LOWLIM 001104R	002	259*	291*	328#	348	353	398	399	452	499	507	514	577	598				

MICROB - MICROCODE MODULE B
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CROSS REFERENCE TABLE -- USER SYMBOLS

SUBNUM	001110R	002	280	330#	345*	396*	451*	496*	575*	624*							
TBLB	000526R	007	181	227#													
TIMINT	000424R	002	148	206#													
TXI	= 010000		68#														
UNIERR	= 020000		88#	202													
WCSADR	= 000000		96#	97													
WCSSIZ	= 020000		92#	97	260	291	292	302	311								
.	= 002314R	002	125	133	138	143	148	169	181	204	219	225	227	263	294		
			301	309	320	321	322	323	324	325							
. ABS.	000000	000															
	000000	001															
MICRB	002314	002															

ERRORS DETECTED: 0
DEFAULT GLOBALS GENERATED: 0

MICROB.OBJ,MICROB.LST/CRF/DOC/NL:TOC/SOL=MICROB.MAC
RUN-TIME: .9 1 .3 SECONDS
RUN-TIME RATIO: 43/2=15.4
CORE USED: 8K (16 PAGES)

DOCUMENT PAGES: 18

MICROC - MICROCODE MODULE C
MICROC.MAC 10-JAN-83 17:53

MACY11 30(1046) 11-JAN-83 09:51 PAGE 1

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1
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3      .TITLE  MICROC - MICROCODE MODULE C
4      000000' .CSECT  MICRC
5
6      .SBTTL  REGISTER DEFINITIONS USED BY THE T11
7
8      021000  IPCSR0 =      21000  :INTERNAL PCSRO ADDRESS
9      021002  DMACSR =      21002  :DMA ENGINE CONTROL STATUS REGISTER
10     021004  DMAT0  =      21004  :DMA ENGINE TO ADDRESS REGISTER #0
11     021006  DMAT1  =      21006  :DMA ENGINE TO ADDRESS REGISTER #1
12     021010  MDMA0  =      21010  :MICROCPU DMA TO ADDRESS REGISTER #0
13     021012  MDMA1  =      21012  :MICROCPU DMA TO ADDRESS REGISTER #1
14     021014  MDMAR0 =      21014  :MICROCPU DMA DATA REGISTER - AUTO INCREMENT R/O
15     021016  MDMAR1 =      21016  :MICROCPU DMA DATA REGISTER - AUTO DECREMENT R/O
16     021020  IPCSR1 =      21020  :INTERNAL PCSR1 ADDRESS
17     021022  DMAF   =      21022  :DMA ENGINE FROM ADDRESS REGISTER
18     021024  DMAWC  =      21024  :DMA ENGINE WORD COUNT REGISTER
19     021026  MDMAW0 =      21026  :MICROCPU DMA DATA REGISTER - AUTO INCREMENT W/O
20     021030  LTAC   =      21030  :LINK TRANSMIT ADDRESS COUNTER REGISTER
21     021032  LRBUF  =      21032  :LINK RECIEVE BUFFER ADDRESS FIFO
22     021034  CLRFFIF =      21034  :CLEAR FIFO
23     021036  MDMAW1 =      21036  :MICROCPU DMA DATA REGISTER - AUTO DECREMENT W/O
24     021040  PCSRSW =      21040  :SWITCH PACK REGISTER
25     021042  MDMSR  =      21042  :MICROCPU DMA STATUS REGISTER
26     021044  LRBUF  =      21044  :LINK RECIEVE BUFFER COMPLETED
27     021060  PHYAD0 =      21060  :PHYSICAL ADDRESS ROM BYTE 0
28     021062  PHYAD1 =      21062  :PHYSICAL ADDRESS ROM BYTE 1
29     021064  PHYAD2 =      21064  :PHYSICAL ADDRESS ROM BYTE 2
30     021066  PHYAD3 =      21066  :PHYSICAL ADDRESS ROM BYTE 3
31     021070  PHYAD4 =      21070  :PHYSICAL ADDRESS ROM BYTE 4
32     021072  PHYAD5 =      21072  :PHYSICAL ADDRESS ROM BYTE 5
33
34     .SBTTL  OTHER DEFINITIONS USED BY THE MICROCODE
35
36     100000  BIT15  =      100000
37     040000  BIT14  =       40000
38     020000  BIT13  =       20000
39     010000  BIT12  =       10000
40     004000  BIT11  =        4000
41     002000  BIT10  =        2000
42     001000  BIT9   =        1000
43     000400  BIT8   =         400
44     000200  BIT7   =         200
45     000100  BIT6   =          00
46     000040  BIT5   =          40
47     000020  BIT4   =          20
48     000010  BIT3   =          10
49     000004  BIT2   =           4
50     000002  BIT1   =           2
51     000001  BIT0   =           1
52     .
53     012400  LASFTP =      BIT8!BIT10!BIT12 ;LOAD AND START FUNCTION TEST PATTERN
54     000340  PRI07  =       340
55     000300  PRI06  =       300
56     000240  PRI05  =       240

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MICROC - MICROCODE MODULE C
MICROC.MAC 10-JAN-83 17:53

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OTHER DEFINITIONS USED BY THE MICROCODE

57	000200	PRI04	=	200	
58	000140	PRI03	=	140	
59	000100	PRI02	=	100	
60	000040	PRI01	=	40	
61	000000	PRI00	=	0	
62		:			
63		:PCSR0 - PORT CONTROL STATUS REGISTER 0			
64		:			
65	100000	SERI	=	BIT15	
66	040000	PCEI	=	BIT14	
67	020000	RXI	=	BIT13	
68	010000	TXI	=	BIT12	
69	004000	DNI	=	BIT11	
70	002000	RCEI	=	BIT10	
71	000400	FATI	=	BIT8	
72		:			
73	000134	SANVEC=	134		:VECTOR ADDRESS FOR THE SANITY TIMER
74	000064	CSRVEC=	64		:VECTOR ADDRESS FOR CSR WRITE INTERRUPT
75	000114	DMAVEC=	114		:VECTOR ADDRESS FOR DMA DONE INTERRUPT
76	000140	PARVEC=	140		:VECTOR ADDRESS FOR LINK MEMORY PARITY ERROR
77	001000	STACK=	1000		:STACK LOCATION
78	000001	INMON=	1		:IN MICROMONITOR STATE
79	000002	INTST=	2		:IN A TEST STATE
80	000003	INERR=	3		:IN ERROR STATE
81	000001	CSRFLG=	BIT0		:CSR WRITE INTERRUPT OCCURED
82	000002	ERRFLG=	BIT1		:UNEXPECTED ERROR OCCURED
83	000004	PARFLG=	BIT2		:PARITY ERROR OCCURED
84	000010	NXMFLG=	BIT3		:NON-EXISTANT MEMORY ERROR OCCURED
85	000020	NPRFLG=	BIT4		:NPR TIMEOUT ERROR OCCURED
86	100000	NPRERR=	BIT15		:PCSR0 FLAG INDICATING NPR ERROR OCCURED
87	040000	NXMERR=	BIT14		:PCSRC FLAG INDICATING NON-EXISTANT MEMORY ERROR OCCURED
88	020000	UNIERR=	BIT13		:PCSR0 FLAG INDICATING UNEXPECTED INTERRUPT OCCURED
89	010000	PARERR=	BIT12		:PCSR0 FLAG INDICATING LINK MEMORY PARITY ERROR OCCURED
90	020000	SIZ4K=	20000		:4K WORDS
91	040000	SIZ8K=	SIZ4K*2		:8K WORDS
92	020000	WCSSIZ=	SIZ4K		:SIZE OF WRITEABLE CONTROL STORE
93	020000	IOSIZ=	SIZ4K		:SIZE OF I/O PAGE
94	040000	ROMSIZ=	SIZ8K		:SIZE OF ROM
95	077774	LINSIZ=	SIZ8K*2-4		:SIZE OF LINK MEMORY
96	000000	WCSADR=	0		:BASE ADDRESS OF WCS
97	020000	IOADR=	WCSADR+WCSSIZ		:BASE ADDRESS OF I/O PAGE
98	040000	ROMADR=	IOADR+IOSIZ		:BASE ADDRESS OF ROM
99	100000	LINADR=	ROMADR+ROMSIZ		:BASE ADDRESS OF LINK MEMORY
100	000000	DATERR=	0		:FLAG INDICATING DATA ERROR OCCURED
101	000001	ADRERR=	1		:FLAG INDICATING ADDRESS ERROR OCCURED
102	177774	MODREG=	LINADR+LINSIZ		:LINK MODE REGISTER
103	177774	ADDREG=	MODREG		:LINK STATION ADDRESS RAM REGISTER
104	177776	CMDREG=	MODREG+2		:LINK COMMAND REGISTER
105					

MICROC - MICROCODE MODULE C
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OTHER DEFINITIONS USED BY THE MICROCODE

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.SBTTL MICROCODE MODULE C

THIS MODULE CONTAINS A MICROMONITOR AND MICROCODE FOR:

- 1-CLEAR INSTRUCTION
- 2-PCSR0 INTERRUPT BIT TEST
- 3-SANITY TIMER TEST
- 4-COMPREHENSIVE LINK MEMORY TEST
- 5-DMA 'TO' ADDRESS REGISTER TEST
- 6-DMA 'FROM' ADDRESS REGISTER TEST
- 7-DMA BLOCK TRANSFER TEST

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MICROC::MTPS      #PRI07      ;DISABLE INTERRUPTS
MOV              #0,@#CMDREG ;TURN OFF THE LINK
MOV              #STACK,SP   ;SETUP STACK
MOV              #INMON,PCSR1;TELL HOST WE ARE IN MICROMONITOR
MOV              PCSR1,@#IPCSR1
MOV              #DNI,@#IPCSR0;TELL HOST THE LOAD AND START FINISHED
MOV              PC,R0       ;GET ADDRESS OF UNEXPECTED ERROR...
ADD              #ERRINT-. ,R0;HANDLER
CLR              R1         ;FILL ALL UNUSED VECTORS WITH TRAP...
10$:MOV          R0,(R1)+    ;HANDLER
MOV              #PRI07,(R1)+
CMP              R1,#1000
BLT              10$

MOV              PC,R0      ;SETUP PARITY TRAP VECTOR
ADD              #PARINT-. ,R0
MOV              R0,@#PARVEC
MOV              #PRI07,@#PARVEC+2

MOV              PC,R0      ;SETUP DMA INTERRUPT VECTOR
ADD              #DMAINT-. ,R0
MOV              R0,@#DMAVEC
MOV              #PRI06,@#DMAVEC+2

MOV              PC,R0      ;SETUP CSR WRITE VECTOR
ADD              #CSRWRT-. ,R0
MOV              R0,@#CSRVEC
MOV              #PRI04,@#CSRVEC+2

MOV              PC,R0      ;SETUP SANITY TIMER VECTOR
ADD              #TIMINT-. ,R0
MOV              R0,@#SANVEC
MOV              #PRI05,@#SANVEC+2

MOV              @#PCSR0W,R0;GET SWITCH PACK BITS
BIS              #176000,R0;MAP THEM INTO HOST I/O PAGE
ASL              R0        ;SHIFT OVER TO POSITION CORRECTLY
ASL              R0

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MICROC - MICROCODE MODULE C
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MICROCODE MODULE C

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162 000200' 006300 ASL R0
163 000202' 062700 000004 ADD #4,R0 :PCSR2 IS PCSR0+4
164 000206' 010067 000316 MOV RO,IPCSR2 :SAVE PCSR2 ADDRESS
165 000212' 012767 000003 000312 MOV #3,IPCSR2+2 :HIGH ORDER BITS 17:16
166 000220' 005067 000276 CLR FLG3 :INITIALIZE FLAG WORD
167 000224' 106427 000000 15$: MTPS #PRI00 :ALLOW INTERRUPTS
168
169 000230' 005767 000266 20$: TST FLG3 :WAIT FOR AN INTERRUPT
170 000234' 001775 BEQ 20$
171
172 000236' 106427 000340 MTPS #PRI07 :RAISE CPU PRIORITY
173 000242' 032767 000001 000252 BIT #CSRFLG,FLG3 :DID HOST GIVE US A COMMAND?
174 000250' 001001 BNE 30$ :YES
175 000252' 000777 BR . :NO, ERROR SO JUST SIT HERE
176 :FOR LACK OF ANYTHING BETTER TO DO
177
178 C00254' 113700 021000 30$: MOVB @#IPCSRO,R0 :GET WHAT HOST WROTE TO PCSRO
179 000260' 042700 177760 BIC #177760,R0 :STRIP ALL BUT COMMAND BITS
180 000264' 022700 000017 CMP #17,R0 :RETURN TO OPERATIONAL MICROCODE?
181 000270' 001425 BEQ 60$ :YES
182 000272' 010701 MOV PC,R1 :GET ADDRESS OF OUR COMMAND TABLE
183 000274' 062701 000206 ADD #TBLC-.,R1
184 000300' 006300 ASL RO :MAKE COMMAND A BYTE OFFSET
185 000302' 060001 ADD RO,R1 :USE IT TO INDEX INTO COMMAND TABLE
186 000304' 061101 ADD (R1),R1 :R1 NOW HAS COMMAND ROUTINE ADDRESS
187 000306' 004711 JSR PC,(R1) :EXECUTE AS COMMANDED FROM HOST
188 000310' 103404 BCS 40$ :ERROR OCCURRED
189 000312' 112767 000001 000206 MOVB #INMON,PCSR1 :INDICATE TO HOST WE ARE BACK IN...
190 000320' 000403 BR 45$ :MICROMONITR
191 000322' 112767 000003 000176 40$: MOVB #INERR,PCSR1 :INDICATE TO HOST ERROR OCCURRED
192 000330' 016737 000172 021020 45$: MOV PCSR1,@#IPCSR1
193 000336' 005067 000160 CLR FLG3 :RESET FLAG WORD
194 000342' 000730 BR 15$ :GO WAIT FOR ANOTHER COMMAND
195
196 000344' 005000 60$: CLR RO :FAKE SUCCESSFUL SELF TEST RESULTS
197 000346' 000137 040006 JMP @#40006 :START OPERATIONAL MICROCODE
198
199 000352' 052767 000001 000142 CSRWRT: BIS #CSRFLG,FLG3 :INDICATE A CSR WRITE INTERRUPT OCCURED
200 000360' 000002 RTI
201
202 000362' 052767 000002 000132 ERRINT: BIS #ERRFLG,FLG3 :INDICATE A UNEXPECTED INTERRUPT OCCURED
203 000370' 012737 020000 021000 MOV #UNIERR,@#IPCSRO :TELL HOST AN UNEXPECTED INTERRUPT
204 :HAPPENED
205 000376' 000777 BR . :JUST SIT HERE AND SPIN WHEELS
206 :COUNT ON THE HOST TO TIMEOUT WAITING
207
208 000400' 005267 000120 TIMINT: INC SANTIM :COUNT TICKS AS THEY OCCUR
209 000404' 000002 RTI
210
211 000406' 013767 021002 000124 DMAINT: MOV @#DMACSR,DMDONE :GET DMA STATUS
212 000414' 032767 040000 000116 BIT #BIT14,DMDONE :DID A NON-EXISTANT MEMORY INTERRUPT OCCUR?
213 000422' 001404 BEQ 10$ :NO
214 000424' 012737 040000 021000 MOV #NXMERR,@#IPCSR0 :YES, TELL HOST A NON-EXISTANT MEMORY
215 :LOCATION WAS ADDRESSED
216 000432' 000407 BR 20$
217 000434' 032767 100000 000076 10$: BIT #BIT15,DMDONE :DID A NPR TIMEOUT OCCUR?
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MICROCODE MODULE C

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000544' 105067 177757
000550' 000241
000552' 000207

.SBTTL MODULE C, MICRO CLEAR FUNCTION

CLRC: CLR B PCSR1+1 ;CLEAR THE SELF TEST FIELD
CLC
RTS PC

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MODULE C, MICROTEST #1

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254 .SBTTL MODULE C, MICROTEST #1
255
256 000554* 112767 000002 177744 MICC1: MOVB #INTST,PCSR1
257 000562* 016737 177740 021020 MOV PCSR1,@#IPCSR1
258 000570* 016737 177734 021010 MOV IPCSR2,@#MDMA0 ;PICK UP ADDRESS OF PCBB
259 000576* 016737 177730 021012 MOV IPCSR2+2,@#MDMA1
260 000604* 013700 021014 MOV @#MDMAR0,R0 ;R0 HAS CONTENTS OF HOST'S PCSR2
261 000610* 013701 021014 MOV @#MDMAR0,R1 ;R1 HAS CONTENTS OF HOST'S PCSR3
262 000614* 010037 021010 MOV R0,@#MDMA0 ;FETCH CONTENTS OF PCBB+0
263 000620* 010137 021012 MOV R1,@#MDMA1
264
265 ;AT THIS POINT MDMAR0 WILL CONTAIN THE INTERRUPT BIT THAT WE ARE TO SET
266
267 000624* 013737 021014 021000 MOV @#MDMAR0,@#IPCSR0 ;SET CORRESPONDING INTERRUPT BIT
268 000632* 112767 000001 177667 MOVB #1,PCSR1+1 ;TELL HOST WHAT TEST WE JUST FINISHED
269 000640* 000241 CLC ;SUCCESS
270 000642* 000207 RTS PC

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MICROC - MICROCODE MODULE C
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 MODULE C, MICROTTEST #2

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274 000644' 112767 000002 177654 MICC2:  MOVB  #INTST,PCSR1      ;TELL HOST WE ARE TESTING
275 000652' 016737 177650 021020      MOV   PCSR1,@#IPCSR1
276 000660' 005067 177640      CLR   SANTIM      ;CLEAR TIMER COUNT
277 000664' 106427 000200      MTPS  #PRI04      ;LOWER PRIORITY TO ALLOW TIMER INTERRUPT
278 000670' 026727 177630 000012 10$:  CMP   SANTIM,#10.  ;WAIT FOR TEN TICKS OF THE TIMER
279 000676' 002774      BLT   10$
280 000700' 106427 000340      MTPS  #PRI07      ;RAISE PRIORITY TO STOP TIMER
281 000704' 112767 000002 177615      MOVB  #2,PCSR1+1  ;TELL HOST WHAT TEST WE JUST FINISHED
282 000712' 016737 177610 021020      MOV   PCSR1,@#IPCSR1
283 000720' 012737 004000 021000      MOV   #DNI,@#IPCSRO
284 000726' 000241      CLC
285 000730' 000207      RTS   PC

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MODULE C, MICROTEST #3

.SBTTL MODULE C, MICROTEST #3

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: THIS MICROTEST CHECKS THE LINK MEMORY. IT DOES SO BY RUNNING A SERIES
: OF MICROSUBTESTS ON THE MEMORY BETWEEN 16K AND 32K (100000-177776).

: THE SUBTESTS ARE:

- A-ACCESS TEST
- B-ADDRESS SHIFT TEST
- C-DATA LATCH TEST
- D-ADDRESS BIT SHIFT #1
- E-ADDRESS BIT SHIFT #2
- F-MARCH TEST

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302 000732* 112767 000002 177566 MICC3:  MOVB  #INTST,PCSR1      ;TELL HOST WE ARE TESTING
303 000740* 016737 177562 021020      MOV   PCSR1,@#IPCSR1
304 000746* 010700      MOV   PC,R0          ;GET ADDRESS OF SUBTEST LIST
305 000750* 062700 000134      ADD   #C3STBL-.,R0
306
307 000754* 011001      10$:  MOV   (R0),R1        ;GET OFFSET FROM ENTRY TO SUBTEST
308 000756* 001437      BEQ   30$           ;NO MORE ENTRIES
309 000760* 060001      ADD   R0,R1        ;CALC ACTUAL SUBTEST ADDRESS
310 000762* 004711      JSR   PC,(R1)      ;GO EXECUTE SUBTEST
311 000764* 103403      BCS   20$          ;ERROR OCCURRED IN SUBTEST
312 000766* 062700 000002      ADD   #2,R0        ;POINT TO NEXT SUBTEST IN LIST
313 000772* 000770      BR    10$
314
315 000774* 016737 177530 021010 20$:  MOV   IPCSR2,@#MDMA0 ;GET CONTENTS OF HOST'S PCSR2 AND PCSR3
316 001002* 016737 177524 021012      MOV   IPCSR2+2,@#MDMA1
317 001010* 013700 021014      MOV   @#MDMAR0,R0  ;R0=CONTENTS OF PCSR2
318 001014* 013702 021014      MOV   @#MDMAR0,R2  ;R2=CONTENTS OF PCSR3
319 001020* 010037 021010      MCV   R0,@#MDMA0   ;POINT TO PCBB+0
320 001024* 010237 021012      MOV   R2,@#MDMA1
321 001030* 016737 000066 021026      MOV   SUBNUM,@#MDMAW0
322
323 001036* 010137 021026      MOV   R1,@#MDMAW0  ;LOAD PCBB+0 WITH SUBTEST #...
324 001042* 010337 021026      MOV   R3,@#MDMAW0  ;AND PCBB+1 WITH ERROR TYPE
325 001046* 010437 021026      MOV   R4,@#MDMAW0  ;LOAD PCBB+2 WITH FAILING ADDRESS
326 001052* 000261      SEC           ;LOAD PCBB+4 WITH GOOD DATA
327 001054* 000401      BR    40$         ;LOAD PCBB+6 WITH BAD DATA
328
329 001056* 000241      30$:  CLC           ;TELL MICROMONITOR THIS TEST WAS SUCCESSFUL
330 001060* 112767 000003 177441 40$:  MOVB  #3,PCSR1+1   ;TELL HOST WHICH TEST THIS WAS
331 001066* 016737 177434 021020      MOV   PCSR1,@#IPCSR1
332 001074* 012737 004000 021000      MOV   #DNI,@#IPCSR0
333 001102* 000207      RTS   PC          ;TELL HOST THIS MICROTEST IS FINISHED
334
335 001104* 000020      C3STBL: MICC3A-.
336 001106* 000160      MICC3B-.
337 001110* 000322      MICC3C-.
338 001112* 000436      MICC3D-.
339 001114* 000704      MICC3E-.
340 001116* 001024      MICC3F-.
341 001120* 000000      .WORD  0          ;END OF LIST

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MODULE C, MICROTST #3

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SEQ 356

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3 3 001122' 000
344 001123' 000
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SUBNUM: .BYTE 0
ERRTYP: .BYTE 0

:CURRENT SUBTEST # BEING EXECUTED
:TYPE OF ERROR, DATA =0, ADDRESS=1

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 MODULE C, MICROTEST #3, MICROSUBTEST A

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.SBTTL MODULE C, MICROTEST #3, MICROSUBTEST A

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:*****
:THIS IS AN ACCESS TEST OF LINK MEMORY. IT WRITES DATA
:ALL THROUGH 16K TO 32K(100000,177772) AND VERIFIES SAME. IT THEN WRITES
:ZEROS . IT ALSO CHECKS FOR PARITY ERRORS.
:*****
    
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MICC3A: MOV      R0,-(SP)           ;SAVE R0
        MOVB     #1,SUBNUM        ;TELL WE ARE IN SUBTEST A
        MTPS     #PRI06          ;ALLOW PARITY ERRORS

        MOV      SP,R0           ;SAVE THE STACK POINTER FOR A MOMENT
        MOV      #LINADR+LINSIZ,SP ;POINT STACK TO TOP OF LINK MEMORY
        MOV      #LINSIZ/2,R5     ;NUMBER OF WORDS IN LINK MEMORY
5$:     JSR      PC,10$          ;WRITE ADDRESS OF NEXT INSTUCTION
10$:    SOB      R5,5$           ;ALL OVER LINK MEMORY

        MOV      R0,SP           ;RESTORE STACK
        MOV      PC,R3           ;GET DATA THAT WAS WRITTEN TO LINK MEMORY
        ADD      #10$-.,R3

        MOV      #LINADR,R1      ;POINT TO BASE OF LINK MEMORY
        MOV      (R1),R'        ;READ DATA
        BIT      #PARFLG,FLG3    ;DID A PARITY ERROR OCCUR?
        BNE     20$             ;YES

15$:    CMP      R3,R4           ;NO, WAS DATA GOOD?
        BEQ     30$             ;YES
20$:    MOVB     #INERR,PCSR1     ;TELL HOST THIS SUBTEST FAILED
        MOVB     #DATERR,ERRTYP  ;TELL MICROMONITOR TYPE OF ERROR
        SEC     40$             ;TELL MICROMONITOR ERROR OCCURRED
        BR      40$

30$:    ADD      #2,R1           ;POINT TO NEXT ADDRESS
        CMP     R1,#LINADR+LINSIZ ;DONE ALL OF LINK MEMORY?
        BNE     12$             ;NOT YET

        MOV      #LINADR,R1      ;OK NOW FILL WITH ZEROES
32$:    CLR     (R1)+
        CMP     R1,#LINADR+LINSIZ
        BNE     32$

35$:    CLC
40$:    MOV     (SP)+,R0         ;INDICATE SUCCESS
        RTS     PC              ;RESTORE R0
    
```

MICROC - MICROCODE MODULE C
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MODULE C, MICROTEST #3, MICROSUBTEST B

.SBTTL MODULE C, MICROTEST #3, MICROSUBTEST B

:ADDRESS SHIFT TEST
:THIS TEST ASSUMES ALL MEMORY BETWEEN 100000 AND 177776 IS ZEROS
:IT CHECKS FOR PROPER BANK SELECTION BY WRITING 1'S IN A LOCATION
:AND CHECKING FOR 1'S IN THE SAME LOCATION OF OTHER 4K BANKS (ADDRESS ERROR).
:IT ALSO CHECKS FOR DATA ERRORS I.E. NON-ZERO DATA IN LOCATIONS NOT
:WRITTEN WITH 1'S AND MAKES SURE LOCATIONS WRITTEN WITH 1'S HAVE 1'S IN THEM

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001266' 010046      MICC3B: MOV      R0,-(SP)      ;SAVE R0
001270' 112767 000002 177624  MOVB     #2,SUBNUM    ;TELL HIM WE ARE IN THE SECOND SUBTEST
                                MOV      #LINADR,R0      ;SET 'WRITTEN TO' ADDRESS
10$:  MOV      #LINADR,R1    ;SET 'READ FROM' ADDRESS
001276' 012700 100000      MOV      #177777,(R0)  ;WRITE DATA INTO LOCATION AT BASE...
001302' 012701 100000      ;OF A 4K BOUNDARY
001306' 012710 177777      ;READ DATA FROM MEMORY ADDRESS WHICH...
                                MOV      (R1),R4        ;IS AN EVEN 4K INCREMENT AWAY
001312' 011104 20$:  CMP      R0,R1          ;IS 'READ FROM' AND 'WRITTEN TO'
                                BEQ      70$            ;ADDRESSES THE SAME?
001314' 020001          CLR      R3          ;YES, SO DATA IN BOTH SHOULD BE THE SAME
                                TST      R4            ;GOOD DATA IS ZEROS
001316' 001416          BEQ      70$            ;DATA IN 'READ FROM' MUST BE 0'S
001320' 005003          ;OK GO CHANGE 'READ FROM' ADDRESS
001322' 005704          ;ERROR OCCURRED BUT WE DON'T KNOW IF...
001324' 001430          ;IT IS A DATA ERROR OR AN ADDRESS ERROR
                                CMP      #177777,R4    ;WAS DATA READ ALL 1'S?
001326' 022704 177777      BNE      40$        ;NO, SO IT WAS A DATA ERROR
001332' 001015          MOVB     #ADRERR,ERRTYP ;YES, IT WAS AN ADDRESS ERROR
001334' 112767 000001 177561  MOVB     #INERR,PCSR1 ;TELL HOST THIS TEST FAILED
001342' 112767 000003 177156  SEC          ;INDICATE FAILURE
001350' 000261          BR       60$        ;LEAVE THIS SUBTEST
001352' 000425          ;DATA READ MUST BE ALL 1'S
001354' 022704 177777 70$:  CMP      -177777,R4    ;IT IS GOOD
001360' 001412          BEQ      50$            ;GOOD DATA IS ONES
001362' 012703 177777      MOV      #177777,R3    ;IT WAS A DATA ERROR
001366' 112767 000000 177527 40$:  MOVB     #DATERR,ERRTYP ;TELL HOST THIS TEST FAILED
001374' 112767 000003 177124  MOVB     #INERR,PCSR1 ;INDICATE FAILURE
001402' 000261          SEC          ;LEAVE THIS SUBTEST
001404' 000410          BR       60$
001406' 062701 020000 50$:  ADD      #20000,R1   ;CHANGE 'READ FROM' ADDRESS BY 4K
001412' 103337          BCC     20$        ;CONTINUE SAME 'WRITTEN TO' IF NOT PAST 32K
001414' 005010          CLR      (R0)        ;CLEAR OLD 'WRITTEN TO' ADDRESS
001416' 062700 020000      ADD      #20000,R0   ;CHANGE 'WRITTEN TO' ADDRESS BY 4K
001422' 103327          BCC     10$        ;CONTINUE IF NOT PAST 32K
001424' 000241          CLC          ;INDICATE SUCCESS OF THIS SUBTEST
001426' 012600 60$:  MOV      (SP)+,R0   ;RESTORE R0
001430' 000207          RTS      PC

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MODULE C, MICROTEST #3, MICROSUBTEST C

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.SBTTL MODULE C, MICROTEST #3, MICROSUBTEST C

:DATA LATCH TEST
:AT THE FIRST LOCATION OF EACH 4K BANK A '1' IS SHIFTED THROUGH EACH
:BIT POSITION AND CHECKED FROM LSB TO MSB.
:THEN IN THE SAME LOCATION A '0' IS SHIFTED THROUGH EACH BIT POSITION
:AND CHECKED.

001432' 010046
001434' 112767 000003 177460
001442' 012701 100000
001446' 012700 000001
001452' 010002
001454' 010011
001456' 011104
001460' 020004
001462' 001411
001464' 112767 000000 177431
001472' 010003
001474' 112767 000003 177024
001502' 000261
001504' 000417
001506' 005702
001510' 001406
001512' 006300
001514' 103357
001516' 005002
001520' 012700 177776
001524' 000753
001526' 000261
001530' 006100
001532' 103750
001534' 062701 020000
001540' 103342
001542' 000241
001544' 012600
001546' 000207

MICC3C: MOV R0,-(SP)
MOV #3,SUBNUM
MOV #LINADR,R1
1\$: MOV #1,R0
MOV R0,R2
2\$: MOV R0,(R1)
MOV (R1),R4
3\$: CMP R0,R4
BEQ 4\$
MOVB #DATERR,ERRTYP
MOV R0,R3
MOVB #INERR,PCSR1
SEC
BR 6\$
4\$: TST R2
BEQ 5\$
ASL R0
BCC 2\$
CLR R2
MOV #177776,R0
BR 2\$
5\$: SEC
ROL R0
BCS 2\$
ADD #SIZ4K,R1
BCC 1\$
CLC
6\$: MOV (SP)+,R0
RTS PC

:SAVE R0
:TELL WEA ARE IN SUBTEST 'C'
:GET BASE ADDRESS OF LINK MEMORY
:DATA = 1 IN LEAST SIGNIFICANT BIT
:INDICATE WE ARE SHIFTING A '1'
:WRITE LOCATION WITH GOOD DATA
:READ DATA FROM SAME LOCATION
:IS DATA THE SAME AS WRITTEN?
:YES, OK
:ERROR IS DATA ERROR
:GOOD DATA
:TELL HOST THIS TEST FAILED
:INDICATE THIS SUBTEST FAILED
:LEAVE THIS SUBTEST
:ARE WE SHIFTING A 1 OR A 0?
:ZERO
:SHIFT THE ONE OVER
:IF THE '1' HAS NOT BEEN SHIFTED...
:THRU THE 16 POSITIONS CONTINUE WITH 1
:ELSE START SHIFTING A '0'
:START WITH LSB = 0 ALL OTHERS 1'S
:CONTINUE WITH SHIFTING A 0
:MOVE '0' OVER ONE BIT POSITION
:HAS '0' BEEN IN ALL POSITIONS?
:NO CONTINUE WITH SHIFTING A 0
:CONTINUE TEST AT NEXT 4K BOUNDARY...
:IF NOT PAST 32K
:INDICATE THIS SUBTEST SUCCESSFUL
:RESTORE R0

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MODULE C, MICROTEST #2, MICROSUBTEST D

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.SBTTL MCDULE C, MICROTEST #2, MICROSUBTEST D

:ADDRESS BIT SHIFT #1

:THIS TEST CHECKS FOR DUAL ADDRESS PROBLEMS BY FIRST WRITING A BACKGROUND
:PATTERN THROUGHOUT LINK MEMORY.
:THEN STARTING AT THE LOWEST LOCATION IN A BANK IT WRITES THE COMPLEMENT
:OF THE BACKGROUND PATTERN. THEN READS THE MEMORY FOR CORRECT CONTENTS
:IT THEN SHIFTS A '1' THROUGH THE ADDRESS POINTER AND REPEATS THE ABOVE.
:IT DOES THIS FOR EACH 4K BANK.

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MICCC3D: MOV      R0,-(SP)          ;SAVE R0
          MOVB     #4,SUBNUM       ;TELL WE ARE IN SUBTEST 4
          MOV      #377,BAKPAT     ;LOAD BAKPAT CONSTANT
1$:      CLR      R3              ;CONTAINS 4K BANK ADDRESS
2$:      MOV      #LINADR,R0       ;WRITE LINK MEMORY WITH BACKGROUND...
3$:      MOV      BAKPAT,(R0)+     ;PATTERN
          CMP      R0,#LINADR+LINSIZ
          BLO     3$
          MOV      BAKPAT,R0      ;R0 CONTAINS GOOD DATA
4$:      CLR      R2              ;R2 WILL BE OUR 'WRITTEN TO' ADDRESS
6$:      BIS      R3,R2           ;INDEX INTO THIS 4K BANK
          CMP      R2,#LINADR     ;IS RESULT LESS THAN LINK MEM BASE
          BLO     16$            ;YES, DON'T USE THIS ADDRESS
          CMP      R2,#LINADR+LINSIZ ;IS RESULT LARGER THAN LINK MEM TOP?
          BHIS   20$            ;YES, DON'T USE THIS ADDRESS EITHER
          SWAB    (R2)           ;WRITE COMPLEMENT OF PATTERN
          CLR      R1              ;R1 WILL BE OUR 'READ FROM' ADDRESS
7$:      BIS      R3,R1           ;INDEX INTO THIS 4K BANK
          CMP      R1,#LINADR     ;IS RESULT LESS THAN LINK MEM BASE?
          BLO     12$            ;YES, DON'T USE THIS ADDRESS
          CMP      R1,#LINADR+LINSIZ ;IS RESULT LARGER THAN LINK MEM TOP?
          BHIS   15$            ;YES, DON'T USE THIS ADDRESS EITHER
          MOV     (R1),R4        ;READ DATA
          CMP     R1,R2         ;IS 'READ FROM' AND 'WRITTEN TO' SAME?
          BEQ    10$           ;YES, GO CHECK DATA
          CMP     R0,R4         ;NO, DATA READ SHOULD BE SAME AS BAKPAT
          BEQ    12$           ;IF SO CONTINUE WITH NEW INDEX
8$:      MOVB    #ADRERR,ERRTYP   ;INDICATE ADDRESS ERROR
          MOV     R0,R3         ;GET GOOD DATA
          SEC     ;INDICATE THIS SUBTEST FAILED
          MOVB   #INERR,PCSR1    ;TELL HOST THIS TEST FAILED
          BR     25$           ;LEAVE THIS SUBTEST
          CMP     R0,BAKPAT     ;DOES R0 CONTAIN SWAPPED DATA?
          BEQ    12$           ;NO
          BR     11$           ;YES
10$:     SWAB    R0             ;MAKE GOOD DATA LIKE SWAPPED BAKPAT
          CMP     R0,R4         ;IS DATA READ SAME AS DATA WRITTEN?
          BNE    8$            ;NO, ERROR
```


MICROC - MICROCODE MODULE C
 MICROC.MAC 10-JAN-83 17:53

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 MODULE C, MICROTEST #3, MICROSUBTEST E

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.SBTTL MODULE C, MICROTEST #3, MICROSUBTEST E

```

:*****
:ADDRESS BIT SHIFT #2
:THIS TEST CHECKS FOR DUAL ADDRESSING PROBLEMS BY WRITING EACH LOCATION
:OF LINK MEMORY WITH ITS ADDRESS AND THEN VERIFYING EACH LOCATION.
:IT THEN DOES THE SAME THING BUT USES THE COMPLEMENT OF THE ADDRESS
:AS DATA.
:*****
    
```

```

MICC3E: MOV      R0,-(SP)          ;SAVE R0
        MOVB     #5,SUBNUM        ;TELL WHICH SUBTEST WE ARE IN
        CLR      R3              ;FLAG INDICATING ADDRESS IS COMPLEMENTED
        MOV      #LINADR,R1       ;GET STARTING ADDRESS OF LINK MEMORY
1$:     MOV      R1,R0           ;GET ADDRESS TO WORK WITH
        TST      R3              ;SHOULD WE COMPLEMENT THE DATA TO STORE?
        BEQ      2$              ;NO STORE AS IS
        COM      P0              ;COMPLEMENT DATA
        MOV      R0,(R1)+         ;WRITE DATA
        CMP      R1,#LINADR+LINSIZ ;IS NEW ADDRESS LARGER THAN LINK MEM?
        BLO      1$              ;NO, KEEP FILLING LINK MEMORY

        MOV      -(R1),R4         ;READ DATA STORED
        CMP      R0,R4           ;IS DATA READ SAME AS WRITTEN?
        BEQ      4$              ;YES
        MOVB     #ADRERR,ERRTYP    ;INDICATE ADDRESS ERROR
        MOV      R0,R3           ;GET GOOD DATA
        SEC      SEC              ;INDICATE FAILURE
        MOVB     #INERR,PCSR1      ;TELL HOST THIS TEST FAILED
        BR       10$             ;LEAVE THIS SUBTEST
        MOV      R1,R0           ;CALC GOOD DATA FOR NEXT LOCATION
        SUB      #2,R0
        TST      R3
        BEQ      5$
        COM      R0
        CMP      R1,#LINADR       ;HAVE WE CHECKED ALL LOCATIONS
        BHI      3$              ;NOT YET
        COM      R3              ;HAVE WE DONE IT COMPLEMENTED?
        BNE      1$              ;NO, REPEAT WITH COMPLEMENT
        CLL      CLL             ;INDICATE SUCCESS
10$:    MOV      (SP)+,R0         ;RESTORE R0
        RTS      PC
    
```


MICROC - MICROCODE MODULE C
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MODULE C, MICROTEST #3, MICROSUBTEST F

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623 .SBTTL MODULE C, MICROTEST #3, MICROSUBTEST F
624
625 :*****
626 :
627 :MARCH TEST
628 :
629 :THIS TEST WRITES A BACKGROUND PATTERN IN ALL OF LINK MEMORY
630 :
631 :1-READ EVERY LOCATION FOR CORRECT DATA, SWAPS BYTES AT EACH LOCATION
632 :AND PROCEED IN MAX TO MIN DIRECTION
633 :2-READ EVERY LOCATION FOR SWAPPED DATA, WRITES ORIGINAL PATTERN IN EACH
634 :LOCATION AND PROCEEDS IN MIN TO MAX DIRECTION
635 :3-REPEAT 1 GOING IN MIN TO MAX DIRECTION
636 :4-REPEAT 2 GOING IN MAX TO MIN DIRECTION
637 :
638 :*****
639
640 002142 010046 MIC3F: MOV R0,-(SP) ;SAVE R0
641 002144 112767 000006 176750 MOVB #6,SUBNUM ;TELL WHICH SUBTEST WE ARE IN
642 002152 005003 CLR R3 ;ADDRESS DIRECTION FLAG 0 = MIN.->MAX
643 002154 012701 100000 10$: MOV #LINADR,R1 ;FILL LINK MEMORY WITH BACKGROUND PATTERN
644 002160 012700 000377 MOV #377,R0 ;BACKGROUND PATTERN=LOW BYTE ALL 1'S
645 002164 010021 12$: MOV #0,(R1)+
646 002166 020127 177774 CMP R1,#LINADR+LINSIZ
647 002172 103774 BLO 12$
648
649 002174 014104 20$: MOV -(R1),R4 ;STARTING FROM THE TOP, READ DATA
650 002176 020004 CMP R0,R4 ;R0 = GOOD DATA, R4 = DATA READ
651 002200 001411 BEQ 30$ ;IF SAME OK
652 002202 112767 000000 176713 MOVB #DATERR,ERRTYP ;INDICATE DATA ERROR
653 002210 010003 MOV R0,R3 ;GET GOOD DATA
654 002212 000261 SEC ;INDICATE FAILURE
655 002214 112767 000003 176304 MOVB #INERR,PCSR1 ;TELL HOST THIS TEST FAILED
656 002222 000462 BR 200$ ;LEAVE THIS SUBTEST
657 002224 000300 30$: SWAB R0 ;NEW GOOD DATA PATTERN
658 002226 010011 MOV R0,(R1) ;STORE AT SAME PLACE
659 002230 011104 MOV (R1),R4 ;READ IT BACK
660 002232 020400 CMP R4,R0 ;R0=GOOD DATA, R4=DATA READ
661 002234 001411 BEQ 40$ ;IF SAME OK
662 002236 112767 000000 176657 MOVB #DATERR,ERRTYP ;INDICATE DATA ERROR
663 002244 010003 MOV R0,R3 ;GET GOOD DATA
664 002246 000261 SEC ;FAILURE
665 002250 112767 000003 176250 MOVB #INERR,PCSR1 ;TELL HOST THIS TEST FAILED
666 002256 000444 BR 200$ ;LEAVE THIS SUBTEST
667 002260 000300 40$: SWAB R0 ;SWITCH GOOD DATA AGAIN
668 002262 001032 BNE 90$ ;IF ORIGINAL PATTERN THEN WE ARE...
669 ;READING THE MEMORY TO CONTAIN A...
670 ;BACKGROUND OF LOW BYTE = ALL 1'S...
671 ;AND WRITING IT BACK SWAPPED AND...
672 ;VERIFING SWAPPED DATA
673 002264 005703 50$: TST R3
674 002266 001032 BNE 100$
675 002270 062701 000002 60$: ADD #2,R1 ;WE ARE GOING MIN->MAX SO ADJUST POINTER
676 002274 020127 177774 CMP R1,#LINADR+LINSIZ ;WE AT MAX?
677 002300 103015 BHIS 80$ ;YES
678 002302 011104 70$: MOV (R1),R4 ;READ DATA

```


MICROC - MICROCODE MODULE C
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MODULE C, MICROTTEST #4

```

704      .SBTTL  MODULE C, MICROTTEST #4
705
706 002374' 112767 000002 176124 MICC4:  MOVB  #INTST,PCSR1      ;TELL HOST WE ARE TESTING
707 002402' 016737 176120 021020      MOV   PCSR1,@#IPCSR1
708 002410' 016737 176114 021010      MOV   IPCSR2,@#MDMA0      ;GET PCBB ADDRESS THROUGH HOST'S PCSR2
709 002416' 016737 176110 021012      MOV   IPCSR2+2,@#MDMA1
710 002424' 013700 021014      MOV   @#MDMAR0,R0        ;R0 HAS CONTENTS OF HOST'S PCSR2
711 002430' 013701 021014      MOV   @#MDMAR0,R1        ;R1 HAS CONTENTS OF HOST'S PCSR3
712 002434' 010037 021010      MOV   R0,@#MDMA0        ;SETUP TO READ PCBB+0
713 002440' 010137 021012      MOV   R1,@#MDMA1
714 002444' 013737 021014 021004      MOV   @#MDMAR0,@#DMATO   ;GET DATA PATTERN AND WRITE IT TO...
715                                     ;DMA 'TO' ADDRESS REGISTER
716 002452' 013737 021004 021026      MOV   @#DMATO,@#MDMAW0   ;READ DATA PATTERN BACK AND WRITE...
717                                     ;BACK INTO HOST MEMORY
718 002460' 112767 000004 176041      MOVB  #4,PCSR1+1         ;TELL HOST WHAT TEST WE JUST FINISHED
719 002466' 016737 176034 021020      MOV   PCSR1,@#IPCSR1
720 002474' 012737 004000 021000      MOV   #DNI,@#IPCSRO
721 002502' 000241
722 002504' 000207
723

```

MICROC - MICROCODE MODULE C
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MODULE C, MICROTEST #5

```

724
725
726
727 002506' 112767 000002 176012 MICCS:  MOVB    #INTST,PCSR1      ;TELL HOST WE ARE TESTING
728 002514' 016737 176006 021020      MOV     PCSR1,@#IPCSR1
729 002522' 012700 100000      MOV     #LINADR,R0      ;BASE ADDRESS OF LINK MEMORY
730 002526' 012701 037776      MOV     #LINSIZ/2,R1    ;NUMBER OF WORDS IN LINK MEMORY
731 002532' 010010      10$:  MOV     R0,(R0)        ;FILL LINK MEMORY WITH THE ADDRESS
732 002534' 005720      TST     (R0)+
733 002536' 005301      DEC     R1              ;OF EACH LOCATION
734 002540' 001374      BNE     10$
735 002542' 016737 175762 021010      MOV     IPCSR2,@#MDMA0   ;GET PCBB THROUGH HOST'S PCSR2
736 002550' 016737 175756 021012      MOV     IPCSR2+2,@#MDMA1
737 002556' 013700 021014      MOV     @#MDMAR0,R0     ;R0 HAS CONTENTS OF HOST'S PCSR2
738 002562' 013701 021014      MOV     @#MDMAR0,R1     ;R1 HAS CONTENTS OF HOST'S PCSR3
739 002566' 010037 021010      MOV     R0,@#DMA0       ;R0 HAS ADDRESS OF PCBB+0
740 002572' 010137 021012      MOV     R1,@#DMA1       ;R1 HAS ITS HIGH ORDER BITS
741 002576' 013737 021014 021022      MOV     @#MDMAR0,@#DMAF ;LOAD DMA 'FROM' ADDRESS REGISTER
742
743 002604' 062700 000002      ADD     #2,R0           ;WITH ADDRESS SUPPLIED FROM HOST
744 002610' 005501      ADC     R1              ;LOAD DMA 'TO' ADDRESS REGISTER
745 002612' 010037 021004      MOV     R0,@#DMAT0      ;WITH PCBB+2 ADDRESS
746 002616' 010137 021006      MOV     R1,@#DMAT1
747 002622' 012737 000002 021024      MOV     #2,@#DMAWC
748 002630' 005067 175704      CLR     DMDCNE
749 002634' 005237 021002      INC     @#DMACSR
750 002640' 106427 000240      MTPS   #PRI05          ;LOAD WORD COUNT TO TRANSFER 1 WORD
751 002644' 005767 175670      20$:  TST     DMDCNE         ;CLEAR DMA DONE FLAG
752 002650' 001775      BEQ     20$            ;START DMA ENGINE
753 002652' 106427 000340      MTPS   #PRI07          ;LOWER CPU PRIORITY
754 002656' 112767 000005 175643      MOVB   #5,PCSR1+1      ;IS THE TRANSFER COMPLETE?
755 002664' 016737 175636 021020      MOV     PCSR1,@#IPCSR1  ;NO, WAIT FOR THE INTERRUPT
756 002672' 012737 004000 021000      MOV     #DNI,@#IPCSRO
757 002700' 000241      CLC
758 002702' 000207      RTS     PC

```

MICROC - MICROCODE MODULE C
 MICROC.MAC 10-JAN-83 17:53

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 MODULE C, MICROTEST #6

.SBTTL MODULE C, MICROTEST #6

759									
760									
761									
762	002704*	112767	000002	175614	MICC6:	MOVB	#INTST,PCSR1		;TELL HOST WE ARE TESTING
763	002712*	016737	175610	021020		MOV	PCSR1,@#IPCSR1		;GET BASE ADDRESS OF LINK MEMORY
764	002720*	012700	100000			MOV	#LINADR,RO		;SIZE OF LINK MEMORY IN WORDS
765	002724*	012701	037776			MOV	#LINSIZ/2,R1		;FILL LINK MEMORY WITH ADDRESS OF
766	002730*	010010			10\$:	MOV	RO,(RO)		
767	002732*	005720				TST	(R0)+		
768	002734*	005301				DEC	R1		;EACH LOCATION
769	002736*	001374				BNE	10\$		
770	002740*	016737	175564	021010		MOV	IPCSR2,@#MDMA0		;GET CONTENTS OF HOST'S PCSR2
771	002746*	016737	175560	021012		MOV	IPCSR2+2,@#MDMA1		
772	002754*	013700	021014			MOV	@#MDMARO,RO		;RO HAS CONTENTS OF HOST'S PCSR2
773	002760*	013701	021014			MOV	@#MDMARO,R1		;R1 HAS CONTENTS OF HOST'S PCSR3
774	002764*	010037	021010			MOV	RO,@#MDMA0		;SETUP TO READ CONTENTS OF PCBB+0
775	002770*	010137	021012			MOV	R1,@#MDMA1		
776	002774*	013737	021014	021004		MOV	@#MDMARO,@#DMATO		;SETUP DMA 'TO' REGISTER FROM PCBB+0
777	003002*	012737	100000	021022		MOV	#LINADR,@#DMAF		;SETUP DMA 'FROM' WITH LINK MEM BASE
778	003010*	012737	003774	021024		MOV	#3774,@#DMAWC		;SETUP MAXIMUM BYTE COUNT
779	003016*	005067	175516			CLR	DMDONE		
780	003022*	005237	021002			INC	@#DMACSR		;TURN ON DMA ENGINE
781	003026*	106427	000240			MTPS	#PRI05		;ALLOW A DMA DONE INTERRUPT
782	003032*	005767	175502		20\$:	TST	DMDONE		;WAIT FOR THE INTERRUPT FROM THE DMA
783	003036*	001775				BEQ	20\$;ENGINE
784	003040*	112767	000006	175461		MOVB	#6,PCSR1+1		;TELL HOST WHAT TEST WE JUST FINISHED
785	003046*	016737	175454	021020		MOV	PCSR1,@#IPCSR1		
786	003054*	012737	004000	021000		MOV	#CNI,@#IPCSRO		;TELL HOST THAT THIS TEST IS DONE
787	003062*	000241				CLC			
788	003064*	000207				RTS	PC		

MICROC - MICROCODE MODULE C
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MODULE C, MICROTST #6

```

789
790      ;SBTTL <DMA "TO" REGISTER RIPPLE TEST>
791      ;
792      ;
793      003066' 112767 000002 175432 MICCS7:: MOV B   #INTST,PCSR1
794      003074' 016737 175426 021020          MOV     PCSR1,@#IPCSR1
795      ;
796      ;
797      003102' 012700 100004          MOV     #LINADR+4,R0
798      003106' 012720 052525          MOV     #052525,(R0)+
799      003112' 012710 031463          MOV     #031463,(R0)
800      ;
801      ;
802      003116' 016737 175406 021010          MOV     IPCSR2,@#MDMA0
803      003124' 016737 175402 021012          MOV     IPCSR2+2,@#MDMA1
804      003132' 013700 021014          MOV     @#MDMAR0,R0
805      003136' 013701 021014          MOV     @#MDMAR0,R1
806      ;
807      ;
808      003142' 010037 021004          MOV     R0,@#DMAT0
809      003146' 012737 100004 021022          MOV     #LINADR+4,@#DMAF
810      003154' 012737 000004 021024          MOV     #4,@#DMAWC
811      003162' 010700          MOV     PC,R0
812      003164' 062700 175222          ADD     #DMAINT-.,R0
813      003170' 010037 000114          MOV     R0,@#DMAVEC
814      003174' 012737 000300 000116          MOV     #PRIO6,@#DMAVEC+2
815      003202' 005067 175332          CLR     DMDONE
816      003206' 005237 021002          INC     @#DMACSR
817      003212' 106427 000240          MTPS   #PRIO5
818      003216' 005767 175316 20$:      TST     DMDONE
819      003222' 001775          BEQ    20$
820      ;
821      ;
822      003224' 112767 000007 175275          MOV B   #7,PCSR1+1
823      003232' 016737 175270 021020          MOV     PCSR1,@#IPCSR1
824      003240' 012737 004000 021000          MOV     #DNI,@#IPCSRO
825      003246' 000241          CLC
826      003250' 000207          RTS    PC
827
828      003252' 003254          MICCSZ::MICCSZ-MICROC+2          ;SIZE OF MICROCODE MODULE C
829
830          000001          .END

```

MICROC - MICROCODE MODULE C
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CROSS REFERENCE TABLE -- USER SYMBOLS

ADDRG=	177774		103#														
ADRERR=	000001		101#	428	538	605											
BAKPAT	000542R	002	245#	511*	514	517	543	571*									
BIT0 =	000001		51#	81													
BIT1 =	000002		50#	82													
BIT10 =	002000		41#	53	70												
BIT11 =	004000		40#	69													
BIT12 =	010000		39#	53	68	89											
BIT13 =	020000		38#	67	88												
BIT14 =	040000		37#	66	87	112											
BIT15 =	100000		36#	65	86	217	220										
BIT2 =	000004		49#	83													
BIT3 =	000010		48#	84													
BIT4 =	000020		47#	85													
BIT5 =	000040		46#														
BIT6 =	000100		45#														
BIT7 =	000200		44#														
BIT8 =	000400		43#	53	71												
BIT9 =	001000		42#														
CLRC	000544R	002	230	251#													
CLRFIF=	021034		22#														
CMDREG=	177776		104#	125*													
CSRFLG=	000001		81#	173	199												
CSRVEC=	000064		74#	150*	151*												
CSWRT	000352R	002	149	199#													
C3STBL	001104R	002	305	335#													
DATERR=	000000		100#	378	435	473	652	662	681								
DMACSR=	021002		9#	211	220*	749*	780*	816*									
DMAF =	021022		17#	741*	777*	809*											
DMAINT	000406R	002	144	211#	812												
DMATO =	021004		10#	714*	716	745*	776*	808*									
DMAT1 =	021006		11#	746*													
DMAVEC=	000114		75#	145*	146*	813*	814*										
DMAWC =	021024		18#	747*	778*	810*											
DMDONE	000540R	002	211*	212	217	244#	748*	751	779*	782	815*	818					
DNI =	004000		69#	129	283	332	720	756	786	824							
ERRFLG=	000002		82#	202													
ERRINT	000362R	002	131	202#													
ERRTYP	001123R	002	344#	378*	428*	435*	473*	538*	605*	652*	662*	681*					
FAT1 =	000400		71#														
FLG3	000522R	002	166*	169	173	193*	199*	202*	225*	239#	372						
INERR =	000003		80#	191	377	429	436	475	541	608	655	665	684				
INMON =	000001		78#	127	189												
INTST =	000002		79#	256	274	302	706	727	762	793							
LOADR =	020000		97#	98													
IOSIZ =	020000		93#	98													
IPCSRO=	021000		8#	129*	178	203*	214*	219*	226*	267*	283*	332*	720*	756*	786*		
			824*														
IPCSR1=	021020		16#	128*	192	257*	275*	282*	303*	331*	707*	719*	728*	755*	763*		
			785*	794*	823*												
IPCSR2	000530R	002	164*	165*	242#	258	259	315	316	708	709	735	736	770	771		
			802	803													
LASFTP=	012400		53#														
LFRBUF=	021032		21#														
LINADR=	100000		99#	102	361	370	383	386	388	412	413	466	513	515	541		
			523	528	530	569	593	599	615	643	646	676	690	696	729		

MICROC - MICROCODE MODULE C
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CROSS REFERENCE TABLE -- USER SYMBOLS

PRI06 = 000300		55#	146	358*	814														
PRI07 = 000340		54#	124*	134	141	172*	280*	753*											
RCEI = 002000		70#																	
ROMADR= 040000		98#	99																
ROMSIZ= 040000		94#	99																
RXI = 020000		67#																	
SANTIM 000524R	002	208*	240#	276*	278														
SANVEC= 000134		73#	155*	156*															
SERI = 100000		65#																	
SIZ4K = 020000		90#	91	92	93	489	556	565											
SIZ8K = 040000		91#	94	95															
STACK = 001000		77#	126																
SUBNUM 001122R	002	321	343#	357*	410*	465*	510*	591*	641*										
TBLC 000502R	002	183	230#																
TIMINT 000400R	00?	154	208#																
TXI = 010000		68#																	
UNIERR= 020000		88#	203																
WCSADR= 000000		96#	97																
WCSSIZ= 020000		92#	97																
. = 003254R	002	131	139	144	149	154	175	183	205	221	230	231	232	233					
		234	235	236	237	305	335	336	337	338	339	340	368	812					
. ABS. 000000	000																		
000000	001																		
MICRC 003254	002																		

ERRORS DETECTED: 0
DEFAULT GLOBALS GENERATED: 0

MICROC.OBJ, MICROC.LST/CRF/DOC/NL:TOC/SOL=MICROC.MAC
RUN-TIME: 1 1 .3 SECONDS
RUN-TIME RATIO: 31/3=9.6
CORE USED: 8K (16 PAGES)

DOCUMENT PAGES: 25

MICROD - MICROCODE MODULE D
MICROD.MAC 10-JAN-83 17:53

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```

1
2
3      .TITLE  MICROD - MICROCODE MODULE D
4      000000' .CSECT  MICRD
5
6      .SBTTL  REGISTER DEFINITIONS USED BY THE T11
7
8      021000  IPCSR0 =      21000  ;INTERNAL PCSRO ADDRESS
9      021002  DMACSR =      21002  ;DMA ENGINE CONTROL STATUS REGISTER
10     021004  DMATO  =      21004  ;DMA ENGINE TO ADDRESS REGISTER #0
11     021006  DMAT1  =      21006  ;DMA ENGINE TO ADDRESS REGISTER #1
12     021010  MDMA0  =      21010  ;MICROCPU DMA TO ADDRESS REGISTER #0
13     021012  MDMA1  =      21012  ;MICROCPU DMA TO ADDRESS REGISTER #1
14     021014  MDMAR0 =      21014  ;MICROCPU DMA DATA REGISTER - AUTO INCREMENT R/O
15     021016  MDMAR1 =      21016  ;MICROCPU DMA DATA REGISTER - AUTO DECREMENT R/O
16     021020  IPCSR1 =      21020  ;INTERNAL PCSR1 ADDRESS
17     021022  DMAF   =      21022  ;DMA ENGINE FROM ADDRESS REGISTER
18     021024  DMAWC  =      21024  ;DMA ENGINE WORD COUNT REGISTER
19     021026  MDMAW0 =      21026  ;MICROCPU DMA DATA REGISTER - AUTO INCREMENT W/O
20     021030  LTAC   =      21030  ;LINK TRANSMIT ADDRESS COUNTER REGISTER
21     021032  LFRBUF =      21032  ;LINK RECIEVE BUFFER ADDRESS FIFO
22     021034  CLRFIF =      21034  ;CLEAR FIFO
23     021036  MDMAW1 =      21036  ;MICROCPU DMA DATA REGISTER - AUTO DECREMENT W/O
24     021040  PCSRSW =      21040  ;SWITCH PACK REGISTER
25     021042  MDMSR  =      21042  ;MICROCPU DMA STATUS REGISTER
26     021044  LRBUF  =      21044  ;LINK RECIEVE BUFFER COMPLETED
27     021060  PHYAD0 =      21060  ;PHYSICAL ADDRESS ROM BYTE 0
28     021062  PHYAD1 =      21062  ;PHYSICAL ADDRESS ROM BYTE 1
29     021064  PHYAD2 =      21064  ;PHYSICAL ADDRESS ROM BYTE 2
30     021066  PHYAD3 =      21066  ;PHYSICAL ADDRESS ROM BYTE 3
31     021070  PHYAD4 =      21070  ;PHYSICAL ADDRESS ROM BYTE 4
32     021072  PHYAD5 =      21072  ;PHYSICAL ADDRESS ROM BYTE 5
33     177774  MODREG =      177774 ;LINK MODE REGISTER
34     177774  ADRREG =      177774 ;LINK STATION ADDRESS RAM REGISTER
35     177776  CMDREG =      177776 ;LINK COMMAND REGISTER
36
37     .SBTTL  OTHER DEFINITIONS USED BY THE MICROCODE
38
39     100000  BIT15  =      100000
40     040000  BIT14  =      40000
41     020000  BIT13  =      20000
42     010000  BIT12  =      10000
43     004000  BIT11  =       4000
44     002000  BIT10  =       2000
45     001000  BIT9   =       1000
46     000400  BIT8   =        400
47     000200  BIT7   =        200
48     000100  BIT6   =        100
49     000040  BIT5   =         40
50     000020  BIT4   =         20
51     000010  BIT3   =         10
52     000004  BIT2   =          4
53     000002  BIT1   =          2
54     000001  BIT0   =          1
55     ;
56     012400  LASFTP =      BIT8!BIT10!BIT12 ;LOAD AND START FUNCTION TEST PATTERN

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MICROD - MICROCODE MODULE D
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OTHER DEFINITIONS USED BY THE MICROCODE

57	000340	PRI07 =	340	
58	000300	PRI06 =	300	
59	000240	PRI05 =	240	
60	000200	PRI04 =	200	
61	000140	PRI03 =	140	
62	000100	PRI02 =	100	
63	000040	PRI01 =	40	
64	000000	PRI00 =	0	
65				
66		;PCSR0 - PORT CONTROL STATUS REGISTER 0		
67				
68	100000	SERI =	BIT15	
69	040000	PCEI =	BIT14	
70	020000	RXI =	BIT13	
71	010000	TXI =	BIT12	
72	004000	DNI =	BIT11	
73	002000	RCEI =	BIT10	
74	000400	FATI =	BIT8	
75				
76		;LINK COMMAND REGISTER		
77				
78	100000	ENABLE =	BIT15	;ENABLE LINK MODULE
79	000200	MODE =	BIT7	;ENABLE MODE REGISTER
80	000100	ARAM =	BIT6	;ENABLE STATION ADDRESS RAM
81				
82		;LINK MODE REGISTER		
83				
84	100000	PROM =	BIT15	;PROMISCUOUS MODE
85	040000	ENAL =	BIT14	;ENABLE MULTICAST
86	004000	ENCR =	BIT11	;ENABLE COLLISION TEST
87	002000	ACLO =	BIT10	;ENABLE ACLO
88	000040	DRTY =	BIT5	;DISABLE RETRY LOGIC
89	000020	COLL =	BIT4	;SIMULATE A COLLISION
90	000010	DTCR =	BIT3	;DISABLE TRANSMIT CRC LOGIC
91	000004	LOOP =	BIT2	;ENABLE LOOPBACK
92				
93	000070	TRNVEC=	70	;VECTOR ADDRESS FOR THE TRANSMITTER
94	000120	RCVVEC=	120	;VECTOR ADDRESS FOR THE RECEIVER
95	000134	SANVEC=	134	;VECTOR ADDRESS FOR THE SANITY TIMER
96	000064	CSRVEC=	64	;VECTOR ADDRESS FOR CSR WRITE INTERRUPT
97	000114	DMAVEC=	114	;VECTOR ADDRESS FOR DMA DONE INTERRUPT
98	000140	PARVEC=	140	;VECTOR ADDRESS FOR LINK MEMORY PARITY ERROR
99	001000	STACK=	1000	;STACK LOCATION
100	000001	INMON=	1	;IN MICROMONITOR STATE
101	000002	INTST=	2	;IN A TEST STATE
102	000003	INERR=	3	;IN ERROR STATE
103	000001	CSRFLG=	BIT0	;CSR WRITE INTERRUPT OCCURED
104	000002	ERRFLG=	BIT1	;UNEXPECTED ERROR OCCURED
105	000004	PARFLG=	BIT2	;PARITY ERROR OCCURED
106	000010	NXMFLG=	BIT3	;NON-EXISTANT MEMORY ERROR OCCURRED
107	000020	NPRFLG=	BIT4	;NPR TIMEOUT OCCURRED
108	000040	TRNFLG=	BIT5	;TRANSMITTER INTERRUPT OCCURRED
109	000100	RCVFLG=	BIT6	;RECEIVER INTERRUPT OCCURRED
110	100000	NPRERR=	BIT15	;PCSR0 FLAG INDICATING NPR ERROR ERROR OCCURRED
111	040000	NXMERR=	BIT14	;PCSR0 FLAG INDICATING NON-EXISTANT MEMORY ERROR OCCURRED
112	020000	UNIERR=	BIT13	;PCSR0 FLAG INDICATING UNEXPECTED INTERRUPT OCCURRED

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OTHER DEFINITIONS USED BY THE MICROCODE

113	010000	PARERR= BIT12	:PCSRO FLAG INDICATING LINK MEMORY PARITY ERROR OCCURRED
114	004000	SIZ1K= 4000	:1K WORDS
115	010000	SIZ2K= SIZ1K*2	:2K WORDS
116	014000	SIZ3K= SIZ1K*3	:3K WORDS
117	020000	SIZ4K= SIZ2K*2	:4K WORDS
118	040000	SIZ8K= SIZ4K*2	:8K WORDS
119	020000	WCSSIZ= SIZ4K	:SIZE OF WRITEABLE CONTROL STORE
120	020000	IOSIZ= SIZ4K	:SIZE OF I/O PAGE
121	040000	ROMSIZ= SIZ8K	:SIZE OF ROM
122	077774	LINSIZ= SIZ8K*2-4	:SIZE OF LINK MEMORY
123	000000	WCSADR= 0	:BASE ADDRESS OF WCS
124	020000	IOADR= WCSADR+WCSSIZ	:BASE ADDRESS OF I/O PAGE
125	040000	ROMADR= IOADR+IOSIZ	:BASE ADDRESS OF ROM
126	100000	LINADR= ROMADR+ROMSIZ	:BASE ADDRESS OF LINK MEMORY
127	002756	MAXBC= 1518.	:MAXIMUM # OF BYTES IN A TRANSMIT BUFFER
128	007777	MYMTBC= 7777	: MAXIMUM COUNT IN LINK XMIT BYTE COUNT FIELD
129	007777	MRECBC= 7777	: MAXIMUM COUNT IN LINK RECEIVE 'MLEN' FIELD
130	000004	CRCSIZ= 4	:NUMBER OF BYTES IN THE CRC
131	000000	DATERR= 0	:FLAG INDICATING DATA ERROR OCCURRED
132	000001	ADRERR= 1	:FLAG INDICATING ADDRESS ERROR OCCURRED
133			

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 OTHER DEFINITIONS USED BY THE MICROCODE

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134
135
136
137 000000' 106427 000340 MICROD::MTPS #PRI07 ;DISABLE INTERRUPTS
138 000004' 012737 000000 177776 MOV #0,@#CMDREG ;TURN OFF THE LINK
139 000012' 012706 001000 MOV #STACK,SP ;SETUP STACK
140 000016' 112767 000001 000620 MOVB #INMON,PCSR1 ;TELL HOST WE ARE IN MICROMONITOR
141 000024' 016737 000614 021020 MOV PCSR1,@#IPCSR1
142 000032' 012737 004000 021000 MOV #DNI,@#IPCSRO ;TELL HOST THE LOAD AND START FINISHED
143 000040' 010700 MOV PC,RO ;GET ADDRESS OF UNEXPECTED ERROR...
144 000042' 062700 000404 ADD #ERRINT-.,RO ;HANDLER
145 000046' 005001 CLR R1 ;FILL ALL UNUSED VECTORS WITH TRAP...
146 000050' 010021 10$: MOV RO,(R1)+ ;HANDLER
147 000052' 012721 000340 MOV #PRI07,(R1)+
148 000056' 020127 001000 CMP R1,#100C
149 000062' 002772 BLT 10$
150
151 000064' 010700 MOV PC,RO ;SETUP PARITY TRAP VECTOR
152 000066' 062700 000462 ADD #PARINT-.,RO
153 000072' 010037 000140 MOV RO,@#PARVEC
154 000076' 012737 000340 000142 MOV #PRI07,@#PARVEC+2
155
156 000104' 010700 MOV PC,RO ;SETUP DMA INTERRUPT VECTOR
157 000106' 062700 000364 ADD #DMAINT-.,RO
158 000112' 010037 000114 MOV RO,@#DMAVEC
159 000116' 012737 000340 000116 MOV #PRI07,@#DMAVEC+2
160
161 000124' 010700 MOV PC,RO ;SETUP CSR WRITE VECTOR
162 000126' 062700 000310 ADD #CSRWRT-.,RO
163 000132' 010037 000064 MOV RO,@#CSRVEC
164 000136' 012737 000200 000066 MOV #PRI04,@#CSRVEC+2
165
166 000144' 010700 MOV PC,RO ;SETUP SANITY TIMER VECTOR
167 000146' 062700 000316 ADD #TIMINT-.,RO
168 000152' 010037 000134 MOV RO,@#SANVEC
169 000156' 012737 000240 000136 MOV #PRI05,@#SANVEC+2
170
171 000164' 010700 MOV PC,RO ;SETUP TRANSMITTER VECTOR
172 000166' 062700 000414 ADD #TRNINT-.,RO
173 000172' 010037 000070 MOV RO,@#TRNVEC
174 000176' 012737 000200 000072 MOV #PRI04,@#TRNVEC+2
175
176 000204' 010700 MOV PC,RO ;SETUP RECEIVER VECTOR
177 000206' 062700 000360 ADD #RCVINT-.,RO
178 000212' 010037 000120 MOV RO,@#RCVVEC
179 000216' 012737 000240 000122 MOV #PRI05,@#RCVVEC+2
180
181 000224' 013700 021040 MOV @#PCSRSW,RO ;GET SWITCH PACK BITS
182 000230' 052700 176000 BIS #176000,RO ;MAP THEM INTO HOST I/O PAGE
183 000234' 006300 ASL RO ;SHIFT OVER TO POSITION CORRECTLY
184 000236' 006300 ASL RO
185 000240' 006300 ASL RO
186 000242' 062700 000004 ADD #4,RO ;PCSR2 IS PCSRO+4
187 000246' 010067 000374 MOV RO,IPCSR2 ;SAVE PCSR2 ADDRESS
188 000252' 012767 000003 000370 MOV #3,IPCSR2+2 ;HIGH ORDER BITS 17:16
189 000260' 005067 000354 CLR FLG4 ;INITIALIZE FLAG WORD
    
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MICROD - MICROCODE MODULE D      MACY11 30(1046) 11-JAN-83 09:52 PAGE 5
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190 000264' 106427 000000      15$:  MTPS      #PRI00      ;ALLOW INTERRUPTS
191
192 000270' 005767 000344      20$:  TST      FLG4      ;WAIT FOR A COMMAND FROM HOST
193 000274' 001775      REQ      20$
194
195 000276' 106427 000340      MTPS      #PRI07      ;RAISE CPU PRIORITY TO SERVICE COMMAND
196 000302' 032767 000001 000330 BIT      #CSRFLG,FLG4      ;DID HOST GIVE US A COMMAND?
197 000310' 001001      BNE      30$      ;YES
198 000312' 000777      BR      .      ;NO, ERROR SO JUST SIT HERE...
199      ;FOR LACK OF ANYTHING BETTER TO DO
200
201 000314' 113700 021000      30$:  MOV      @#IPCSRO,R0      ;GET WHAT HOST WROTE TO PCSRO
202 000320' 042700 177760      BIC      #177760,R0      ;STRIP ALL BUT COMMAND BITS
203 000324' 001004      BNE      35$      ;WAS IT THE CLEAR FUNCTION?
204 000326' 012737 000001 021020 MOV      #INMON,@#IPCSR1      ;YES, CLEAR OUT THE TEST # BITS
205 000334' 000432      BR      50$
206 000336' 022700 000017      35$:  CMP      #17,R0      ;START OPERATIONAL MICROCODE?
207 000342' 001432      BEQ      60$
208 000344' 162700 000001      SUB      #1,R0
209 000350' 010701      MOV      PC,R1      ;GET ADDRESS OF OUR COMMAND TABLE
210 000352' 062701 000240      ADD      #TBLD-.,R1
211 000356' 006300      ASL      R0      ;MAKE COMMAND A BYTE OFFSET
212 000360' 060001      ADD      R0,R1      ;USE IT TO INDEX INTO COMMAND TABLE
213 000362' 061101      ADD      (R1),R1      ;R1 NOW HAS COMMAND ROUTINE ADDRESS
214 000364' 004711      JSR      PC,(R1)      ;EXECUTE AS COMMANDED FROM HOST
215 000366' 103404      BCS      40$      ;ERROR OCCURRED
216 000370' 112767 000001 000246 MOV      #INMON,PCSR1      ;INDICATE TO HOST WE ARE BACK IN...
217 000376' 000403      BR      45$      ;MICROMONITR
218 000400' 112767 000003 000236 40$:  MOV      #INERR,PCSR1      ;INDICATE TO HOST ERROR OCCURRED
219 000406' 016737 000232 021020 45$:  MOV      PCSR1,@#IPCSR1
220 000414' 012737 004000 021000 MOV      #DNI,@#IPCSRO      ;TELL HOST THIS MICROTEST FINISHED
221 000422' 005067 000212      50$:  CLR      FLG4      ;RESET FLAG WORD
222 000426' 000716      BR      15$      ;GO WAIT FOR ANOTHER COMMAND
223
224 000430' 005000      60$:  CLR      R0      ;FAKE SELF TEST RESULTS
225 000432' 000137 040006      JMP      @#40006      ;START OPERATIONAL MICROCODE
226
227 000436' 052767 000001 000174 CSRWRT: BIS      #CSRFLG,FLG4      ;INDICATE A CSR WRITE INTERRUPT OCCURED
228 000444' 000002      RTI
229
230 000446' 052767 000002 000164 ERRINT: BIS      #ERRFLG,FLG4      ;INDICATE A UNEXPECTED INTERRUPT OCCURED
231 000454' 012737 020000 021000 MOV      #UNIERR,@#IPCSRO      ;TELL HOST AN UNEXPECTED INTERRUPT
232      ;HAPPENED
233 000462' 000777      BR      .      ;JUST SIT HERE AND SPIN WHEELS
234      ;COUNT ON HOST TO TIME OUT
235
236 000464' 005267 000152      TIMINT: INC      SANTIM      ;COUNT TICKS AS THEY OCCUR
237 000470' 000002      RTI
238
239 000472' 013767 021002 000156 DMAINT: MOV      @#DMACSR,DMDONE      ;GET DMA STATUS
240 000500' 032767 040000 000150 BIT      #BIT14,DMDONE      ;DID A NON-EXISTANT MEMORY INTERRUPT OCCUR?
241 000506' 001404      BEQ      10$      ;NO
242 000510' 012737 040000 021000 MOV      #NXMERR,@#IPCSRO      ;YES, TELL HOST A NON-EXISTANT MEMORY
243      ;LOCATION WAS ADDRESSED
244 000516' 000407      BR      20$
245 000520' 032767 100000 000130 10$:  BIT      #BIT15,DMDONE      ;DID A NPR TIMEOUT OCCUR?

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M

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246 000526' 001407          BEQ      30$          :NO
247 000530' 012737 100000 021060  MOV     #NPRERR,@#IPCSRO :TELL HOST NPR TIMEOUT HAPPENED
248 000536' 012737 100000 021002 20$:  MOV     #BIT15,@#DMACSR :CLEAR THE INTERRUPT IN THE DMA ENGINE
249 000544' 000777          BR       .           :SIT HERE AND SPIN WHEELS
250 000546' 000002          30$:  RTI
251
252
253 000550' 052767 000004 000062 PARINT: BIS     #PARFLG,FLG4 :SET PARITY ERROR OCCURRED
254 000556' 012737 010000 021000  MOV     #PARERR,@#IPCSRO :TELL HOST A LINK MEMORY PARITY ERROR
255                                     OCCURRED
256 000564' 000777          BR       .           :SIT HERE AND SPIN WHEELS
257
258 000566' 005737 021044          RCVINT: TST    @#LRBUF   :READ BUFFER DONE REGISTER...
259                                     WHICH CLEARS THE INTERRUPT
260 000572' 052767 000100 000040  BIS     #RCVFLG,FLG4   :SET RECEIVER INTERRUPT OCCURRED
261 000600' 000002          RTI
262
263 000602' 052767 000040 000030 TRNINT: BIS     #TRNFLG,FLG4 :SET TRANSMITTER INTERRUPT OCCURRED
264 000610' 000002          RTI
265
266 000612' 000056          TBLD:  .WORD  MICD1-.  :TRANSMITTER DONE TEST
267 000614' 000236          .WORD  MICD2-.  :RECEIVER DONE TESTS
268 000616' 000432          .WORD  MICD3-.  : DATA BYTE FRAMING TEST
269 000620' 000734          .WORD  MICD4-.  : DATA WORD FRAMING TEST
270 000622' 001244          .WORD  MICD5-.  : DATA PATH PATTERN
271 000624' 001604          .WORD  MICD6-.  : STATUS MUX TEST
272 000626' 002046          .WORD  MICD7-.  : LINK BYTE COUNT TEST
273 000630' 002354          .WORD  MICD8-.  : LINK MEMORY ARBITRATION TEST
274 000632' 003014          .WORD  MICD9-.  : LINK BYTE COUNTER MAXIMUM TEST
275 000634' 003206          .WORD  MICD10-. :FIFO TEST
276 000636' 003460          .WORD  MICD11-. :LINK MEMORY ADDRESS TEST
277
278 000640' 000000          FLG4:  .WORD  0       :FLAG WORD
279 000642' 000000          SANTIM: .WORD  0      :COUNT FOR SANITY TIMER
280 000644' 000000          PCSR1: .WORD  0      :COPY OF WHAT GOES TO PCSR1
281 000646' 000000 000000  IPCSR2: .WORD  0,0    :ADDRESS IN HOST MEMORY FOR PCSR2
282 000652' 000000 000000  PCBADR: .WORD  0,0    :ADDRESS IN HOST MEMORY FOR PCB
283 000656' 000000          DMDONE: .WORD  0      :
284 000660' 000000          RBUFP:  .WORD  0      :POINTER TO RECIEVE BUFFFR
285 000662' 000000          TBUFP:  .WORD  0      :POINTER TO XMIT BUFFER
286 000664' 000000          DBUFP:  .WORD  0      :POINTER TO DMA ENGINE BUFFER
287 000666' 000000          MBUFP:  .WORD  0      :POINTER TO MICROCPU DMA BUFFER
288

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MICROCODE MODULE D

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289 000670' 112767 000002 177746 MICD1:  MOVB  #INTST,PCSR1      ;TELL HOST WE ARE TESTING
290 000676' 016737 177742 021020      MOV  PCSR1,@#IPCSR1
291 000704' 012703 177777      MOV  #177777,R3      ;FILL RECIEVE BUFFER WITH 1'S
292 000710' 012700 100000      MOV  #LINADR,R0      ;RECIEVE BUFFER STARTS HERE
293 000714' 010067 177740      MOV  R0,RBUF
294 000720' 010320      10$:  MOV  R3,(R0)+
295 000722' 020027 104000      CMP  R0,#LINADR+SIZ1K ;FILL ENTIRE BUFFER
296 000726' 103774      BLO  10$
297 000730' 005003      CLR  R3              ;FILL XMIT BUFFER WITH 0'S
298 000732' 010067 177724      MOV  R0,TBUF        ;XMIT BUFFER STARTS 'K AWAY FROM RECIEVE
299 000736' 010320      20$:  MOV  R3,(R0)+
300 000740' 020027 110000      CMP  R0,#LINADR+SIZ2K
301 000744' 103774      BLO  20$
302
303 000746' 012737 100200 177776      MOV  #MODE!ENABLE,@#CMDREG ;ENABLE LINK MODULE AND SELECT MODE REG
304 000754' 012737 100004 177774      MOV  #PROM!LOOP,@#MODREG  ;SET PROMISCUIOUS MODE AND ENABLE LOOPBACK
305
306 000762' 016701 177674      MOV  TBUF,R1        ;POINT TO XMIT BUFFER
307 000766' 005021      CLR  (R1)+          ;CLEAR OUT STATUS WORD
308 000770' 012721 002752      MOV  #MAXBC-CRCSIZ,(R1)+ ;SET BYTE COUNT TO MAXIMUM ALLOWED
309 000774' 005037 021034      CLR  @#CLRFIF      ;CLEAR THE FIFO
310 001000' 005067 177634      CLR  FLG4          ;CLEAR THE INTERRUPT FLAG
311 001004' 016737 177650 021032      MOV  RBUF,@#LFRBUF  ;TELL LINK WHERE RECIEVE BUFF IS
312 001012' 016737 177644 021030      MOV  TBUF,@#LTAC   ;TELL LINK WHERE TRANSMIT BUFF IS...
313                                     ;WHICH WILL START A XMIT OPERATION
314 001020' 106427 000140      MTPS #PRI03        ;ALLOW XMITTER TO INTERRUPT
315 001024' 032767 000040 177606 30$:  BIT  #TRNFLG,FLG4  ;WAIT FOR XMIT INTERRUPT
316 001032' 001774      BEQ  30$
317 001034' 106427 000340      MTPS #PRI07        ;DISABLE INTERRUPTS
318 001040' 112767 000001 177577      MOVB #1,PCSR1+1    ;TELL HOST THIS IS TEST #1
319 001046' 000241      CLC
320 001050' 000207      RTS
321      PC

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322 001052' 112767 000002 177564 MICD2:  MOVB  #INTST,PCSR1      ;TELL HOST WE ARE TESTING
323 001060' 016737 177560 021020      MOV  PCSR1,@#IPCSR1
324 001066' 012703 177777      MOV  #177777,R3      ;FILL RECIEVE BUFFER WITH 1'S
325 001072' 012700 100000      MOV  #LINADR,R0      ;RECIEVE BUFFER STARTS HERE
326 001076' 010067 177556      MOV  R0,RBUF
327 001102' 010320      10$:  MOV  R3,(R0)+
328 001104' 020027 104000      CMP  R0,#LINADR+SIZ1K ;FILL ENTIRE BUFFER
329 001110' 103774      BLO  10$
330 001112' 005003      CLR  R3              ;FILL XMIT BUFFER WITH 0'S
331 001114' 010067 177542      MOV  R0,TBUF
332 001120' 010320      20$:  MOV  R3,(R0)+
333 001122' 020027 110000      CMP  R0,#LINADR+SIZ2K ;XMIT BUFFER STARTS 1K AWAY FROM RECIEVE
334 001126' 103774      BLO  20$
335
336 001130' 012737 100200 177776      MOV  #MODE!ENABLE,@#CMDREG ;ENABLE LINK MODULE AND SELECT MODE REG
337 001136' 012737 100004 177774      MOV  #PROM!LOOP,@#MODREG  ;SET PROMISCUIOUS MODE AND ENABLE LOOPBACK
338
339 001144' 016701 177512      MOV  TBUF,R1        ;POINT TO XMIT BUFFER
340 001150' 005021      CLR  (R1)+          ;CLEAR OUT STATUS WORD
341 001152' 012721 002752      MOV  #MAXBC-CRCSIZ,(R1)+ ;SET BYTE COUNT TO MAXIMUM ALLOWED
342 001156' 005037 021034      CLR  @#CLRFIF      ;CLEAR THE FIFO
343 001162' 005067 177452      CLR  FLG4          ;CLEAR THE INTERRUPT FLAG
344 001166' 016737 177466 021032      MOV  RBUF,@#LFRBUF  ;TELL LINK WHERE RECIEVE BUFF IS
345 001174' 016737 177462 021030      MOV  TBUF,@#LTAC   ;TELL LINK WHERE TRANSMIT BUFF IS...
346                                     ;WHICH WILL START A XMIT OPERATION
347 001202' 106427 000200      MTPS #PRI04        ;ALLOW RECEIVER TO INTERRUPT
348 001206' 032767 000100 177424 30$:  BIT  #RCVFLG,FLG4  ;WAIT FOR RECEIVE INTERRUPT
349 001214' 001774      BEQ  30$
350
351 001216' 106427 000140      MTPS #PRI03        ; ALLOW XMTR TO INTERRUPT
352 001222' 032767 000040 177410 40$:  BIT  #TRNFLG,FLG4  ; WAIT FOR XMIT INTERRUPT
353 001230' 001774      BEQ  40$
354
355 001232' 106427 000340      MTPS #PRI07        ;DISABLE INTERRUPTS
356 001236' 112767 000002 177401      MOVB #2,PCSR1+1    ;TELL HOST THIS IS TEST #2
357 001244' 000241      CLC
358 001246' 000207      RTS  PC

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359
360      ; DATA BYTE FRAMING TEST
361      ;
362      ; THIS IS 'MICROCODE' FOR DATA BYTE FRAMING TEST. FILLS XMIT BUFFER
363      ; WITH PATTERN 00000J0011111111 (BINARY) AND TRANSMITS OVER LOOPBACK.
364      ; CHECKS RECEIVE BUFFER FOR SAME PATTERN. REPORTS ERRENT PATTERN,
365      ; OFFSET FROM FRONT OF BUFFER, AND RECEIVE BUFFER STATUS WORD TO
366      ; HOST
367      ;
368      ;SBTTL  MODULE D, MICROTST #3
369
370 001250' 112767 000002 177366 MICD3·  MOV#  #INTST,PCSR1      ; TELL HOST WE ARE TESTING
371 001250' 016737 177362 021020      MOV   PCSR1,@#IPCSR1
372
373      ; *****
374      ; ***** FILL THE RECEIVE BUFFER WITH BACKGROUND *****
375      ; *****
376
377 001264' 012700 100000      MOV   #LINADR,R0      ; RECEIVE BUFFER STARTS HERE
378 001270' 010067 177364      MOV   R0,RBUFP
379 001274' 005020      10$: CLR   (R0)+      ; FILL RECEIVE BUFFER WITH ZEROS
380 001276' 020027 104000      CMP   R0,#LINADR+SIZ1K ; FILL ENTIRE BUFFER
381 001302' 103774      BLO  10$
382
383
384      ; *****
385      ; ***** FILL TRANSMIT BUFFER WITH TEST PATTERN *****
386      ; *****
387
388 001304' 012703 000377      MOV   #0377,R3      ; WORST CASE FOR CLOCKING
389 001310' 010067 177346      MOV   R0,TBUFP
390 001314' 010320      20$: MOV   R3,(R0)+      ; FILL XMIT BUFFER WITH PATTERN
391 001316' 020027 110000      CMP   R0,#LINADR+SIZ2K ; S 2 AT TOP
392 001322' 103774      BLO  20$
393
394
395      ; *****
396      ; ***** SET UP LINK FOR DATAGRAM LOOPBACK OPERATION *****
397      ; *****
398
399 001324' 012737 100200 177776      MOV   #MODE!ENABLE,@#CMDREG ; ENABLE LINK, SELECT MODE REG
400 001332' 012737 100004 177774      MOV   #PROM!LOOP,@#MODREG  ; PROM MODE AND LOOPBACK
401
402 001340' 016701 177316      MOV   TBUFP,R1      ; POINT AT XMIT BUFFER
403 001344' 005021      CLR   (R1)+      ; CLEAR OUT STATUS WORD
404 001346' 012721 002752      MOV   #MAXBC-CRCSIZ,(R1)+ ; SET BYTE COUNT TO MAX ALLOWED
405 001352' 005037 021034      CLR   @#CLRFIF      ; CLEAR THE FIFO
406 001356' 005067 177256      CLR   FLG4      ; CLEAR INTERRUPT FLAG
407 001362' 016737 177272 021032      MOV   RBUFP,@#LFRBUF      ; TELL UNA WHERE RECEIVE BUFF IS
408 001370' 016737 177266 021030      MOV   TBUFP,@#LTAC      ; TELL UNA WHERE XMIT BUFF IS
409
410 001376' 106427 000140      MTPS  #PRI03      ; ALLJW XMITTER AND RECEIVER TO INTERRUPT
411 001402' 032767 000100 177230 30$: BIT   #RCVFLG,FLG4      ; WAIT FOR RECEIVER INTERRUPT
412 001410' 001774      BEQ  30$
413
414 001412' 032767 000040 177220 35$: BIT   #TRN!LG,FLG4      ;WAIT FOR TRANSMIT INTERRUPT TOO

```

MICROD - MICROCODE MODULE D
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MODULE D, MICROTST #3

M
M

```

415 001420' 001774          BEQ      35$
416
417 001422' 106427 000340    MTPS   #PRI07          ; DISABLE INTERRUPTS
418
419
420
421
422
423
424 001426' 012700 100000    MOV     #LINADR,R0      ; VERIFY RECEIVE BUFFER CONTENT
425 001432' 012005          MOV     (R0)+,R5        ; SAVE STATUS IN CASE ERROR
426 001434' 005004          CLR     R4              ; TRACK OFFSET IN CASE ERROR
427 001436' 062700 000002    ADD     #2,R0           ; DON'T NEED 'LENGTH' IN BUFFER
428
429 001442' 011001          40$:  MOV     (R0),R1      ; READ THE BUFFER
430 001444' 020103          CMP     R1,R3          ; R3 CONTAINS ORIGINAL PATTERN
431 001446' 001012          BNE    70$            ; GO TO ERROR EXIT
432 001450' 005200          INC     R0              ; COULDN'T BUMP TILL AFTER TEST
433 001452' 005204          INC     R4              ; TRACK OFFSET FROM BUFFER START
434 001454' 022704 002752    CMP     #MAXBC-CRCSIZ,R4 ; COMPARE ALL BUFFER ENTRIES
435 001460' 001370          BNE    40$
436 001462' 000241          CLC                    ; TELL MICROMONITOR SUCCESS
437
438
439
440
441
442 001464' 112767 000003 177153 50$:  MOVB   #3,PCSR1+1      ; TELL HOST TEST 3 DONE
443 001472' 000207          RTS     PC              ; RETURN TO SENDER
444
445
446
447
448
449
450 001474' 016737 177146 021010 70$:  MOV     IPCSR2,@#MDMA0  ; PICK UP ADDRESS OF PCBB
451 001502' 016737 177142 021012    MOV     IPCSR2+2,@#MDMA1
452 001510' 013700 021014          MOV     @#MDMAR0,R0    ; R0=CONTENTS OF PCSR2
453 001514' 013702 021014          MOV     @#MDMAR0,R2    ; R2=CONTENTS OF PCSR3
454 001520' 010037 021010          MOV     R0,@#MDMA0     ; POINT TO PCBB+0
455 001524' 010237 021012          MOV     R2,@#MDMA1
456
457 001530' 010537 021026          MOV     R5,@#MDMAW0    ; WRITE STATUS WORD TO HOST
458 001534' 010337 021026          MOV     R3,@#MDMAW0    ; WRITE ORIGINAL PATTERN BACK
459 001540' 010137 021026          MOV     R1,@#MDMAW0    ; WRITE ERRENT PATTERN TO HOST
460 001544' 010437 021026          MOV     R4,@#MDMAW0    ; WRITE ERROR OFFSET TO HOST
461
462 001550' 000261          SEC                    ; TELL MICROMONITOR ERROR OCCURRED
463 001552' 000744          BR     50$             ; GO EXIT THROUGH NORMAL
464

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MICROD - MICROCODE MODULE D
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MODULE D, MICROTEST #3

```

465
466      ; DATA WORD FRAMING TEST
467
468      ; THIS IS 'MICROCODE' FOR DATA WORD FRAMING TEST. FILLS XMIT BUFFER
469      ; WITH PATTERN 00000000000000001111111111111111 (BINARY) AND TRANSMITS
470      ; OVER THE LOOPBACK.
471
472      ; CHECKS RECEIVE BUFFER FOR SAME PATTERN. REPORTS ERRENT PATTERN,
473      ; OFFSET FROM FRONT OF BUFFER, AND RECEIVE BUFFER STATUS WORD TO
474      ; HOST
475
476      .SBTTL  MODULE D,MICROTEST #4
477
478      ; *****
479      ; ***** TELL HOST WE ARE BUSY *****
480      ; *****
481
482 001554' 112767 000002 177062 MICD4:  MOVB  #INTST,PCSR1      ; TELL HOST WE ARE TESTING
483 0C1562' 016737 177056 021020      MOV   PCSR1,@#IPCSR1
484
485
486      ; *****
487      ; ***** FILL RECEIVE BUFFER WITH BACKGROUND PATTERN *****
488      ; *****
489
490 001570' 012700 100000      MOV   #LINADR,R0      ; RECEIVE BUFFER STARTS HLRE
491 001574' 010067 177060      MOV   R0,RBUFP
492 001600' 005020      10$:  CLR   (R0)+      ; FILL RECEIVE BUFFER WITH ZEROS
493 001602' 020027 104000      CMP   R0,#LINADR+SIZ1K ; FILL ENTIRE BUFFER
494 001606' 103774      BLO  10$
495
496
497      ; *****
498      ; ***** FILL XMIT BUFFER WITH TEST PATTERN *****
499      ; *****
500
501 001610' 012703 177777      MOV   #177777,R3      ; WORST CASE FOR CLOCKING
502 001614' 010067 177042      MOV   R0,TBUFP      ; SAVE COPY OF ADDRESS
503 001620' 010320      20$:  MOV   R3,(R0)+      ; FILL XMIT BUFFER WITH PATTERN
504 001622' 005103      CGM   R3      ; FLIP IT OVER
505 001624' 020027 110000      CMP   R0,#LINADR+SIZ2K ; STOP AT TOP
506 001630' 103773      BLO  20$
507
508
509      ; *****
510      ; ***** SET UP LINK FOR DATAGRAM LOOPBACK *****
511      ; *****
512
513 001632' 012737 100200 177776      MOV   #MODE!ENABLE,@#CMDREG ; ENABLE LINK, SELECT MODE REG
514 001640' 012737 100004 177774      MOV   #PROM!LOOP,@#MODREG  ; PROM MODE AND LOOPBACK
515
516 001646' 016701 177010      MOV   TBUFP,R1      ; POINT AT XMIT BUFFER
517 001652' 005021      CLR   (R1)+      ; CLEAR OUT STATUS WORD
518 001654' 012721 002752      MOV   #MAXBC-CRCSIZ,(R1)+ ; SET BYTE COUNT TO MAX ALLOWED
519 001660' 005037 021034      CLR   @#CLRFFIF      ; CLEAR THE FIFO
520 001664' 005067 176750      CLR   FLG4      ; CLEAR INTERRUPT FLAG

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MICROD - MICROCODE MODULE D
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MODULE D,MICROTEST #4

```

521 001670' 016737 176764 021032      MOV      RBUF, @#LFRBUF      ; TELL UNA WHERE RECEIVE BUFF IS
522 001676' 016737 176760 021030      MOV      TBUF, @#LTAC       ; TELL UNA WHERE XMIT BUFF IS
523
524 00174' 106427 000140                MTPS     #PRI03             ; ALLOW XMITTER AND RECEIVER TO INTERRUPT
525 001710' 032767 000100 176722 30$:  BIT      #RCVFLG, FLG4      ; WAIT FOR RECEIVER INTERRUPT
526 001716' 001774                BEQ      30$
527
528 001720' 032767 000040 176712 35$:  BIT      #TRNFLG, FLG4      ; WAIT FOR XMIT INTERRUPT TOO
529 001726' 001774                BEQ      35$
530
531 001730' 106427 000340                MTPS     #PRI07             ; DISABLE INTERRUPTS
532
533
534      ; *****
535      ; ***** VERIFY THE CONTENTS OF RECEIVE BUFFER *****
536      ; *****
537
538 001734' 012700 100000                MOV      #LINADR, R0        ; VERIFY RECEIVE BUFFER CONTENT
539 001740' 012005                MOV      (R0)+, R5         ; SAVE STATUS IN CASE ERROR
540 001742' 005004                CLR      R4                ; TRACK OFFSET IN CASE ERROR
541 001744' 062700 000002                ADD      #2, R0            ; DON'T NEED 'LENGTH' IN BUFFER
542
543 001750' 005003                CLR      R3                ; NEED A ZERO
544 001752' 005103 40$:      COM      R3                ; FLIP IT OVER
545 001754' 011001                MOV      (R0), R1         ; READ DATA BACK
546 001756' 020103                CMP      R1, R3
547 001760' 001012                BNE     70$                ; GO TO ERROR EXIT
548 001762' 005200                INC      R0                ; NEW ADDRESS AFTER TESTING
549 001764' 005204                INC      R4                ; NEXT OFFSET
550 001766' 022704 002752                CMP      #MAXBC-CRCSIZ, R4
551 001772' 103767                BLO     40$                ; END OF LOOP
552 001774' 000241                CLC                        ; TELL MICROMONITOR SUCCESS
553
554      ; *****
555      ; ***** FALLTHROUGH EXIT IF NO ERROR *****
556      ; *****
557
558 001776' 112767 000004 176641 50$:  MOVB    #4, PCSR1+1        ; TELL HIM TEST FINISHED
559 002004' 000207                RTS      PC                ; RETURN TO SENDER
560
561      ; *****
562      ; ***** ERROR EXIT *****
563      ; *****
564
565 002006' 016737 176634 021010 70$:  MOV      IPCSR2, @#MDMA0    ; PICK UP ADDRESS OF PCBB
566 002014' 016737 176630 021012      MOV      IPCSR2+2, @#MDMA1
567 002022' 013700 021014                MOV      @#MDMAR0, R0      ; R0=CONTENTS OF PCSR2
568 002026' 013702 021014                MOV      @#MDMAR0, R2      ; R2=CONTENTS OF PCSR3
569 002032' 010037 021010                MOV      R0, @#MDMA0       ; POINT TO PCBB+0
570 002036' 010237 021012                MOV      R2, @#MDMA1
571
572 002042' 010537 021026                MOV      R5, @#MDMAW0      ; WRITE STATUS WORD TO HOST
573 002046' 010337 021026                MOV      R3, @#MDMAW0      ; WRITE ORIGINAL TO HOST
574 002052' 010137 021026                MOV      R1, @#MDMAW0      ; WRITE ERRENT PATTERN TO HOST
575 002056' 010437 021026                MOV      R4, @#MDMAW0      ; WRITE ERROR OFFSET TO HOST
576

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MICROD - MICROCODE MO L E D
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MODULE D,MICROTEST #4

577 002062' 000261
578 002064' 000744
579

SEC
BR 50\$

;TELL MICROMONITOR ERROR OCCURRED
; GO EXIT THROUGH NORMA_

M
M

MICROD - MICROCODE MODULE D
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MODULE D,MICROTEST #4

```

580
581      : DATA PATH PATTERN TEST
582      :
583      : THIS IS 'MICROCODE' FOR DATA PATH PATTERN TEST. RETRIEVES DATA PATTERN
584      : FROM HOST MEMORY. FILLS XMIT BUFFER WITH PATTERN AND SENDS DATAGRAM
585      : OVER THE LOOPBACK.
586      :
587      : CHECKS RECEIVE BUFFER FOR SAME PATTERN. REPORTS ERRENT PATTERN,
588      : OFFSET FROM FRONT OF BUFFER, AND RECEIVE BUFFER STATUS WORD TO
589      : HOST
590
591      .SB*TL  MODULE D,MICROTEST #5
592
593      : *****
594      : ***** TELL HOST WE ARE BUSY *****
595      : *****
596
597 002066' 112767 000002 176550 MICD5:  MOVB  #INTST,PCSR1          ; TELL HOST WE ARE TESTING
598 002074' 016737 176544 021020      MOV   PCSR1,@#IPCSR1
599
600      : *****
601      : ***** RETRIEVE PATTERN FROM HOST MEMORY *****
602      : *****
603
604 002102' 016737 176540 021010      MOV   IPCSR2,@#MDMA0          ; SET TO GET HOST PCBB ADDRESS
605 002110' 016737 176534 021012      MOV   IPCSR2+2,@#MDMA1
606 002116' 013700 021014      MOV   @#MDMAR0,R0           ; R0 NOW CONTAINS PCBB LOW
607 002122' 013701 021014      MOV   @#MDMAR0,R1           ; R1 NOW CONTAINS PCBB HIGH
608 002126' 010037 021010      MOV   R0,@#MDMA0            ; POINT AT PCBB
609 002132' 010137 021012      MOV   R1,@#MDMA1
610 002136' 013703 021014      MOV   @#MDMAR0,R3           ; R3 NOW HOLDS DATA PATTERN
611
612
613      : *****
614      : ***** FILL RECEIVE BUFFER WITH BACKGROUND PATTERN *****
615      : *****
616
617 002142' 012700 100000      MOV   #LINADR,R0            ; RECEIVE BUFFER STARTS HERE
618 002146' 010067 176506      MOV   R0,RBUF
619 002152' 005020      10$: CLR   (R0)+                ; FILL RECEIVE BUFFER WITH ZEROS
620 002154' 020027 104000      CMP   R0,#LINADR+SIZ1K     ; FILL ENTIRE BUFFER
621 002160' 103774      BLO  10$
622
623
624      : *****
625      : ***** FILL XMIT BUFFER WITH TEST PATTERN *****
626      : *****
627
628 002162' 010067 176474      20$: MOV   R0,TBUF              ; SAVE COPY OF ADDRESS
629 002166' 010320      MOV   R3,(R0)+            ; FILL XMIT BUFFER WITH PATTERN
630 002170' 020027 110000      CMP   R0,#LINADR+SIZ2K    ; STOP AT TOP
631 002174' 103774      BLO  20$
632
633
634      : *****
635      : ***** SET UP LINK FOR DATAGRAM LOOPBACK *****

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MICROD - MICROCODE MODULE D
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MODULE D,MICROTEST #5

```

636 ; *****
637
638 002176' 012737 100200 177776      MOV      #MODE!ENABLE,@#CMDREG      ; ENABLE LINK, SELECT MODE REG
639 002204' 012737 100004 177774      MOV      #PROM!LOOP,@#MODREG        ; PROM MODE AND LOOPBACK
640
641 002212' 016701 176444                MOV      TBUF, R1                    ; POINT AT XMIT BUFFER
642 002216' 005021                CLR      (R1)+                        ; CLEAR OUT STATUS WORD
643 002220' 012721 002752                MOV      #MAXBC-CRCSIZ,(R1)+        ; SET BYTE COUNT TO MAX ALLOWED
644 002224' 005037 021034                CLR      @#CLRFIF                    ; CLEAR THE FIFO
645 002230' 005067 176404                CLR      FLG4                        ; CLEAR INTERRUPT FLAG
646 002234' 016737 176420 021032        MOV      RBUF, @#LFRBUF              ; TELL UNA WHERE RECEIVE BUFF IS
647 002242' 016737 176414 021030        MOV      TBUF, @#LTAC                ; TELL UNA WHERE XMIT BUFF IS
648
649 002250' 106427 000140                MTPS    #PRI03                       ; ALLOW XMITTER AND RECEIVER TO INTERRUPT
650 002254' 032767 000100 176356 30$:    BIT      #RCVFLG, FLG4               ; WAIT FOR RECEIVER INTERRUPT
651 002262' 001774                                BEQ
652
653 002264' 032767 000040 176346 35$:    BIT      #TRNFLG, FLG4               ; WAIT FOR XMIT INTERRUPT TOO
654 002272' 001774                                BEQ
655
656 002274' 106427 000340                MTPS    #PRI07                       ; DISABLE INTERRUPTS
657
658
659 ; *****
660 ; ***** VERIFY THE CONTENTS OF RECEIVE BUFFER *****
661 ; *****
662
663 002300' 012700 100000                MOV      #LINADR, R0                 ; VERIFY RECEIVE BUFFER CONTENT
664 002304' 012002                MOV      (R0)+, R2                   ; SAVE STATUS IN CASE ERROR
665 002306' 005004                CLR      R4                           ; TRACK OFFSET IN CASE ERROR
666 002310' 062700 000002                ADD      #2, R0                       ; DON'T NEED 'LENGTH' IN BUFFER
667
668 002314' 011005 40$:                MOV      (R0), R5                     ; READ DATA BACK
669 002316' 020503                CMP      R5, R3                       ; R3 HOLDS ORIGINAL PATTERN
670 002320' 001012                BNE     70$                           ; GO TO ERROR EXIT
671 002322' 005200                INC      R0                           ; NEW ADDRESS AFTER TESTING
672 002324' 005204                INC      R4                           ; NEXT OFFSET
673 002326' 022704 002752                CMP      #MAXBC-CRCSIZ, R4           ;
674 002332' 103770                BLO     40$                           ; END OF LOOP
675 002334' 000241                CLC                                    ; TELL MICROMONITOR SUCCESS
676
677 ; *****
678 ; ***** FALLTHROUGH EXIT IF NO ERROR *****
679 ; *****
680
681 002336' 112767 000005 176301 50$:    MOV      #5, PCSR1+1                 ; EXIT HERE IF NO ERROR
682 002344' 000207                RTS      PC                           ; RETURN TO SENDER
683
684
685 ; *****
686 ; ***** ERROR EXIT *****
687 ; *****
688
689 002346' 016737 176274 021010 70$:    MOV      IPCSR2, @#MDMA0              ; SET TO GET HOST PCBB ADDRESS
690 002354' 016737 176270 021012        MOV      IPCSR2+2, @#MDMA1
691 002362' 013700 021014        MOV      @#MDMAR0, R0                ; R0 NOW CONTAINS PCBB LOW

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MICROD - MICROCODE MODULE D
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 MODULE D,MICROTEST #5

692	002366'	013701	021014	MOV	@#MDMAR0,R1	:	R1 NOW CONTAINS PCBB HIGH
693							
694	002372'	062700	000002	ADD	#2,R0	:	INDEX DOWN TO PCBB+2
695	002376'	005501		ADC	R1		
696							
697	002400'	010037	021010	MOV	R0,@#MDMA0	:	POINT TO PCBB+0
698	002404'	010137	021012	MOV	R1,@#MDMA1		
699							
700	002410'	010237	021026	MOV	R2,@#MDMAW0	:	WRITE STATUS WORD TO HOST
701	002414'	010537	021026	MOV	R5,@#MDMAW0	:	WRITE ERRENT PATTERN TO HOST
702	002420'	010437	021026	MOV	R4,@#MDMAW0	:	WRITE ERROR OFFSET TO HOST
703							
704	002424'	000261		SEC		:	TELL MICROMONITOR ERROR OCCURRED
705	002426'	000743		BR	50\$:	GO EXIT THROUGH NORMAL
706							

MICROD - MICROCODE MODULE D
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MODULE D,MICROTEST #5

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713
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717
718
719
720
721
722
723 002430' 112767 000002 176206 MICD6:  MOVB  #INTST,PCSR1 ; TELL HOST WE ARE TESTING
724 002436' 016737 176202 021020      MOV   PCSR1,@#IPCSR1
725
726
727
728
729
730
731 002444' 012703 177777      MOV   #177777,R3 ; FILL RECEIVE BUFFER WITH ONES
732 002450' 012700 100000      MOV   #LINADR,R0 ; RECEIVE BUFFER STARTS HERE
733 002454' 010067 176200      MOV   R0,RBUF    ; SAVE A COPY
734
735 002460' 010320 104000 10$:  MOV   R3,(R0)+ ; FILL THE BUFFER
736 002462' 020027      CMP   R0,#LINADR+SIZ1K
737 002466' 103774      BLO  10$
738
739
740
741
742
743 002470' 010067 176166      MOV   R0,TBUF    ; SAVE A COPY OF TRANSMIT ADDRESS
744 002474' 005003      CLR   R3         ; NEED A ZERO
745 002476' 010320 110000 20$:  MOV   R3,(R0)+ ; XMIT BUFFER STARTS 1K FROM RECEIVE
746 002500' 020027      CMP   R0,#LINADR+SIZ2K
747 002504' 103774      BLO  20$
748
749
750
751
752
753
754 002506' 012737 100200 177776      MOV   #MODE!ENABLE,@#CMDREG ; ENABLE LINK MODE, SEL MODE REG
755 002514' 012737 100004 177774      MOV   #PROM!LOOP,@#MODREG  ; PROMIS, ENABLE LOOPBACK
756
757 002522' 016701 176134      MOV   TBUF,R1   ; POINT TO XMIT BUFFER
758 002526' 005103      COM   R3        ; NEED SOME 1'S
759 002530' 010321      MOV   R3,(R1)+ ; BACKGROUND- SHOW STATUS OVLAY
760 002532' 012721 002752      MOV   #MAXBC-CRCSIZ,(R1)+ ; SET BYTE COUNT TO MAX ALLOWED
761 002536' 005037 021034      CLR   @#CLRIF   ; CLEAR THE INTERRUPT FLAG
762 002542' 005067 176072      CLR   FLG4      ; CLEAR THE INTERRUPT FLAG

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MICROD - MICROCODE MODULE D
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MODULE D,MICROTEST #5

```

763 002546' 016737 176106 021032      MOV      RBUF, @#LFRBUF      ; TELL LINK WHERE RECEIVE IS
764 002554' 016737 176102 021030      MOV      TBUF, @#LTAC      ; TELL LINK WHERE XMIT IS
765 002562' 106427 000140      MTPS    #PRIO3            ; WAIT FOR AN INTERRUPT
766 002566' C32767 000040 176044 30$:  BIT      #TRNFLG, FLG4    ; WAIT FOR XMIT INTERRUPT
767 002574' 001774      BEQ     30$
768 002576' 032767 000100 176034 35$:  BIT      #RCVFLG, FLG4    ; WAIT FOR RECEIVER BEFORE GOING ANY FURTHER
769 002604' 001774      BEQ     35$

```

770
771
772
773
774
775
776

: *****
: ***** GET STATUS WORDS FROM TRANSMIT BUFFER *****
: *****

```

777 002606' 016700 176050      MOV      TBUF, R0          ; POINT AT XMIT BUFFER
778 002612' 012003      MOV      (R0)+, R3        ; R5 NOW HOLDS TX0 STATUS WORD
779 002614' 011004      MOV      (R0), R4         ; R6 NOW HOLDS TX1 STATUS WORD

```

780
781
782
783
784
785

: *****
: ***** GET HOST MEMORY ADDRESS AND WRITE STATUS WORDS *****
: *****

```

786 002616' 016737 176024 021010      MOV      IPCSR2, @#MDMA0   ; PICK UP ADDRESS OF PCBB
787 002624' 016737 176020 021012      MOV      IPCSR2+2, @#MDMA1
788 002632' 013700 021014      MOV      @#MDMAR0, R0     ; R0=CONTENTS OF PCSR2
789 002636' 013702 021014      MOV      @#MDMAR0, R2     ; R2=CONTENTS OF PCSR3
790 002642' 010037 021010      MOV      R0, @#MDMA0     ; POINT AT PCBB+0
791 002646' 010237 021012      MOV      R2, @#MDMA1
792
793 002652' 010337 021026      MOV      R3, @#MDM'W0    ; WRITE TX0 TO HOST MEMORY
794 002656' 010437 021026      MOV      R4, @#MDMAW0    ; WRITE TX1 TO HOST MEMORY
795
796 002662' 112767 000006 175755      MOVB    #6, PCSR1+1
797 002670' 000241      CLC
798 002672' 000207      RTS      PC
799
800

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MICROD . MICROCODE MODULE D
MICROD.MAC 10-JAN-83 17:53

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MODULE D,MICROTEST #5

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: LINK BYTE COUNTER TEST
: THIS IS 'MICROCODE' FOR THE LINK BYTE COUNTER TEST.
: GETS A BYTE COUNT FROM THE HOST MEMORY. LOOPS BACK A DATAGRAM
: WITH THE BYTE COUNT TO THE RECEIVE BUFFER. VERIFIES THE RECEIVE
: BUFFER TO:
: 1. MAKE SURE RECEIVE BYTE COUNT IS THE SAME AS THAT DESIGNATED
: BY THE TRANSMIT BUFFER BYTE COUNT ENTRY
: 2. MAKE SURE RECEIVE BUFFER WAS ACTUALLY OVERWRITTEN WITH THE
: BYTE COUNT WRITTEN IN THE TRANSMIT BYTE COUNT BUFFER
: ENTRY.
: WRITES THE VALUE OF THE RECEIVE BUFFER BYTE COUNT TO HOST MEMORY.

.SBTTL LINK BYTE COUNTER TEST

: *****
: ***** TELL HOST WE ARE BUSY *****
: *****

002674' 112767 000002 175742 MICD7: MOV #INTST,PCSR1 ; TELL HOST WE ARE TESTING
002702' 016737 175736 021020 MOV PCSR1,@#IPCSR1

: *****
: ***** FILL RECEIVE BUFFER WITH BACKGROUND PATTERN *****
: *****

002710' 012700 100000 MOV #LINADR,R0 ; RECEIVE BUFFER STARTS HERE
002714' 012703 177777 MOV #177777,R3 ; GET ALL ONES
002720' 010067 175734 MOV R0,RBUF ; SAVE ADDRESS OF RECV BUFFER
10\$: MOV R3,(R0)+ ; FILL UP THE BUFFER
CMP R0,#LINADR+SIZ1K ; OVERFILL- SHOW RECV OVERLAY
BLO 10\$

: *****
: ***** FILL TRANSMIT BUFFER WITH TEST PATTERN *****
: *****

002734' 010067 175722 MOV R0,TBUF ; SAVE ADDRESS XMIT BUFFER
002740' 005020 20\$: CLR (R0)+ ; ZEROS FOR OVERLAY
002742' 020027 110000 CMP R0,#LINADR+SIZ2K ; FILL XMIT BUFFER WITH PATTERN
002746' 103774 BLO 20\$; STOP AT THE TOP

: *****
: ***** RETRIEVE BYTE COUNT FROM HOST MEMORY *****
: *****

002750' 016737 175672 021010 MOV IPCSR2,@#MDMA0 ; SET TO GET HOST PCBB ADDRESS
002756' 016737 175666 021012 MOV IPCSR2+2,@#MDMA1

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MICROD - MICROCODE MODULE D
MICROD.MAC 10-JAN-83 17:53

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LINK BYTE COUNTER TEST

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857 002764' 013700 021014      MOV      @#MDMAR0,R0      ; R0 NOW CONTAINS PCBB LO
858 002770' 013701 021014      MOV      @#MDMAR0,R1      ; R1 NOW CONTAINS PCBB HI
859 002774' 010037 021010      MOV      R0,@#MDMA0      ; POINT AT PCBB
860 003000' 010137 021012      MOV      R1,@#MDMA1
861 003004' 013703 021014      MOV      @#MDMAR0,R3      ; R3 NOW CONTAINS BYTE COUNT
862
863
864      ; *****
865      ; ***** SET UP LINK FOR DATAGRAM LOOPBACK *****
866      ; *****
867
868 003010' 012737 100200 177776  MOV      #MODE!ENABLE,@#CMDREG ; ENABLE LINK, SELECT MODE REG
869 003016' 012737 100014 177774  MOV      #PROM!LOOP!DTCR,@#MODREG ; PROM MODE AND LOOPBACK
870
871 003024' 016704 175632      MOV      TBUF0,R4      ; POINT AT XMIT BUFFER
872 003030' 005024      CLR      (R4)+      ; CLEAR OUT STATUS WORD
873 003032' 010324      MOV      R3,(R4)+      ; WRITE PASSED BYTE COUNT
874 003034' 005037 021034      CLR      @#CLRFIF      ; CLEAR THE FIFO
875 003040' 005067 175574      CLR      FLG4      ; CLEAR INTERRUPT FLAG
876 003044' 016737 175610 021032  MOV      RBUF0,@#LFRBUF      ; TELL UNA WHERE RECIEVE BUFF IS
877 003052' 016737 175604 021030  MOV      TBUF0,@#LTAC      ; TELL UNA WHERE XMIT BUFF IS
878
879 003060' 106427 000140      MTPS     #PRI03      ; ALLOW XMITTER TO INTERRUPT
880 003064' 032767 000040 175546 30$  BIT      #TRNFLG,FLG4      ; WAIT FOR INTERRUPT
881 003072' 001774      BEQ
882 003074' 032767 000100 175536 35$  BIT      #RCVFLG,FLG4      ;WAIT FOR RECEIVER INTERRUPT TOO
883 003102' 001774      BEQ      35$
884
885
886 003104' 106427 000340      MTPS     #PRI07      ; DISABLE INTERRUPTS
887
888
889      ; *****
890      ; ***** COUNT ZEROS IN RECEIVE BUFFER *****
891      ; *****
892
893 003110' 016704 175544      MOV      RBUF0,R4      ; VERIFY RECEIVE BUFFER CONTENTS
894 003114' 062704 000002      ADD      #2,R4      ; && COULD REPORT STATUS &&
895 003120' 012402      MOV      (R4)+,R2      ; SAVE 'MLN' BUFFER LENGTH
896 003122' 005005      CLR      R5      ; COUNT DATA WORDS TRANSFERRED
897
898 003124' 112403 000000 40$  MOV8     (R4)+,R3      ; READ DATA BACK
899 003126' 122703 000000      CMPB    #0,R3      ; IS IT ZERO?
900 003132' 001004      BNE     45$      ; EXIT IF AT THE EDGE
901 003134' 005205      INC     R5      ; BUMP THE TALLY
902 003136' 020527 002756      CMP     R5,#MAXBC      ; DONE YET?
903 003142' 103770      BLO     40$      ; IF NOT, KEEP GOING
904
905 003144' 062700 000002 45$  ADD      #2,R0      ; INDEX DOWN TO PCBB+2
906 003150' 005501      ADC     R1
907
908 003152' 010037 021010      MOV      R0,@#MDMA0      ; POINT TO PCBB+2
909 003156' 010137 021012      MOV      R1,@#MDMA1
910
911 003162' 010237 021026      MOV      R2,@#MDMAW0      ; WRITE RECEIVE BYTE COUNT
912 003166' 010537 021026      MOV      R5,@#MDMAW0      ; WRITE BUFFER BYTE COUNT

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MICROD - MICROCODE MODULE D
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LINK BYTE COUNTER TEST

913									
914	003172'	112767	000007	175445	MOVB	#7,PCSR1+1			; TELL HIM WHAT TEST IT IS
915	003200'	000241			CLC				
916	003202'	000207			RTS	PC			

MICROD - MICROCODE MODULE D
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LINK BYTE COUNTER TEST

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: LINK MEMORY ARBITRATION TEST

.SBTTL LINK MEMORY ARBITRATION TEST

: THIS IS MICROCODE FOR THE LINK ARBITRATION TEST.
: THIS MICROCODE WILL HAVE THE DMA ENGINE, THE T-11, AND BOTH STATE
: MACHINES ATTEMPTING TO ACCESS LINK MEMORY AT THE SAME TIME.

: EACH PROCESS OPERATES ON DATA THAT IS 'UNIQUE FOR IDENTIFICATION.

: *****
: ***** TELL HOST WE ARE TESTING *****
: *****

MICD8: MOVB #INTST,@#IPCSR1 ; TELL HOST WE ARE TESTING
MOV PCSR1,@#IPCSR1

: *****
: ***** RETRIEVE HOST MEMORY FROM PCBB *****
: *****

MOV IPCSR2,@#MDMA0 ; SET TO GET HOST PCBB ADDRESS
MOV IPCSR2+2,@#MDMA1
MOV @#MDMAR0,R4 ; R4 NOW HOLDS PCBB LOW
MOV @#MDMAR0,R5 ; R5 NOW HOLDS PCBB HIGH

: *****
: ***** FILL RECEIVE BUFFER WITH ZEROS *****
: *****

10\$: MOV #LINADR,R0 ; FILL RECEIVE BUFFER
MOV R0,RBUF ; SAVE A COPY OF POINTER
CLR (R0)+ ; CLEAR IT OUT
CMP R0,#LINADR+SIZ1K
BLO 10\$

: *****
: ***** FILL TRANSMIT BUFFER WITH 33 HEX **
: ***** THATS 31463 OCTAL *****
: *****

20\$: MOV R0,TBUF ; FILL TRANSMIT BUFFER
MOV #31463,(R0)+ ; 31463 = 3333 HEX (THE REAL THING)
CMP R0,#LINADR+SIZ2K
BLO 20\$

: *****
: ***** FILL DMA BUFFER WITH OF HEX ***

MICROD - MICROCODE MODULE D
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LINK MEMORY ARBITRATION TEST

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973
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976 003302' 010067 175356
977 003306' 012720 007417
978 003312' 020027 114000
979 003316' 103773
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987 003320' 010067 175342
988 003324' 012720 177777
989 003330' 020027 120000
990 003334' 103773
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997 003336' 010402
998 003340' 010503
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1000 003342' 062702 000006
1001 003346' 005503
1002
1003 003350' 010237 021004
1004 003354' 010337 021006
1005 003360' 016737 175300 021022
1006 003366' 012737 003774 021024
1007
1008 003374' 010700
1009 003376' 062700 175074
1010 003402' 010037 000114
1011 003406' 012737 000300 000116
1012 003414' 005067 175236
1013
1014
1015
1016
1017
1018 003420' 012737 100200 177776
1019 003426' 012737 100004 177774
1020
1021 003434' 016701 175222
1022 003440' 062701 000002
1023 003444' 012721 002752
1024 003450' 005037 021034
1025 003454' 005067 175160
1026 003460' 016737 175174 021032
1027 003466' 016700 175174
1028 003472' 012702 002000

: ***** THATS 7417 OCTAL *****
: *****
30$: MOV R0,DBUFP ; SAVE COPY OF POINTER
MOV #7417,(R0)+
CMP R0,#LINADR+SIZ3K
BLO 30$

: *****
: ***** FILL BUFFER WITH FF HEX ***
: ***** THATS 177777 OCTAL *****
: *****
40$: MOV R0,MBUFP ; T-11 BUFFER POINTER
MOV #177777,(R0)+
CMP R0,#LINADR+SIZ4K
BLO 40$

: *****
: ***** GET DMA ENGINE READY TO GO *****
: *****
MOV R4,R2 ; GET HOST ADDRESS OF PCBB
MOV R5,R3
ADD #6,R2 ; DUMP BUFFER AFTER ERROR CODES
ADC R3
MOV R2,@#DMATO ; 'TO' REGISTERS INCREMENTS
MOV R3,@#DMAT1
MOV DBUFP,@#DMAF ; WHERE 'FROM' REG IS LOCATED
MOV #3774,@#DMAWC ; WILL XFER 1K-2 WORDS
MOV PC,R0 ; CALCULATE THE INTERRUPT VECTOR
ADD #DMAINT-,R0 ; THROW IN THE OFFSET
MOV R0,@#DMAVEC
MOV #PRI06,@#DMAVEC+2 ;PRIORITY OF INTERRUPT SERVICE ROUTINE
CLR DMDONE ; FLAG

: *****
: ***** SET UP THE STATE MACHINES *****
: *****
MOV #MODE!ENABLE,@#CMDREG
MOV #PROM!LOOP,@#MODREG
MOV TBUFP,R1 ; GET ADDRESS OF XMIT BUFFER
ADD #2,R1 ; SKIP TO BYTE COUNT
MOV #MAXBC-(CRCSIZ,(R1)+ ; SET BYTE COUNT TO MAX ALLOWED
CLR @#CLRIF ; CLEAR INTERRUPT FLAG
CLR *FLG4 ; CLEAR INTERRUPT FLAG
MOV RBUFP,@#LFRBUF ; TELL LINK WHERE RECEIVE BUF IS
MOV MBUFP,R0 ;POINT TO MICROCPU BUFFER
MOV #SIZ1K/2,R2 ;WORD COUNT FOR MICROCPU LOOP

```


MICROD - MICROCODE MODULE D
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LINK MEMORY ARBITRATION TEST

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1029 003476' 012701 125252          MOV      #125252,R1          ;DATA FOR MICROCPU LOOP
1030
1031          ; *****
1032          ; ***** STATE THE MACHINES *****
1033          ; *****
1034          ; IS EVERYBODY READY?
1035
1036 003502' 016737 175154 021030     MCV      TBUF, @#LTAC      ; THIS STARTS THE STATE MACHINES
1037
1038 003510' 005237 021002          INC      @#DMACSR         ; THIS STARTS DMA ENGINE
1039
1040 003514' 106427 000140          MTPS    #PRI03           ; ALLOW INTERRUPTS
1041
1042 003520' 010120          50$:    MOV      R1, (R0)+    ; START FILLING LINK MEMORY...
1043 003522' 077202          SOB      R2, 50$         ; WITH MICROCPU
1044
1045          ; THE MICROCPU IS DONE
1046
1047 003524' 032767 000100 175106 60$:  BIT      #RCVFLG, FLG4    ; IS THE RECEIVE DONE?
1048 003532' 001774          BEQ      60$            ; NOT YET
1049
1050 003534' 032767 000040 175076 70$:  BIT      #TRNFLG, FLG4    ; IS THE TRANSMIT DONE?
1051 003542' 001774          BEQ      70$            ; NOT YET
1052
1053 003544' 005767 175106          80$:    TST      DMDONE      ; IS THE DMA ENGINE DONE?
1054 003550' 001775          BEQ      80$            ; NOT YET
1055
1056          ; EVERYBODY IS DONE, SO NOW CHECK THE...
1057          ; DATA
1058
1059 003552' 016700 175102          MOV      RBUF, R0        ; POINT TO RECEIVE BUFFER
1060 003556' 062700 000004          ADD      #4, R0          ; INDEX DOWN TO DATA PART
1061 003562' 012702 001365          MOV      #<MAXBC-CRCSIZ>/2, R2 ; AMOUNT OF DATA TO CHECK
1062 003566' 022710 031463          90$:    CMP      #31463, (R0)   ; IS THE DATA CORRECT?
1063 003572' 001005          BNE      100$           ; NO
1064 003574' 062700 000002          ADD      #2, R0          ; YES, POINT TO NEXT WORD OF DATA
1065 003600' 077206          SOB      R2, 90$        ; CONTINUE CHECKING DATA UNTIL DONE
1066 003602' 000241          CLC
1067 003604' 000414          BR       110$
1068
1069 003606' 010437 021010          100$:   MOV      R4, @#MDMA0      ; POINT TO HOST PCBB
1070 003612' 010537 021012          MOV      R5, @#MDMA1
1071 003616' 012737 031463 021026     MOV      #31463, @#MDMAW0 ; GIVE HOST EXPECTED PATTERN IN PCBB
1072 003624' 011037 021026          MOV      (R0), @#MDMAW0 ; GIVE HOST ACTUAL PATTERN IN PCBB+2
1073 003630' 010037 021026          MOV      R0, @#MDMAW0   ; GIVE HOST LINK MEMORY ADDRESS
1074 003634' 000261          SEC                    ; TELL MICROMONITOR ERROR OCCURRED
1075
1076 003636' 112767 000010 175001 110$:  MOVB    #8., PCSR1+1     ; TELL HOST WHAT TEST THIS IS
1077 003644' 000207          RTS      PC              ; RETURN TO MICROMONITOR
1078

```

MICROD - MICROCODE MODULE D
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LINK MEMORY ARBITRATION TEST

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1086 003646' 012767 000002 174770
1087 003654' 016737 174764 021020
1088
1089
1090
1091
1092
1093
1094 003662' 012700 100000
1095 003666' 010067 174766
1096 003672' 062700 004000
1097 003676' 010067 174760
1098
1099
1100
1101
1102
1103
1104 003702' 012737 100200 177776
1105 003710' 012737 100004 177774
1106
1107 003716' 016704 174740
1108 003722' 005024
1109 003724' 012724 007777
1110 003730' 005037 021034
1111 003734' 005067 174700
1112 003740' 016737 174714 021032
1113 003746' 016737 174710 021030
1114
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1116
1117
1118
1119
1120 003754' 106427 000100
1121 003760' 032767 000040 174652
1122 003766' 001774
1123
1124 003770' 032767 000100 174642
1125 003776' 001774
1126
1127 004000' 106427 000340
1128
1129
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1134 004004' 016704 174650

```

.SBTTL LINK BYTE COUNTER MAXIMUM TEST

```

*****
***** TELL THE HOST WE ARE TESTING *****
*****

```

```

MICD9: MOV #INTST,PCSR1 ; TELL HOST WE ARE TESTING
        MOV PCSR1,@#IPCSR1

```

```

*****
***** CALCULATE BUFFER ADDRESS POINTERS *****
*****

```

```

        MOV #LINADR,R0 ; RELATIVE TO LINK MEMORY
        MOV R0,RBUF
        ADD #SIZ1K,R0
        MOV R0,TBUF

```

```

*****
***** SET UP LINK FOR A LOOPBACK *****
*****

```

```

        MOV #MODE!ENABLE,@#CMDREG
        MOV #PROM!LOOP,@#MODREG
;
        MOV TBUF,R4 ; SET UP XMIT BUFFER
        CLR (R4)+
        MOV #MxMTBC,(R4)+
        CLR @#CLRFIF
        CLR FLG4
        MOV RBUF,@#LFRBUF
        MOV TBUF,@#LTAC

```

```

*****
***** WAIT FOR INTERRUPTS *****
*****

```

```

        MTPS #PRI02
10$: BIT #TRNFLAG,FLG4
        BEQ 10$
;
20$: BIT #RCVFLAG,FLG4
        BEQ 20$
;
        MTPS #PRI07

```

```

*****
***** CHECK RECEIVE BUFFER BYTE COUNT *****
*****

```

```

        MOV RBUF,R4

```

MICROD - MICROCODE MODULE D
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LINK BYTE COUNTER MAXIMUM TEST

SEQ 397

1135	004010'	062704	000002						
1136	004014'	012402				ADD	#2,R4		: POINT AT MLEN
1137	004016'	022702	007777			MOV	(R4)+,R2		
1138	004022'	001005				CMP	#MRECBC,R2		
1139	004024'	000241				BNE	40\$		
1140	004026'	112767	000011	174611	30\$:	CLC			
1141	004034'	000207				MOVB	#9.,PCSR1+1		
1142						RTS	PC		
1143	004036'	000261			40\$:	SEC			: ERROR EXIT
1144	004040'	000772				BR	30\$		

MICROD - MICROCODE MODULE D
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 MODULE D, MICROTST #10 - FIFO TEST

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1145 .SBTTL MODULE D, MICROTST #10 - FIFO TEST
1146
1147 :*****
1148 :
1149 :THIS IS THE MICROCODE FOR THE FIFO TEST.
1150 :IT TRANSMITS A PACKET FROM BUFFER 0, SETS UP THE RECEIVER TO RECEIVE TO
1151 :THE BUFFER SPECIFIED BY THE HOST IN PCBB+0. AFTER THE INTERRUPT THE BUFFER
1152 :DONE FIFO CONTENTS ARE READ AND PASSED TO THE HOST IN PCBB+2.
1153 :
1154 :*****
1155
1156 004042' 112767 000002 174574 MICD10: MOVB #INTST,PCSR1 ;TELL HOST WE ARE TESTING
1157 004050' 016737 174570 021020 MOV PCSR1,@#IPCSR1
1158
1159 :GET RECEIVE BUFFER ADDRESS FROM HOST MEMORY
1160
1161 004056' 016737 174564 021010 MOV IPCSR2,@#MDMA0 ;GET CONTENTS OF HOST'S PCSR2+3
1162 004064' 016737 174560 021012 MOV IPCSR2+2,@#MDMA1
1163 004072' 013700 021014 MOV @#MDMAR0,R0 ;R0 = CONTENTS OF HOST'S PCSR2
1164 004076' 013701 021014 MOV @#MDMAR0,R1 ;R1 = CONTENTS OF HOST'S PCSR3
1165 004102' 010037 021010 MOV R0,@#MDMA0 ;POINT TO PCBB+0
1166 004106' 010137 021012 MOV R1,@#MDMA1
1167 004112' 013767 021014 174540 MOV @#MDMAR0,RBUF ;GET RECEIVE BUFFER ADDRESS
1168
1169 :CLEAR ALL OF LINK MEMORY
1170
1171 004120' 012702 100000 MOV #LINADR,R2
1172 004124' 005022 100000 10$: CLR (R2)+
1173 004126' 020227 177774 CMP R2,#LINADR+LINSIZ
1174 004132' 103774 BLO 10$
1175
1176 :FILL THE TRANSMIT BUFFER WITH 1'S
1177
1178 004134' 012702 100000 MOV #LINADR,R2 ;USE BUFFER 0 FOR TRANSMIT
1179 004140' 010267 174516 MOV R2,TBUF ;SAVE IT
1180 004144' 012722 177777 20$: MOV #177777,(R2)+
1181 004150' 020227 104000 CMP R2,#LINADR+SIZ1k
1182 004154' 103773 BLO 20$
1183
1184 :SET UP LINK FOR PROMISCUOUS MODE AND INTERNAL LOOPBACK. TRANSMIT THE MAX
1185 :SIZE PACKET. CLEAR THE FIFO AND GIVE THE LINK A RECEIVER BUFFER AND A TRANSMIT
1186 :BUFFER TO START THE OPERATION.
1187
1188 004156' 012737 100200 177776 MOV #MODE!ENABLE,@#CMDREG ;TURN ON THE LINK AND SELECT MODE REG
1189 004164' 012737 100004 177774 MOV #PROM!LOOP,@#MODREG ;SET PROMISCUOUS MODE AND LOOPBACK
1190 004172' 016702 174464 MOV TBUF,R2 ;GET TRANSMIT BUFFER
1191 004176' 005022 CLR (R2)+ ;CLEAR THE STATUS WORD
1192 004200' 012712 002752 MOV #MAXBC-CRCSIZ,(R2) ;SET TO MAX BYTE COUNT
1193 004204' 005037 021034 CLR @#CLRFIF ;CLEAR THE FIFO
1194 004210' 005067 174424 CLR FLG4 ;CLEAR THE INTERRUPT FLAG
1195 004214' 016737 174440 021032 MOV RBUF,@#LFRBUF ;LOAD THE FIFO WITH A RECEIVE BUFFER
1196 004222' 016737 174434 021030 MOV TBUF,@#LTAC ;GIVE TRANSMIT BUFFER TO START TRANSMIT
1197
1198 :LOWER THE PROCESSOR PRIORITY AND WAIT FOR THE INTERRUPTS
1199
1200 004230' 106427 000140 MTPS #PRI03 ;ALLOW BOTH TRANSMIT AND RECEIVER INTERRUPT
    
```

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MICROD - MICROCODE MODULE D      MACY11 30(1046) 11-JAN-83 09:52 PAGE 28
MICROD.MAC 10-JAN-83 17:53      MODULE D, MICROTTEST #10 - FIFO TEST
1201 004234' 032767 000100 174376 30$: BIT #RCVFLG,FLG4 ;WAIT FOR RECEIVER FIRST
1202 004242' 001774 BEQ 30$
1203
1204 004244' 032767 000040 174366 40$: BIT #TRNFLG,FLG4 ;THEN THE TRANSMITTER
1205 004252' 001774 BEQ 40$
1206
1207 004254' 106427 000340 MTPS #PRI07 ;DISABLE FURTHER INTERRUPTS
1208
1209 ;READ RECEEVER BUFFER DONE FIFO AND PASS BACK TO HOST IN PCBB+2
1210 ;
1211 004260' 062700 000002 ADD #2,R0 ;POINT TO PCBB+2
1212 004264' 005501 ADC R1
1213 004266' 010037 021010 MOV R0,@#MDMA0
1214 004272' 010137 021012 MOV R1,@#MDMA1
1215 004276' 013737 021044 021026 MOV @#LRBUF,@#MDMAW0 ;PASS BUFFER DONE DATA TO HOST
1216
1217 004304' 112767 000012 174333 MOVB #10.,PCSR1+1 ;INDICATE WHAT TEST WE JUST FINISHED
1218 004312' 000241 CLC ;INDICATE SUCCESS
1219 004314' 000207 RTS PC
1220

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MICROD - MICROCODE MODULE D
MICROD.MAC 10-JAN-83 17:53

MACY11 30(1046) 11-JAN-83 09:52 PAGE 29
MODULE D, MICROTST #11 - LINK ADDRESS TEST

.SBTTL MODULE D, MICROTST #11 - LINK ADDRESS TEST

:THIS THE MICROCODE FOR THE RECEIVER AND TRANSMITTER LINK MEMORY ADDRESS TESTS.
:IT FILLS ALL OF LINK MEMORY WITH 0'S THEN FILLS THE TRANSMIT BUFFER OBTAINED
:FROM THE HOST'S PCBB+2 WITH 1'S. IT SETS UP THE RECEIVER TO RECEIVE A PACKET
:INTO THE BUFFER OBTAINED FROM THE HOST'S PCBB+0. THE PACKET OF 1'S IS
:TRANSMITTED WITH LOOPBACK ENABLED IN PROMISCUIOUS MODE. AFTER THE INTERRUPT
:ALL LINK MEMORY OUTSIDE OF THE TRANSMIT AND RECEIVE BUFFERS IS CHECKED
:TO SEE IF AND DATA WAS WRITTEN TO AN INCORRECT ADDRESS. IF SO THE FAULTY
:ADDRESS IS PASSED BACK TO THE HOST IN PCBB+4 AND THE GOOD DATA IN PCBB+6 AND
:THE BAD DATA IN PCBB+10.

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004316' 112767 000002 174320
004324' 016737 174314 021020

004332' 016737 174310 021010
004340' 016737 174304 021012
004346' 013700 021014
004352' 013701 021014
004356' 010037 021010
004362' 010137 021012
004366' 013767 021014 174264
004374' 013767 021014 174260

004402' 012702 100000
004406' 005022
004410' 020227 177774
004414' 103774

004416' 016702 174240
004422' 005022
004424' 012722 002756

004430' 012701 002756
004434' 006201
004436' 012722 177777
004442' 005301
004444' 001374

004446' 012737 000200 177776
004454' 012737 100014 177774

MICD11: MOVB #INTST,PCSR1 ; TELL HOST WE ARE TESTING
MOV PCSR1,@#IPCSR1

:GET THE RECEIVER BUFFER FROM THE HOST'S PCBB+0 AND THE TRANSMIT BUFFER FROM
:THE HOST'S PCBB+2
MOV IPCSR2,@#MDMA0 ;GET CONTENTS OF HOST'S PCSR2+3
MOV IPCSR2+2,@#MDMA1
MOV @#MDMAR0,R0 ;R0 = CONTENTS OF HOST'S PCSR2
MOV @#MDMAR0,R1 ;R1 = CONTENTS OF HOST'S PCSR3
MOV R0,@#MDMA0 ;POINT TO PCBB+0
MOV R1,@#MDMA1
MOV @#MDMAR0,RBUFP ;GET RECEIVER BUFFER FROM PCBB+0
MOV @#MDMAR0,TBUFP ;GET TRANSMIT BUFFER FROM PCBB+2

:FILL ALL OF LINK MEMORY WITH 0'S
10\$: MOV #LINADR,R2
CLR (R2)+
CMP R2,#LINADR+LINSIZ
BLO 10\$

MOV TBUFP,R2 ;POINT TO BASE OF TRANSMIT BUFFER
CLR (R2)+ ;CLEAR STATUS WORD
MOV #MAXBC,(R2)+ ;PUT IN BYTE COUNT

:FILL TRANSMIT BUFFER WITH 1'S
20\$: MOV #MAXBC,R1
ASR R1
MOV #177777,(R2)+
DEC R1
BNE 20\$

:SET UP LINK FOR PROMISCUIOUS MODE AND INTERNAL LOOPBACK. TRANSMIT THE PACKET
MOV #MODE,@#CMDREG ;SELECT THE MODE REGISTER
MOV #PROM!LOOP!DTCR,@#MODREG ;ENABLE PROMISCUIOUS MODE AND LOOPBACK

MICROD - MICROCODE MODULE D
MICROD.MAC 10-JAN-83 17:53

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MODULE D, MICROTST #11 - LINK ADDRESS TEST

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1277
1278 004462' 012737 100000 177776      MOV      #ENABLE,@#CMDREG      ;AND DISABLE TRANSMIT CRC
1279 004470' 005037 021034              CLF      @#CLRIF              ;TURN ON THE LINK
1280 004474' 005067 174140              CLR      FLG4                 ;CLEAR THE FIFO
1281 004500' 016737 174154 021032      MOV      RBUF, @#LFRBUF       ;CLEAR THE INTERRUPT WORD
1282 004506' 016737 174150 021030      MOV      TBUF, @#LTAC         ;GIVE THE LINK A RECEIVE BUFFER
1283                                          ;GIVE THE LINK A TRANSMIT BUFFER
1284                                          ;TO START THE OPERATION
1285                                          ;
1286                                          ;LOWER THE PROCESSOR PRIORITY AND WAIT FOR THE INTERRUPT
1287 004514' 106427 000140              MTPS     #PRI03              ;ALLOW BOTH TRANSMIT AND RECEIVE INTERRUPT
1288 004520' 032767 000100 174112 30$:   BIT      #RCVFLG,FLG4         ;WAIT FOR THE RECEIVE INTERRUPT
1289 004526' 001774                      BEQ      30$
1290 004530' 032767 000040 174102 40$:   BIT      #TRNFLG,FLG4
1291 004536' 001774                      BEQ      40$
1292
1293 004540' 106427 000340              MTPS     #PRI07              ;DISABLE FURTHER INTERRUPTS
1294
1295                                          ;CHECK RECEIVE BUFFER TO MAKE SURE IT RECEIVED NON-ZERO DATA
1296                                          ;
1297 004544' 012703 177777              MOV      #177777,R3          ;PASS THIS NON-ZERO DATA IF FAILURE
1298 004550' 016702 174104              MOV      RBUF,R2            ;GET POINTER TO RECEIVER BUFFER
1299 004554' 062702 000004              ADD      #4,R2              ;SKIP STATUS AND BYTE COUNT
1300 004560' 012701 002756              MOV      #MAXBC,R1          ;NUMBER OF BYTES WE SENT
1301 004564' 006201                      ASR      R1                  ;MAKE IT WORDS
1302 004566' 005712                      TST      (R2)                ;IS THERE NON-ZERO DATA HERE?
1303 004570' 001432                      BEQ      90$                 ;NO, ERROR!
1304 004572' 062702 000002              ADD      #2,R2              ;POINT TO NEXT WORD IN RECEIVER BUFFER
1305 004576' 005301                      DEC      R1
1306 004600' 001372                      BNE      45$
1307
1308                                          ;CHECK ALL LINK MEMORY EXCEPT THE TRANSMIT AND RECEIVE BUFFERS FOR ANY NON-ZERO
1309                                          ;DATA
1310                                          ;
1311 004602' 005003                      CLR      R3                  ;PASS THIS ZERO DATA IF FAILURE
1312 004604' 012702 100000              MOV      #LINADR,R2         ;START AT BASE OF LINK MEMORY
1313 004610' 020267 174046 50$:   CMP      R2,TBUF            ;ARE WE AT THE TRANSMIT BUFFER?
1314 004614' 001002                      BNE      60$                 ;NO
1315 004616' 062702 002762              ADD      #MAXBC+4,R2        ;YES, SKIP OVER THE HEADER AND THE DATA
1316 004622' 020267 174032 60$:   CMP      R2,RBUF            ;ARE WE AT THE RECEIVE BUFFER?
1317 004626' 001002                      BNE      70$                 ;NO
1318 004630' 062702 002762              ADD      #MAXBC+4,R2        ;YES, SKIP OVER THE HEADER AND THE DATA
1319 004634' 005712                      TST      (R2)                ;IS ANY NON-ZERO DATA IN HERE?
1320 004636' 001007                      BNE      90$                 ;YES, ERROR!
1321 004640' 062702 000002              ADD      #2,R2              ;POINT TO NEXT WORD IN LINK MEMORY
1322 004644' 020227 177774              CMP      R2,#LINADR+LINSIZ  ;HAVE WE CHECKED ALL OF LINK MEMORY?
1323 004650' 103757                      BLO      50$                 ;NOT YET
1324
1325                                          ;TEST WAS SUCESSFULL
1326                                          ;
1327 004652' 000241                      CLC                          ;INDICATE SUCCESS
1328 004654' 000407                      BR       100$                ;LEAVE
1329
1330                                          ;PASS THE ADDRESS BACK TO THE HOST IN PCBB+4 AND THE DATA IN PCBB+6
1331
1332 004656' 010237 021026 90$:   MOV      R2,@#MDMAW0         ;PASS ADDRESS TO PCBB+4

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MICROD - MICROCODE MODULE D
MICROD.MAC 10-JAN-83 17:53

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MODULE D, MICROTTEST #11 - LINK ADDRESS TEST

1333 004662' 010337 021026
1334 004666' 011237 021026
1335 004672' 000261
1336
1337 004674' 112767 000013
1338 004702' 000207
1339
1340
1341 004704' 004806
1342 000001

MOV R3,@#MDMAWO
MOV (R2),@#MDMAWO
SEC
MOVB #11.,PCSR1+1
RTS PC

;PASS GOOD DATA TO PCBB+6
;PASS DATA TO PCBB+10
;INDICATE FAILURE
;TELL HOST WHAT TEST JUST FINISHED

MICDSZ::MICDSZ-MICROD+2
.END

MICROD - MICROCODE MODULE D MACY11 30(1046) 11-JAN-83 09:52 PAGE 36
MICROD.MAC 10-JAN-83 17:53 CROSS REFERENCE TABLE -- USER SYMBOLS

MICRD 000000 001
 004706 002

ERRORS DETECTED: 0
DEFAULT GLOBALS GENERATED: 0

MICROD.OBJ, MICROD.LST/CRF/DOC/NL:TOC/SOL=MICROD.MAC
RUN-TIME: 1 2 .4 SECONDS
RUN-TIME RATIO: 35/4=7.7
CORE USED: 9K (18 PAGE.)

DOCUMENT PAGES: 35

MICROE - MICROCODE MODULE E
MICROE.MAC 10-JAN-83 17:53

MACY11 30(1046) 11-JAN-83 09:53 PAGE 1

```

1      .TITLE MICROE - MICROCODE MODULE E
2      : && DEDICATED THE CRC CIRCUITRY TO THE RECEIVE SIDE OF THE LINK
3
4      000000'
5      .CSECT MICRE
6
7      .SBTTL REGISTER DEFINITIONS USED BY THE T11
8
9      021000 IPCSRO = 21000 ;INTERNAL PCSRO ADDRESS
10     021002 DMACSR = 21002 ;DMA ENGINE CONTROL STATUS REGISTER
11     021004 DMAT0 = 21004 ;DMA ENGINE TO ADDRESS REGISTER #0
12     021006 DMAT1 = 21006 ;DMA ENGINE TO ADDRESS REGISTER #1
13     021010 MDMA0 = 21010 ;MICROCPU DMA TO ADDRESS REGISTER #0
14     021012 MDMA1 = 21012 ;MICROCPU DMA TO ADDRESS REGISTER #1
15     021014 MDMAR0 = 21014 ;MICROCPU DMA DATA REGISTER - AUTO INCREMENT R/O
16     021016 MDMAR1 = 21016 ;MICROCPU DMA DATA REGISTER - AUTO DECREMENT R/O
17     021020 IPCSR1 = 21020 ;INTERNAL PCSR1 ADDRESS
18     021022 DMAF = 21022 ;DMA ENGINE FROM ADDRESS REGISTER
19     021024 DMAWC = 21024 ;DMA ENGINE WORD COUNT REGISTER
20     021026 MDMAW0 = 21026 ;MICROCPU DMA DATA REGISTER - AUTO INCREMENT W/O
21     021030 LTAC = 21030 ;LINK TRANSMIT ADDRESS COUNTER REGISTER
22     021032 LFRBUF = 21032 ;LINK RECIEVE BUFFER ADDRESS FIFO
23     021034 CLRIF = 21034 ;CLEAR FIFO
24     021036 MDMAW1 = 21036 ;MICROCPU DMA DATA REGISTER - AUTO DECREMENT W/O
25     021040 PCSRSW = 21040 ;SWITCH PACK REGISTER
26     021042 MDMSR = 21042 ;MICROCPU DMA STATUS REGISTER
27     021044 LRBUF = 21044 ;LINK RECIEVE BUFFER COMPLETED
28     021060 PHYAD0 = 21060 ;PHYSICAL ADDRESS ROM BYTE 0
29     021062 PHYAD1 = 21062 ;PHYSICAL ADDRESS ROM BYTE 1
30     021064 PHYAD2 = 21064 ;PHYSICAL ADDRESS ROM BYTE 2
31     021066 PHYAD3 = 21066 ;PHYSICAL ADDRESS ROM BYTE 3
32     021070 PHYAD4 = 21070 ;PHYSICAL ADDRESS ROM BYTE 4
33     021072 PHYAD5 = 21072 ;PHYSICAL ADDRESS ROM BYTE 5
34     177774 MODREG = 177774 ;LINK MODE REGISTER
35     177774 ADRREG = 177774 ;LINK STATION ADDRESS RAM REGISTER
36     177776 CMDREG = 177776 ;LINK COMMAND REGISTER
37
38     .SBTTL OTHER DEFINITIONS USED BY THE MICROCODE
39
40     100000 BIT15 = 100000
41     040000 BIT14 = 40000
42     020000 BIT13 = 20000
43     010000 BIT12 = 10000
44     004000 BIT11 = 4000
45     002000 BIT10 = 2000
46     001000 BIT9 = 1000
47     000400 BIT8 = 400
48     000200 BIT7 = 200
49     000100 BIT6 = 100
50     000040 BIT5 = 40
51     000020 BIT4 = 20
52     000010 BIT3 = 10
53     000004 BIT2 = 4
54     000002 BIT1 = 2
55     000001 BIT0 = 1
56     012400 ;LASFTP = BIT8!BIT10!BIT12 ;LOAD AND START FUNCTION TEST PATTERN

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MICROE - MICROCODE MODULE E
MICROE.MAC 10-JAN-83 17:53

MACY11 30(1046) 11-JAN-83 09:53 PAGE 2
OTHER DEFINITIONS USED BY THE MICROCODE

57	000340	PRI07 =	340	
58	000300	PRI06 =	300	
59	000240	PRI05 =	240	
60	000200	PRI04 =	200	
61	000140	PRI03 =	140	
62	000100	PRI02 =	100	
63	000040	PRI01 =	40	
64	000000	PRI00 =	0	
65		.		
66		;	PCSR0 - PORT CONTROL STATUS REGISTER 0	
67		.		
68	100000	SERI =	BIT15	
69	040000	PCEI =	BIT14	
70	020000	RXI =	BIT13	
71	010000	TXI =	BIT12	
72	004000	DNI =	BIT11	
73	002000	RCEI =	BIT10	
74	000400	FATI =	BIT8	
75		.		
76		;	LINK COMMAND REGISTER	
77		.		
78	100000	ENABLE =	BIT15	;ENABLE LINK MODULE
79	000200	MODE =	BIT7	;ENABLE MODE REGISTER
80	000100	ARAM =	BIT6	;ENABLE STATION ADDRESS RAM
81		.		
82		;	LINK MODE REGISTER	
83		.		
84	100000	PROM =	BIT15	;PROMISCUIOUS MODE
85	040000	ENAL =	BIT14	;ENABLE MULTICAST
86	004000	ENCR =	BIT11	;ENABLE COLLISION TEST
87	002000	ACLO =	BIT10	;ENABLE ACLO
88	000040	DRTY =	BIT5	;DISABLE RETRY LOGIC
89	000020	COLL =	BIT4	;SIMULATE A COLLISION
90	000010	DTCR =	BIT3	;DISABLE TRANSMIT CRC LOGIC
91	000004	LOOP =	BIT2	;ENABLE LOOPBACK
92		.		
93	000070	TRNVEC=	70	;VECTOR ADDRESS FOR THE TRANSMITTER
94	000120	RCVVEC=	120	;VECTOR ADDRESS FOR THE RECEIVER
95	000134	SANVEC=	134	;VECTOR ADDRESS FOR THE SANITY TIMER
96	000064	CSRVEC=	64	;VECTOR ADDRESS FOR CSR WRITE INTERRUPT
97	000114	DMAVEC=	114	;VECTOR ADDRESS FOR DMA DONE INTERRUPT
98	000140	PARVEC=	140	;VECTOR ADDRESS FOR LINK MEMORY PARITY ERROR
99	001000	STACK=	1000	;STACK LOCATION
100	000001	INMON=	1	;IN MICROMONITOR STATE
101	000002	INTST=	2	;IN A TEST STATE
102	000003	INERR=	3	;IN ERROR STATE
103	000001	CSRFLG=	BIT0	;CSR WRITE INTERRUPT OCCURED
104	000002	ERRFLG=	BIT1	;UNEXPECTED ERROR OCCURED
105	000004	PARFLG=	BIT2	;PARITY ERROR OCCURED
106	000010	NXMFLG=	BIT3	;NON-EXISTANT MEMORY ERROR OCCURRED
107	000020	NPRFLG=	BIT4	;NPR TIMEOUT OCCURRED
108	000040	TRNFLG=	BIT5	;TRANSMITTER INTERRUPT OCCURRED
109	000100	RCVFLG=	BIT6	;RECEIVER INTERRUPT OCCURRED
110	100000	NPRERR=	BIT15	;PCSR0 FLAG INDICATING NPR ERROR OCCURRED
1 1	040000	NXMERR=	BIT14	;PCSR0 FLAG INDICATING NON-EXISTANT MEMORY ERROR OCCURRED
1 2	020000	UNIERR=	BIT13	;PCSR0 FLAG INDICATING UNEXPECTED INTERRUPT OCCURRED

MICROE - MICROCODE MODULE E
MICROE.MAC 10-JAN-83 17:53

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OTHER DEFINITIONS USED BY THE MICROCODE

113	010000	PARERR= BIT12	:PCSR0 FLAG INDICATING LINK MEMORY PARITY ERROR OCCURRED
114	020000	MTCH= BIT13	:MATCH BIT
115	004000	SIZ1K= 4000	:1K WORDS
116	010000	SIZ2K= SIZ1K*2	:2K WORDS
117	020000	SIZ4K= SIZ2K*2	:4K WORDS
118	040000	SIZ8K= SIZ4K*2	:8K WORDS
119	020000	WCSSIZ= SIZ4K	:SIZE OF WRITEABLE CONTROL STORE
120	020000	IOSIZ= SIZ4K	:SIZE OF I/O PAGE
121	040000	ROMSIZ= SIZ8K	:SIZE OF ROM
122	077774	LINSIZ= SIZ8K*2-4	:SIZE OF LINK MEMORY
123	000000	WCSADR= 0	:BASE ADDRESS OF WCS
124	020000	IOADR= WCSADR+WCSSIZ	:BASE ADDRESS OF I/O PAGE
125	040000	ROMADR= IOADR+IOSIZ	:BASE ADDRESS OF ROM
126	100000	LINADR= ROMADR+ROMSIZ	:BASE ADDRESS OF LINK MEMORY
127	000100	MINBC= 64.	:64 BYTES
128	002752	MAXBC= 1518.-4.	:MAXIMUM # OF BYTES IN A TRANSMIT BUFFER
129	000000	DATERR= 0	:FLAG INDICATING DATA ERROR OCCURRED
130	000001	ADRERR= 1	:FLAG INDICATING ADDRESS ERROR OCCURRED
131			

MICROE - MICROCODE MODULE E
 MICROE.MAC 10-JAN-83 17:53

MACY11 30(1046) 11-JAN-83 09:53 PAGE 4
 OTHER DEFINITIONS USED BY THE MICROCODE

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132
133      .SBTTL MICROCODE MODULE E
134
135 000000' 106427 000340      MICROE::MTPS      #PRI07      ;DISABLE INTERRUPTS
136 000004' 012737 000000 177776      MOV      #0,@#LMDREG      ;TURN OFF THE LINK
137 000012' 012706 001000      MOV      #STACK,SP      ;SETUP STACK
138 000016' 112767 000001 000602      MOVB    #INMON,PCSR1    ;TELL HOST WE ARE IN MICROMONITOR
139 000024' 016737 000576 021020      MOV      PCSR1,@#IPCSR1
140 000032' 012737 004000 021000      MOV      #DNI,@#IPCSRO
141 000040' 010700      MOV      PC,R0      ;TELL HOST THE LOAD AND START FINISHED
142 000042' 062700 000404      ADD      #ERRINT-.,R0    ;GET ADDRESS OF UNEXPECTED ERROR...
143 000046' 005001      CLR      R1      ;HANDLER
144 000050' 010021      10$:    MOV      R0,(R1)+      ;FILL ALL UNUSED VECTORS WITH TRAP...
145 000052' 012721 000340      MOV      #PRI07,(R1)+    ;HANDLER
146 000056' 020127 001000      CMP      R1,#1000
147 000062' 002772      BLT     10$
148
149 000064' 010700      MOV      PC,R0      ;SETUP PARITY TRAP VECTOR
150 000066' 062700 000462      ADD      #PARINT-.,R0
151 000072' 010037 000140      MOV      R0,@#PARVEC
152 000076' 012737 000340 000142      MOV      #PRI07,@#PARVEC+2
153
154 000104' 010700      MOV      PC,R0      ;SETUP DMA INTERRUPT VECTOR
155 000106' 062700 000364      ADD      #DMAINT-.,R0
156 000112' 010037 000114      MOV      R0,@#DMAVEC
157 000116' 012737 000340 000116      MOV      #PRI07,@#DMAVEC+2
158
159 000124' 010700      MOV      PC,R0      ;SETUP CSR WRITE VECTOR
160 000126' 062700 000310      ADD      #CSRWRT-.,R0
161 000132' 010037 000064      MOV      R0,@#CSRVEC
162 000136' 012737 000200 000066      MOV      #PRI04,@#CSRVEC+2
163
164 000144' 010700      MOV      PC,R0      ;SETUP SANTITY TIMER VECTOR
165 000146' 062700 000316      ADD      #TIMINT-.,R0
166 000152' 010037 000134      MOV      R0,@#SANVEC
167 000156' 012737 000240 000136      MOV      #PRI05,@#SANVEC+2
168
169 000164' 010700      MOV      PC,R0      ;SETUP TRANSMITTER VECTOR
170 000166' 062700 000414      ADD      #TRNINT-.,R0
171 000172' 010037 000070      MOV      R0,@#TRNVEC
172 000176' 012737 000200 000072      MOV      #PRI04,@#TRNVEC+2
173
174 000204' 010700      MOV      PC,R0      ;SETUP RECEIVER VECTOR
175 000206' 062700 000360      ADD      #RCVINT-.,R0
176 000212' 010037 000120      MOV      R0,@#RCVVEC
177 000216' 012737 000240 000122      MOV      #PRI05,@#RCVVEC+2
178
179 000224' 013700 021040      MOV      @#PCSRW,R0      ;GET SWITCH PACK BITS
180 000230' 052700 176000      BIS      #176000,R0      ;MAP THEM INTO HOST I/O PAGE
181 000234' 006300      ASL     R0      ;SHIFT OVER TO POSITION CORRECTLY
182 000236' 006300      ASL     R0
183 000240' 006300      ASL     R0
184 000242' 062700 000004      ADD      #4,R0      ;PCSR2 IS PCSR0+4
185 000246' 010067 000356      MOV      R0,IPCSR2      ;SAVE PCSR2 ADDRESS
186 000252' 012767 000003 000352      MOV      #3,IPCSR2+2    ;HIGH ORDER BITS 17:16
187 000260' 005067 000336      CLR     FLG5      ;INITIALIZE FLAG WORD
    
```


MICROE - MICROCODE MODULE E
MICROE.MAC 10-JAN-83 17:53

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MICROCODE MODULE E

188	000264'	106427	000000		15\$:	MTPS	#PRI00		:ALLOW INTERRUPTS
189									
190	000270'	005767	000326		20\$:	TST	rLG5		:WAIT FOR A COMMAND FROM HOST
191	000274'	001775				BEQ	20\$		
192									
193	000276'	106427	000340			MTPS	#PRI07		:RAISE CPU PRIORITY TO SERVICE COMMAND
194	000302'	032767	000001	000312		BIT	#CSRFLG,FLG5		:DID HOST GIVE US A COMMAND?
195	000310'	001001				BNE	30\$:YES
196	000312'	000777				BR	.		:NO, ERROR SO JUST SIT HERE...
197									:FOR LACK OF ANYTHING BETTER TO DO
198									
199	000314'	113700	021000		30\$:	MOVB	@#IPCSRO,R0		:GET WHAT HOST WROTE TO PCSRO
200	000320'	042700	177760			BIC	#177760,R0		:STRIP ALL BUT COMMAND BITS
201	000324'	001004				BNE	35\$:WAS IT THE CLEAR FUNCTION?
202	000326'	012737	000001	021020		MOV	#INMON,@#IPCSR1		:YES, CLEAR OUT THE TEST # BITS
203	000334'	000432				BR	50\$		
204	000336'	022700	000017		35\$:	CMP	#17,R0		:RESTART OPERATIONAL MICROCODE?
205	000342'	001432				BEQ	60\$:YES
206	000344'	162700	000001			SUB	#1,R0		
207	000350'	010701				MOV	PC,R1		:GET ADDRESS OF OUR COMMAND TABLE
208	000352'	062701	000240			ADD	#TBLD-.,R1		
209	000356'	006300				ASL	R0		:MAKE COMMAND A BYTE OFFSET
210	000360'	060001				ADD	R0,R1		:USE IT TO INDEX INTO COMMAND TABLE
211	000362'	061101				ADD	(R1),R1		:R1 NOW HAS COMMAND ROUTINE ADDRESS
212	000364'	004711				JSR	PC,(R1)		:EXECUTE AS COMMAND FROM HOST
213	000366'	103404				BCS	40\$:ERROR OCCURRED
214	000370'	112767	000001	000230		MOVB	#INMON,PCSR1		:INDICATE TO HOST WE ARE BACK IN...
215	000376'	000403				BR	45\$:MICROMONITR
216	000400'	112767	000003	000220	40\$:	MOVB	#INERR,PCSR1		:INDICATE TO HOST ERROR OCCURRED
217	000406'	016737	000214	021020	45\$:	MOV	PCSR1,@#IPCSR1		
218	000414'	012737	004000	021000		MOV	#DNI,@#IPCSRO		:TELL HOST THIS MICROTEST FINISHED
219	000422'	005067	000174		50\$:	CLR	FLG5		:RESET FLAG WORD
220	000426'	000716				BR	15\$:GO WAIT FOR ANOTHER COMMAND
221									
222	000430'	005000			60\$:	CLR	R0		:FAKE SUCCESSFUL SELF TEST RESULTS
223	000432'	000137	040006			JMP	@#40006		:START OPERATIONAL MICROCODE
224									
225	000436'	052767	000001	000156	CSRWRT:	BIS	#CSRFLG,FLG5		:INDICATE A CSR WRITE INTERRUPT OCCURED
226	000444'	000002				RTI			
227									
228	000446'	052767	000002	000146	ERRINT:	BIS	#ERRFLG,FLG5		:INDICATE A UNEXPECTED INTERRUPT OCCURED
229	000454'	012737	020000	021000		MOV	#UNERR,@#IPCSRO		:TELL HOST AN UNEXPECTED INTERRUPT
230									:HAPPENED
231	000462'	000777				BR	.		:JUST SIT HERE AND SPIN WHEELS
232									:COUNT ON HOST TO TIMEOUT
233									
234	000464'	005267	000134		TIMINT:	INC	SANTIM		:COUNT TICKS AS THEY OCCUR
235	000470'	000002				RTI			
236									
237	000472'	013767	021002	000140	DMAINT:	MOV	@#DMACSR,DMDONE		:GET DMA STATUS
238	000500'	032767	040000	000132		BIT	#BIT14,DMDONE		:DID A NON-EXISTANT MEMORY INTERRUPT OCCUR?
239	000506'	001404				BEQ	10\$:NO
240	000510'	012737	040000	021000		MOV	#NXMERR,@#IPCSRO		:YES, TELL HOST A NON-EXISTANT MEMORY
241									:LOCATION WAS ADDRESSED
242	000516'	000407				BR	20\$		
243	000520'	032767	100000	000112	10\$:	BIT	#BIT15,DMDONE		:DID A NPR TIMEOUT OCCUR?

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244 000526' 001407          BEQ      30$          :NO
245 000530' 012737 100000 021000  MOV     #NPRERR,@#IPCSRO :TELL HOST NPR TIMEOUT HAPPENED
246 000536' 012737 100000 021002 20$: MOV     #BIT15,@#DMACSR :CLEAR THE INTERRUPT IN THE DMA ENGINE
247 000544' 000777          BR       .           :SIT HERE AND SPIN WHEELS
248 000546' 000002          30$: RTI          .
249
250 000550' 052767 000004 000044 PARINT: BIS     #PARFLG,FLG5 :SET PARITY ERROR OCCURRED
251 000556' 012737 010000 021000  MOV     #PARERR,@#IPCSRO :TELL HOST A LINK MEMORY PARITY ERROR
252                                     OCCURRED
253 000564' 000777          BR       .           :SIT HERE AND SPIN WHEELS
254
255 000566' 005737 021044          RCVINT: TST    @#LRBUF   :READ BUFFER DONE REGISTER...
256                                     :WHICH CLEARS THE INTERRUPT
257 000572' 052767 000100 000022          BIS     #RCVFLG,FLG5 :SET RECEIVER INTERRUPT OCCURRED
258 000600' 000002          RTI
259
260 000602' 052767 000040 000012 TRNINT: BIS     #TRNFLG,FLG5 :SET TRANSMITTER INTERRUPT OCCURRED
261 000610' 000002          RTI
262
263 000612' 000034          TBLD:  .WORD  MICE1-.  : STATION ADDRESS PATTERN
264 000614' 000614          .WORD  MICE2-.  : STATION ADDRESS REJECTION
265 000616' 001064          .WORD  MICE3-.  : STATION ADDRESS POSITION
266 000620' 001620          .WORD  MICE4-.  : MULTICAST ADDRESS TEST
267
268 000622' 000000          FLG5:  .WORD  0       :FLAG WORD
269 000624' 000000          SANTIM: .WORD  0       :COUNT FOR SANITY TIMER
270 000626' 000000          PCSR1: .WORD  0       :COPY OF WHAT GOES TO PCSR1
271 000630' 000000 000000          IPCSR2: .WORD  0,0     :ADDRESS IN HOST MEMORY FOR PCSR2
272 000634' 000000 000000          PCBADR: .WORD  0,0     :ADDRESS IN HOST MEMORY FOR PCB
273 000640' 000000          DMDONE: .WORD  0       :
274 000642' 000000          RBUF:  .WORD  0       :POINTER TO RECIEVE BUFFER
275 000644' 000000          TBUF:  .WORD  0       :POINTER TO XMIT BUFFER
276

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SBTTL STATION ADDRESS PATTERN TEST

MICROCODE FOR STATION ADDRESS PATTERN TEST. GETS A 'PATTERN' TO BE USED AS A STATION ADDRESS FOR TESTING THE STATION ADDRESS RAM AND DETECTION CIRCUITRY. FILLS THE STATION ADDRESS RAM. LOOPS A DATAGRAM WITH A STATION ADDRESS IDENTICAL TO THE ADDRESS FILLING STATION ADDRESS RAM. IF ALL IS WORKING, DATAGRAM SHOULD BE RECEIVED OK.

***** TELL HIM WE ARE TESTING *****

000002 177752
000746 021020

MICE1: MOVB #INST,PCSR1 . TELL HOST WE ARE TESTING
MOV PCSR1,PCSR1

***** PICK UP HOST ADDRESS OF PCBB *****

000737 177742 021010
000738 177736 021012
000739 013700 021014
000740 013701 021014

MOV IPCSR2,PCSR2 . PICK UP ADDRESS OF PCBB
MOV IPCSR2+2,PCSR2
MOV PCSR2,R0 . R0=CONTENTS OF PCSR2
MOV PCSR2,R1 . R1=CONTENTS OF PCSR2

000736 010037 021010
000737 010137 021012

MOV R0,PCSR2 . POINT TO HOST PCBB
MOV R1,PCSR2

000716 013702 021014

MOV PCSR2,R2 . R2 NOW HOLDS LS ADDRESS WORD

000722 062700 000002
000726 005501

ADD #2,R0 . INDEX TO NEXT HOST WORD
ADC R1

000730 013703 021014

MOV PCSR2,R3 . R3 NOW HOLDS MIDDLE PATTERN

000734 062700 000002
000740 005501

ADD #2,R0 . INDEX TO NEXT HOST WORD
ADC R1

000742 013704 021014

MOV PCSR2,R4 . R4 NOW HOLDS MS PATTERN WORD

000746 010446
000750 010346
000752 010246

MOV R4,-(SP) . SAVE THEM FOR LATER
MOV R3,-(SP)
MOV R2,-(SP)

***** MAKE A STATION ADDRESS FILE FROM THE PASSED PATTERN *****

000754 010700
000756 062700 000342
000762 012705 000014
000766 010220

MOV PC,R0 . POINT AT SA FILE
ADD #FILE-...
MOV #12,R5 . NEED TOTAL OF 12 ENTRIES
10S: MOV R2,R5 . LS WORD OF PATTERN

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STATION ADDRESS PA...ES

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333 000770' 010320      MOV      R3,R0      : MIDDLE WORD OF PATTERN
334 000772' 010420      MOV      R4,R0      : MS WORD OF PATTERN
335 000774' 005305      DEC      R5          : END OF LOOP
336 000776' 001373      BNE     CS          :
337
338
339
340
341
342
343 001000' 012737 000200 177776      MOV      #MODE,2@PCMDREG      : SET LOOPBACK BEFORE LOADING
344 001006' 012737 000004 177774      MOV      #LOOP,2@PCMDREG
345
346 001014' 010701      MOV      PC,R1          : FORM POS/IND POINTER
347 001016' 062701 000302      ADD      #AFILE-..R
348
349 001022' 005000      CLR      R0            : THIS WILL CLEAR HIGH BITS
350
351 001024' 012746 000120      MOV      #PARAM+20,-(SP)      : SA RAM STARTS AT +20 LOCATIONS
352
353 001030' 012702 000003      MOV      #3,R2          : 3 WORDS PER ADDRESS/PATTERN
354 001034' 012704 000020      MOV      #16,R4         : SIXTEEN BITS PER WORD
355 001040' 012705 000014      MOV      #12,R5         : 12 POSITIONS IN SA RAM
356
357 001044' 010103      MOV      R1,R3          : COPY THE POINTER
358
359 001046' 006013      RCR      (R3)          : GET LSB OF ALL 12 ADDRESSES
360 001050' 006100      RJL      R0            : R0 WILL HOLD ORTHOGONAL WORD
361 001052' 062703 000006      ADD      #6,R3          : 6 BYTES PER ADDRESS/PATTERN
362 001056' 077505      SOB      R5,40$        : GO TILL DONE
363
364 001060' 011637 177776      MOV      (SP),2@CMDREG      : SET MODE TO WRITE SA RAM
365 001064' 010037 177774      MOV      R0,2@ADDRREG      : ORTHOGONAL WORD TO SA RAM
366 001070' 005216      INC      (SP)          : BUMP STATION ADDRESS
367 001072' 077416      SOB      R4,30$        : DO ANOTHER ONE
368
369 001074' 062701 000002      ADD      #2,R1          : ADVANCE TO NEXT WORD
370 001100' 077223      SOB      R2,20$        : LOOP TILL DONE
371
372 001102' 012600      MOV      (SP)+,R0       : R0 IS A JUNK REGISTER
373
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375
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378
379 001104' 012700 100000      MOV      #LINADR,R0       : RECEIVE BUFFER STARTS HERE
380 001110' 010067 177526      MOV      R0,RBUFP
381 001114' 005020      CLR      (R0)+          : FILL RECEIVE BUFFER WITH ZEROS
382 001116' 020027 104000      CMP      R0,#LINADR+SIZ1K : FILL THE BUFFER
383 001122' 103774      BLO     50$            : FILL ENTIRE BUFFER
384
385
386
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STATION ADDRESS PATTERN TEST

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389
390 001124' 012703 000377      :      MOV      #0377,R3      ; WORST CASE FOR CLOCKING
391 001130' 010067 177510      :      MOV      R0,TBUF      ;
392 001134' 010320      60$:  MOV      R3,(R0)+      ; FILL XMIT BUFFER WITH PATTERN
393 001136' 020027 110000      :      CMP      R0,#LINADR+SIZ2K ; STOP AT TOP
394 001142' 103774      :      BLO      60$
395
396
397
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399
400
401 001144' 012737 100020 177776 :      MOV      #ENABLE+20,@#CMDREG ; LEAVE 20 IN COMMAND REGISTER
402
403 001152' 016701 177466      :      MOV      TBUF,R1      ; POINT AT XMIT BUFFER
404 001156' 005021      :      CLR      (R1)+      ; CLEAR OUT STATUS WORD
405 001160' 012721 000100      :      MOV      #MINBC,(R1)+ ; SET BYTE COUNT TO MIN ALLOWED
406
407 001164' 012621      :      MOV      (SP)+,(R1)+ ; GET BACK HIGH ADDRESS PATTERN
408 001166' 012621      :      MOV      (SP)+,(R1)+ ;
409 001170' 012621      :      MOV      (SP)+,(R1)+ ; GET BACK LOW ADDRESS PATTERN
410
411 001172' 005037 021034      :      CLR      @#CLRFIF      ; CLEAR THE FIFO
412 001176' 005067 177420      :      CLR      FLG5      ; CLEAR INTERRUPT FLAG
413 001202' 016737 177434 021032 :      MOV      RBUF,@#LFRBUF ; TELL UNA WHERE RECEIVE BUF IS
414 001210' 016737 177430 021030 :      MOV      TBUF,@#LTAC   ; TELL UNA WHERE XMIT BUF IS
415
416 001216' 106427 000140      :      MTPS     #PRI03      ; ALLOW XMIT AND REC TO INTER
417 001222' 112767 000003 177376 :      MOV      #INERR,PCSR1 ; TELL HOST IN CASE OF FAILURE
418 001230' 016737 177372 021020 :      MOV      PCSR1,@#IPCSR1
419
420 001236' 016701 177402      :
421 001242' 011102      65$:  MOV      TBUF,R1      ; WAIT FOR MATCH BIT FIRST
422 001244' 032702 020000      :      MOV      (R1),R2
423 001250' 001774      :      BIT      #MTCH,R2
424
425 001252' 112767 000002 177346 :      BEQ      65$
426 001260' 016737 177342 021020 :      MOV      #INTST,PCSR1 ; TELL HOST WE GOT BY IT
427
428 001266' 032767 000100 177326 :      MOV      PCSR1,@#IPCSR1
429 001274' 001774      :
430
431 001276' 032767 000040 177316 :      BIT      #RCVFLG,FLG5 ; WAIT FOR RECEIVE INTER
432 001304' 001774      :      BEQ      70$
433
434 001306' 112767 000001 177313 :      BIT      #TRNFLG,FLG5 ; WAIT FOR XMIT INT TOO!
435 001314' 000241      :      BEQ      80$
436 001316' 000207      :
437
438
439 001320' 000000 000000 000000 :      MOV      #1,PCSR1+1 ; TELL HOST WE ARE DONE
440 001326' 000000 000000 000000 :      CLC
441 001334' 000000 000000 000000 :      RTS      ?C
442 001342' 000000 000000 000000 :
443 001350' 000000 000000 000000 :
444 001356' 000000 000000 000000 :

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AFIL: .WORD 0,0,0 ; WORD #1
      .WORD 0,0,0
      .WORD 0,0,0
      .WORD 0,0,0
      .WORD 0,0,0
      .WORD 0,0,0

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445	001364'	000000	000000	000000	.WORD	0,0,0
446	001372'	000000	000000	000000	.WORD	0,0,0
447	001400'	000000	000000	000000	.WORD	0,0,0
448	001406'	000000	000000	000000	.WORD	0,0,0
449	001414'	000000	000000	000000	.WORD	0,0,0
450	001422'	000000	000000	000000	.WORD	0,0,0

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001430' 112767 000002 177170
001436' 016737 177164 021020
001444' 005067 177154
001450' 012737 000200 177776
001456' 012737 000004 177774
001464' 012704 000060
001470' 005000
001472' 012701 000120
001476' 010137 177776
001502' 010037 177774
001506' 005201
001510' 077406
001512' 012700 100000
001516' 010067 177120
001522' 005020
001524' 020027 104000
001530' 103774
001532' 012703 000377
001536' 010067 177102
001542' 010320
001544' 020027 110000
001550' 103774

```
.SBTTL STATION ADDRESS REJECTION TEST

THIS TEST WILL VERIFY THAT THE STATION ADDRESS LOGIC WILL NOT
ACCEPT A STATION ADDRESS IF THAT STATION ADDRESS IS NOT PRESENT
IN THE STATION ADDRESS RAM. STATION ADDRESS RAM WILL BE FILLED
WITH AN ADDRESS. DIFFERENT ADDRESS WILL BE PUT IN DATAGRAM FIELD
AND LOOPED BACK. LINK SHOULD NOT RECOGNIZE THE DATAGRAM.

*****
***** TELL HIM WE ARE TESTING *****
*****

MICE2:  MOVB  #INTST,PCSR1      ; TELL HOST WE ARE TESTING
        MOV   PCSR1,@#IPCSR1

        CLR   SANTIM          ; CLEAR FLAG FOR TIMER

*****
***** FILL STATION ADDRESS RAM WITH KNOWN (PHONEY) ADDRESS *****
*****

        MOV   #MODE,@#CMDREG   ; SET LOOP TO LOAD SA RAM
        MOV   #LOOP,@#MODREG

        MOV   #48.,R4          ; COUNTER
        CLR   R0

10$:    MOV   #ARAM+20,R1      ; STATION ADDRESS STARTS AT +20
        MOV   R1,@#CMDREG     ; SELECT STATION ADDRESS RAM
        MOV   R0,@#ADRREG     ; PARK IN RAM
        INC   R1              ; NEXT LOCATION
        SOB   R4,10$         ; DO THEM ALL

*****
***** FILL THE RECEIVE BUFFER WITH BACKGROUND *****
*****

        MOV   #LINADR,R0      ; RECEIVE BUFFER STARTS HERE
        MOV   R0,RBUFP
20$:    CLR   (R0)+           ; FILL RECEIVE BUFFER WITH ZEROS
        CMP   R0,#LINADR+SIZ1K ; FILL THE BUFFER
        BLO  20$            ; FILL ENTIRE BUFFER

*****
***** FILL TRANSMIT BUFFER WITH TEST PATTERN *****
*****

        MOV   #0377,R3        ; WORST CASE FOR CLOCKING
        MOV   R0,IBUFP
30$:    MOV   R3,(R0)+       ; FILL XMIT BUFFER WITH PATTERN
        CMP   R0,#LINADR+SIZ2K ; STOP AT TOP
        BLO  30$
```

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STATION ADDRESS REJECTION TEST

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507
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509
510 ***** SET UP LINK FOR DATAGRAM LOOPBACK OPERATION *****
511 *****
512
513 001552' 012737 100020 177776      MOV      #ENABLE+20,@#CMDREG      ; POINTER TO FRONT OF RAM
514
515 001560' 016701 177060      MOV      TBUF,R1                ; POINT AT XMIT BUFFER
516 001564' 005021                CLR      (R1)+                  ; CLEAR OUT STATUS WORD
517 001566' 012721 000100      MOV      #MINBC,(R1)+          ; SET BYTE COUNT TO MIN ALLOWED
518
519
520 *****
521 ***** GET PHYSICAL ADDRESS INTO DATAGRAM *****
522 *****
523
524 001572' 012700 177777      MOV      #177777,R0            ; && STAND-IN FOR PHYSICAL ADDR
525 001576' 010021                MOV      R0,(R1)+
526 001600' 010021                MOV      R0,(R1)+
527 001602' 010021                MOV      R0,(R1)+
528
529
530 001604' 005037 021034      CLR      @#CLRFIF              ; CLEAR THE FIFO
531 001610' 005067 177006      CLR      FLG5                  ; CLEAR INTERRUPT FLAG
532 001614' 016737 177022 021032  MOV      RBUF,@#LFRBUF         ; TELL UNA WHERE RECEIVE BUF IS
533 001622' 016737 177016 021030  MOV      TBUF,@#LTAC           ; TELL UNA WHERE XMIT BUF IS
534
535 001630' 106427 000140      MTPS     #PRI03                ; ALLOW XMIT AND REC TO INTER
536 001634' 026727 176764 000002 40$:  CMP      SANTIM,#2             ; WAIT FOR 2 SECONDS
537 001642' 002012                BGE      50$                   ; EXIT NORMALLY IF TIMER DONE
538 001644' 032767 000100 176750  BIT      #RCVFLG,FLG5          ; WAIT FOR RECEIVER INTERRUPT
539 001652' 001770                BEQ      40$                   ; IF NONE, WE'RE OK
540
541
542 *****
543 ***** ERROR FALLTHROUGH *****
544 *****
545
546 001654' 032767 000040 176740 45$:  BIT      #TRNFLG,FLG5          ; WAIT FOR XMIT INT TOO!
547 001662' 001774                BEQ      45$
548
549 001664' 000261                SEC
550 001666' 000401                BR       60$                   ; INDICATE ERROR
551
552
553 *****
554 ***** EXIT FOR NO ERROR *****
555 *****
556
557 001670' 000241                CLC
558 001672' 112767 000002 176727 50$:  MOVB     #2,PCSR1+1           ; INDICATE SUCCESS
559 001700' 000207                RTS      PC                    ; TELL HOST WE ARE DONE
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561

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:SBTTL STATION ADDRESS RAM POSITION TEST

THIS TEST WILL CHECK THAT A STATION ADDRESS IS RECOGNIZED
REGARDLESS OF WHICH OF THE 12 RAM POSITIONS THE ADDRESS
RESIDES. THE PHYSICAL ADDRESS WILL BE WRITTEN TO EACH OF THE 12
STATION ADDRESS RAM POSITIONS WITH THE REST OF THE POSITIONS
FILLED WITH KNOWN DATA. A DATAGRAM WITH THE PHYSICAL ADDRESS
WILL BE LOOPED AROUND. THE TEST WILL VERIFY THE DATAGRAM IS
RECEIVED.

***** TELL HIM WE ARE TESTING *****

MICE3: MOVB #INTST,PCSR1 ; TELL HOST WE ARE TESTING
MOV PCSR1,@#IPCSR1
: CLR SANTIM ; CLEAR THE FLAG

***** PICK UP HOST ADDRESS OF PCBB *****

MOV IPCSR2,@#MDMA0 ; PICK UP ADDRESS OF PCBB
MOV IPCSR2+2,@#MDMA1
MOV @#MDMAR0,R0 ; R0=CONTENTS OF PCSR2
MOV @#MDMAR0,R1 ; R1=CONTENTS OF PCSR3
: MOV R0,@#MDMA0 ; POINT TO HOST PCBB
MOV R1,@#MDMA1
: MOV @#MDMAR0,R2 ; R2 NOW HOLDS SA RAM POSITION

***** FILL THE BUFFER WITH PHONEY STATION ADDRESSES *****

MOV PC,R0 ; CALCULATE POS/IND ADDRESS
ADD #BFILE-.,R0
: MOV #36.,R5 ; THERE ARE 12 ADDRESSES
10\$: CLR (R0)+ ; FILL WITH ZERO (ADDRESS)
DEC R5
BNE 10\$

***** PUT PHYSICAL ADDRESS IN RAM POSITION *****

MOV PC,R0 ; CALCULATE POS/IND ADDRESS
ADD #BFILE-.,R0

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STATION ADDRESS RAM POSITION TEST

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618
619 002010' 005302          20$: DEC      R2          ; PARAMTER IS NEVER > 1
620 002012' 001403          BEQ      30$          ; GO TO WORK
621 002014' 062700 000006  ADD      #6,R0
622 002020' 000773          BR       20$          ; GO ROUND
623
624 002022' 012701 177777  30$: MOV      #177777,R1    ; && STAND-IN FOR PHSICAL ADDR
625 002026' 010120          MOV      R1,(R0)+
626 002030' 010120          MOV      R1,(R0)+
627 002032' 010120          MOV      R1,(R0)+
628
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632
633
634 002034' 012737 000200 177776  MOV      #MODE,@#CMDREG    ; SET LOOP TO LOAD SA RAM
635 002042' 012737 000004 177774  MOV      #LOOP,@#MODREG
636
637 002050' 010701          MOV      PC,R1          ; FORM A POS/IND ADDRESS
638 002052' 062701 000256  ADD      #BFIL-.,R1      ; ADD THE OFFSET
639
640 002056' 005000          CLR      R0            ; THIS CLEARS HIGH BITS
641
642 002060' 012746 000120  MOV      #ARAM+20,-(SP)   ; FOR MODE REG/NEED A REGISTER
643
644 002064' 012702 000003  MOV      #3,R2          ; 3 WORDS PER ADDRESS/PATTERN
645 002070' 012704 000020  40$: MOV      #16.,R4    ; SIXTEEN BITS PER WORD
646 002074' 012705 000014  50$: MOV      #12.,R5    ; 12 POSITIONS IN SA RAM
647
648 002100' 010103          MOV      R1,R3          ; COPY THE POINTER
649
650 002102' 006013          60$: ROR      (R3)        ; GET LSB OF ALL 12 ADDRESSES
651 002104' 006100          ROL      R0            ; R0 WILL HOLD ORTHOGONAL WORD
652 002106' 062703 000006  ADD      #6,R3          ; 6 BYTES PER ADDRESS/PATTERN
653 002112' 077505          SOB     R5,60$         ; GO TILL DONE
654
655 002114' 011637 177776  MOV      (SP),@#CMDREG   ; SET MODE TO WRITE SA RAM
656 002120' 010037 177774  MOV      R0,@#ADRREG    ; ORTHOGONAL WORD TO SA RAM
657 002124' 005216          INC     (SP)           ; BUMP STATION ADDRESS
658 002126' 077416          SOB     R4,50$         ; DO ANOTHER ONE
659
660 002130' 062701 000002  ADD      #2,R1          ; ADVANCE TO NEXT WORD
661 002134' 077223          SOB     R2,40$         ; LOOP TILL DONE
662
663 002136' 012600          MOV     (SP)+,R0        ; POP TO BYTE BUCKET
664
665
666
667
668
669
670 002140' 012700 100000  MOV      #LINADR,R0     ; RECEIVE BUFFER STARTS HERE
671 002144' 010067 176472  MOV      R0,RBUF
672 002150' 005020 70$: CLR      (R0)+        ; FILL RECEIVE BUFFER WITH ZEROS
673 002152' 020027 104000  CMP     R0,#LINADR+SIZ1K ; FILL THE BUFFER

```

MICROE - MICROCODE MODULE E
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STATION ADDRESS RAM POSITION TEST

```

674 002156' 103774          BLO      70$          ; FILL ENTIRE BUFFER
675
676
677
678
679
680
681 002160' 012703 000377    MOV      #0377,R3          ; WORST CASE FOR CLOCKING
682 002164' 010067 176454    MOV      R0,TBUF          ;
683 002170' 010320          80$:    MOV      R3,(R0)+        ; FILL XMIT BUFFER WITH PATTERN
684 002172' 020027 110000    CMP      R0,#LINADR+SIZ2K ; STOP AT TOP
685 002176' 103774          BLO      80$
686
687
688
689
690
691
692 002200' 012737 100020 177776  MCV      #ENABLE+20,@#CMDREG ; ENABLE LINK
693
694 002206' 016701 176432    MOV      TBUF,R1          ; POINT AT XMIT BUFFER
695 002212' 005021          CLR      (R1)+            ; CLEAR OUT STATUS WORD
696 002214' 012721 000100    MOV      #MINBC,(R1)+     ; SET BYTE COUNT TO MIN ALLOWED
697
698 002220' 012700 177777    MOV      #177777,R0       ; && STAND IN PHYSICAL ADDRESS
699 002224' 010021          MOV      R0,(R1)+
700 002226' 010021          MOV      R0,(R1)+
701 002230' 010021          MOV      R0,(R1)+
702
703 002232' 005037 021034    CLR      @#CLRIF          ; CLEAR THE FIFO
704 002236' 005067 176360    CLR      FLG5             ; CLEAR INTERRUPT FLAG
705 002242' 016737 176374 021032  MOV      RBUF,@#LFRBUF    ; TELL UNA WHERE RECEIVE BUF IS
706 002250' 016737 176370 021030  MOV      TBUF,@#LTAC      ; TELL UNA WHERE XMIT BUF IS
707
708 002256' 106427 000140          MTPS     #PRI03           ; ALLOW XMIT AND REC TO INTER
709 002262' 026727 176336 000002 90$:    CMP      SNTIM,#2         ; WAIT FOR COUNTS TO ACCUMULATE
710 002270' 002015          BGE      120$
711 002272' 032767 000100 176322  BIT      #RCVFLG,FLG5     ; THIS SETS IF DATAGRAM LOOPS
712 002300' 001770          BEQ      90$
713
714 002302' 032767 000040 176312 100$:  BIT      #TRNFLG,FLG5     ; WAIT FOR XMIT TOO
715 002310' 001774          BEQ      100$
716
717 002312' 000241          CLC
718 002314' 112767 000003 176305 110$:  MOV      #3,PCSR1+1      ; TELL HIM WHAT TEST WE'RE IN
719 002322' 000207          RTS      PC
720
721
722 002324' 000261          SEC
723 002326' 000772          BR      110$            ; INDICATE ERROR
724
725
726 002330' 000000 000000 000000 000000  BFILE:: .WORD 0,0,0
727 002336' 000000 000000 000000 000000  .WORD 0,0,0
728 002344' 000000 000000 000000 000000  .WORD 0,0,0
729 002352' 000000 000000 000000 000000  .WORD 0,0,0

```

MICROE - MICROCODE MODULE E
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STATION ADDRESS RAM POSITION TEST

730	002360'	000000	000000	000000	.WORD	0,0,0
731	002366'	000000	000000	000000	.WORD	0,0,0
732	002374'	000000	000000	000000	.WORD	0,0,0
733	002402'	000000	000000	000000	.WORD	0,0,0
734	002410'	000000	000000	000000	.WORD	0,0,0
735	002416'	000000	000000	000000	.WORD	0,0,0
736	002424'	000000	000000	000000	.WORD	0,0,0
737	002432'	000000	000000	000000	.WORD	0,0,0

MICROE - MICROCODE MODULE E
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STATION ADDRESS RAM POSITION TEST

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002440' 112767 000002 176160
002446' 016737 176154 021020
002454' 005067 176144
002460' 012737 000200 177776
002466' 012737 000004 177774
002474' 012704 000060
002500' 005000
002502' 012701 000120
002506' 010137 177776
002512' 010037 177774
002516' 005201
002520' 077406
002522' 012700 100000
002526' 010067 176110
002532' 005020
002534' 020027 104000
002540' 103774
002542' 012703 000377
002546' 010067 176072
002552' 010320
002554' 020027 110000
002560' 103774

```

:SBTTL MULTICAST ADDRESS TEST
:
:*****
:***** TELL HIM WE ARE TESTING *****
:*****
MICE4:  MOVB  #INTST,PCSR1          ; TELL HOST WE ARE TESTING
        MOV   PCSR1,@#IPCSR1
:
        CLR   SANTIM              ; CLEAR FLAG FOR TIMER
:
:*****
:***** FILL STATION ADDRESS RAM W' TH KNOWN (PHONEY) ADDRESS *****
:*****
        MOV   #MODE,@#CMDREG      ; SET LOOP TO LOAD SA RAM
        MOV   #LOOP,@#MODREG
:
        MOV   #48.,R4             ; COUNTER
        CLR   R0
:
10$:    MOV   #ARAM+20,R1          ; STATION ADDRESS STARTS AT +20
        MOV   R1,@#CMDREG         ; SELECT STATION ADDRESS RAM
        MOV   R0,@#ADRREG         ; PARK IN RAM
        INC   R1                  ; NEXT LOCATION
        SOB   R4,10$             ; DO THEM ALL
:
:*****
:***** FILL THE RECEIVE BUFFER WITH BACKGROUND *****
:*****
        MOV   #LINADR,R0          ; RECEIVE BUFFER STARTS HERE
        MOV   R0,RBUF
20$:    CLR   (R0)+                ; FILL RECEIVE BUFFER WITH ZEROS
        CMP   R0,#LINADR+SIZ1K   ; FILL THE BUFFER
        BLO  20$                 ; FILL ENTIRE BUFFER
:
:*****
:***** FILL TRANSMIT BUFFER WITH TEST PATTERN *****
:*****
        MOV   #0377,R3            ; WORST CASE FOR CLOCKING
        MOV   R0,TBUF
30$:    MOV   R3,(R0)+            ; FILL XMIT BUFFER WITH PATTERN
        CMP   R0,#LINADR+SIZ2K   ; STOP AT TOP
        BLO  30$
:
:*****
:***** SET UP LINK FOR DATAGRAM LOOPBACK OPERATION *****
:*****

```

MICROE - MICROCODE MODULE E
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MULTICAST ADDRESS TEST

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794 002562' 012737 000200 177776      MOV      #MODE,@#CMDREG      ; ENABLE LINK, SELECT MODE
795 002570' 012737 040004 177774      MOV      #ENAL!LOOP,@#MODREG ; ENABLE LOOPBACK
796 002576' 012737 100020 177776      MOV      #ENABLE+20,@#CMDREG ; POINTER TO FRONT OF RAM
797                                     :
798 002604' 016701 176034      MOV      TBUF,R1             ; POINT AT XMIT BUFFER
799 002610' 005021                CLR      (R1)+               ; CLEAR OUT STATUS WORD
800 002612' 012721 000100      MOV      #MINBC,(R1)+       ; SET BYTE COUNT TO MIN ALLOWED
801                                     :
802                                     :
803                                     :
804                                     : *****
805                                     : ***** GET PHYSICAL ADDRESS INTO DATAGRAM *****
806                                     : *****
807 002616' 012700 177777      MOV      #177777,R0         ; && STAND-IN FOR PHYSICAL ADDR
808 002622' 010011                MOV      R0,(R1)
809 002624' 052721 000001      BIS      #01,(R1)+         ; SET MULTICAST ADDRESS BIT
810 002630' 010021                MOV      R0,(R1)+
811 002632' 010021                MOV      R0,(R1)+
812                                     :
813                                     :
814 002634' 005037 021034      CLR      @#CLRFIF           ; CLEAR THE FIFO
815 002640' 005067 175756      CLR      FLG5              ; CLEAR INTERRUPT FLAG
816 002644' 016737 175772 021032  MOV      RBUF,@#LFRBUF      ; TELL UNA WHERE RECEIVE BUF IS
817 002652' 016737 175766 021030  MOV      TBUF,@#LTAC        ; TELL UNA WHERE XMIT BUF IS
818                                     :
819 002660' 106427 000140      MTPS     #PRI03            ; ALLOW XMIT AND REC TO INTER
820 002664' 026727 175734 000002 40$:  CMP      SANTIM,#2         ; && LEAVE IT 2 SECONDS
821 002672' 002015                BGE      60$               ; EXIT IF TIMER DONE
822 002674' 032767 000100 175720  BIT      #RCVFLG,FLG5      ; WAIT FOR RECEIVER INTERRUPT
823 002702' 001770                BEQ      40$
824                                     :
825 002704' 032767 000040 175710 45$:  BIT      #TRNFLG,FLG5      ; WAIT FOR XMIT INT TOO!
826 002712' 001774                BEQ      45$
827                                     :
828 002714' 000241                CLC
829 002716' 112767 000004 175703 50$:  MOV      #4,PCSR1+1        ; INDICATE SUCCESS
830 002724' 000207                RTS      PC                 ; TELL HOST WE ARE DONE
831                                     :
832                                     :
833 002726' 000261                SEC
834 002730' 000772                BR       50$                ; INDICATE ERROR
835                                     :
836                                     :

```

MICROE - MICROCODE MODULE E
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MULTICAST ADDRESS TEST

837
838 002732' 002734
839
840 000001

·
·MICESZ::MICESZ-MICROE+2
·
·END

MICROE - MICROCODE MODULE E MACY11 30(1046) 11-JAN-83 09:53 PAGE 23
 MICROE.MAC 10-JAN-83 17:53 CROSS REFERENCE TABLE -- USER SYMBOLS

RCVFLG=	000100		109#	257	428	538	711	822										
RCVINT	000566R	002	175	255#														
RCVVEC=	000120		94#	176*	177*													
ROMADR=	040000		125#	126														
ROMSIZ=	040000		121#	126														
RXI =	020000		70#															
SANTIM	000624R	002	234*	269#	467*	536	580*	709	748*	820								
SANVEC=	000134		95#	166*	167*													
SERI =	100000		68#															
SIZ1K =	004000		115#	116	382	494	673	775										
SIZ2K =	010000		116#	117	393	505	684	786										
SIZ4K =	020000		117#	118	119	120												
SIZ8K =	040000		118#	121	122													
STACK =	001000		99#	137														
TBLD	000612R	002	208	263#														
TBUFP	000644R	002	275#	391*	403	414	420	503*	515	533	682*	694	706	784*	798			
			817															
TIMINT	000464R	002	165	234#														
TRNFLG=	000040		108#	260	431	546	714	825										
TRNINT	000602R	002	170	260#														
TRNVEC=	000070		93#	171*	172*													
TXI =	010000		71#															
UNIERR=	020000		112#	229														
WCSADR=	000000		123#	124														
WCSSIZ=	020000		119#	124														
.	= 002734R	002	142	150	155	160	165	170	175	196	208	231	247	253	263			
			264	265	266	329	347	603	616	638								
. ABS.	000000	000																
	000000	001																
MICRE	002734	002																

ERRORS DETECTED: 0
 DEFAULT GLOBALS GENERATED: 0

MICROE.OBJ,MICROE.LST/CRF/DOC/NL:TOC/SOL=MICROE.MAC
 RUN-TIME: .9 1 .3 SECONDS
 RUN-TIME RATIO: 86/3=28.7
 CORE USED: 9K (18 PAGES)

DOCUMENT PAGES: 22

MICROF - MICROCODE MODULE F
MICROF.MAC 10-JAN-83 17:53

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```

1      .TITLE MICROF - MICROCODE MODULE F
2      : && DEDICATED THE CRC CIRCUITRY TO THE RECEIVE SIDE OF THE LINK
3
4      000000'
5      .CSECT MICRF
6
7      .SBTTL REGISTER DEFINITIONS USED BY THE T11
8
9      021000 IPCSR0 = 21000 ;INTERNAL PCSRO ADDRESS
10     021002 DMACSR = 21002 ;DMA ENGINE CONTROL STATUS REGISTER
11     021004 DMATO  = 21004 ;DMA ENGINE TO ADDRESS REGISTER #0
12     021006 DMAT1  = 21006 ;DMA ENGINE TO ADDRESS REGISTER #1
13     021010 MDMAO  = 21010 ;MICROCPU DMA TO ADDRESS REGISTER #0
14     021012 MDMA1  = 21012 ;MICROCPU DMA TO ADDRESS REGISTER #1
15     021014 MDMARO = 21014 ;MICROCPU DMA DATA REGISTER - AUTO INCREMENT R/O
16     021016 MDMAR1 = 21016 ;MICROCPU DMA DATA REGISTER - AUTO DECREMENT R/O
17     021020 IPCSR1 = 21020 ;INTERNAL PCSR1 ADDRESS
18     021022 DMAF   = 21022 ;DMA ENGINE FROM ADDRESS REGISTER
19     021024 DMAWC  = 21024 ;DMA ENGINE WORD COUNT REGISTER
20     021026 MDMAW0 = 21026 ;MICROCPU DMA DATA REGISTER - AUTO INCREMENT W/O
21     021030 LTAC   = 21030 ;LINK TRANSMIT ADDRESS COUNTER REGISTER
22     021032 LFRBUF = 21032 ;LINK RECIEVE BUFFER ADDRESS FIFO
23     021034 CLRIF  = 21034 ;CLEAR FIFO
24     021036 MDMAW1 = 21036 ;MICROCPU DMA DATA REGISTER - AUTO DECREMENT W/O
25     021040 PCSRSW = 21040 ;SWITCH PACK REGISTER
26     021042 MDMSR  = 21042 ;MICROCPU DMA STATUS REGISTER
27     021044 LRBUF  = 21044 ;LINK RECIEVE BUFFER COMPLETED
28     021060 PHYAD0 = 21060 ;PHYSICAL ADDRESS ROM BYTE 0
29     021062 PHYAD1 = 21062 ;PHYSICAL ADDRESS ROM BYTE 1
30     021064 PHYAD2 = 21064 ;PHYSICAL ADDRESS ROM BYTE 2
31     021066 PHYAD3 = 21066 ;PHYSICAL ADDRESS ROM BYTE 3
32     021070 PHYAD4 = 21070 ;PHYSICAL ADDRESS ROM BYTE 4
33     021072 PHYAD5 = 21072 ;PHYSICAL ADDRESS ROM BYTE 5
34     177774 MODREG = 177774 ;LINK MODE REGISTER
35     177774 ADDRREG = 177774 ;LINK STATION ADDRESS RAM REGISTER
36     177776 CMDREG = 177776 ;LINK COMMAND REGISTER
37
38     .SBTTL OTHER DEFINITIONS USED BY THE MICROCODE
39
40     100000 BIT15 = 100000
41     040000 BIT14 = 40000
42     020000 BIT13 = 20000
43     010000 BIT12 = 10000
44     004000 BIT11 = 4000
45     002000 BIT10 = 2000
46     001000 BIT9  = 1000
47     000400 BIT8  = 400
48     000200 BIT7  = 200
49     000100 BIT6  = 100
50     000040 BIT5  = 40
51     000020 BIT4  = 20
52     000010 BIT3  = 10
53     000004 BIT2  = 4
54     000002 BIT1  = 2
55     000001 BIT0  = 1
56     012400 LASFTP = BIT8!BIT10!BIT12 ;LOAD AND START FUNCTION TEST PATTERN

```

MICROF - MICROCODE MODULE F
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 OTHER DEFINITIONS USED BY THE MICROCODE

57	000340	PRI07 =	340	
58	000300	PRI06 =	300	
59	000240	PRI05 =	240	
60	000200	PRI04 =	200	
61	000140	PRI03 =	140	
62	000100	PRI02 =	100	
63	000040	PRI01 =	40	
64	000000	PRI00 =	0	
65		:		
66		:	PCSR0 - PORT CONTROL STATUS REGISTER 0	
67		:		
68	100000	SERI =	BIT15	
69	040000	PCEI =	BIT14	
70	020000	RXI =	BIT13	
71	010000	TXI =	BIT12	
72	004000	DNI =	BIT11	
73	002000	RCEI =	BIT10	
74	000400	FATI =	BIT8	
75		:		
76		:	LINK COMMAND REGISTER	
77		:		
78	100000	ENABLE =	BIT15	:ENABLE LINK MODULE
79	000200	MODE =	BIT7	:ENABLE MODE REGISTER
80	000100	ARAM =	BIT6	:ENABLE STATION ADDRESS RAM
81		:		
82		:	LINK MODE REGISTER	
83		:		
84	100000	PROM =	BIT15	:PROMISCUIOUS MODE
85	040000	ENAL =	BIT14	:ENABLE MULTICAST
86	004000	ENCR =	BIT11	:ENABLE COLLISION TEST
87	002000	ACLO =	BIT10	:ENABLE ACLO
88	000040	DRY =	BIT5	:DISABLE RETRY LOGIC
89	000020	COLL =	BIT4	:SIMULATE A COLLISION
90	000010	DTCR =	BIT3	:DISABLE TRANSMIT CRC LOGIC
91	000004	LOOP =	BIT2	:ENABLE LOOPBACK
92	000001	HDX =	BIT0	:HALF DUPLEX BIT
93		:		
94	000070	TRNVEC=	70	:VECTOR ADDRESS FOR THE TRANSMITTER
95	000120	RCVVEC=	120	:VECTOR ADDRESS FOR THE RECEIVER
96	000134	SANVEC=	134	:VECTOR ADDRESS FOR THE SANITY TIMER
97	000064	CSRVEC=	64	:VECTOR ADDRESS FOR CSR WRITE INTERRUPT
98	000114	DMAVEC=	114	:VECTOR ADDRESS FOR DMA DONE INTERRUPT
99	000140	PARVEC=	140	:VECTOR ADDRESS FOR LINK MEMORY PARITY ERROR
100	001000	STACK=	1000	:STACK LOCATION
101	000001	INMON=	1	:IN MICROMONITOR STATE
102	000002	INTST=	2	:IN A TEST STATE
103	000003	INERR=	3	:IN ERROR STATE
104	000001	CSRFLG=	BIT0	:CSR WRITE INTERRUPT OCCURED
105	000002	ERRFLG=	BIT1	:UNEXPECTED ERROR OCCURED
106	000004	PARFLG=	BIT2	:PARITY ERROR OCCURED
107	000010	NXMFLG=	BIT3	:NON-EXISTANT MEMORY ERROR OCCURRED
108	000020	NPRFLG=	BIT4	:NPR TIMEOUT OCCURRED
109	000040	TRNFLG=	BIT5	:TRANSMITTER INTERRUPT OCCURRED
110	000100	RCVFLG=	BIT6	:RECEIVER INTERRUPT OCCURRED
111	100000	NPRERR=	BIT15	:PCSR0 FLAG INDICATING NPR ERROR OCCURRED
112	040000	NXMERR=	BIT14	:PCSR0 FLAG INDICATING NON-EXISTANT MEMORY ERROR OCCURRED

MICROF - MICROCODE MODULE F
MICROF.MAC 10-JAN-83 17:53

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OTHER DEFINITIONS USED BY THE MICROCODE

113	020000	UNIERR= BIT13	:PCSRO FLAG INDICATING UNEXPECTED INTERRUPT OCCURRED
114	010000	PARERR= BIT12	:PCSRO FLAG INDICATING LINK MEMORY PARITY ERROR OCCURRED
115	004000	SIZ1K= 4000	:1K WORDS
116	010000	SIZ2K= SIZ1K*2	:2K WORDS
117	020000	SIZ4K= SIZ2K*2	:4K WORDS
118	040000	SIZ8K= SIZ4K*2	:8K WORDS
119	020000	WCSSIZ= SIZ4K	:SIZE OF WRITEABLE CONTROL STORE
120	020000	IOSIZ= SIZ4K	:SIZE OF I/O PAGE
121	040000	ROMSIZ= SIZ8K	:SIZE OF ROM
122	077774	LINSIZ= SIZ8K*2-4	:SIZE OF LINK MEMORY
123	000000	WCSADR= 0	:BASE ADDRESS OF WCS
124	020000	IOADR= WCSADR+WCSSIZ	:BASE ADDRESS OF I/O PAGE
125	040000	ROMADR= IOADR+IOSIZ	:BASE ADDRESS OF ROM
126	100000	LINADR= ROMADR+ROMSIZ	:BASE ADDRESS OF LINK MEMORY
127	000100	MINBC= 64	: 64 BYTES
128	002752	MAXBC= 1518.-4.	:MAXIMUM # OF BYTES IN A TRANSMIT BUFFER
129	000000	DATERR= 0	:FLAG INDICATING DATA ERROR OCCURRED
130	000001	ADRERR= 1	:FLAG INDICATING ADDRESS ERROR OCCURRED
131		.	
132	177777	INITH = -1	: MINUS ONE (INITIAL CRC VALUE)
133	177777	INITL = -1	
134	166670	POLYH = 166670	: FUNCTION POLYNOMIAL HIGH WORD
135	101440	POLYL = 101440	: FUNCTION POLYNOMIAL LOW WORD
136	157273	EXCRCH = 157273	: EXPECTED CRC HIGH
137	020343	EXCRCL = 020343	: EXPECTED CRC LOW

MICROF - MICROCODE MODULE F
 MICROF.MAC 10-JAN-83 17:53

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 OTHER DEFINITIONS USED BY THE MICROCODE

```

138
139
140          .SBTTL MICROCODE MODULE F
141 000000' 106427 000340          MICROF::MTPS      #PRI07          ;DISABLE INTERRUPTS
142 000004' 012737 000000 177776      MOV      #0,@#CMDREG      ;TURN OFF THE LINK
143 000012' 012706 001000          MOV      #STACK,SP      ;SETUP STACK
144 000016' 112767 000001 000604      MOV      #INMON,PCSR1    ;TELL HOST WE ARE IN MICROMONITOR
145 000024' 016737 000600 021020      MOV      PCSR1,@#IPCSR1
146 000032' 012737 004000 021000      MOV      #DNI,@#IPCSRO
147 000040' 010700          MOV      PC,R0          ;TELL HOST THE LOAD AND START FINISHED
148 000042' 062700 000404          ADD      #ERRINT-.,R0    ;GET ADDRESS OF UNEXPECTED ERROR...
149 000046' 005001          CLR      R1             ;HANDLER
150 000050' 010021          10$: MOV      R0,(R1)+       ;FILL ALL UNUSED VECTORS WITH TRAP...
151 000052' 012721 000340          MOV      #PRI07,(R1)+   ;HANDLER
152 000056' 020127 001000          CMP      R1,#1000
153 000062' 002772          BLT     10$
154
155 000064' 010700          MOV      PC,R0          ;SETUP PARITY TRAP VECTOR
156 000066' 062700 000462          ADD      #PARINT-.,R0
157 000072' 010037 000140          MOV      R0,@#PARVEC
158 000076' 012737 000340 000142      MOV      #PRI07,@#PARVEC+2
159
160 000104' 010700          MOV      PC,R0          ;SETUP DMA INTERRUPT VECTOR
161 000106' 062700 000364          ADD      #DMAINT-.,R0
162 000112' 010037 000114          MOV      R0,@#DMAVEC
163 000116' 012737 000340 000116      MOV      #PRI07,@#DMAVEC+2
164
165 000124' 010700          MOV      PC,R0          ;SETUP CSR WRITE VECTOR
166 000126' 062700 000310          ADD      #CSRWRT-.,R0
167 000132' 010037 000064          MOV      R0,@#CSRVEC
168 000136' 012737 000200 000066      MOV      #PRI04,@#CSRVEC+2
169
170 000144' 010700          MOV      PC,R0          ;SETUP SANTITY TIMER VECTOR
171 000146' 062700 000316          ADD      #TIMINT-.,R0
172 000152' 010037 000134          MOV      R0,@#SANVEC
173 000156' 012737 000240 000136      MOV      #PRI05,@#SANVEC+2
174
175 000164' 010700          MOV      PC,R0          ;SETUP TRANSMITTER VECTOR
176 000166' 062700 000414          ADD      #TRNINT-.,R0
177 000172' 010037 000070          MOV      R0,@#TRNVEC
178 000176' 012737 000200 000072      MOV      #PRI04,@#TRNVEC+2
179
180 000204' 010700          MOV      PC,R0          ;SETUP RECEIVER VECTOR
181 000206' 062700 000360          ADD      #RCVINT-.,R0
182 000212' 010037 000120          MOV      R0,@#RCVVEC
183 000216' 012737 000240 000122      MOV      #PRI05,@#RCVVEC+2
184
185 000224' 013700 021040          MOV      @#PCSRW,R0     ;GET SWITCH PACK BITS
186 000230' 052700 176000          BIS      #176000,R0     ;MAP THEM INTO HOST I/O PAGE
187 000234' 006300          ASL     R0             ;SHIFT OVER TO POSITION CORRECTLY
188 000236' 006300          ASL     R0
189 000240' 006300          ASL     R0
190 000242' 062700 000004          ADD      #4,R0          ;PCSR2 IS PCSR0+4
191 000246' 010067 000360          MOV      R0,IPCSR2     ;SAVE PCSR2 ADDRESS
192 000252' 012767 000003 000354      MOV      #3,IPCSR2+2   ;HIGH ORDER BITS 17:16
193 000260' 005067 000340          CLR     FLG6           ;INITIALIZE FLAG WORD
    
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194	000264'	106427	000000		15\$:	MTPS	#PRI00		:ALLOW INTERRUPTS
195									
196	000270'	005767	000330		20\$:	TST	FLG6		:WAIT FOR A COMMAND FROM HOST
197	000274'	001775				BEQ	20\$		
198									
199	000276'	106427	000340			MTPS	#PRI07		:RAISE CPU PRIORITY TO SERVICE COMMAND
200	000302'	032767	000001	000314		BIT	#CSRFLG,FLG6		:DID HOST GIVE US A COMMAND?
201	000310'	001001				BNE	30\$:YES
202	000312'	000777				BR	.		:NO, ERROR SO JUST SIT HERE...
203									:FOR LACK OF ANYTHING BETTER TO DO
204									
205	000314'	113700	021000		30\$:	MOVB	@#IPCSRO,R0		:GET WHAT HOST WROTE TO PCSRO
206	000320'	042700	177760			BIC	#177760,R0		:STRIP ALL BUT COMMAND BITS
207	000324'	001004				BNE	35\$:WAS IT THE CLEAR FUNCTION?
208	000326'	012737	000001	021020		MOV	#INMON,@#IPCSR1		:YES, CLEAR OUT THE TEST # BITS
209	000334'	000432				BR	50\$		
210	000336'	022700	000017		35\$:	CMP	#17,R0		:RESTART OPERATIONAL MICROCODE?
211	000342'	001432				BEQ	60\$:YES
212	000344'	162700	000001			SUB	#1,R0		
213	000350'	010701				MOV	PC,R1		:GET ADDRESS OF OUR COMMAND TABLE
214	000352'	062701	000240			ADD	#TBLD-.,R1		
215	000356'	006300				ASL	R0		:MAKE COMMAND A BYTE OFFSET
216	000360'	060001				ADD	R0,R1		:USE IT TO INDEX INTO COMMAND TABLE
217	000362'	061101				ADD	(R1),R1		:R1 NOW HAS COMMAND ROUTINE ADDRESS
218	000364'	004711				JSR	PC,(R1)		:EXECUTE AS COMMANDED FROM HOST
219	000366'	103404				BCS	40\$:ERROR OCCURRED
220	000370'	112767	000001	000232		MOVB	#INMON,PCSR1		:INDICATE TO HOST WE ARE BACK IN...
221	000376'	000403				BR	45\$:MICROMONITR
222	000400'	112767	000003	000222	40\$:	MOVB	#INERR,PCSR1		:INDICATE TO HOST ERROR OCCURRED
223	000406'	016737	000216	021020	45\$:	MOV	PCSR1,@#IPCSR1		
224	000414'	012737	004000	021000		MOV	#DNI,@#IPCSRO		:TELL HOST THIS MICROTEST FINISHED
225	000422'	005067	000176		50\$:	CLR	FLG6		:RESET FLAG WORD
226	000426'	000716				BR	15\$:GO WAIT FOR ANOTHER COMMAND
227									
228	000430'	005000			60\$:	CLR	R0		:FAKE SUCCESSFUL SELF TEST RESULTS
229	000432'	000137	040006			JMP	@#40006		:START OPERATIONAL MICROCODE
230									
231	000436'	052767	000001	000160	CSRWRT:	BIS	#CSRFLG,FLG6		:INDICATE A CSR WRITE INTERRUPT OCCURED
232	000444'	000002				RTI			
233									
234	000446'	052767	000002	000150	ERRINT:	BIS	#ERRFLG,FLG6		:INDICATE A UNEXPECTED INTERRUPT OCCURED
235	000454'	012737	020000	021000		MOV	#UNIERR,@#IPCSRO		:TELL HOST AN UNEXPECTED INTERRUPT
236									:HAPPENED
237									:JUST SIT HERE AND SPIN WHEELS
238	000462'	000777				BR	.		:COUNT ON HOST TO TIMEOUT
239									
240	000464'	005267	000136		TIMINT:	INC	SANTIM		:COUNT TICKS AS THEY OCCUR
241	000470'	000002				RTI			
242									
243	000472'	013767	021002	000142	DMAINT:	MOV	@#DMACSR,DMDONE		:GET DMA STATUS
244	000500'	032767	040000	000134		BIT	#BIT14,DMDONE		:DID A NON-EXISTANT MEMORY INTERRUPT OCCUR?
245	000506'	001404				BEQ	10\$:NO
246	000510'	012737	040000	021000		MOV	#NXMERR,@#IPCSRO		:YES, TELL HOST A NON-EXISTANT MEMORY
247									:LOCATION WAS ADDRESSED
248	000516'	000407				BR	20\$		
249	000520'	032767	100000	000114	10\$:	BIT	#BIT15,DMDONE		:DID A NPR TIMEOUT OCCUR?

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250 000526' 001407          BEQ      30$          ;NO
251 000530' 012737 100000 021000  MOV     #NPRERR,@#IPCSRO ;TELL HOST NPR TIMEOUT HAPPENED
252 000536' 012737 100000 021002 20$:  MOV     #BIT15,@#DMACSR ;CLEAR THE INTERRUPT IN THE DMA ENGINE
253 000544' 000777          BR      .           ;SIT HERE AND SPIN WHEELS
254 000546' 000002          30$:  RTI
255
256 000550' 052767 000004 000046  PARINT: BIS     #PARFLG,FLG6 ;SET PARITY ERROR OCCURRED
257 000556' 012737 010000 021000  MOV     #PARERR,@#IPCSRO ;TELL HOST A LINK MEMORY PARITY ERROR
258                                ;OCCURRED
259 000564' 000777          BR      .           ;JUST SIT HERE AND SPIN WHEELS
260                                ;COUNT ON HOST TO TIMEOUT
261
262 000566' 005737 021044          RCVINT: TST    @#LRBUF ;READ BUFFER DONE REGISTER...
263                                ;WHICH CLEARS THE INTERRUPT
264 000572' 052767 000100 000024          BIS     #RCVFLG,FLG6 ;SET RECEIVER INTERRUPT OCCURRED
265 000600' 000002          RTI
266
267 000602' 052767 000040 000014  TRNINT: BIS     #TRNFLG,FLG6 ;SET TRANSMITTER INTERRUPT OCCURRED
268 000610' 000002          RTI
269
270 000612' 000170          TBLD:  .WORD  MICF1-. ; CRC DATA PATTERN
271 000614' 000460          .WORD  MICF2-. ; CRC ERROR TEST
272 000616' 001032          .WORD  MICF3-. ; CRC PATTERN LENGTH
273 000620' 001544          .WORD  MICF4-. ; BUFFER RECOVERY TEST
274 000622' 002222          .WORD  MICF5-. ; HALF-DUPLEX TEST
275
276 000624' 000000          FLG6:  .WORD  0 ; FLAG WORD
277 000626' 000000          SANTIM: .WORD  0 ; COUNT FOR SANITY TIMER
278 000630' 000000          PCSR1: .WORD  0 ; COPY OF WHAT GOES TO PCSR1
279 000632' 000000 000000          IPCSR2: .WORD  0,0 ; ADDRESS IN HOST MEMORY FOR PCSR2
280 000636' 000000 000000          PCBADR: .WORD  0,0 ; ADDRESS IN HOST MEMORY FOR PCB
281 000642' 000000          DMDONE: .WORD  0
282 000644' 000000          RBUFP:  .WORD  0 ; POINTER TO RECIEVE BUFFER
283 000646' 000000          TBUFP:  .WORD  0 ; POINTER TO XMIT BUFFER
284 000650' 000000          CRC1:  .WORD  0 ; HOLDS LAST DATA BYTE AND 1ST CRC BYTE
285 000652' 000000          CRC23: .WORD  0 ; HOLDS 2ND AND 3RD CRC BYTES
286 000654' 000000          CRC4:  .WORD  0 ; HOLDS 4TH CRC BYTE
287

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000656' 106746
000660' 010046
000662' 010146
000664' 010246
000666' 012704 177777
000672' 012705 177777
000676' 112001
000700' 004767 000014
000704' 077204
000706' 012602
000710' 012601
000712' 012600
000714' 106426
000716' 000207

000720' 106746
000722' 010146
000724' 010246
000726' 010346
000730' 042701 177400
000734' 074105
000736' 012702 166670
000742' 012703 101440
000746' 012701 000010
000752' 000241
000754' 006004
000756' 006005
000760' 103002
000762' 074204
000764' 074305
000766' 077107
000770' 012603
000772' 012602
000774' 012601
000776' 106426
001000' 000207

***** SUBROUTINES *****

BLKCRC: MFPS -(SP)
MOV R0,-(SP)
MOV R1,-(SP)
MOV R2,-(SP)
MOV #INITH,R4
MOV #INITL,R5
10\$: MOVB (R0)+,R1
JSR PC,GETCRC
20\$: SOB R2,10\$
MOV (SP)+,R2
MOV (SP)+,R1
MOV (SP)+,R0
MTPS (SP)+
RTS PC

GETCRC: MFPS -(SP)
MOV R1,-(SP)
MOV R2,-(SP)
MOV R3,-(SP)
BIC #C377,R1
XOR R1,R5
MOV #POLYH,R2
MOV #POLYL,R3
MOV #8.,R1
10\$: CLC
ROR R4
ROR R5
BCC 20\$
XOR R2,R4
XOR R3,R5
20\$: SOB R1,10\$
MOV (SP)+,R3
MOV (SP)+,R2
MOV (SP)+,R1
MTPS (SP)+
RTS PC

: SAVE PSW
: SAVE R0

: INITIAL CRC HIGH WORD
: INITIAL CRC LOW WORD
: GET NEXT BYTE
: CALCULATE CRC
: LOOP TILL DONE

: SAVE REGISTERS

: CLEAR HIGH BYTE
: MERGE NEW BYTE WITH OLD CRC
: GET POLYNOMIAL HIGH WORD
: GET CRC POLYNOMIAL LOW WORD
: LOOP COUNT
: CLEAR THE CARRY
: SHIFT RIGHT THE CRC
: 32 BITS WORTH
: SKIP IF BIT 0 NOT SET
: EXCLUSIVE OR IN THE POLY
: BOTH HIGH AND LOW WORDS
: END LOOP ON ALL 8 BITS
: RESTORE REGISTERS

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:SBTTL CRC DATA PATTERN TEST

***** TELL HIM WE ARE TESTING *****

MICF1: MOVB #INTST,PCSR1 ; TELL HOST WE ARE TESTING
MOV PCSR1,@#IPCSR1

***** RETRIEVE PATTERN FROM HOST MEMORY *****

MOV IPCSR2,@#MDMA0 ; SET TO GET HOST PCBB ADDRESS
MOV IPCSR2+2,@#MDMA1
MOV @#MDMARO,R0 ; R5 NOW CONTAINS PCBB LOW
MOV @#MDMARO,R1 ; R1 NOW CONTAINS PCBB HIGH
MOV R0,@#MDMA0 ; POINT AT PCBB
MOV R1,@#MDMA1
MOV @#MDMARO,R3 ; R3 NOW HOLDS DATA PATTERN

***** FILL RECEIVE BUFFER WITH BACKGROUND PATTERN *****

MOV #LINADR,R0 ; RECEIVE BUFFER STARTS HERE
MOV R0,RBUF
10\$: CLR (R0)+ ; FILL RECEIVE BUFFER WITH ZEROS
CMP R0,#LINADR+SIZ1K ; STOP AT THE TOP
BLO 10\$

***** FILL XMIT BUFFER WITH TEST PATTERN *****

MOV R0,TBUF ; SAVE COPY OF ADDRESS
20\$: MOV R3,(R0)+ ; FILL XMIT BUFFER WITH PATTERN
CMP R0,#LINADR+SIZ2K
BLO 20\$

***** SET UP LINK FOR DATAGRAM LOOPBACK *****

MOV #MODE,@#CMDREG ; ENABLE LINK, SELECT MODE REG
MOV #PROM!LOOP,@#MODREG ; PROM MODE AND LOOPBACK
MOV #ENABLE,@#CMDREG ; ENABLE IT

MOV TBUF,R1 ; POINT AT XMIT BUFFER
CLR (R1)+ ; CLEAR OUT STATUS WORD
MOV #MAXBC,(R1)+ ; CLEAR OUT THE FIFO
CLR @#CLRIF ; CLEAR THE FIFO

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CRC DATA PATTERN TEST

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390 001152' 005067 177446          CLR      FLG6
391 001156' 016737 177462 021032  MOV      RBUF, @#LFRBUF      ; TELL DEUNA RECEIVE BUF LOC
392 001164' 016737 177456 021030  MOV      TBUF, @#LTAC       ; TELL DEUNA WHERE XMIT BUF IS
393                                     :
394 001172' 106427 000140          MTPS     #PRI03              ; ALLOW INTERRUPTS
395 001176' 032767 000100 177420 30$: BIT      #RCVFLG, FLG6      ; WAIT FOR RECEIVER INTERRUPT
396 001204' 001774                BEQ      30$
397                                     :
398 001206' 032767 000040 177410 40$: BIT      #TRNFLG, FLG6      ; WAIT FOR XMIT INTERRUPT TOO
399 001214' 001774                BEQ      40$
400                                     :
401 001216' 106427 000340          MTPS     #PRI07              ; DISABLE INTERRUPTS
402                                     :
403                                     :
404                                     :
405                                     : ***** CALCULATE THE CRC ON THE RECEIVER BUFFER CONTENTS *****
406                                     : *****
407                                     :
408 001222' 016700 177416          MOV      RBUF, R0           ; POINT AT RECEIVE BUFFER
409 001226' 062700 000004          ADD      #4, R0             ; BY-PASS THE STATUS AND BC
410 001232' 012702 002756          MOV      #MAXBC+4, R2      ; INCLUDE CRC BYTES
411 001236' 004767 177414          JSR     PC, BLKCRC
412 001242' 020427 157273          CMP     R4, #EXCRCH
413 001246' 001010          BNE     50$                ; ERROR IF NOT
414 001250' 020527 020343          CMP     R5, #EXCRCL
415 001254' 001005          BNE     50$
416 001256' 000241          CLC
417 001260' 112767 000001 177343 45$: MOVB    #1, PCSR1+1      ; INDICATE SUCCESS
418 001266' 000207          RTS      PC                ; TELL WHAT TEST WE JUST FINISHED
419                                     :
420                                     :
421                                     : ***** ERROR EXIT *****
422                                     : *****
423                                     :
424                                     :
425 001270' 000261          50$:   SEC
426 001272' 000772          BR      45$                ; TELL HIM WE MADE ERROR
427                                     : LEAVE

```

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CRC DATA PATTERN TEST

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001274' 112767 000002 177326
001302' 016737 177322 021020

001310' 016737 177316 021010
001316' 016737 177312 021012
001324' 013704 021014
001330' 013705 021014
001334' 010437 021010
001340' 010537 021012
001344' 013703 021014

001350' 012700 100000
001354' 010067 177264
001360' 005020
001362' 020027 104000
001366' 103774

001370' 010067 177252
001374' 010320
001376' 020027 110000
001402' 103774

001404' 016700 177236
001410' 062700 000004
001414' 012702 002746
001420' 004767 177232
001424' 016700 177216
001430' 062700 000004
001434' 062700 002746

```
SBTTL CRC ERROR TEST
*****
***** TELL HIM WE ARE TESTING *****
*****
MICF2:  MOVB  #INTST,PCSR1          ; TELL HOST WE ARE TESTING
        MOV   PCSR1,@#IPCSR1
*****
***** RETRIEVE PATTERN FROM HOST MEMORY *****
*****
        MOV   IPCSR2,@#MDMA0      ; SET TO GET HOST PCBB ADDRESS
        MOV   IPCSR2+2,@#MDMA1
        MOV   @#MDMAR0,R4         ; R4 NOW CONTAINS PCBB LOW
        MOV   @#MDMAR0,R5         ; R5 NOW CONTAINS PCBB HIGH
        MOV   R4,@#MDMA0          ; POINT AT PCBB
        MOV   R5,@#MDMA1
        MOV   @#MDMAR0,R3        ; R3 NOW HOLDS DATA PATTERN
*****
***** FILL RECEIVE BUFFER WITH BACKGROUND PATTERN *****
*****
        MOV   #LINADR,R0          ; RECEIVE BUFFER STARTS HERE
        MOV   R0,RBUF
10$:    CLR   (R0)+
        CMP   R0,#LINADR+SIZ1K    ; FILL RECEIVE BUFFER WITH ZEROS
        BLO  10$                 ; STOP AT THE TOP
*****
***** FILL XMIT BUFFER WITH TEST PATTERN *****
*****
20$:    MOV   R0,TBUF             ; SAVE COPY OF ADDRESS
        MOV   R3,(R0)+           ; FILL XMIT BUFFER WITH PATTERN
        CMP   R0,#LINADR+SIZ2K
        BLO  20$
*****
***** CALCULATE CRC ON TRANSMIT BUFFER *****
*****
        MOV   TBUF,R0            ; POINT AT RECEIVE BUFFER
        ADD   #4,R0              ; SKIP 1ST WORD AND BYTE COUNT
        MOV   #MAXBC-4,R2       ; DO A BUNCH
        JSR   PC,BLKCR0
        MOV   TBUF,R0            ; POINT AT XMIT BUFFER
        ADD   #4,R0              ; BY-PASS STATUS AND BC
        ADD   #MAXBC-4,R0       ; ADD BUFFER OFFSET
```

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CRC ERROR TEST

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484 001440' 005104          COM      R4          ; MUST SEND THE COMPLEMENT
485 001442' 005105          COM      R5
486 001444' 005205          INC      R5          ; INTRODUCE AN ERROR IN CRC!
487 001446' 010520          MOV      R5,(R0)+    ; APPEND THE CRC WORD
488 001450' 010420          MOV      R4,(R0)+    ; APPEND THE OTHER CRC WORD
489
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495 001452' 012737 000200 177776      MOV      #MODE,@#CMDREG      ; ENABLE, SELECT MODE
496 001460' 012737 100014 177774      MOV      #PROM!LOOP!DTCR,@#MODREG ; PROM MODE AND LOOPBACK
497 001466' 012737 100000 177776      MOV      #ENABLE,@#CMDREG    ; ENABLE THE XMTR
498
499 001474' 016701 177146      MOV      TBUF,R1          ; POINT AT XMIT BUFFER
500 001500' 005021          CLR      (R1)+          ; CLEAR OUT STATUS WORD
501 001502' 012721 002752      MOV      #MAXBC,(R1)+    ; CLEAR OUT THE FIFO
502 001506' 005037 021034      CLR      @#CLRFIF        ; CLEAR THE FIFO
503 001512' 005067 177106      CLR      FLG6
504 001516' 016737 177122 021032      MOV      RBUF,@#LFRBUF    ; TELL DEUNA RECEIVE BUF LOC
505 001524' 016737 177116 021030      MOV      TBUF,@#LTAC      ; TELL DEUNA WHERE XMIT BUF IS
506
507 001532' 106427 000140      MTPS     #PRI03          ; ALLOW INTERRUPTS
508 001536' 032767 000100 177060 30$ : BIT      #RCVFLG,FLG6    ; WAIT FOR RECEIVER INTERRUPT
509 001544' 001774          BEQ
510
511 001546' 032767 000040 177050 40$ : BIT      #TRNFLG,FLG6    ; WAIT FOR XMIT INTERRUPT TOO
512 001554' 001774          BEQ
513
514 001556' 106427 000340      MTPS     #PRI07          ; DISABLE INTERRUPTS
515
516
517
518
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520
521 001562' 012700 100000      MOV      #LINADR,R0      ; GET RECEIVE STATUS WORD
522 001566' 011003          MOV      (R0),R3        ; GET STATUS WORD
523
524 001570' 016737 177036 021010      MOV      IPCSR2,@#MDMA0   ; SET TO GET HOST PCBB
525 001576' 016737 177032 021012      MOV      IPCSR2+2,@#MDMA1
526 001604' 013700 021014          MOV      @#MDMAR0,R0     ; R0 NOW CONTAINS PCBB LOW
527 001610' 013701 021014          MOV      @#MDMAR0,R1     ; R1 NOW CONTAINS PCBB HI
528 001614' 062700 000002      ADD      #2,R0           ; BUMP TO NEXT HOST WORD
529 001620' 005501          ADC      R1
530 001622' 010037 021010      MOV      R0,@#MDMA0     ; POINT TO PCBB
531 001626' 010137 021012      MOV      R1,@#MDMA1
532 001632' 010337 021026      MOV      R3,@#MDMAW0    ; WRITE STATUS WORD TO HOST
533
534
535 001636' 112767 000002 176765      MOVB     #2,PCSR1+1      ; TELL HIM WHICH TEST IT IS
536 001644' 000241          CLC
537 001646' 000207          RTS      PC

```

***** SET UP LINK FOR DATAGRAM LOOPBACK *****

***** WRITE STATUS REGISTER TO HOST MEMORY *****

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CRC ERROR TEST

```

538
539      :SBTTL  CRC PATTERN LENGTH TEST
540
541      :*****
542      :***** TELL HIM WE ARE TESTING *****
543      :*****
544
545 001650' 112767 000002 176752 MICF3:  MOVB  #INTST,PCSR1          ; TELL HOST WE ARE TESTING
546 001656' 016737 176746 021020      MOV   PCSR1,@#IPCSR1
547
548
549      :*****
550      :***** RETRIEVE PATTERN FROM HOST MEMORY *****
551      :*****
552
553 001664' 016737 176742 021010      MOV   IPCSR2,@#MDMA0          ; SET TO GET HOST PCBB ADDRESS
554 001672' 016737 176736 021012      MOV   IPCSR2+2,@#MDMA1
555 001700' 013704 021014      MOV   @#MDMAR0,R4           ; R4 NOW CONTAINS PCBB LOW
556 001704' 013705 021014      MOV   @#MDMAR0,R5           ; R5 NOW CONTAINS PCBB HIGH
557 001710' 010437 021010      MOV   R4,@#MDMA0           ; POINT AT PCBB
558 001714' 010537 021012      MOV   R5,@#MDMA1
559 001720' 013703 021014      MOV   @#MDMAR0,R3           ; R3 NOW HOLDS DATA PATTERN
560
561
562      :*****
563      :***** FILL RECEIVE BUFFER WITH BACKGROUND PATTERN *****
564      :*****
565
566 001724' 012700 100000      MOV   #LINADR,R0           ; RECEIVE BUFFER STARTS HERE
567 001730' 010067 176710      MOV   R0,RBUF
568 001734' 005020 104000 10$:  CLR   (R0)+                ; FILL RECEIVE BUFFER WITH ZEROS
569 001736' 020027 104000      CMP   R0,#LINADR+SIZ1K     ; STOP AT THE TOP
570 001742' 103774
571
572
573      :*****
574      :***** FILL XMIT BUFFER WITH TEST PATTERN *****
575      :*****
576
577 001744' 010067 176676      MOV   R0,TBUF              ; SAVE COPY OF ADDRESS
578 001750' 010320 110000 20$:  MOV   R3,(R0)+            ; FILL XMIT BUFFER WITH PATTERN
579 001752' 020027 110000      CMP   R0,#LINADR+SIZ2K
580 001756' 103774      BLO  20$
581
582
583      :*****
584      :***** RETRIEVE BYTE COUNT FROM HOST MEMORY *****
585      :*****
586
587 001760' 016737 176646 021010      MOV   IPCSR2,@#MDMA0          ; SET TO GET HOST PCBB
588 001766' 016737 176642 021012      MOV   IPCSR2+2,@#MDMA1
589 001774' 013704 021014      MOV   @#MDMAR0,R4           ; R4 NOW CONTAINS PCBB LO
590 002000' 013705 021014      MOV   @#MDMAR0,R5           ; R5 NOW CONTAINS PCBB HI
591 002004' 062704 000002      ADD   #2,R4                 ; BUMP LOW BY TWO
592 002010' 005505      ADC   R5
593 002012' 010437 021010      MOV   R4,@#MDMA0           ; POINT AT PCBB

```

MICROF - MICROCODE MODULE F
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CRC PATTERN LENGTH TEST

```

594 002016' 010537 021012      MOV      R5,@#MDMA1
595 G02022' 013703 021014      MOV      @#MDMAR0,R3      ; R3 NOW HOLDS BYTE COUNT
596
597
598
599
600
601
602 002026' 016700 176614      MOV      TBUF,R0          ; POINT AT RECEIVE BUFFER
603 002032' 062700 000004      ADD      #4,R0            ; BY-PASS STATUS AND BC
604 002036' 010302                MOV      R3,R2            ; R3 CONTAINS BYTE COUNT
605 002040' 004767 176612      JSR      PC,BLKCR0
606 002044' 016700 176576      MOV      TBUF,R0          ; POINT AT XMIT BUFFER
607 002050' 062700 000004      ADD      #4,R0            ; BY-PASS STATUS AND BC
608 002054' 060300                ADD      R3,R0            ; ADD BUFFER BYTE COUNT
609 002056' 005104                COM      R4                ; COMPLEMENT BEFORE SENDING
610 002060' 005105                COM      R5
611 002062' 032700 000001      BIT      #1,R0            ;ARE WE LOOKING AT AN ODD BOUNDARY?
612 002066' 001427                BEQ      25$              ;NO, GOOD IT MAKES THINGS EASIER
613
614 002070' 162700 000001      SUB      #1,R0            ;POINT BACK TO EVEN BOUNDARY
615 002074' 011067 176550      MOV      (R0),CRC1        ;GET DATA BYTE AND JUNK
616 002100' 110567 176545      MOV      R5,CRC1+1        ;REPLACE JUNK WITH 1ST CRC BYTE
617 002104' 000305                SWAB     R5                ;POSITION 2ND CRC BYTE
618 002106' 110567 176540      MOV      R5,CRC23         ;GET 2ND CRC BYTE
619 002112' 110467 176535      MOV      R4,CRC23+1       ;GET 3RD CRC BYTE
620 002116' 000304                SWAB     R4                ;POSITION 4TH CRC BYTE
621 002120' 110467 176530      MOV      R4,CRC4          ;GET 4TH CRC BYTE
622 002124' 105067 176525      CLRB    CRC4+1           ;CLEAR JUNK FOR THE HELL OF IT
623 002130' 016720 176514      MOV      CRC1,(R0)+        ;STORE DATA AND 1ST CRC BYTE IN BUFFER
624 002134' 016720 176512      MOV      CRC2,(R0)+        ;STORE 2ND AND 3RD CRC BYTES IN BUFFER
625 002140' 016710 176510      MOV      CRC4,(R0)         ;STORE 4TH CRC BYTE IN BUFFER
626 002144' 000402                BR       26$              ;CARRY ON
627
628 002146' 010520 25$: MOV      R5,(R0)+          ; APPEND THE CRC WORD
629 002150' 010420 25$: MOV      R4,(R0)+          ; APPEND THE OTHER CRC WORD
630 002152'
631
632
633
634
635
636
637 002152' 012737 000200 177776      MOV      #MODE,@#CMDREG   ; ENABLE, SELECT MODE
638 002160' 012737 100014 177774      MOV      #PROM!LOOP!DTCR,@#MODREG ; PROM MODE AND LOOPBACK
639 002166' 012737 100000 177776      MOV      #ENABLE,@#CMDREG ; LET HER GO!
640
641 002174' 016701 176446      MOV      TBUF,R1          ; POINT AT XMIT BUFFER
642 002200' 005021                CLR      (R1)+            ; CLEAR OUT STATUS WORD
643 002202' 062703 000004      ADD      #4,R3            ; ACCOUNT FOR CRC BYTES
644 002206' 010321                MOV      R3,(R1)+         ; WRITE THE BYTE COUNT
645 002210' 005037 021034      CLR      @#CLRFIF         ; CLEAR THE FIFO
646 002214' 005067 176404      CLR      FLG6
647 002220' 016737 176420 021032      MOV      RBUF,@#LFRBUF    ; TELL DEUNA RECEIVE BUF LOC
648 002226' 016737 176414 021030      MOV      TBUF,@#LTAC      ; TELL DEUNA WHERE XMIT BUF IS
649

```

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CRC PATTERN LENGTH TEST

```

650 002234' 106427 000140      MTPS      #PRI03      ; ALLOW INTERRUPTS
651 002240' 032767 000100 176356 30$: BIT      #RCVFLG,FLG6 ; WAIT FOR RECEIVER INTERRUPT
652 002246' 001774      BEQ      30$
653
654 002250' 032767 000040 176346 40$: BIT      #TRNFLG,FLG6 ; WAIT FOR XMIT INTERRUPT TOO
655 002256' 001774      BEQ      40$
656
657 002260' 106427 000340      MTPS      #PRI07      ; DISABLE INTERRUPTS
658
659
660
661
662
663
664 002264' 016700 176354      MOV      RBUF,R0      ; GET RECEIVE STATUS WORD
665 002270' 011003      MOV      (R0),R3      ; GET STATUS WORD
666
667 002272' 016737 176334 021010  MOV      IPCSR2,@#MDMA0 ; SET TO GET HOST PCBB
668 002300' 016737 176330 021012  MOV      IPCSR2+2,@#MDMA1
669 002306' 013700 021014      MOV      @#MDMAR0,R0 ; R0 NOW CONTAINS PCBB LOW
670 002312' 013701 021014      MOV      @#MDMAR0,R1 ; R1 NOW CONTAINS PCBB HI
671 002316' 062700 000004      ADD      #4,R0        ; BUMP TO NEXT HOST WORD
672 002322' 005501      ADC      R1
673 002324' 010037 021010      MOV      R0,@#MDMA0 ; POINT TO PCBB
674 002330' 010137 021012      MOV      R1,@#MDMA1
675 002334' 010337 021026      MOV      R3,@#MDMAW0 ; WRITE STATUS WORD TO HOST
676
677
678 002340' 016702 176302      MOV      TBUF,R2      ; GET XMIT BUFFER STATUS
679 002344' 011203      MOV      (R2),R3
680 002346' 010337 021026      MOV      R3,@#MDMAW0 ; WRITE XMIT STATUS TO HOST
681 002352' 112767 000003 176251  MOVB    #3,PCSR1+1 ; TELL HIM WHICH TEST IT IS
682 002360' 000241      CLC
683 002362' 000207      RTS      PC

```


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CRC PATTERN LENGTH TEST

684
685
686
687
688
689
690
691 002364' 112767 000002 176236
692 002372' 016737 176232 021020
693
694 002400' 005067 176222
695
696
697
698
699
700
701 002404' 012700 100000
702 002410' 010067 176230
703 002414' 005020
704 002416' 020027 104000
705 002422' 103774
706
707
708
709
710
711
712 002424' 010067 176216
713 002430' 012720 125252
714 002434' 020027 110000
715 002440' 103773
716
717
718
719
720
721
722 002442' 016737 176164 021010
723 002450' 016737 176160 021012
724 002456' 013700 021014
725 002462' 013701 021014
726 002466' 010037 021010
727 002472' 010137 021012
728 002476' 013703 021014
729
730
731
732
733
734
735 002502' 012737 000200 177776
736 002510' 012737 100014 177774
737 002516' 012737 100000 177776
738
739 002524' 016704 176116

```

:SBTTL RECEIVE BUFFER RECOVERY - RUNT TEST
:*****
:***** TELL HIM WE ARE TESTING *****
:*****
MICF4:  MOVB  #INTST,PCSR1      ; TELL HOST WE ARE TESTING
        MOV   PCSR1,@#IPCSR1
:
        CLR   SANTIM          ; CLEAR FLAG FOR TIMER
:*****
:***** CLEAR THE RECEIVE BUFFER *****
:*****
        MOV   #LINADR,R0      ; RECEIVER BUFFER STARTS HERE
        MOV   R0,RBUF
10$:    CLR   (R0)+            ; FILL BUFFER WITH ZEROS
        CMP   R0,#LINADR+SIZ1K ; STOP AT THE TOP
        BLO  10$
:*****
:***** FILL XMIT WITH RUNT MARKER *****
:*****
20$:    MOV   R0,TBUF          ; SAVE COPY OF THE ADDRESS
        MOV   #125252,(R0)+
        CMP   R0,#LINADR+SIZ2K
        BLO  20$
:*****
:***** RETRIEVE BYTE COUNT FROM HOST MEMORY *****
:*****
        MOV   IPCSR2,@#MDMA0  ; SET TO GET HOST PCBB ADDRESS
        MOV   IPCSR2+2,@#MDMA1
        MOV   @#MDMAR0,R0     ; R4 NOW CONTAINS PCBB LOW
        MOV   @#MDMAR0,R1     ; P5 NOW CONTAINS PCBB HIGH
        MOV   R0,@#MDMA0     ; POINT AT PCBB
        MOV   R1,@#MDMA1
        MOV   @#MDMAR0,R3    ; R3 NOW HOLDS BYTE COUNT
:*****
:***** SET UP LINK FOR RUNT DATAGRAM LOOPBACK *****
:*****
        MOV   #MODE,@#CMDREG  ; DISABLE XMIT CRC - TRUE BC
        MOV   #PROM!LOOP!DTCR,@#MCDREG
        MOV   #ENABLE,@#CMDREG
:
        MOV   TBUF,R4        ; POINT AT XMIT BUFFER

```

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RECEIVE BUFFER RECOVERY - RUNT TEST

```

740 002530' 005024          CLR      (R4)+          ; CLEAR OUT STATUS WORD
741 002532' 010324          MOV      R3,(R4)+      ; WRITE PASSED BYTE COUNT
742                               ;
743 002534' 005037 021034    CLR      @#CLRFIF      ; CLEAR THE FIFO
744 002540' 005067 176060    CLR      FLG6          ; CLEAR THE INTERRUPT FLAG
745 002544' 016737 176074 021032  MOV     RBUF, @#LFRBUF ; TELL UNA WHERE RECEIVE BUF IS
746 002552' 016737 176070 021030  MOV     TBUF, @#LTAC   ; TELL UNA WHERE XMIT BUF IS
747                               ;
748 002560' 106427 000140    MTPS     #PRI03        ; ALLOW XMITTER TO INTERRUPT
749 002564' 026727 176036 000002 30$:  CMP     SANTIM,#2      ; WAIT AT MOST 2 SECONDS
750 002572' 002012          BGE     50$           ; EXIT NORMALLY IF TIMER DONE
751 002574' 032767 000100 176022  BIT     #RCVFLG,FLG6  ; WAIT FOR RECEIVER INTERRUPT
752 002602' 001770          BEQ     30$           ; IF NONE, WERE OK
753                               ;
754                               ;
755                               ; *****
756                               ; ***** ERROR FALLTHROUGH *****
757                               ; *****
758                               ;
759 002604' 032767 000040 176012 40$:  BIT     #TRNFLG,FLG6  ; WAIT FOR XMIT INT TOO!
760 002612' 001774          BEQ     40$
761                               ;
762 002614' 000261          EC              ; TELL HOST UNA SCREWED UP!
763 002616' 000506          JR         90$           ; EXIT WITHOUT WRITING PTR
764                               ;
765                               ;
766                               ; *****
767                               ; ***** NOW TRY LEGITIMATE BUFFER SIZE *****
768                               ; *****
769                               ;
770 002620' 016700 176022    50$:  MOV     TBUF,R0          ; GET TRANSMIT BUFFER POINTER
771 002624' 012720 052525    60$:  MOV     #52525,(R0)+
772 002630' 020027 110000    CMP     R0,#LINADR+SIZ2K
773 002634' 103773          BLO     60$
774                               ;
775                               ;
776                               ; *****
777                               ; ***** SET UP LINK FOR MINIMUM DATAGRAM MESSAGE *****
778                               ; *****
779                               ;
780 002636' 012737 000200 177776  MOV     #MODE,@#CMDREG ; ENABLE LINK, SEL MODE REG
781 002644' 012737 100004 177774  MOV     #PROM!LOOP,@#MODREG
782 002652' 012737 100000 177776  MOV     #ENABLE,@#CMDREG
783                               ;
784 002660' 016704 175762    MOV     TBUF,R4        ; POINT AT XMIT BUFFER
785 002664' 005024          CLR     (R4)+          ; CLEAR STATUS WORD
786 002666' 012724 000100    MOV     #MINBC,(R4)+  ; SEND SMALL DATAGRAM
787                               ;
788 002672' 005067 175726    CLR     FLG6          ;
789 002676' 016737 175742 021032  MOV     RBUF, @#LFRBUF ; TELL UNA WHERE RECEIVE BUF IS
790 002704' 016737 175736 021030  MOV     TBUF, @#LTAC   ; TELL UNA WHERE XMIT BUF IS
791                               ;
792 002712' 106427 000140    MTPS     #PRI03        ; ALLOW XMITR TO INTERRUPT
793 002716' 032767 000100 175700 70$:  BIT     #RCVFLG,FLG6  ; WAIT FOR RECEIVER INTERRUPT
794 002724' 001774          BEQ     70$
795                               ;

```

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RECEIVE BUFFER RECOVERY - RUNT TEST

```

796 002726' 032767 000040 175670 80$: BIT #TRNFLG,FLG6 ; WAIT FOR XMIT INT TOO!
797 002734' 001774 : BEQ 80$
798 :
799 002736' 106427 000340 : MTPS #PRI07 ; DISABLE INTERRUPTS
800 :
801 :
802 : *****
803 : ***** WRITE PARAMETERS TO HOST PCBB *****
804 : *****
805 :
806 002742' 013703 021044 : MOV @#LRBUF,R3 ; GET LINK POINTER
807 :
808 002746' 016700 175672 : MOV RBUF,R0 ; POINT AT RECEIVER BUFFER
809 002752' 062700 000004 : ADD #4,R0
810 002756' 011004 : MOV (R0),R4 ; GET PATTERN WORD
811 :
812 :
813 002760' 016737 175646 021010 : MOV IPCSR2,@#MDMA0 ; SET TO GET HOST PCBB
814 002766' 016737 175642 021012 : MOV IPCSR2+2,@#MDMA1
815 002774' 013700 021014 : MOV @#MDMAR0,R0 ; R0 NOW CONTAINS PCBB LO
816 003000' 013701 021014 : MOV @#MDMAR0,R1 ; R1 NOW CONTAINS PCBB HI
817 003004' 062700 000002 : ADD #2,R0
818 003010' 005501 : ADC R1
819 003012' 010037 021010 : MOV R0,@#MDMA0 ; POINT AT PCBB+2
820 003016' C10137 021012 : MOV R1,@#MDMA1
821 :
822 003022' 010337 021026 : MOV R3,@#MDMAW0 ; WRITE LINK POINTER
823 003026' 010437 021026 : MOV R4,@#MDMAW0 ; WRITE DATA BYTE
824 :
825 :
826 003032' 000241 : CLC
827 003034' 112767 000004 175567 90$: MOVB #4,PCSR1+1 ; TELL HOST WE ARE DONE
828 003042' 000207 : RTS PC

```

MICROF - MICROCODE MODULE F
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HALF-DUPLEX TEST

```

829 .SBTTL HALF-DUPLEX TEST
830
831 *****
832 ***** TELL HIM WE ARE TESTING *****
833 *****
834
835 003044' 112767 000002 175556 MICF5: MOVB #INTST,PCSR1 ; TELL HOST WE ARE TESTING
836 003052' 016737 175552 021020 MOV PCSR1,@#IPCSR1
837
838 003060' 005067 175542 CLR SANTIM ; CLEAR FLAG FOR TIMER
839
840
841 *****
842 ***** CLEAR THE RECEIVE BUFFER *****
843 *****
844
845 003064' 012700 100000 MOV #LINADR,R0 ; RECEIVER BUFFER STARTS HERE
846 003070' 010067 175550 MOV R0,RBUF
847 003074' 005020 104000 10$: CLR (R0)+ ; FILL BUFFER WITH ZEROS
848 003076' 020027 104000 CMP R0,#LINADR+S!Z1K ; STOP AT THE TOP
849 003102' 103774 BLO 10$
850
851 *****
852 ***** FILL XMIT WITH RUNT MARKER *****
853 *****
854
855
856 003104' 010067 175536 MOV R0,TBUF ; SAVE COPY OF THE ADDRESS
857 003110' 012720 125252 20$: MOV #125252,(R0)+
858 003114' 020027 110000 CMP R0,#LINADR+S!Z2K
859 003120' 103773 BLO 20$
860
861 *****
862 ***** SET UP LINK FOR RUNT DATAGRAM LOOPBACK *****
863 *****
864
865
866 003122' 012737 000200 177776 MOV #MODE,@#CMDREG ; DISABLE XMIT CRC - TRUE BC
867 003130' 012737 100005 177774 MOV #PROM!LOOP!HDX,@#MODREG
868 003136' 012737 100000 177776 MOV #ENABLE,@#CMDREG
869
870 003144' 016704 175476 MOV TBUF,R4 ; POINT AT XMIT BUFFER
871 003150' 005024 CLR (R4)+ ; CLEAR OUT STATUS WORD
872 003152' 010324 MOV R3,(R4)+ ; WRITE PASSED BYTE COUNT
873
874
875 003154' 005037 021034 CLR @#CLRFIF ; CLEAR THE FIFO
876 003160' 005067 175440 CLR FLG6 ; CLEAR THE INTERRUPT FLAG
877 003164' 016737 175454 021032 MOV RBUF,@#LFRBUF ; TELL UNA WHERE RECEIVE BUF IS
878 003172' 016737 175450 021030 MOV TBUF,@#LTAC ; TELL UNA WHERE XMIT BUF IS
879
880 003200' 106427 000140 MTPS #PRI03 ; ALLOW XMITTER TO INTERRUPT
881 003204' 026727 175416 000002 30$: CMP SANTIM,#2 ; WAIT AT MOST 2 SECONDS
882 003212' 002012 BGE 50$ ; EXIT NORMALLY IF TIMER DONE
883 003214' 032767 000100 175402 BIT #RCVFLG,FLG6 ; WAIT FOR RECEIVER INTERRUPT
884 003222' 001770 BEQ 30$ ; IF NONE, WERE OK

```

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HALF-DUPLEX TEST

```

885
886
887
888
889
890 003224' 032767 000040 175372 40$: BIT #TRNFLG,FLG6 ; WAIT FOR XMIT INT TOO!
891 003232' 001774 BEQ 40$
892
893 003234' 000261 SEC ; TELL HOST UNA SCREWED UP!
894 003236' 000503 BR 90$ ; EXIT WITHOUT WRITING PTR
895
896
897
898
899
900
901 003240' 016700 175402 50$: MOV TBUF,RO ; GET TRANSMIT BUFFER POINTER
902 003244' 012720 052525 60$: MOV #52525,(R0)+
903 003250' 020027 110000 CMP RO,#LINADR+SIZ2K
904 003254' 103773 BLO 60$
905
906
907
908
909
910
911 003256' 012737 000200 177776 MOV #MODE,@#CMDREG ; ENABLE LINK, SEL MODE REG
912 003264' 012737 100004 177774 MOV #PROM!LOOP,@#MODREG
913 003272' 012737 100000 177776 MOV #ENABLE,@#CMDREG
914
915 003300' 016704 175342 MOV TBUF,R4 ; POINT AT XMIT BUFFFF
916 003304' 005024 CLR (R4)+ ; CLEAR STATUS WORD
917 003306' 012724 000100 MOV #MINBC,(R4)+ ; SEND SMALL DATAGRAM
918
919 003312' 005067 175306 CLR FLG6
920 003316' 016737 175322 021032 MOV RBUF,@#LFRBUF ; TELL UNA WHERE RECEIVE BUF IS
921 003324' 016737 175316 021030 MOV TBUF,@#LTAC ; TELL UNA WHERE XMIT BUF IS
922
923 003332' 106427 000140 MTPS #PRI03 ; ALLOW XMITR TO INTERRUPT
924 003336' 032767 000100 175260 70$: BIT #RCVFLG,FLG6 ; WAIT FOR RECEIVER INTERRUPT
925 003344' 001774 BEQ 70$
926
927 003346' 032767 000040 175250 80$: BIT #TRNFLG,FLG6 ; WAIT FOR XMIT INT TOO!
928 003354' 001774 BEQ 80$
929
930 003356' 106427 000340 MTPS #PRI07 ; DISABLE INTERRUPTS
931
932
933
934
935
936
937 003362' 013703 021044 MOV @#LRBUF,R3 ; GET LINK POINTER
938
939 003366' 016700 175252 MOV RBUF,RO ; POINT AT RECEIVER BUFFER
940 003372' 062700 000004 ADD #4,RO

```

***** ERROR FALLTHROUGH *****

***** NOW SEE IF BUFFER RECOVERS *****

***** SET UP LINK FOR MINIMUM DATAGRAM MESSAGE *****

***** WRITE PARAMETERS TO HOST PCBB *****

MICROF - MICROCODE MODULE F
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 HALF-DUPLEX TEST

```

941 003376' 011004          MOV      (R0),R4          ; GET PATTERN WORD
942
943
944 003400' 016737 175226 021010  MOV      IPCSR2,@#MDMA0      ; SET TO GET HOST PCBB
945 003406' 016737 175222 021012  MOV      IPCSR2+2,@#MDMA1
946 003414' 013700 021014          MOV      @#MDMAR0,R0        ; R0 NOW CONTAINS PCBB LO
947 003420' 013701 021014          MOV      @#MDMAR0,R1        ; R1 NOW CONTAINS PCBB HI
948 003424' 010037 021010          MOV      R0,@#MDMA0
949 003430' 010137 021012          MOV      R1,@#MDMA1
950
951 003434' 010337 021026          MOV      R3,@#MDMAW0      ; WRITE LINK POINTER
952 003440' 010437 021026          MOV      R4,@#MDMAW0      ; WRITE DATA BYTE
953
954
955 003444' 000241          CLC
956 003446' 112767 000005 175155 90$: MOVB     #5,PCSR1+1      ; TELL HOST WE ARE DONE
957 003454' 000207          RTS      PC
958
959 003456' 003460          MICFSZ::MICFSZ-MICROF+2
960
961          000001          .END

```


MICROF - MICROCODE MODULE F
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 CROSS REFERENCE TABLE -- USER SYMBOLS

PHYAD3=	021066		30#															
PHYAD4=	021070		31#															
PHYAD5=	021072		32#															
POLYM =	166670		134#	317														
POLYL =	101440		135#	318														
PRI00 =	000000		64#	194*														
PRI01 =	000040		63#															
PRI02 =	000100		62#															
PRI03 =	000140		61#	394*	507*	650*	748*	792*	879*	923*								
PRI04 =	000200		60#	168	178													
PRI05 =	000240		59#	173	183													
PRI06 =	000300		58#															
PRI07 =	000340		57#	141*	151	158	163	199*	401*	514*	657*	799*	930*					
PROM =	100000		84#	383	496	638	736	781	867	912								
RBUFP	000644R	002	282#	362*	391	408	457*	504	567*	647	664	702*	745	789	808			
			846*	876	920	939												
RCEI =	002000		73#															
RCVFLG=	000100		110#	264	395	508	651	751	793	882	924							
RCVINT	000566R	002	181	262#														
RCVVEC=	000120		95#	182*	183*													
ROMADR=	040000		125#	126														
ROMSIZ=	040000		121#	126														
RXI =	020000		70#															
SANTIM	000626R	002	240*	277#	694*	749	838*	880										
SANVEC=	000134		96#	172*	173*													
SERI =	100000		68#															
SIZ1K =	004000		115#	116	364	459	569	704	848									
SIZ2K =	010000		116#	117	374	469	579	714	772	858	903							
SIZ4K =	020000		117#	118	119	120												
SIZ8K =	040000		118#	121	122													
STACK =	001000		100#	143														
TBLD	000612R	002	214	270#														
TBUFP	000646R	002	283#	372*	386	392	467*	477	481	499	505	577*	602	606	641			
			648	678	712*	739	746	770	784	790	856*	870	877	901	915			
			921															
TIMINT	000464R	002	171	240#														
TRNFLG=	000040		109#	267	398	511	654	759	796	890	927							
TRNINT	000602R	002	176	267#														
TRNVEC=	000070		94#	177*	178*													
TXI =	010000		71#															
UNIERR=	020000		113#	235														
WCSADR=	030000		123#	124														
WCSSIZ=	020000		119#	124														
.	= 003460R	002	148	156	161	166	171	176	181	202	214	238	253	259	270			
			271	272	273	274												
. ABS.	000000	000																
	000000	001																
MICRF	003460	002																

ERRORS DETECTED: 0
 DEFAULT GLOBALS GENERATED: 0

MICROF.OBJ, MICROF.LST/CRF/DOC/NL:TOC/SOL=MICROF.MAC

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CROSS REFERENCE TABLE -- USER SYMBOLS

RUN-TIME: 1 1 .3 SECONDS
RUN-TIME RATIO: $40/3=11.5$
CORE USED: 9K (18 PAGES)

DOCUMENT PAGES: 24

MICROG - MICROCODE MODULE G
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OTHER DEFINITIONS USED BY THE MICROCODE

```

132
133      .SBTTL MICROCODE MODULE G
134
135 000000' 106427 000340      MICROG::MTPS      #PRI07      ;DISABLE INTERRUPTS
136 000004' 012706 001000      MOV      #STACK,SP      ;SETUP STACK
137 000010' 112767 000001 000600      MCVB     #INMON,PCSR1    ;TELL HOST WE ARE IN MICROMONITOR
138 000016' 016737 000574 021020      MOV      PCSR1,@#IPCSR1
139 000024' 012737 004000 021000      MOV      #DNI,@#IPCSR0    ;TELL HOST THE LOAD AND START FINISHED
140 000032' 010700      MOV      PC,R0      ;GET ADDRESS OF UNEXPECTED ERROR...
141 000034' 062700 000404      ADD      #ERRINT-.,R0    ;HANDLER
142 000040' 005001      CLR      R1      ;FILL ALL UNUSED VECTORS WITH TRAP...
143 000042' 010021 10$:      MOV      R0,(R1)+      ;HANDLER
144 000044' 012721 000340      MOV      #PRI07,(R1)+
145 000050' 020127 001000      CMP      R1,#1000
146 000054' 002772      BLT     10$
147
148 000056' 010700      MOV      PC,R0      ;SETUP PARITY TRAP VECTOR
149 000060' 062700 000462      ADD      #PARINT-.,R0
150 000064' 010037 000140      MOV      R0,@#PARVEC
151 000070' 012737 000340 000142      MOV      #PRI07,@#PARVEC+2
152
153 000076' 010700      MOV      PC,R0      ;SETUP DMA INTERRUPT VECTOR
154 000100' 062700 000364      ADD      #DMAINT-.,R0
155 000104' 010037 000114      MOV      R0,@#DMAVEC
156 000110' 012737 000340 000116      MOV      #PRI07,@#DMAVEC+2
157
158 000116' 010700      MOV      PC,R0      ;SETUP CSR WRITE VECTOR
159 000120' 062700 000310      ADD      #CSRWRT-.,R0
160 000124' 010037 000064      MOV      R0,@#CSRVEC
161 000130' 012737 000200 000066      MOV      #PRI04,@#CSRVEC+2
162
163 000136' 010700      MOV      PC,R0      ;SETUP SANITY TIMER VECTOR
164 000140' 062700 000316      ADD      #TIMINT-.,R0
165 000144' 010037 000134      MOV      R0,@#SANVEC
166 000150' 012737 000240 000136      MOV      #PRI05,@#SANVEC+2
167
168 000156' 010700      MOV      PC,R0      ;SETUP TRANSMITTER VECTOR
169 000160' 062700 000414      ADD      #TRNINT-.,R0
170 000164' 010037 000070      MOV      R0,@#TRNVEC
171 000170' 012737 000200 000072      MOV      #PRI04,@#TRNVEC+2
172
173 000176' 010700      MOV      PC,R0      ;SETUP RECEIVER VECTOR
174 000200' 062700 000360      ADD      #RCVINT-.,R0
175 000204' 010037 000120      MOV      R0,@#RCVVEC
176 000210' 012737 000240 000122      MOV      #PRI05,@#RCVVEC+2
177
178 000216' 013700 021040      MOV      @#PCSRSW,R0    ;GET SWITCH PACK BITS
179 000222' 052700 176000      BIS      #176000,R0    ;MAP THEM INTO HOST I/O PAGE
180 000226' 006300      ASL     R0      ;SHIFT OVER TO POSITION CORRECTLY
181 000230' 006300      ASL     R0
182 000232' 006300      ASL     R0
183 000234' 062700 000004      ADD      #4,R0      ;PCSR2 IS PCSR0+4
184 000240' 010067 000354      MOV      R0,IPCSR2    ;SAVE PCSR2 ADDRESS
185 000244' 012767 000003 000350      MOV      #3,IPCSR2+2  ;HIGH ORDER BITS 17:16
186 000252' 005067 000334      CLR     FLG7      ;INITIALIZE FLAG WORD
187 000256' 106427 000000 15$:      MTPS     #PRI00      ;ALLOW INTERRUPTS

```

NO
NOI
BCI
BEI
BGI
BGI
BG
BG
BG
BG
BG
BG
BG
BG
BG
BG
BG
BG
BG
BG
BG
BG
BG
BG
BG
BG
BN
BN
BR
BR
CK
CL
CL
CL
CO
DE
DE
DI
DI
DO
DO
EN
EN
EN
EN
EN
EN
EN
EN
EN

MICROG - MICROCODE MODULE G
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MICROCODE MODULE G

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188
189 000262' 005767 000324      20$:  TST      FLG7      ;WAIT FOR A COMMAND FROM HOST
190 000266' 001775                BEQ      20$
191
192 000270' 106427 000340      MTPS     #PRI07     ;RAISE CPU PRIORITY TO SERVICE COMMAND
193 000274' 032767 000001 000310  BIT      #CSRFLG,FLG7 ;DID HOST GIVE US A COMMAND?
194 000302' 001001                BNE     30$         ;YES
195 000304' 000777                BR      .           ;NO, ERROR SO JUST SIT HERE...
196                                ;FOR LACK OF ANYTHING BETTER TO DO
197
198 000306' 113700 021000      30$:  MOVB     @#IPCSRO,R0 ;GET WHAT HOST WROTE TO PCSRO
199 000312' 042700 177760                BIC     #177760,R0 ;STRIP ALL BUT COMMAND BITS
200 000316' 001004                BNE     35$         ;WAS IT THE CLEAR FUNCTION?
201 000320' 012737 000001 021020  MOV      #INMON,@#IPCSR1 ;YES, CLEAR OUT THE TEST # BITS
202 000326' 000432                BR      50$
203 000330' 022700 000017      35$:  CMP      #17,R0     ;RESTART OPERATIONAL MICROCODE?
204 000334' 001432                BEQ     60$         ;YES
205 000336' 162700 000001                SUB     #1,R0
206 000342' 010701                MOV     PC,R1       ;GET ADDRESS OF OUR COMMAND TABLE
207 000344' 062701 000240                ADD     #TBLG-.,R1
208 000350' 006300                ASL     R0          ;MAKE COMMAND A BYTE OFFSET
209 000352' 060001                ADD     R0,R1       ;USE IT TO INDEX INTO COMMAND TABLE
210 000354' 061101                ADD     (R1),R1     ;R1 NOW HAS COMMAND ROUTINE ADDRESS
211 000356' 004711                JSR     PC,(R1)     ;EXECUTE AS COMMANDED FROM HOST
212 000360' 103404                BCS     40$         ;ERROR OCCURRED
213 000362' 112767 000001 000226  MOVB     #INMON,PCSR1 ;INDICATE TO HOST WE ARE BACK IN...
214 000370' 000403                BR      45$         ;MICROMONITR
215 000372' 112767 000003 000216  40$:  MOVB     #INERR,PCSR1 ;INDICATE TO HOST ERROR OCCURRED
216 000400' 016737 000212 021020  45$:  MOV     PCSR1,@#IPCSR1
217 000406' 012737 004000 021000  MOV     #DNI,@#IPCSRO
218 000414' 005067 000172      50$:  CLR     FLG7       ;TELL HOST THIS MICROTEST FINISHED
219 000420' 000716                BR      15$         ;RESET FLAG WORD
220                                ;GO WAIT FOR ANOTHER COMMAND
221
222 000422' 005000      60$:  CLR     R0          ;FAKE SUCCESSFUL SELF TEST RESULTS
223 000424' 000137 040006                JMP     @#40006     ;START OPERATIONAL MICROCODE
224
225 000430' 052767 000001 000154  CSRWRT: BIS     #CSRFLG,FLG7 ;INDICATE A CSR WRITE INTERRUPT OCCURED
226 000436' 000002                RTI
227
228 000440' 052767 000002 000144  ERRINT: BIS     #ERRFLG,FLG7 ;INDICATE A UNEXPECTED INTERRUPT OCCURED
229 000446' 012737 020000 021000  MOV     #UNIERR,@#IPCSRO ;TELL HOST AN UNEXPECTED INTERRUPT
230                                ;HAPPENED
231 000454' 000777                BR      .           ;JUST SIT HERE AND SPIN WHEELS
232                                ;COUNT ON THE HOST TO TIMEOUT WAITING
233
234 000456' 005267 000132      TIMINT: INC     SANTIM ;COUNT TICKS AS THEY OCCUR
235 000462' 000002                RTI
236
237 000464' 013767 021002 000136  DMAINT: MOV     @#DMACSR,DMDONE ;GET DMA STATUS
238 000472' 032767 040000 000130  BIT     #BIT14,DMDONE ;DID A NON-EXISTANT MEMORY INTERRUPT OCCUR?
239 000500' 001404                BEQ     10$         ;NO
240 000502' 012737 040000 021000  MOV     #NXMERR,@#IPCSRO ;YES, TELL HOST A NON-EXISTANT MEMORY
241                                ;LOCATION WAS ADDRESSED
242
243 000510' 000407                BR      20$
244 000512' 032767 100000 000110  10$:  BIT     #BIT15,DMDONE ;DID A NPR TIMEOUT OCCUR?

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MICROCODE MODULE G

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279 000642 112767 000002 177746 MICG1: MOVB #INTST,PCSR1 ;TELL HOST WE ARE IN A TEST
280 000650 016737 177742 021020 MOV PCSR1,@#IPCSR1
281
282 000656 012700 100000 MOV #LINADR,R0 ;FILL RECEIVE BUFFER WITH ZEROS
283 000662 010067 177744 MOV R0,RBUF
284 000666 005020 10$: CLR (R0)+
285 000670 020027 104000 CMP R0,#LINADR+SIZ1K
286 000674 103774 BLO 10$
287
288 000676 010067 177732 MOV R0,TBUF ;FILL TRANSMIT BUFFER WITH 1'S
289 000702 012720 177777 20$: MOV #177777,(R0)+
290 000706 020027 110000 CMP R0,#LINADR+SIZ2K
291 000712 103773 BLO 20$
292
293 000714 016737 177700 021010 MOV IPCSR2,@#MDMA0 ;GET HOST'S PCBB ADDRESS
294 000722 016737 177674 021012 MOV IPCSR2+2,@#MDMA1
295 000730 013704 021014 @#MDMAR0,R4 ;R4 HOLDS PCBB LOW ADDRESS
296 000734 013705 021014 @#MDMAR0,R5 ;R5 HOLDS PCBB HIGH ADDRESS
297 000740 010437 021010 MOV R4,@#MDMA0 ;POINT TO PCBB+0
298 000744 010537 021012 MOV R5,@#MDMA1
299 000750 013703 021014 MOV @#MDMAR0,R3 ;GET WHAT HOST WANTS TO GO INTO LINK
300 ;MODE REGISTER
301
302 000754 012737 100200 177776 MOV #MODE!ENABLE,@#CMDREG ;ENABLE LINK MODULE AND SELECT MODE REG
303 000762 010337 177774 MOV R3,@#MODREG ;LOAD MODE REGISTER WITH HOST'S VALUE
304
305 000756 016702 177642 MOV TBUF,R2 ;GET BEGINNING OF TRANSMIT BUFFER
306 000772 005722 TST (R2)+ ;SKIP FIRST WORD
307 000774 012722 002752 MOV #MAXBC-CRCSIZ,(R2)+ ;SET TO TRANSMIT MAX LENGTH PACKET
308 001000 005037 021034 CLR @#CLRFIF ;CLEAR THE FIFO
309 001004 005067 177602 CLR FLG7 ;CLEAR OUT THE FLAG WORD
310 001010 016737 177616 021032 MOV RBUF,@#LFRBUF ;TELL UNA WHERE RECEIVE BUFF IS
311 001016 016737 177612 021030 MOV TBUF,@#LTAC ;TELL UNA WHERE TRANSMIT BUFF IS
312 ;START TRANSMIT OPERATION
313
314 001024 106427 000140 MTPS #PRI03 ;ALLOW INTERRUPTS
315 001030 012767 000040 177554 30$: BIT #TRNFLG,FLG7 ;WAIT FOR TRANSMIT DONE
316 001036 001774 BEQ 30$
317
318 001040 106427 000340 MTPS #PRI07 ;DISABLE ANY MORE INTERRUPTS
319
320 001044 016700 177564 MOV TBUF,R0 ;POINT TO TRANSMIT BUFFER
321 001050 012001 MOV (R0)+,R1 ;GET FIRST TRANSMIT STATUS WORD
322 001052 012002 MOV (R0)+,R2 ;GET SECOND TRANSMIT STATUS WORD
323
324 001054 062704 000002 ADD #2,R4 ;POINT TO HOSTS PCBB+2
325 001060 005505 ADC R5
326 001062 010437 021010 MOV R4,@#MDMA0
327 001066 010537 021012 MOV R5,@#MDMA1
328 001072 010137 021026 MOV R1,@#MDMAR0 ;DUMP FIRST STATUS WORD TO PCBB+2
329 001076 010237 021026 MOV R2,@#MDMAR0 ;DUMP SECOND STATUS WORD TO PCBB+4
330
331 001102 112767 000001 177507 MOVB #1,PCSP1+1 ;TELL HOST WHAT TEST JUST FINISHED
332 001110 000241 CLC
333 001112 000207 RTS PC
334

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335 001114' 112767 000002 177474 MICG2: MOVB #INTST,PCSR1 ;TELL HOST THAT WE ARE IN A TEST
336 001122' 016737 177470 021020 MOV PCSR1,@#IPCSR1
337
338 001130' 012700 100000 MOV #LINADR,R0 ;FILL RECEIVE BUFFER WITH ALL 1'S
339 001134' 010067 177472 MOV R0,RBUF
340 001140' 012720 177777 10$: MOV #177777,(R0)+
341 001144' 020027 104000 CMP R0,#LINADR+SIZ1K
342 001150' 103773 BLO 10$
343
344 001152' 010067 177456 20$: MOV R0,TBUF ;FILL TRANSMIT BUFFER WITH ALL 0'S
345 001156' 005020 CLR (R0)+
346 001160' 020027 110000 CMP R0,#LINADR+SIZ2K
347 001164' 103774 BLO 20$
348
349 001166' 016737 177426 021010 MOV IPCSR2,@#MDMA0 ;GET HOST'S PCBB ADDRESS
350 001174' 016737 177422 021012 MOV IPCSR2+2,@#DMA1
351 001202' 013700 021014 MOV @#MDMAR0,R0 ;R0 HOLDS HOST'S PCBB LOW ADDRESS
352 001206' 013701 021014 MOV @#MDMAR0,R1 ;R1 HOLDS HOST'S PCBB HI ADDRESS
353 001212' 010037 021010 MOV R0,@#DMA0 ;POINT TO PCBB+0
354 001216' 010137 021012 MOV R1,@#DMA1
355 001222' 013703 021014 MOV @#MDMAR0,R3 ;GET BYTE COUNT FOR TRANSMIT OPERATION
356
357 001226' 012737 100200 177776 MOV #MODE!ENABLE,@#CMDREG ;ENABLE LINK MODULE AND SELECT MODE REG
358 001234' 012737 100004 177774 MOV #PROM!LOOP,@#MODREG ;ACCEPT ALL PACKETS IN LOOP BACK
359
360 001242' 016704 177366 MOV TBUF,R4 ;POINT TO TRANSMIT BUFFER
361 001246' 005724 TST (R4)+ ;SKIP FIRST WORD
362 001250' 010324 MOV R3,(R4)+ ;SET BYTE COUNT TO HOST'S VALUE
363 001252' 005037 021034 CLR @#CLRFIF ;CLEAR THE FIFO
364 001256' 005067 177330 CLR FLG7 ;CLEAR THE FLAG WORD
365 001262' 016737 177344 021032 MOV RBUF,@#LFRBUF ;TELL UNA WHERE RECEIVE BUFF IS
366 001270' 016737 177340 021030 MOV TBUF,@#LTAC ;TELL UNA WHERE TRANSMIT BUFF IS AND
367 ;START THE TRANSMIT OPERATION
368
369 001276' 106427 000140 MTPS #PRI03 ;ALLOW INTERRUPTS
370 001302' 032767 000040 177302 30$: BIT #TRNFLG,FLG7 ;WAIT FOR TRANSMIT DONE INTERRUPT
371 001310' 001774 BEQ 30$
372
373 001312' 106427 000340 MTPS #PRI07 ;DISABLE INTERRUPTS
374
375 001316' 016702 177312 MOV TBUF,R2 ;POINT TO TRANSMIT BUFFER
376 001322' 005722 TST (R2)+ ;SKIP FIRST STATUS WORD
377 001324' 012203 MOV (R2)+,R3 ;GET SECOND STATUS WORD
378 001326' 042703 176000 BIC #176000,R3 ;STRIP ALL BUT TDR VALUE BITS
379 001332' 062700 000002 ADD #2,R0 ;POINT TO HOST'S PCBB+2
380 001336' 005501 ADC R1
381 001340' 010037 021010 MOV R0,@#DMA0
382 001344' 010137 021012 MOV R1,@#DMA1
383 001350' 010337 021026 MOV R3,@#DMAW0 ;DUMP TDR COUNTER VALUE TO PCBB+2
384
385 001354' 112767 000002 177235 MOVB #2,PCSR1+1 ;TELL HOST WHAT TEST JUST FINISHED
386 001362' 000241 CLC
387 001364' 000207 RTS PC
388
389 001366' 112767 000002 177222 MICG3: MOVB #INTST,PCSR1 ;TELL HOST THAT WE ARE IN A TEST
390 001374' 016737 177216 021020 MOV PCSR1,@#IPCSR1
    
```

LN
#C
LC
TR
LO
HI
**
MO
SE
<.

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391
392 001402' 012700 100000      MOV      #LINADR,R0      ;FILL RECEIVE BUFFER WITH ALL 1'S
393 001406' 010067 177220      MOV      R0,RBUF
394 001412' 012720 177777      10$:    MOV      #177777,(R0)+
395 001416' 020027 104000      CMP      R0,#LINADR+SIZ1K
396 001422' 103773      BLO
397
398 001424' 010067 177204      20$:    MOV      R0,TBUF
399 001430' 005020      CLR      (R0)+      ;FILL TRANSMIT BUFFER WITH ALL 0'S
400 001432' 020027 110000      CMP      R0,#LINADR+SIZ2K
401 001436' 103774      BLO
402
403 001440' 016737 177154 021010      MOV      IPCSR2,@#MDMA0      ;GET HOST'S PCBB ADDRESS
404 001446' 016737 177150 021012      MOV      IPCSR2+2,@#MDMA1
405 001454' 013700 021014      MOV      @#MDMAR0,R0      ;R0 HOLDS HOST'S PCBB LOW ADDRESS
406 001460' 013701 021014      MOV      @#MDMAR0,R1      ;R1 HOLDS HOST'S PCBB HI ADDRESS
407 001464' 010037 021010      MOV      R0,@#MDMA0      ;POINT TO PCBB+0
408 001470' 010137 021012      MOV      R1,@#MDMA1
409 001474' 013703 021014      MOV      @#MDMAR0,R3      ;GET BYTE COUNT FOR TRANSMIT OPERATION
410
411 001500' 012737 100200 177776      MOV      #MODE!ENABLE,@#CMDREG      ;ENABLE LINK MODULE AND SELECT MODE REG
412 001506' 012737 100024 177774      MOV      #PROM!LOOP!COLL,@#MODREG      ;ACCEPT ALL PACKETS IN LOOP BACK AND
413                                     ;FORCE A COLLISION
414
415 001514' 016704 177114      MOV      TBUF,R4      ;POINT TO TRANSMIT BUFFER
416 001520' 005724      TST      (R4)+      ;SKIP FIRST WORD
417 001522' 010324      MOV      R3,(R4)+      ;SET BYTE COUNT TO HOST'S VALUE
418 001524' 005037 021034      CLR      @#CLRFIF      ;CLEAR THE FIFO
419 001530' 005067 177056      CLR      FLG7      ;CLEAR THE FLAG WORD
420 001534' 016737 177072 021032      MOV      RBUF,@#LFRBUF      ;TELL UNA WHERE RECEIVE BUFF IS
421 001542' 016737 177066 021030      MOV      TBUF,@#LTAC      ;TELL UNA WHERE TRANSMIT BUFF IS AND
422                                     ;START THE TRANSMIT OPERATION
423
424 001550' 005005      CLR      R5      ;CLEAR COUNTER
425 001552' 106427 000140      MTPS     #PRI03      ;ALLOW INTERRUPTS
426 001556' 032767 000040 177026 30$:    BIT      #TRNFLG,FLG7      ;IS TRANSMITTER DONE?
427 001564' 001002      BNE      35$      ;YES
428 001566' 005205      INC      R5      ;NO, COUNT TIME
429 001570' 000772      BR       30$
430 001572' 106427 000340      35$:    MTPS     #PRI07      ;DISABLE INTERRUPTS
431
432 001576' 062700 000002      ADD      #2,R0      ;POINT TO HOST'S PCBB+2
433 001602' 005501      ADC      R1
434 001604' 010037 021010      MOV      R0,@#MDMA0
435 001610' 010137 021012      MOV      R1,@#MDMA1
436 001614' 010537 021026      MOV      R5,@#MDMAW0      ;DUMP COUNTER VALUE TO PCBB+2
437
438 001620' 112767 000003 176771      MOVB     #,PCSR1+1      ;TELL HOST WHAT TEST JUST FINISHED
439 001626' 000241      CLC
440 001630' 000207      RTS      PC
441
442 001632' 001634      MICGSZ: :MICGSZ-MICROG+2
443 000001      .END

```


MICROG - MICROCODE MODULE G
MICROG.MAC 10-JAN-83 17:54

MACY11 30(1046) 11-JAN-83 09:54 PAGE 13
CROSS REFERENCE TABLE -- USER SYMBOLS

ROMADR=	040000		125#	126																	
ROMSIZ=	040000		121#	126																	
RXI =	020000		70#																		
SANTIM	000614R	002	233*	269#																	
SANVEC=	000134		95#	165*	166*																
SERI =	100000		68#																		
SIZ1K =	004000		114#	115	116	285	341	395													
SIZ2K =	010000		115#	117	290	346	400														
SIZ3K =	014000		116#																		
SIZ4K =	020000		117#	118	119	120															
SIZ8K =	040000		118#	121	122																
STACK =	001000		99#	136																	
TBLG	000604R	002	207	264#																	
TBUF P	000634R	002	275#	288*	305	311	320	344*	360	366	375	398*	415	421							
TIMINT	000456R	002	164	233#																	
TRNFLG=	000040		108#	261	315	370	426														
TRNINT	000574R	002	169	261#																	
TRNVEC=	000070		93#	170*	171*																
TXI =	010000		71#																		
UNIERR=	020000		112#	228																	
WCSADR=	000000		123#	124																	
WCSSIZ=	020000		119#	124																	
.	001634R	002	141	149	154	159	164	169	174	195	207	230	247	253	264						
			265	266																	
. ABS.	000000	000																			
	000000	001																			
MICRG	001634	002																			

ERRORS DETECTED: 0
DEFAULT GLOBALS GENERATED: 0

MICROG.OBJ,MICROG.LST/CRF/DOC/NL:TOC/SOL=MICROG.MAC
RUN-TIME: .6 .9 .3 SECONDS
RUN-TIME RATIO: 30/2=14.8
(CORE USED: 9K (18 PAGES))

DOCUMENT PAGES: 12

NO MORE MICROCODE MODULES
NOMORE.MAC 20-DEC-82 16:44

MACY11 30(1046) 11-JAN-83 09:55 PAGE 2

SEQ 465

1
2 000000'
3
4 000000'
5 000000'
6 000004'
7 000001

.TITLE NO MORE MICROCODE MODULES
.CSECT NOMORE
.MCALL SVC
SVC
LASTAD
L\$LAST::
.END

NO MORE MICROCODE MODULES
 NOMORE.MAC 20-DEC-82 16:44

MACY11 30(1046) 11-JAN-83 09:55 PAGE 4
 CROSS REFERENCE TABLE -- USER SYMBOLS

ASSEMB=	000010	S
C\$AU	= 000052	S#
C\$AUTO=	000061	S#
C\$BRK	= 000022	S#
C\$BSEG=	000004	S#
C\$BSUB=	000002	S#
C\$CEFG=	000045	S#
C\$CLCK=	000062	S#
C\$CLEA=	000012	S#
C\$CLOS=	000035	S#
C\$CLP1=	000006	S#
C\$CVEC=	000036	S#
C\$DCLN=	000044	S#
C\$DODU=	000051	S#
C\$DRPT=	000024	S#
C\$DU	= 000053	S#
C\$EDIT=	000003	S#
C\$ERDF=	000055	S#
C\$ERHR=	000056	S#
C\$ERRO=	000060	S#
C\$ERSF=	000054	S#
C\$ERSO=	000057	S#
C\$ESCA=	000010	S#
C\$ESEG=	000005	S#
C\$ESUB=	000003	S#
C\$ETST=	000001	S#
C\$EXIT=	000032	S#
C\$GETB=	000026	S#
C\$GETW=	000027	S#
C\$GMAN=	000043	S#
C\$GPHR=	000042	S#
C\$GPLO=	000030	S#
C\$GPRI=	000040	S#
C\$INIT=	000011	S#
C\$INLP=	000020	S#
C\$MANI=	000050	S#
C\$MEM	= 000031	S#
C\$MSG	= 000023	S#
C\$OPEN=	000034	S#
C\$PNTB=	000014	S#
C\$PNTF=	000017	S#
C\$PNTS=	000016	S#
C\$PNTX=	000015	S#
C\$QIO	= 000377	S#
C\$RDBU=	000007	S#
C\$REFG=	000047	S#
C\$RESE=	000033	S#
C\$REVI=	000003	S#
C\$RFLA=	000021	S#
C\$RPT	= 000025	S#
C\$SEFG=	000046	S#
C\$SPRI=	000041	S#
C\$SVEC=	000037	S#
C\$TPRI=	000013	S#
DIAGMC=	000000	S
E\$END	= 002100	S#

NO MORE MICROCODE MODULES
NOMORE.MAC 20-DEC-82 16:44

MACV11 30(1046) 11-JAN-83 09:55 PAGE 5
CROSS REFERENCE TABLE -- USER SYMBOLS

ESLOAD=	000035	5#
FSAU =	000015	5#
FSAUTO=	000020	5#
F\$BGN =	000040	5#
F\$CLEA=	000007	5#
F\$DU =	000016	5#
F\$END =	000041	5#
F\$HARD=	000004	5#
F\$HW =	000013	5#
F\$INIT=	000006	5#
F\$JMP =	000050	5#
F\$MOD =	000000	5#
F\$MSG =	000011	5#
F\$PROT=	000021	5#
F\$PWR =	000017	5#
F\$RPT =	000012	5#
F\$SEG =	000003	5#
F\$SOFT=	000005	5#
F\$SRV =	000010	5#
F\$SUB =	000002	5#
F\$SW =	000014	5#
F\$TEST=	000001	5#
G\$CNTQ=	000200	5#
G\$DELM=	000372	5#
G\$DISP=	000003	5#
G\$EXCP=	000400	5#
G\$HILI=	000002	5#
G\$LOLI=	000001	5#
G\$NO =	000000	5#
G\$OFFS=	000400	5#
G\$OF SI=	000376	5#
G\$PRMA=	000001	5#
G\$PRMD=	000002	5#
G\$PRML=	000000	5#
G\$RADA=	000140	5#
G\$RADB=	000000	5#
G\$RADJ=	000040	5#
G\$RADL=	000120	5#
G\$RADO=	000020	5#
G\$XFER=	000004	5#
G\$YES =	000010	5#
ISAU =	000041	5#
ISAUTO=	000041	5#
ISCLN =	000041	5#
ISDU =	000041	5#
ISINIT=	000041	5#
ISMOD =	000041	5#
ISMSG =	000041	5#
ISPROT=	000041	5#
ISPTAB=	000041	5#
ISPWR =	000041	5#
ISRPT =	000041	5#
ISSEG =	000041	5#
ISSETU=	000041	5#
ISSRV =	000041	5#
ISSUB =	000041	5#

NO MORE MICROCODE MODULES
NOMORE.MAC 20-DEC-82 16:44

MACY11 30(1046) 11-JAN-83 09:55 PAGE 6
CROSS REFERENCE TABLE -- USER SYMBOLS

ISTST = 000041	5#		
J\$JMP = 000167	5#		
L\$LAST 000004RG 002	6#		
OSAPTS= 000000	5#		
OSAU = 000000	5#		
OSBGNR= 000000	5#		
OSBGNS= 000000	5#		
OSDU = 000000	5#		
OSERRT= 000000	5#		
OSGNSW= 000000	5#		
OSPOIN= 000000	5#		
OSSETU= 000000	5#	6	
SVCGBL= 000000	5#	6#	7
SVCINS= 177777	5#	6	
SVCSUB= 177777	5#		
SVCTAG= 177777	5#		
SVCTST= 177777	5#		
S\$LSYM= 010000	5#		
T\$ERRN= 000000	5#		
T\$GMAN= 000000	5#		
T\$LAST= 000001	5#	6#	
T\$LSYM= 010000	5#		
T\$LYNO= 000000	7#		
T\$NEST= 177777	5#		
T\$PTNU= 000000	5#		
T\$SAVL= 177777	5#		
T\$SEGL= 177777	5#		
T\$SUBN= 000000	5#		
T\$TAGL= 177777	5#		
T\$TAGN= 010000	5#		
T\$TEST= 000000	5#	7	
T\$TSTM= 177777	5#		
T\$TSTS= 000000	5#		
X\$ALWA= 000000	5#		
X\$FALS= 000040	5#		
X\$OFFS= 000400	5#		
X\$TRUE= 000020	5#		

NO MORE MICROCODE MODULES
 NOMORE.MAC 20-DEC-82 16:44

MACY11 30(1046) 11-JAN-83 09:55 PAGE 8
 CROSS REFERENCE TABLE -- MACRO NAMES

BCOMPL	1#	5#
BERROR	1#	5#
BGNAU	1#	5#
BGNAUT	1#	5#
BGNCLN	1#	5#
BGNDU	1#	5#
BGNHRD	1#	5#
BGNHW	1#	5#
BGNINI	1#	5#
BGNMOD	1#	5#
BGNMSG	1#	5#
BGNPRO	1#	5#
BGNPTA	1#	5#
BGNRPT	1#	5#
BGNSEG	1#	5#
BGNSET	1#	5#
BGNSFT	1#	5#
BGNSRV	1#	5#
BGNSUB	1#	5#
BGNSW	1#	5#
BGNTST	1#	5#
BNCOMP	1#	5#
BNERRO	1#	5#
BREAK	1#	5#
BRESET	1#	5#
CKLOOP	1#	5#
CLOCK	1#	5#
CLOSE	1#	5#
CLRVEC	1#	5#
COMMEN	1#	5#
DELAY	1#	5#
DESCRI	1#	5#
DEVTYP	1#	5#
DISPAT	1#	5#
DISPLA	1#	5#
DOCLN	1#	5#
DODU	1#	5#
DORPT	1#	5#
ENDAU	1#	5#
ENDAUT	1#	5#
ENDCLN	1#	5#
ENDCOM	1#	5#
ENDDU	1#	5#
ENDHRD	1#	5#
ENDHW	1#	5#
ENDINI	1#	5#
ENDMOD	1#	5#
ENDMSG	1#	5#
ENDPRO	1#	5#
ENDPTA	1#	5#
ENDRPT	1#	5#
ENDSEG	1#	5#
ENDSET	1#	5#
ENDSFT	1#	5#
ENDSRV	1#	5#
ENDSUB	1#	5#

NO MORE MICROCODE MODULES
NOMORE.MAC 20-DFC-82 16:44

MACY11 30(1046) 11-JAN-83 09:55 PAGE 9
CROSS REFERENCE TABLE -- MACPD NAMES

ENDSW	1#	5#
ENDTST	1#	5#
EQUALS	1#	5#
ERRDF	1#	5#
ERRHRD	1#	5#
ERROR	1#	5#
ERRSF	1#	5#
ERRSOF	1#	5#
ERRTBL	1#	5#
ESCAPE	1#	5#
EXIT	1#	5#
FEQUAL	1#	5#
GETBYT	1#	5#
GETPRI	1#	5#
GETWOR	1#	5#
GMANIA	1#	5#
GMANID	1#	5#
GMANII	1#	5#
GPHARD	1#	5#
GPRMA	1#	5#
GPRMD	1#	5#
GPRML	1#	5#
HEADER	1#	5#
INLOOP	1#	5#
IOSETU	1#	5#
IOSTAR	1#	5#
KT11	1#	5#
LASTAD	1#	5#
MANUAL	1#	5#
MEMORY	1#	5#
MSBYTE	1#	5#
MSCHEC	1#	5#
MSCNTO	1#	5#
MSCOUN	1#	5#
MSDATA	1#	5#
MSDECR	1#	5#
MSDEFA	1#	5#
MSENDE	1#	5#
MSERRI	1#	5#
MSESCA	1#	5#
MSESCS	1#	5#
MSXCP	1#	5#
MSEXIT	1#	5#
MSXSE	1#	5#
MSXTJ	1#	5#
MSGEN	1#	5#
MSGENB	1#	5#
MSGETS	1#	5#
MSGETT	1#	5#
MSGNGB	1#	5#
MSGNIN	1#	5#
MSGNLS	1#	5#
MSGNSU	1#	5#
MSGNTA	1#	5#
MSGNTE	1#	5#
MSHAPT	1#	5#

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NO MORE MICROCODE MODULES
NOMORE.MAC 20-DEC-82 16:44

MACV11 30(1046) 11-JAN-83 09:55 PAGE 10
CROSS REFERENCE TABLE -- MACRO NAMES

M\$HNAP	1#	5#
M\$INCR	1#	5#
M\$IOSE	1#	5#
M\$LDRO	1#	5#
M\$MASK	1#	5#
M\$MCHI	1#	5#
M\$MCLO	1#	5#
M\$MSK1	1#	5#
M\$POP	1#	5#
M\$PRIN	1#	5#
M\$PUSH	1#	5#
M\$PUT	1#	5#
M\$PUT1	1#	5#
M\$RADI	1#	5#
M\$RBRO	1#	5#
M\$RNRO	1#	5#
M\$SETS	1#	5#
M\$STAR	1#	5#
M\$SVC	1#	5#
M\$TLAB	1#	5#
M\$STL	1#	5#
M\$WORD	1#	5#
M\$XFER	1#	5#
OPEN	1#	5#
POINTE	1#	5#
PRINTB	1#	5#
PRINTF	1#	5#
PRINTS	1#	5#
PRINTX	1#	5#
READBU	1#	5#
REDEF	1#	5#
RFLAGS	1#	5#
SETPRI	1#	5#
SETVEC	1#	5#
SLASH	1#	5#
STARS	1#	5#
SVC	1#	3#
XFER	1#	5#
XFERF	1#	5#
XFERT	1#	5#

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. ABS.	000000	000
	000000	001
NOMORE	000004	002

ERRORS DETECTED: 0
DEFAULT GLOBALS GENERATED: 0

NOMORE.OBJ,NOMORE.LST/CRF/DOC/NL:TOC/SOL SVC34R.P11,NOMORE.MAC
RUN-TIME: 2 2 .4 SECONDS
RUN-TIME RATIO: 56/5=11.0
CORE USED: 17K (34 PAGES)

DOCUMENT PAGES: 7

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NO MORE MICROCODE MODULES
NOMORE.MAC 20-DEC-82 16:44

MACV11 30(1046) 11-JAN-83 09:55 PAGE 11
CROSS REFERENCE TABLE -- MACRO NAMES

LNKX11 V023 12-JAN-83 1:15

#CZUAAA.BIC/B:2000,CZUAAA.MAP=CZUAAA,MICROA,MICROB,MICROC,MICROD,MICROE,MICROF,MICROG,NOMORE/E

LOAD MAP

TRANSFER ADDRESS: 000001

LOW LIMIT: 002000

HIGH LIMIT: 100772

MODULE	PARAME	ADDRESS	SIZE
SECTION	ENTRY		
<. ABS.>		000000	000000
ADR		000020	
ADRERR		000001	
BIT0		000001	
BIT00		000001	
BIT01		000002	
BIT02		000004	
BIT03		000010	
BIT04		000020	
BIT05		000040	
BIT06		000100	
BIT07		000200	
BIT08		000400	
BIT09		001000	
BIT1		000002	
BIT10		002000	
BIT11		004000	
BIT12		010000	
BIT13		020000	
BIT14		040000	
BIT15		100000	
BIT2		000004	
BIT3		000010	
BIT4		000020	
BIT5		000040	
BIT6		000100	
BIT7		000200	
BIT8		000400	
BIT9		001000	
BOE		000400	
CLEAR		000000	
CRCSIZ		000004	
DATERR		000000	
DIM		000020	
DNI		004000	
EF.CON		000036	
EF.NEW		000035	
EF.PWR		000034	
EF.RES		000037	
EF.STA		000040	
ENP		000400	
ERRS		040000	
EVL		000004	
FAT1		000400	
GETCMD		000002	

GETPCB	000001
HOE	100000
IBE	010000
ICAB	040000
IDU	000040
IE	000100
IER	020000
INITM	177777
INITL	177777
INTE	000100
INTR	000200
IOADR	020000
IOSIZ	020000
ISR	000100
IXE	004000
LASM	000001
LIM	000021
LINADR	100000
LINSIZ	077774
LOE	040000
LOT	000010
MAXBYT	002756
MINBYT	000100
NIHLT	000006
NIUNI	000007
NPRERR	100000
NXMERR	040000
OWN	100000
PARERR	010000
PCEI	040000
PCTO	000200
PFNOP1	000000
PFNOP2	000005
PNOP	000006
PNT	001000
POLYM	166670
POLYL	101440
POLY16	120001
PRI	002000
PRILD	000001
PRI00	000000
PRI01	000040
PRI02	000100
PRI03	000140
PRI04	000200
PRI05	000240
PRI06	000300
PRI07	000340
PSTATE	000007
RACC	000013
RAC S	000017
RC	000012
RCEI	002000
RDPA	000002
READY	000002
RESET	000000
RLSA	000024
RMAL	000006
RMT(000010

ROMADR	040000	
ROMSIZ	040000	
RPA	000004	
RPS	000016	
RRF	000010	
RSET	000040	
RSIDP	000022	
RTM	000014	
RUN	000003	
RXI	020000	
SECOND	000077	
SERI	100000	
SET	000001	
SFT	037400	
SFTB0	000400	
SFTB1	001000	
SFTB2	002000	
SFTB3	004000	
SFTB4	010000	
SFTB5	020000	
SIZ1K	004000	
SIZ2K	010000	
SIZ4K	020000	
SIZ8K	040000	
SLFT	000003	
STP	001000	
TXI	010000	
UAM	000200	
UNIERR	020000	
UNIHLT	000005	
WCSADR	000000	
WCSSIZ	020000	
WLSA	000025	
WMAL	000007	
WPA	000005	
WRF	000011	
WSIDP	000023	
WTM	000015	
XPWR	100000	
<UNAREP>	002000	000000
<UNAREP>	002000	053774
BITNAM	002310	
BITNUM	002306	
BITSTA	002312	
BNAMT0	002356	
BNAMT1	002416	
BNAMT2	002456	
BTMSG	054764	
BTMSG0	055243	
BTMSG1	055301	
BTMSG2	055334	
BTMSG3	055400	
BTBL	055104	
CHKDN1	021330	
CHKINT	022050	
CHKMON	021776	
CLKBR	002276	
CLKCSR	002274	
CLKFRE	002302	

CLKSRV	023674
CLKTAB	002274
CLKVEC	002300
CLRDNI	021374
CNTTAB	002624
COLTST	006046
CPUPRI	002674
CRCDAT	005621
CRCERR	005656
CRCPAT	005704
CRC16	021042
CRC32	020722
CSRNUM	002304
DATAID	004067
DBFRAM	004624
DEFADR	055033
DEFHDR	054770
DFPTBL	002262
DMABLK	004501
DMAFRM	004431
DMATO	004363
DPA	054750
DPPAT	004720
DWFRAM	004662
ERRINT	002666
FIFTST	005166
FORM1	006234
FORM10	007010
FORM11	007074
FORM12	007123
FORM13	007216
FORM15	007267
FORM16	007343
FORM17	007421
FORM18	007504
FORM19	007562
FORM2	006271
FORM20	007637
FORM21	007677
FORM22	007751
FORM23	010031
FORM24	010101
FORM25	010133
FORM26	010206
FORM27	010261
FORM28	010331
FORM29	010336
FORM3	006327
FORM30	010361
FORM31	010411
FORM32	010437
FORM33	010534
FORM34	010572
FORM35	010655
FORM36	010755
FORM37	011013
FORM38	011124
FORM39	011215
FORM4	006376

FORM40	011256
FORM41	011341
FORM42	011407
FORM43	011416
FORM44	011424
FORM45	011503
FORM46	011531
FORM47	011560
FORM48	011607
FORM49	011704
FORM5	006445
FORM50	011760
FORM51	012047
FORM52	012101
FORM53	012137
FORM54	012211
FORM55	012237
FORM56	012310
FORM57	012363
FORM58	012433
FORM59	012511
FORM6	006510
FORM60	012604
FORM61	012671
FORM62	012757
FORM63	013027
FORM64	013066
FORM65	013137
FORM66	013165
FORM67	013252
FORM68	013336
FORM69	013433
FORM7	006566
FORM70	013551
FORM71	013613
FORM72	013655
FORM73	013727
FORM74	013772
FORM75	014047
FORM76	014115
FORM77	014151
FORM78	014225
FORM79	014265
FORM8	006650
FORM80	014334
FORM81	014413
FORM82	014456
FORM9	006732
FREMEM	002324
FRESIZ	002322
FRSTIM	002672
HAFDUP	006016
HEXDAT	054766
HEXDPA	021130
HEXM	021212
HEXL	021250
HEXTBL	055057
HEXVAL	054767
INTBIT	004251

INTVEC	004214
LASFT	004121
LNKARB	005334
LNKBYT	005022
LNKMEM	004333
LPMSG	054762
LPMSG0	055146
LPMSG1	055205
LPTBL	055100
LSACP	002110
LSAPT	002036
LSAU	023640
LSAUT	002070
LSAUTO	023470
LSCCP	002106
LSCLEA	023472
LSCO	002032
LSDEPO	002011
LSDESC	002704
LSDESP	002076
LSDEVP	002060
SDISP	002124
SDLY	002116
SDTP	002040
SDTYP	002034
SDU	023632
SDUT	002072
SDVTY	002676
LSEF	002052
SENV	002044
SETP	002102
SEXP1	002046
SEXP4	002064
SEXP5	002066
SHARD	055464
SHIME	002120
SHPCP	002016
SHPTP	002022
SHW	002262
SICP	002104
SINIT	023124
SLADP	002026
SLOAD	002100
SLUN	002074
SMREV	002050
SNAME	002000
SPR!O	002042
SPROT	023116
SPRT	002112
SREPP	002062
SREV	002010
SRPT	023110
SSPC	002056
SSPCP	002020
SSPTP	002024
SSTA	002030
STEST	002114
STIML	002014
SUNIT	002012

MAXCNT	005122
METER	002332
MICMOD	002320
MICRO	002326
MSG1	014750
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MSG11	015414
MSG12	015500
MSG13	015524
MSG14	015572
MSG15	015620
MSG16	015650
MSG17	015742
MSG18	016034
MSG19	016064
MSG2	015012
MSG20	016160
MSG21	016250
MSG22	016340
MSG23	016454
MSG24	016476
MSG25	016626
MSG26	017014
MSG27	017060
MSG28	017126
MSG29	017174
MSG3	015056
MSG30	017276
MSG31	017344
MSG32	017510
MSG33	017606
MSG34	017634
MSG35	017704
MSG36	020002
MSG37	020024
MSG38	020046
MSG39	020070
MSG4	015120
MSG40	020112
MSG41	020210
MSG42	020262
MSG43	020432
MSG44	020454
MSG45	020616
MSG46	020700
MSG5	015142
MSG6	015212
MSG7	015300
MSG8	015322
MSG9	015344
MUCAST	005563
NEXMEM	002664
NOCLK	003566
NOPERR	004001
ODDBYT	005060
PATERN	002516
PAT1	002516
PAT2	002520
PAT3	002522

PAT4	002524
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PAT6	002530
PCBB	002604
PCOMND	002316
PCSRO	002336
PCSROC	002346
PCSR1	002340
PCSR1C	002350
PCSR2	002342
PCSR2C	002352
PCSR3	002344
PCSR3C	002354
PRTPAR	006154
PWHEN	002314
RACERR	003663
RACMG1	014546
RACMG2	014574
RACMG3	014632
RACMG4	014704
RACMG7	014726
RBRRUN	005743
RCVDON	004572
RETLOG	006124
REUNA	022104
RLNKAD	005207
ROMDMP	004042
RREV	054756
RRWER	003741
RSETER	003717
SLFTST	004021
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SMSG74	031251
SMSG75	031263
SMSG76	031275
SMSG77	031307
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SPAT5	002562
SPAT6	002570
SPAT7	002576
STAMUX	004756
STAPAT	005400
STAPOS	005512
STARFJ	005444
STMSG	026202
STIBL	026204
SWADDR	002334
SWHDR	055114
SWPACK	054760
TDRCNT	006074
TIMOFF	021314
TIMON	021276
TIMTST	004311
TLNKAD	005260
TRAP4	023646
TRNDON	004540

T1	023710
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T13	033214
T14	033632
T15	034122
T16	034630
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T18	035614
T19	036172
T2	024122
T20	036552
T21	037100
T22	037410
T23	037720
T24	040300
T25	040660
T26	041254
T27	042016
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T29	043016
T3	024454
T30	043336
T31	043726
T32	044312
T33	044676
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T41	050410
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T43	051446
T44	052536
T45	053260
T46	053674
T5	024704
T6	025366
T7	025450
T8	025546
T9	025770
UDBB	002614
UNACSR	002266
UNAI NT	002670
UNAPRI	002272
UNASRV	023656
UNAVEC	002270
UNIT	002330
UNLOD	003515
WCSMEM	004165
\$ADRER	003457
\$AFTER	003340
\$BEFOR	003331
\$BITO	003267

\$BIT1	003261	
\$BIT10	003172	
\$BIT11	003163	
\$BIT12	003154	
\$BIT13	003145	
\$BIT14	003136	
\$BIT15	003127	
\$BIT2	003253	
\$BIT3	003245	
\$BIT4	003237	
\$BIT5	003231	
\$BIT6	003223	
\$BIT7	003215	
\$BIT8	003207	
\$BIT9	003201	
\$CLR	003323	
\$DATER	003444	
\$DNI	002776	
\$FATI	003017	
\$GTCMD	003355	
\$GTPCB	003346	
\$ICAB	003074	
\$INTE	003041	
\$INTR	003030	
\$NCLR	003311	
\$NOP	003403	
\$NSET	003275	
\$PARER	003500	
\$PATCH	055574	
\$PCEI	002745	
\$PCTO	003105	
\$PDNDM	003415	
\$RCEI	003006	
\$RESET	003436	
\$RMTC	003116	
\$RSET	003052	
\$RXI	002756	
\$SERI	002734	
\$SET	003305	
\$SLFT	003371	
\$STOP	003431	
\$STRT	003407	
\$TXI	002766	
\$XPWR	003063	
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MICROA	055774	
<MICRB >	056020	002314
MICBSZ	060332	
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<MICRC >	060334	003254
MICCSZ	063606	
MICCC7	063422	
MICROC	060334	
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MICDSZ	070514	
MICROD	063610	
<MICRE >	070516	002734
BFILE	073046	

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MICESZ 073450
MICROE 070516
<MICRF > 073452 003460
MICFSZ 077130
MICPOF 073452
<MICRG > 077132 001634
MICGSZ 100764
MICROG 077132
<NOMORE> 100766 000004
LSLAST 100772

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```

*****
MODULE MICROA
SECTION ENTRY ADDRESS SIZE
< > 100772 000000

```

```

*****
MODULE MICROB
SECTION ENTRY ADDRESS SIZE
< > 100772 000000

```

```

*****
MODULE MICROC
SECTION ENTRY ADDRESS SIZE
< > 100772 000000

```

```

*****
MODULE MICROD
SECTION ENTRY ADDRESS SIZE
< > 100772 000000

```

```

*****
MODULE MICROE
SECTION ENTRY ADDRESS SIZE
< > 100772 000000

```

```

*****
MODULE MICROF
SECTION ENTRY ADDRESS SIZE
< > 100772 000000

```

```

*****
MODULE MICROG
SECTION ENTRY ADDRESS SIZE
< > 100772 000000

```

```

*****
MODULE NO
SECTION ENTRY ADDRESS SIZE
< > 100772 000000

```

```

RUN-TIME 5 SECONDS
5K CORE USED

```