

TEST

TEST 8 MOVE
MD-11-DOH-A

EP-DOH-A-DL-A
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This microfiche card contains a grid of frames. The first column on the left contains frames with text, likely serving as a table of contents or index. The remaining columns contain frames with data, which appears to be organized in a grid format. The data is too small to read clearly but seems to consist of multiple columns of text or numbers.

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IDENTIFICATION

PRODUCT CODE: MAINDEC-11-DOAA TO DOLA-D
 PRODUCT NAME: FIRST LEVEL 1-12
 DATE CREATED: 17 MARCH 1970
 MAINTAINER: DIAGNOSTIC GROUP
 AUTHOR: JOHN HITTELL

THIS GROUP OF TESTS CONSIST OF:

PROCESSOR TEST	MAINDEC
TEST 1	BRANCH 11-DOAA
2	CON BRANCH 11-DOBA
3	UNARY 11-DOCA
4	UNARY AND BINARYS 11-DODA
5	ROTATE/SHIFT 11-DOEA
6	COMPARE 11-DOFA
7	COMPARE NOT 11-DOGA
8	MOVE 11-DOHA
9	BIS,BIC AND BIT 11-DOIA
10	ADD 11-DOJA
11	SUBTRACT 11-DOKA
12	JMP 11-DOLA

11-DOAA 11-DOBA 11-DOCA 11-DODA 11-DOEA 11-DOFA 11-DOGA 11-DOHA 11-DOIA 11-DOJA 11-DOKA 11-DOLA

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- 5. OPERATING PROCEDURE
 - 5.1 OPERATIONAL SWITCH SETTINGS
 - 5.1.1 NO SWITCHES ARE USED
 - 5.1.3
 - 5.2 SUBROUTINE ABSTRACTS
 - 5.2.1 BEGIN SA 200
 - 5.2.2 SCOPE
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 - IS A "NOP" THAT IS PLACED BETWEEN EACH SUBTEST IN THE INSTRUCTION SECTION. IF A SCOPE LOOP IS NEEDED, INSERT A BRANCH TO THE PREVIOUS SCOPE LOCATION AT THE CURRENT SCOPE LOCATION. (NOTE NOP=240)
 - 5.2.3 HLT
 -
 - INDICATES THE UNIQUE ADDRESS THAT TAGS THE FAILING SUBTEST. THE INCORRECT DATA AT THE TIME OF THE FAILURE MAY OR MAY NOT BE DISPLAYED IN REGISTER ZERO, WHICH IS THE DATA REGISTER ON A HALT.
 - 5.3 PROGRAM AND/OR OPERATOR ACTION
 - 5.3.1 LOADING AND STARTING AT 200 STARTS THE TEST. IF AN ERROR IS DETECTED, THERE WILL BE A HALT. WHEN A HALT OCCURS AND IT IS NECESSARY TO SCOPE ON IT, PLACE INSERT A BRANCH INSTRUCTION IN THE SCOPE LOCATION FOLLOWING THE HALT. THE BRANCH INSTRUCTION SHOULD BRANCH YOU TO THE PREVIOUS SCOPE LOCATION.
- 6. ERRORS
 - 6.1 ALL ERRORS WILL CAUSE A HALT.
 - 6.2 ERROR RECOVERY
 - ON TRAP ERRORS - RESTART AT 200.
 - DEPRESS CONTINUE TO CONTINUE TEST
- 7. RESTRICTIONS
 - 7.1 STARTING RESTRICTION
 - NONE

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7.2 OPERATIONAL RESTRICTION
NONE

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8. MISCELLANEOUS

IF A HALT OCCURES IN THE TRAP OR INTERRUPT VECTOR AREA, REGISTER SIX SHOULD BE EXAMINED TO DETERMINE ITS CONTENTS, THEN USE REGISTER SIX CONTENTS AS AN ADDRESS TO DETERMINE THE LOCATION THE PROGRAM WAS AT, WHEN THE INTERRUPT OR TRAP OCCURRED. (MEMORY AS SPECIFIED BY R6 CONTAINS THE PC OF THE INSTRUCTION FOLLOWING THE INSTRUCTION WHERE THE TRAP OCCURED)

8.1 EXECUTION TIME

TIME BETWEEN BELLS

FOR TAPE 1	5	MINUTES
FOR TAPE 2	2.5	MINUTES
FOR TAPE 3	3	MINUTES
FOR TAPE 4	6	MINUTES
FOR TAPE 5	5	MINUTES
FOR TAPE 6	6	MINUTES
FOR TAPE 7	5	MINUTES
FOR TAPE 8	6	MINUTES
FOR TAPE 9	3	MINUTES
FOR TAPE 10	4	MINUTES
FOR TAPE 11	2	MINUTES
FOR TAPE 12	4	MINUTES

9. PROGRAM DESCRIPTION

- TEST 1 THIS IS A TEST OF THE UNCONDITIONAL BRANCH INSTRUCTION. IT PROVES THAT A LONG PROGRAM CAN BE LOADED AND THE INSTRUCTIONS CAN BE EXECUTED THROUGHOUT MEMORY.
- TEST 2 THIS IS A CONDITIONAL BRANCH TEST USING ALL OF THE CONDITIONAL BRANCH INSTRUCTIONS. IT TESTS THAT ALL COMBINATIONS OF CONDITION CODES ARE RESPONDED TO CORRECTLY. (THE CONDITION CODES ARE MANIPULATED WITH THE MICRO-PROGRAM OPERATIONS.)
- TEST 3 THIS IS A TEST OF THE UNARY INSTRUCTIONS AND THEIR EFFECTS UPON THE CONDITION CODES ONLY. MOSTLY REGISTER OPERATIONS.
- TEST 4 THIS IS A COMBINED USE OF UNARY AND BINARY INSTRUCTIONS TESTING THE RESULTS AND THE CONDITION CODES.
- TEST 5 THIS TEST ROTATE AND SHIFT INSTRUCTION, BOTH THE REGISTERS AND MEMORY LOCATIONS, WORD AND BYTE, ARE TESTED.
- TEST 6 THIS IS A TEST OF THE COMPARE INSTRUCTIONS FOR

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EQUALITY. ALL CONDITIONAL BRANCHES THAT WILL
BRANCH ON ANY FORM OF EQUALITY ARE ALSO TESTED.
BOTH WORD AND BYTE OPERATIONS ARE USED.

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TEST 7 COMPARE NOT. THIS IS A TEST OF THE COMPARE INSTRUCTIONS WHERE NO VALUES ARE EQUAL. HERE BRANCHES FOR NOT EQUAL, GREATER THAN AND LESS THAN, ETC., ARE TESTED.

TEST 8 CHECKS THE MOVE INSTRUCTION BOTH TO MEMORY AND REGISTERS.

TEST 9 CHECK THE BIS, BIC AND BIT INSTRUCTION BOTH TO MEMORY AND TO REGISTERS.

TEST 10 TESTS THE ADD INSTRUCTION.

TEST 11 TESTS THE SUBTRACT INSTRUCTION.

TEST 12 TEST OF THE JUMP INSTRUCTION IN MOST MODES.

NOTE THAT STACK OPERATION AND INSTRUCTIONS THAT USE THEM ARE NOT TESTED IN THIS GROUP OF TEST.

- 10. LISTING
- 11. FLOW CHART(S)

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:TEST 9
:TEST MOVE INSTRUCTION PDP11
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000200
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000240

. =200
HLT=HALT
SCOPE=240

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262           ;TEST MOVE INSTRUCTION
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265 000200 012700 000000      MOV      #0,%0      ;MOVE 0 TO REGISTER ZERO
266 000204 022700 000000      CMP      #0,%0      ;COMPARE REGISTER ZERO FOR 0
267 000210 001401              BEQ      .+4         ;TEST RESULTS OF MOV
268 000212 000000              HLT                      ;MOV INSTRUCTION FAILED
269 000214 012700 177777      MOV      #-1,%0     ;MOVE -1 TO REGISTER ZERO
270 000220 022700 177777      CMP      #-1,%0     ;COMPARE REGISTER ZERO FOR -1
271 000224 001401              BEQ      .+4         ;TEST RESULTS OF MOV
272 000226 000000              HLT                      ;MOV INSTRUCTION FAILED
273 000230 000240              SCOPE
274 000232 112700 000000      MOVVB   #0,%0      ;MOVE .BYTE 0 TO REGISTER ZERO
275 000236 122700 000000      CMPVB   #0,%0      ;COMPARE REGISTER ZERO FOR N
276 000242 001401              BEQ      .+4         ;TEST RESULTS OF MOV
277 000244 000000              HLT                      ;MOVVB INSTRUCTION FAILED
278 000246 000240              SCOPE
279 000250 112700 177777      MOVVB   #-1,%0     ;MOVE .BYTE -1 TO REGISTER ZERO
280 000254 122700 177777      CMPVB   #-1,%0     ;COMPARE REGISTER ZERO FOR N
281 000260 001401              BEQ      .+4         ;TEST RESULTS OF MOV
282 000262 000000              HLT                      ;MOVVB INSTRUCTION FAILED
283 000264 012700 125252      MOV      #125252,%0 ;MOVE 125252 TO REGISTER ZERO
284 000270 022700 125252      CMP      #125252,%0 ;COMPARE REGISTER ZERO FOR 125252
285 000274 001401              BEQ      .+4         ;TEST RESULTS OF MOV
286 000276 000000              HLT                      ;MOV INSTRUCTION FAILED
287 000300 012700 052525      MOV      #52525,%0  ;MOVE 52525 TO REGISTER ZERO
288 000304 022700 052525      CMP      #52525,%0  ;COMPARE REGISTER ZERO FOR 52525
289 000310 001401              BEQ      .+4         ;TEST RESULTS OF MOV
290 000312 000000              HLT                      ;MOV INSTRUCTION FAILED
291 000314 000240              SCOPE
292 000316 112700 125252      MOVVB   #125252,%0 ;MOVE .BYTE 125252 TO REGISTER ZERO
293 000322 122700 125252      CMPVB   #125252,%0 ;COMPARE REGISTER ZERO FOR N
294 000326 001401              BEQ      .+4         ;TEST RESULTS OF MOV
295 000330 000000              HLT                      ;MOVVB INSTRUCTION FAILED
296 000332 000240              SCOPE
297 000334 112700 052525      MOVVB   #52525,%0  ;MOVE .BYTE 52525 TO REGISTER ZERO
298 000340 122700 052525      CMPVB   #52525,%0  ;COMPARE REGISTER ZERO FOR N
299 000344 001401              BEQ      .+4         ;TEST RESULTS OF MOV
300 000346 000000              HLT                      ;MOVVB INSTRUCTION FAILED
301 000350 000001              N=1
302 000350 012700 000001      MOV      #N,%0     ;MOVE N TO REGISTER ZERO
303 000354 022700 000001      CMP      #N,%0     ;COMPARE REGISTER ZERO FOR N
304 000360 001401              BEQ      .+4         ;TEST RESULTS OF MOV
305 000362 000000              HLT                      ;MOV INSTRUCTION FAILED
306 000364 000002              N=N+N
307 000364 012700 000002      MOV      #N,%0     ;MOVE N TO REGISTER ZERO
308 000370 022700 000002      CMP      #N,%0     ;COMPARE REGISTER ZERO FOR N
309 000374 001401              BEQ      .+4         ;TEST RESULTS OF MOV
310 000376 000000              HLT                      ;MOV INSTRUCTION FAILED
311 000378 000004              N=N+N
312 000400 012700 000004      MOV      #N,%0     ;MOVE N TO REGISTER ZERO
313 000404 022700 000004      CMP      #N,%0     ;COMPARE REGISTER ZERO FOR N
314 000410 001401              BEQ      .+4         ;TEST RESULTS OF MOV
315 000412 000000              HLT                      ;MOV INSTRUCTION FAILED
316 000414 000010              N=N+N
  
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317	000414	012700	000010	MOV	#N,%0	; MOVE N TO REGISTER ZERO
318	000420	022700	000010	CMP	#N,%0	; COMPARE REGISTER ZERO FOR N
319	000424	001401		BEQ	+.4	; TEST RESULTS OF MOV
320	000426	000000		HLT		; MOV INSTRUCTION FAILED
321		000020		N=N+N		
322	000430	012700	000020	MOV	#N,%0	; MOVE N TO REGISTER ZERO
323	000434	022700	000020	CMP	#N,%0	; COMPARE REGISTER ZERO FOR N
324	000440	001401		BEQ	+.4	; TEST RESULTS OF MOV
325	000442	000000		HLT		; MOV INSTRUCTION FAILED
326		000040		N=N+N		
327	000444	012700	000040	MOV	#N,%0	; MOVE N TO REGISTER ZERO
328	000450	022700	000040	CMP	#N,%0	; COMPARE REGISTER ZERO FOR N
329	000454	001401		BEQ	+.4	; TEST RESULTS OF MOV
330	000456	000000		HLT		; MOV INSTRUCTION FAILED
331		000100		N=N+N		
332	000460	012700	000100	MOV	#N,%0	; MOVE N TO REGISTER ZERO
333	000464	022700	000100	CMP	#N,%0	; COMPARE REGISTER ZERO FOR N
334	000470	001401		BEQ	+.4	; TEST RESULTS OF MOV
335	000472	000000		HLT		; MOV INSTRUCTION FAILED
336		000200		N=N+N		
337	000474	012700	000200	MOV	#N,%0	; MOVE N TO REGISTER ZERO
338	000500	022700	000200	CMP	#N,%0	; COMPARE REGISTER ZERO FOR N
339	000504	001401		BEQ	+.4	; TEST RESULTS OF MOV
340	000506	000000		HLT		; MOV INSTRUCTION FAILED
341		000400		N=N+N		
342	000510	012700	000400	MOV	#N,%0	; MOVE N TO REGISTER ZERO
343	000514	022700	000400	CMP	#N,%0	; COMPARE REGISTER ZERO FOR N
344	000520	001401		BEQ	+.4	; TEST RESULTS OF MOV
345	000522	000000		HLT		; MOV INSTRUCTION FAILED
346		001000		N=N+N		
347	000524	012700	001000	MOV	#N,%0	; MOVE N TO REGISTER ZERO
348	000530	022700	001000	CMP	#N,%0	; COMPARE REGISTER ZERO FOR N
349	000534	001401		BEQ	+.4	; TEST RESULTS OF MOV
350	000536	000000		HLT		; MOV INSTRUCTION FAILED
351		002000		N=N+N		
352	000540	012700	002000	MOV	#N,%0	; MOVE N TO REGISTER ZERO
353	000544	022700	002000	CMP	#N,%0	; COMPARE REGISTER ZERO FOR N
354	000550	001401		BEQ	+.4	; TEST RESULTS OF MOV
355	000552	000000		HLT		; MOV INSTRUCTION FAILED
356		004000		N=N+N		
357	000554	012700	004000	MOV	#N,%0	; MOVE N TO REGISTER ZERO
358	000560	022700	004000	CMP	#N,%0	; COMPARE REGISTER ZERO FOR N
359	000564	001401		BEQ	+.4	; TEST RESULTS OF MOV
360	000566	000000		HLT		; MOV INSTRUCTION FAILED
361		010000		N=N+N		
362	000570	012700	010000	MOV	#N,%0	; MOVE N TO REGISTER ZERO
363	000574	022700	010000	CMP	#N,%0	; COMPARE REGISTER ZERO FOR N
364	000600	001401		BEQ	+.4	; TEST RESULTS OF MOV
365	000602	000000		HLT		; MOV INSTRUCTION FAILED
366		020000		N=N+N		
367	000604	012700	020000	MOV	#N,%0	; MOVE N TO REGISTER ZERO
368	000610	022700	020000	CMP	#N,%0	; COMPARE REGISTER ZERO FOR N
369	000614	001401		BEQ	+.4	; TEST RESULTS OF MOV
370	000616	000000		HLT		; MOV INSTRUCTION FAILED
371		040000		N=N+N		
372	000620	012700	040000	MOV	#N,%0	; MOVE N TO REGISTER ZERO

373	000624	022700	040000	CMP	#N,%0	;COMPARE REGISTER ZERO FOR N
374	000630	001401		BEQ	+.4	;TEST RESULTS OF MOV
375	000632	000000		HLT		;MOV INSTRUCTION FAILED
376		100000		N=N+N		
377	000634	012700	100000	MOV	#N,%0	;MOVE N TO REGISTER ZERO
378	000640	022700	100000	CMP	#N,%0	;COMPARE REGISTER ZERO FOR N
379	000644	001401		BEQ	+.4	;TEST RESULTS OF MOV
380	000646	000000		HLT		;MOV INSTRUCTION FAILED
381		000000		N=N+N		
382		000001		N=1		
383	000650	000240		SCOPE		
384	000652	112700	000001	MOVB	#N,%0	;MOVE .BYTE N TO REGISTER ZERO
385	000656	122700	000001	CMPB	#N,%0	;COMPARE REGISTER ZERO FOR N
386	000662	001401		BEQ	+.4	;TEST RESULTS OF MOV
387	000664	000000		HLT		;MOVB INSTRUCTION FAILED
388		000002		N=N+N		
389	000666	000240		SCOPE		
390	000670	112700	000002	MOVB	#N,%0	;MOVE .BYTE N TO REGISTER ZERO
391	000674	122700	000002	CMPB	#N,%0	;COMPARE REGISTER ZERO FOR N
392	000700	001401		BEQ	+.4	;TEST RESULTS OF MOV
393	000702	000000		HLT		;MOVB INSTRUCTION FAILED
394		000004		N=N+N		
395	000704	000240		SCOPE		
396	000706	112700	000004	MOVB	#N,%0	;MOVE .BYTE N TO REGISTER ZERO
397	000712	122700	000004	CMPB	#N,%0	;COMPARE REGISTER ZERO FOR N
398	000716	001401		BEQ	+.4	;TEST RESULTS OF MOV
399	000720	000000		HLT		;MOVB INSTRUCTION FAILED
400		000010		N=N+N		
401	000722	000240		SCOPE		
402	000724	112700	000010	MOVB	#N,%0	;MOVE .BYTE N TO REGISTER ZERO
403	000730	122700	000010	CMPB	#N,%0	;COMPARE REGISTER ZERO FOR N
404	000734	001401		BEQ	+.4	;TEST RESULTS OF MOV
405	000736	000000		HLT		;MOVB INSTRUCTION FAILED
406		000020		N=N+N		
407	000740	000240		SCOPE		
408	000742	112700	000020	MOVB	#N,%0	;MOVE .BYTE N TO REGISTER ZERO
409	000746	122700	000020	CMPB	#N,%0	;COMPARE REGISTER ZERO FOR N
410	000752	001401		BEQ	+.4	;TEST RESULTS OF MOV
411	000754	000000		HLT		;MOVB INSTRUCTION FAILED
412		000040		N=N+N		
413	000756	000240		SCOPE		
414	000760	112700	000040	MOVB	#N,%0	;MOVE .BYTE N TO REGISTER ZERO
415	000764	122700	000040	CMPB	#N,%0	;COMPARE REGISTER ZERO FOR N
416	000770	001401		BEQ	+.4	;TEST RESULTS OF MOV
417	000772	000000		HLT		;MOVB INSTRUCTION FAILED
418		000100		N=N+N		
419	000774	000240		SCOPE		
420	000776	112700	000100	MOVB	#N,%0	;MOVE .BYTE N TO REGISTER ZERO
421	001002	122700	000100	CMPB	#N,%0	;COMPARE REGISTER ZERO FOR N
422	001006	001401		BEQ	+.4	;TEST RESULTS OF MOV
423	001010	000000		HLT		;MOVB INSTRUCTION FAILED
424		000200		N=N+N		
425	001012	000240		SCOPE		
426	001014	112700	000200	MOVB	#N,%0	;MOVE .BYTE N TO REGISTER ZERO
427	001020	122700	000200	CMPB	#N,%0	;COMPARE REGISTER ZERO FOR N
428	001024	001401		BEQ	+.4	;TEST RESULTS OF MOV

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429	001026	000000		HLT		;MOVB INSTRUCTION FAILED
430		000400		N=N+N		
431		000000		N=0		
432	001030	000240		SCOPE		
433	001032	112700	000000	MOVB	#0,%0	;MOVE .BYTE 0 TO REGISTER ZERO
434	001036	122700	000000	CMPB	#0,%0	;COMPARE REGISTER ZERO FOR N
435	001042	001401		BEQ	+.4	;TEST RESULTS OF MOV
436	001044	000000		HLT		;MOVB INSTRUCTION FAILED
437		000001		N=N+1		
438	001046	000240		SCOPE		
439	001050	112700	000001	MOVB	#1,%0	;MOVE .BYTE 1 TO REGISTER ZERO
440	001054	122700	000001	CMPB	#1,%0	;COMPARE REGISTER ZERO FOR N
441	001060	001401		BEQ	+.4	;TEST RESULTS OF MOV
442	001062	000000		HLT		;MOVB INSTRUCTION FAILED
443		000002		N=N+1		
444	001064	000240		SCOPE		
445	001066	112700	000002	MOVB	#2,%0	;MOVE .BYTE 2 TO REGISTER ZERO
446	001072	122700	000002	CMPB	#2,%0	;COMPARE REGISTER ZERO FOR N
447	001076	001401		BEQ	+.4	;TEST RESULTS OF MOV
448	001100	000000		HLT		;MOVB INSTRUCTION FAILED
449		000003		N=N+1		
450	001102	000240		SCOPE		
451	001104	112700	000003	MOVB	#3,%0	;MOVE .BYTE 3 TO REGISTER ZERO
452	001110	122700	000003	CMPB	#3,%0	;COMPARE REGISTER ZERO FOR N
453	001114	001401		BEQ	+.4	;TEST RESULTS OF MOV
454	001116	000000		HLT		;MOVB INSTRUCTION FAILED
455		000004		N=N+1		
456	001120	000240		SCOPE		
457	001122	112700	000004	MOVB	#4,%0	;MOVE .BYTE 4 TO REGISTER ZERO
458	001126	122700	000004	CMPB	#4,%0	;COMPARE REGISTER ZERO FOR N
459	001132	001401		BEQ	+.4	;TEST RESULTS OF MOV
460	001134	000000		HLT		;MOVB INSTRUCTION FAILED
461		000005		N=N+1		
462	001136	000240		SCOPE		
463	001140	112700	000005	MOVB	#5,%0	;MOVE .BYTE 5 TO REGISTER ZERO
464	001144	122700	000005	CMPB	#5,%0	;COMPARE REGISTER ZERO FOR N
465	001150	001401		BEQ	+.4	;TEST RESULTS OF MOV
466	001152	000000		HLT		;MOVB INSTRUCTION FAILED
467		000006		N=N+1		
468	001154	000240		SCOPE		
469	001156	112700	000006	MOVB	#6,%0	;MOVE .BYTE 6 TO REGISTER ZERO
470	001162	122700	000006	CMPB	#6,%0	;COMPARE REGISTER ZERO FOR N
471	001166	001401		BEQ	+.4	;TEST RESULTS OF MOV
472	001170	000000		HLT		;MOVB INSTRUCTION FAILED
473		000007		N=N+1		
474	001172	000240		SCOPE		
475	001174	112700	000007	MOVB	#7,%0	;MOVE .BYTE 7 TO REGISTER ZERO
476	001200	122700	000007	CMPB	#7,%0	;COMPARE REGISTER ZERO FOR N
477	001204	001401		BEQ	+.4	;TEST RESULTS OF MOV
478	001206	000000		HLT		;MOVB INSTRUCTION FAILED
479		000010		N=N+1		
480	001210	000240		SCOPE		
481	001212	112700	000010	MOVB	#10,%0	;MOVE .BYTE 10 TO REGISTER ZERO
482	001216	122700	000010	CMPB	#10,%0	;COMPARE REGISTER ZERO FOR N
483	001222	001401		BEQ	+.4	;TEST RESULTS OF MOV
484	001224	000000		HLT		;MOVB INSTRUCTION FAILED

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485		000011		N=N+1		
486	001226	000240		SCOPE		
487	001230	112700	000011	MOVB	#11,%0	;MOVE .BYTE 11 TO REGISTER ZERO
488	001234	122700	000011	CMPB	#11,%0	;COMPARE REGISTER ZERO FOR N
489	001240	001401		BEQ	+.4	;TEST RESULTS OF MOV
490	001242	000000		HLT		;MOVB INSTRUCTION FAILED
491		000012		N=N+1		
492	001244	000240		SCOPE		
493	001246	112700	000012	MOVB	#12,%0	;MOVE .BYTE 12 TO REGISTER ZERO
494	001252	122700	000012	CMPB	#12,%0	;COMPARE REGISTER ZERO FOR N
495	001256	001401		BEQ	+.4	;TEST RESULTS OF MOV
496	001260	000000		HLT		;MOVB INSTRUCTION FAILED
497		000013		N=N+1		
498	001262	000240		SCOPE		
499	001264	112700	000013	MOVB	#13,%0	;MOVE .BYTE 13 TO REGISTER ZERO
500	001270	122700	000013	CMPB	#13,%0	;COMPARE REGISTER ZERO FOR N
501	001274	001401		BEQ	+.4	;TEST RESULTS OF MOV
502	001276	000000		HLT		;MOVB INSTRUCTION FAILED
503		000014		N=N+1		
504	001300	000240		SCOPE		
505	001302	112700	000014	MOVB	#14,%0	;MOVE .BYTE 14 TO REGISTER ZERO
506	001306	122700	000014	CMPB	#14,%0	;COMPARE REGISTER ZERO FOR N
507	001312	001401		BEQ	+.4	;TEST RESULTS OF MOV
508	001314	000000		HLT		;MOVB INSTRUCTION FAILED
509		000015		N=N+1		
510	001316	000240		SCOPE		
511	001320	112700	000015	MOVB	#15,%0	;MOVE .BYTE 15 TO REGISTER ZERO
512	001324	122700	000015	CMPB	#15,%0	;COMPARE REGISTER ZERO FOR N
513	001330	001401		BEQ	+.4	;TEST RESULTS OF MOV
514	001332	000000		HLT		;MOVB INSTRUCTION FAILED
515		000016		N=N+1		
516	001334	000240		SCOPE		
517	001336	112700	000016	MOVB	#16,%0	;MOVE .BYTE 16 TO REGISTER ZERO
518	001342	122700	000016	CMPB	#16,%0	;COMPARE REGISTER ZERO FOR N
519	001346	001401		BEQ	+.4	;TEST RESULTS OF MOV
520	001350	000000		HLT		;MOVB INSTRUCTION FAILED
521		000017		N=N+1		
522	001352	000240		SCOPE		
523	001354	112700	000017	MOVB	#17,%0	;MOVE .BYTE 17 TO REGISTER ZERO
524	001360	122700	000017	CMPB	#17,%0	;COMPARE REGISTER ZERO FOR N
525	001364	001401		BEQ	+.4	;TEST RESULTS OF MOV
526	001366	000000		HLT		;MOVB INSTRUCTION FAILED
527		000020		N=N+1		
528	001370	000240		SCOPE		
529	001372	112700	000020	MOVB	#20,%0	;MOVE .BYTE 20 TO REGISTER ZERO
530	001376	122700	000020	CMPB	#20,%0	;COMPARE REGISTER ZERO FOR N
531	001402	001401		BEQ	+.4	;TEST RESULTS OF MOV
532	001404	000000		HLT		;MOVB INSTRUCTION FAILED
533		000021		N=N+1		
534	001406	000240		SCOPE		
535	001410	112700	000021	MOVB	#21,%0	;MOVE .BYTE 21 TO REGISTER ZERO
536	001414	122700	000021	CMPB	#21,%0	;COMPARE REGISTER ZERO FOR N
537	001420	001401		BEQ	+.4	;TEST RESULTS OF MOV
538	001422	000000		HLT		;MOVB INSTRUCTION FAILED
539		000022		N=N+1		
540	001424	000240		SCOPE		

541	001426	112700	000022	MOVB	#22,%0	:MOVE .BYTE 22 TO REGISTER ZERO
542	001432	122700	000022	CMPB	#22,%0	:COMPARE REGISTER ZERO FOR N
543	001436	001401		BEQ	+.4	:TEST RESULTS OF MOV
544	001440	000000		HLT		:MOVB INSTRUCTION FAILED
545		000023		N=N+1		
546	001442	000240		SCOPE		
547	001444	112700	000023	MOVB	#23,%0	:MOVE .BYTE 23 TO REGISTER ZERO
548	001450	122700	000023	CMPB	#23,%0	:COMPARE REGISTER ZERO FOR N
549	001454	001401		BEQ	+.4	:TEST RESULTS OF MOV
550	001456	000000		HLT		:MOVB INSTRUCTION FAILED
551		000024		N=N+1		
552	001460	000240		SCOPE		
553	001462	112700	000024	MOVB	#24,%0	:MOVE .BYTE 24 TO REGISTER ZERO
554	001466	122700	000024	CMPB	#24,%0	:COMPARE REGISTER ZERO FOR N
555	001472	001401		BEQ	+.4	:TEST RESULTS OF MOV
556	001474	000000		HLT		:MOVB INSTRUCTION FAILED
557		000025		N=N+1		
558	001476	000240		SCOPE		
559	001500	112700	000025	MOVB	#25,%0	:MOVE .BYTE 25 TO REGISTER ZERO
560	001504	122700	000025	CMPB	#25,%0	:COMPARE REGISTER ZERO FOR N
561	001510	001401		BEQ	+.4	:TEST RESULTS OF MOV
562	001512	000000		HLT		:MOVB INSTRUCTION FAILED
563		000026		N=N+1		
564	001514	000240		SCOPE		
565	001516	112700	000026	MOVB	#26,%0	:MOVE .BYTE 26 TO REGISTER ZERO
566	001522	122700	000026	CMPB	#26,%0	:COMPARE REGISTER ZERO FOR N
567	001526	001401		BEQ	+.4	:TEST RESULTS OF MOV
568	001530	000000		HLT		:MOVB INSTRUCTION FAILED
569		000027		N=N+1		
570	001532	000240		SCOPE		
571	001534	112700	000027	MOVB	#27,%0	:MOVE .BYTE 27 TO REGISTER ZERO
572	001540	122700	000027	CMPB	#27,%0	:COMPARE REGISTER ZERO FOR N
573	001544	001401		BEQ	+.4	:TEST RESULTS OF MOV
574	001546	000000		HLT		:MOVB INSTRUCTION FAILED
575		000030		N=N+1		
576	001550	000240		SCOPE		
577	001552	112700	000030	MOVB	#30,%0	:MOVE .BYTE 30 TO REGISTER ZERO
578	001556	122700	000030	CMPB	#30,%0	:COMPARE REGISTER ZERO FOR N
579	001562	001401		BEQ	+.4	:TEST RESULTS OF MOV
580	001564	000000		HLT		:MOVB INSTRUCTION FAILED
581		000031		N=N+1		
582	001566	000240		SCOPE		
583	001570	112700	000031	MOVB	#31,%0	:MOVE .BYTE 31 TO REGISTER ZERO
584	001574	122700	000031	CMPB	#31,%0	:COMPARE REGISTER ZERO FOR N
585	001600	001401		BEQ	+.4	:TEST RESULTS OF MOV
586	001602	000000		HLT		:MOVB INSTRUCTION FAILED
587		000032		N=N+1		
588	001604	000240		SCOPE		
589	001606	112700	000032	MOVB	#32,%0	:MOVE .BYTE 32 TO REGISTER ZERO
590	001612	122700	000032	CMPB	#32,%0	:COMPARE REGISTER ZERO FOR N
591	001616	001401		BEQ	+.4	:TEST RESULTS OF MOV
592	001620	000000		HLT		:MOVB INSTRUCTION FAILED
593		000033		N=N+1		
594	001622	000240		SCOPE		
595	001624	112700	000033	MOVB	#33,%0	:MOVE .BYTE 33 TO REGISTER ZERO
596	001630	122700	000033	CMPB	#33,%0	:COMPARE REGISTER ZERO FOR N

597	001634	001401		BEQ	.+4		:TEST RESULTS OF MOV
598	001636	000000		HLT			:MOVB INSTRUCTION FAILED
599		000034		N=N+1			
600	001640	000240		SCOPE			
601	001642	112700	000034	MOVB	#34,%0		:MOVE .BYTE 34 TO REGISTER ZERO
602	001646	122700	000034	CMPB	#34,%0		:COMPARE REGISTER ZERO FOR N
603	001652	001401		BEQ	.+4		:TEST RESULTS OF MOV
604	001654	000000		HLT			:MOVB INSTRUCTION FAILED
605		000035		N=N+1			
606	001656	000240		SCOPE			
607	001660	112700	000035	MOVB	#35,%0		:MOVE .BYTE 35 TO REGISTER ZERO
608	001664	122700	000035	CMPB	#35,%0		:COMPARE REGISTER ZERO FOR N
609	001670	001401		BEQ	.+4		:TEST RESULTS OF MOV
610	001672	000000		HLT			:MOVB INSTRUCTION FAILED
611		000036		N=N+1			
612	001674	000240		SCOPE			
613	001676	112700	000036	MOVB	#36,%0		:MOVE .BYTE 36 TO REGISTER ZERO
614	001702	122700	000036	CMPB	#36,%0		:COMPARE REGISTER ZERO FOR N
615	001706	001401		BEQ	.+4		:TEST RESULTS OF MOV
616	001710	000000		HLT			:MOVB INSTRUCTION FAILED
617		000037		N=N+1			
618	001712	000240		SCOPE			
619	001714	112700	000037	MOVB	#37,%0		:MOVE .BYTE 37 TO REGISTER ZERO
620	001720	122700	000037	CMPB	#37,%0		:COMPARE REGISTER ZERO FOR N
621	001724	001401		BEQ	.+4		:TEST RESULTS OF MOV
622	001726	000000		HLT			:MOVB INSTRUCTION FAILED
623		000040		N=N+1			
624	001730	000240		SCOPE			
625	001732	112700	000040	MOVB	#40,%0		:MOVE .BYTE 40 TO REGISTER ZERO
626	001736	122700	000040	CMPB	#40,%0		:COMPARE REGISTER ZERO FOR N
627	001742	001401		BEQ	.+4		:TEST RESULTS OF MOV
628	001744	000000		HLT			:MOVB INSTRUCTION FAILED
629		000041		N=N+1			
630	001746	000240		SCOPE			
631	001750	112700	000041	MOVB	#41,%0		:MOVE .BYTE 41 TO REGISTER ZERO
632	001754	122700	000041	CMPB	#41,%0		:COMPARE REGISTER ZERO FOR N
633	001760	001401		BEQ	.+4		:TEST RESULTS OF MOV
634	001762	000000		HLT			:MOVB INSTRUCTION FAILED
635		000042		N=N+1			
636	001764	000240		SCOPE			
637	001766	112700	000042	MOVB	#42,%0		:MOVE .BYTE 42 TO REGISTER ZERO
638	001772	122700	000042	CMPB	#42,%0		:COMPARE REGISTER ZERO FOR N
639	001776	001401		BEQ	.+4		:TEST RESULTS OF MOV
640	002000	000000		HLT			:MOVB INSTRUCTION FAILED
641		000043		N=N+1			
642	002002	000240		SCOPE			
643	002004	112700	000043	MOVB	#43,%0		:MOVE .BYTE 43 TO REGISTER ZERO
644	002010	122700	000043	CMPB	#43,%0		:COMPARE REGISTER ZERO FOR N
645	002014	001401		BEQ	.+4		:TEST RESULTS OF MOV
646	002016	000000		HLT			:MOVB INSTRUCTION FAILED
647		000044		N=N+1			
648	002020	000240		SCOPE			
649	002022	112700	000044	MOVB	#44,%0		:MOVE .BYTE 44 TO REGISTER ZERO
650	002026	122700	000044	CMPB	#44,%0		:COMPARE REGISTER ZERO FOR N
651	002032	001401		BEQ	.+4		:TEST RESULTS OF MOV
652	002034	000000		HLT			:MOVB INSTRUCTION FAILED

653		000045		N=N+1		
654	002036	000240		SCOPE		
655	002040	112700	000045	MOVB	#45,%0	; MOVE .BYTE 45 TO REGISTER ZERO
656	002044	122700	000045	CMPB	#45,%0	; COMPARE REGISTER ZERO FOR N
657	002050	001401		BEQ	+.4	; TEST RESULTS OF MOV
658	002052	000000		HLT		; MOVB INSTRUCTION FAILED
659		000046		N=N+1		
660	002054	000240		SCOPE		
661	002056	112700	000046	MOVB	#46,%0	; MOVE .BYTE 46 TO REGISTER ZERO
662	002062	122700	000046	CMPB	#46,%0	; COMPARE REGISTER ZERO FOR N
663	002066	001401		BEQ	+.4	; TEST RESULTS OF MOV
664	002070	000000		HLT		; MOVB INSTRUCTION FAILED
665		000047		N=N+1		
666	002072	000240		SCOPE		
667	002074	112700	000047	MOVB	#47,%0	; MOVE .BYTE 47 TO REGISTER ZERO
668	002100	122700	000047	CMPB	#47,%0	; COMPARE REGISTER ZERO FOR N
669	002104	001401		BEQ	+.4	; TEST RESULTS OF MOV
670	002106	000000		HLT		; MOVB INSTRUCTION FAILED
671		000050		N=N+1		
672	002110	000240		SCOPE		
673	002112	112700	000050	MOVB	#50,%0	; MOVE .BYTE 50 TO REGISTER ZERO
674	002116	122700	000050	CMPB	#50,%0	; COMPARE REGISTER ZERO FOR N
675	002122	001401		BEQ	+.4	; TEST RESULTS OF MOV
676	002124	000000		HLT		; MOVB INSTRUCTION FAILED
677		000051		N=N+1		
678	002126	000240		SCOPE		
679	002130	112700	000051	MOVB	#51,%0	; MOVE .BYTE 51 TO REGISTER ZERO
680	002134	122700	000051	CMPB	#51,%0	; COMPARE REGISTER ZERO FOR N
681	002140	001401		BEQ	+.4	; TEST RESULTS OF MOV
682	002142	000000		HLT		; MOVB INSTRUCTION FAILED
683		000052		N=N+1		
684	002144	000240		SCOPE		
685	002146	112700	000052	MOVB	#52,%0	; MOVE .BYTE 52 TO REGISTER ZERO
686	002152	122700	000052	CMPB	#52,%0	; COMPARE REGISTER ZERO FOR N
687	002156	001401		BEQ	+.4	; TEST RESULTS OF MOV
688	002160	000000		HLT		; MOVB INSTRUCTION FAILED
689		000053		N=N+1		
690	002162	000240		SCOPE		
691	002164	112700	000053	MOVB	#53,%0	; MOVE .BYTE 53 TO REGISTER ZERO
692	002170	122700	000053	CMPB	#53,%0	; COMPARE REGISTER ZERO FOR N
693	002174	001401		BEQ	+.4	; TEST RESULTS OF MOV
694	002176	000000		HLT		; MOVB INSTRUCTION FAILED
695		000054		N=N+1		
696	002200	000240		SCOPE		
697	002202	112700	000054	MOVB	#54,%0	; MOVE .BYTE 54 TO REGISTER ZERO
698	002206	122700	000054	CMPB	#54,%0	; COMPARE REGISTER ZERO FOR N
699	002212	001401		BEQ	+.4	; TEST RESULTS OF MOV
700	002214	000000		HLT		; MOVB INSTRUCTION FAILED
701		000055		N=N+1		
702	002216	000240		SCOPE		
703	002220	112700	000055	MOVB	#55,%0	; MOVE .BYTE 55 TO REGISTER ZERO
704	002224	122700	000055	CMPB	#55,%0	; COMPARE REGISTER ZERO FOR N
705	002230	001401		BEQ	+.4	; TEST RESULTS OF MOV
706	002232	000000		HLT		; MOVB INSTRUCTION FAILED
707		000056		N=N+1		
708	002234	000240		SCOPE		

709	002236	112700	000056	MOVB	#56,%0	; MOVE .BYTE 56 TO REGISTER ZERO
710	002242	122700	000056	CMPB	#56,%0	; COMPARE REGISTER ZERO FOR N
711	002246	001401		BEQ	+.4	; TEST RESULTS OF MOV
712	002250	000000		HLT		; MOVB INSTRUCTION FAILED
713		000057		N=N+1		
714	002252	000240		SCOPE		
715	002254	112700	000057	MOVB	#57,%0	; MOVE .BYTE 57 TO REGISTER ZERO
716	002260	122700	000057	CMPB	#57,%0	; COMPARE REGISTER ZERO FOR N
717	002264	001401		BEQ	+.4	; TEST RESULTS OF MOV
718	002266	000000		HLT		; MOVB INSTRUCTION FAILED
719		000060		N=N+1		
720	002270	000240		SCOPE		
721	002272	112700	000060	MOVB	#60,%0	; MOVE .BYTE 60 TO REGISTER ZERO
722	002276	122700	000060	CMPB	#60,%0	; COMPARE REGISTER ZERO FOR N
723	002302	001401		BEQ	+.4	; TEST RESULTS OF MOV
724	002304	000000		HLT		; MOVB INSTRUCTION FAILED
725		000061		N=N+1		
726	002306	000240		SCOPE		
727	002310	112700	000061	MOVB	#61,%0	; MOVE .BYTE 61 TO REGISTER ZERO
728	002314	122700	000061	CMPB	#61,%0	; COMPARE REGISTER ZERO FOR N
729	002320	001401		BEQ	+.4	; TEST RESULTS OF MOV
730	002322	000000		HLT		; MOVB INSTRUCTION FAILED
731		000062		N=N+1		
732	002324	000240		SCOPE		
733	002326	112700	000062	MOVB	#62,%0	; MOVE .BYTE 62 TO REGISTER ZERO
734	002332	122700	000062	CMPB	#62,%0	; COMPARE REGISTER ZERO FOR N
735	002336	001401		BEQ	+.4	; TEST RESULTS OF MOV
736	002340	000000		HLT		; MOVB INSTRUCTION FAILED
737		000063		N=N+1		
738	002342	000240		SCOPE		
739	002344	112700	000063	MOVB	#63,%0	; MOVE .BYTE 63 TO REGISTER ZERO
740	002350	122700	000063	CMPB	#63,%0	; COMPARE REGISTER ZERO FOR N
741	002354	001401		BEQ	+.4	; TEST RESULTS OF MOV
742	002356	000000		HLT		; MOVB INSTRUCTION FAILED
743		000064		N=N+1		
744	002360	000240		SCOPE		
745	002362	112700	000064	MOVB	#64,%0	; MOVE .BYTE 64 TO REGISTER ZERO
746	002366	122700	000064	CMPB	#64,%0	; COMPARE REGISTER ZERO FOR N
747	002372	001401		BEQ	+.4	; TEST RESULTS OF MOV
748	002374	000000		HLT		; MOVB INSTRUCTION FAILED
749		000065		N=N+1		
750	002376	000240		SCOPE		
751	002400	112700	000065	MOVB	#65,%0	; MOVE .BYTE 65 TO REGISTER ZERO
752	002404	122700	000065	CMPB	#65,%0	; COMPARE REGISTER ZERO FOR N
753	002410	001401		BEQ	+.4	; TEST RESULTS OF MOV
754	002412	000000		HLT		; MOVB INSTRUCTION FAILED
755		000066		N=N+1		
756	002414	000240		SCOPE		
757	002416	112700	000066	MOVB	#66,%0	; MOVE .BYTE 66 TO REGISTER ZERO
758	002422	122700	000066	CMPB	#66,%0	; COMPARE REGISTER ZERO FOR N
759	002426	001401		BEQ	+.4	; TEST RESULTS OF MOV
760	002430	000000		HLT		; MOVB INSTRUCTION FAILED
761		000067		N=N+1		
762	002432	000240		SCOPE		
763	002434	112700	000067	MOVB	#67,%0	; MOVE .BYTE 67 TO REGISTER ZERO
764	002440	122700	000067	CMPB	#67,%0	; COMPARE REGISTER ZERO FOR N

765	002444	001401		BEQ	.+4		;TEST RESULTS OF MOV
766	002446	000000		HLT			;MOVB INSTRUCTION FAILED
767		000070		N=N+1			
768	002450	000240		SCOPE			
769	002452	112700	000070	MOVB	#70,%0		;MOVE .BYTE 70 TO REGISTER ZERO
770	002456	122700	000070	CMPB	#70,%0		;COMPARE REGISTER ZERO FOR N
771	002462	001401		BEQ	.+4		;TEST RESULTS OF MOV
772	002464	000000		HLT			;MOVB INSTRUCTION FAILED
773		000071		N=N+1			
774	002466	000240		SCOPE			
775	002470	112700	000071	MOVB	#71,%0		;MOVE .BYTE 71 TO REGISTER ZERO
776	002474	122700	000071	CMPB	#71,%0		;COMPARE REGISTER ZERO FOR N
777	002500	001401		BEQ	.+4		;TEST RESULTS OF MOV
778	002502	000000		HLT			;MOVB INSTRUCTION FAILED
779		000072		N=N+1			
780	002504	000240		SCOPE			
781	002506	112700	000072	MOVB	#72,%0		;MOVE .BYTE 72 TO REGISTER ZERO
782	002512	122700	000072	CMPB	#72,%0		;COMPARE REGISTER ZERO FOR N
783	002516	001401		BEQ	.+4		;TEST RESULTS OF MOV
784	002520	000000		HLT			;MOVB INSTRUCTION FAILED
785		000073		N=N+1			
786	002522	000240		SCOPE			
787	002524	112700	000073	MOVB	#73,%0		;MOVE .BYTE 73 TO REGISTER ZERO
788	002530	122700	000073	CMPB	#73,%0		;COMPARE REGISTER ZERO FOR N
789	002534	001401		BEQ	.+4		;TEST RESULTS OF MOV
790	002536	000000		HLT			;MOVB INSTRUCTION FAILED
791		000074		N=N+1			
792	002540	000240		SCOPE			
793	002542	112700	000074	MOVB	#74,%0		;MOVE .BYTE 74 TO REGISTER ZERO
794	002546	122700	000074	CMPB	#74,%0		;COMPARE REGISTER ZERO FOR N
795	002552	001401		BEQ	.+4		;TEST RESULTS OF MOV
796	002554	000000		HLT			;MOVB INSTRUCTION FAILED
797		000075		N=N+1			
798	002556	000240		SCOPE			
799	002560	112700	000075	MOVB	#75,%0		;MOVE .BYTE 75 TO REGISTER ZERO
800	002564	122700	000075	CMPB	#75,%0		;COMPARE REGISTER ZERO FOR N
801	002570	001401		BEQ	.+4		;TEST RESULTS OF MOV
802	002572	000000		HLT			;MOVB INSTRUCTION FAILED
803		000076		N=N+1			
804	002574	000240		SCOPE			
805	002576	112700	000076	MOVB	#76,%0		;MOVE .BYTE 76 TO REGISTER ZERO
806	002602	122700	000076	CMPB	#76,%0		;COMPARE REGISTER ZERO FOR N
807	002606	001401		BEQ	.+4		;TEST RESULTS OF MOV
808	002610	000000		HLT			;MOVB INSTRUCTION FAILED
809		000077		N=N+1			
810	002612	000240		SCOPE			
811	002614	112700	000077	MOVB	#77,%0		;MOVE .BYTE 77 TO REGISTER ZERO
812	002620	122700	000077	CMPB	#77,%0		;COMPARE REGISTER ZERO FOR N
813	002624	001401		BEQ	.+4		;TEST RESULTS OF MOV
814	002626	000000		HLT			;MOVB INSTRUCTION FAILED
815		000100		N=N+1			
816	002630	000240		SCOPE			
817	002632	112700	000100	MOVB	#100,%0		;MOVE .BYTE 100 TO REGISTER ZERO
818	002636	122700	000100	CMPB	#100,%0		;COMPARE REGISTER ZERO FOR N
819	002642	001401		BEQ	.+4		;TEST RESULTS OF MOV
820	002644	000000		HLT			;MOVB INSTRUCTION FAILED

821		000101		N=N+1		
822	002646	000240		SCOPE		
823	002650	112700	000101	MOVB	#101,%0	; MOVE .BYTE 101 TO REGISTER ZERO
824	002654	122700	000101	CMPB	#101,%0	; COMPARE REGISTER ZERO FOR N
825	002660	001401		BEQ	+.4	; TEST RESULTS OF MOV
826	002662	000000		HLT		; MOVB INSTRUCTION FAILED
827		000102		N=N+1		
828	002664	000240		SCOPE		
829	002666	112700	000102	MOVB	#102,%0	; MOVE .BYTE 102 TO REGISTER ZERO
830	002672	122700	000102	CMPB	#102,%0	; COMPARE REGISTER ZERO FOR N
831	002676	001401		BEQ	+.4	; TEST RESULTS OF MOV
832	002700	000000		HLT		; MOVB INSTRUCTION FAILED
833		000103		N=N+1		
834	002702	000240		SCOPE		
835	002704	112700	000103	MOVB	#103,%0	; MOVE .BYTE 103 TO REGISTER ZERO
836	002710	122700	000103	CMPB	#103,%0	; COMPARE REGISTER ZERO FOR N
837	002714	001401		BEQ	+.4	; TEST RESULTS OF MOV
838	002716	000000		HLT		; MOVB INSTRUCTION FAILED
839		000104		N=N+1		
840	002720	000240		SCOPE		
841	002722	112700	000104	MOVB	#104,%0	; MOVE .BYTE 104 TO REGISTER ZERO
842	002726	122700	000104	CMPB	#104,%0	; COMPARE REGISTER ZERO FOR N
843	002732	001401		BEQ	+.4	; TEST RESULTS OF MOV
844	002734	000000		HLT		; MOVB INSTRUCTION FAILED
845		000105		N=N+1		
846	002736	000240		SCOPE		
847	002740	112700	000105	MOVB	#105,%0	; MOVE .BYTE 105 TO REGISTER ZERO
848	002744	122700	000105	CMPB	#105,%0	; COMPARE REGISTER ZERO FOR N
849	002750	001401		BEQ	+.4	; TEST RESULTS OF MOV
850	002752	000000		HLT		; MOVB INSTRUCTION FAILED
851		000106		N=N+1		
852	002754	000240		SCOPE		
853	002756	112700	000106	MOVB	#106,%0	; MOVE .BYTE 106 TO REGISTER ZERO
854	002762	122700	000106	CMPB	#106,%0	; COMPARE REGISTER ZERO FOR N
855	002766	001401		BEQ	+.4	; TEST RESULTS OF MOV
856	002770	000000		HLT		; MOVB INSTRUCTION FAILED
857		000107		N=N+1		
858	002772	000240		SCOPE		
859	002774	112700	000107	MOVB	#107,%0	; MOVE .BYTE 107 TO REGISTER ZERO
860	003000	122700	000107	CMPB	#107,%0	; COMPARE REGISTER ZERO FOR N
861	003004	001401		BEQ	+.4	; TEST RESULTS OF MOV
862	003006	000000		HLT		; MOVB INSTRUCTION FAILED
863		000110		N=N+1		
864	003010	000240		SCOPE		
865	003012	112700	000110	MOVB	#110,%0	; MOVE .BYTE 110 TO REGISTER ZERO
866	003016	122700	000110	CMPB	#110,%0	; COMPARE REGISTER ZERO FOR N
867	003022	001401		BEQ	+.4	; TEST RESULTS OF MOV
868	003024	000000		HLT		; MOVB INSTRUCTION FAILED
869		000111		N=N+1		
870	003026	000240		SCOPE		
871	003030	112700	000111	MOVB	#111,%0	; MOVE .BYTE 111 TO REGISTER ZERO
872	003034	122700	000111	CMPB	#111,%0	; COMPARE REGISTER ZERO FOR N
873	003040	001401		BEQ	+.4	; TEST RESULTS OF MOV
874	003042	000000		HLT		; MOVB INSTRUCTION FAILED
875		000112		N=N+1		
876	003044	000240		SCOPE		

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877	003046	112700	000112	MOVB	#112,%0	;MOVE .BYTE 112 TO REGISTER ZERO
878	003052	122700	000112	CMPB	#112,%0	;COMPARE REGISTER ZERO FOR N
879	003056	001401		BEQ	+.4	;TEST RESULTS OF MOV
880	003060	000000		HLT		;MOVB INSTRUCTION FAILED
881		000113		N=N+1		
882	003062	000240		SCOPE		
883	003064	112700	000113	MOVB	#113,%0	;MOVE .BYTE 113 TO REGISTER ZERO
884	003070	122700	000113	CMPB	#113,%0	;COMPARE REGISTER ZERO FOR N
885	003074	001401		BEQ	+.4	;TEST RESULTS OF MOV
886	003076	000000		HLT		;MOVB INSTRUCTION FAILED
887		000114		N=N+1		
888	003100	000240		SCOPE		
889	003102	112700	000114	MOVB	#114,%0	;MOVE .BYTE 114 TO REGISTER ZERO
890	003106	122700	000114	CMPB	#114,%0	;COMPARE REGISTER ZERO FOR N
891	003112	001401		BEQ	+.4	;TEST RESULTS OF MOV
892	003114	000000		HLT		;MOVB INSTRUCTION FAILED
893		000115		N=N+1		
894	003116	000240		SCOPE		
895	003120	112700	000115	MOVB	#115,%0	;MOVE .BYTE 115 TO REGISTER ZERO
896	003124	122700	000115	CMPB	#115,%0	;COMPARE REGISTER ZERO FOR N
897	003130	001401		BEQ	+.4	;TEST RESULTS OF MOV
898	003132	000000		HLT		;MOVB INSTRUCTION FAILED
899		000116		N=N+1		
900	003134	000240		SCOPE		
901	003136	112700	000116	MOVB	#116,%0	;MOVE .BYTE 116 TO REGISTER ZERO
902	003142	122700	000116	CMPB	#116,%0	;COMPARE REGISTER ZERO FOR N
903	003146	001401		BEQ	+.4	;TEST RESULTS OF MOV
904	003150	000000		HLT		;MOVB INSTRUCTION FAILED
905		000117		N=N+1		
906	003152	000240		SCOPE		
907	003154	112700	000117	MOVB	#117,%0	;MOVE .BYTE 117 TO REGISTER ZERO
908	003160	122700	000117	CMPB	#117,%0	;COMPARE REGISTER ZERO FOR N
909	003164	001401		BEQ	+.4	;TEST RESULTS OF MOV
910	003166	000000		HLT		;MOVB INSTRUCTION FAILED
911		000120		N=N+1		
912	003170	000240		SCOPE		
913	003172	112700	000120	MOVB	#120,%0	;MOVE .BYTE 120 TO REGISTER ZERO
914	003176	122700	000120	CMPB	#120,%0	;COMPARE REGISTER ZERO FOR N
915	003202	001401		BEQ	+.4	;TEST RESULTS OF MOV
916	003204	000000		HLT		;MOVB INSTRUCTION FAILED
917		000121		N=N+1		
918	003206	000240		SCOPE		
919	003210	112700	000121	MOVB	#121,%0	;MOVE .BYTE 121 TO REGISTER ZERO
920	003214	122700	000121	CMPB	#121,%0	;COMPARE REGISTER ZERO FOR N
921	003220	001401		BEQ	+.4	;TEST RESULTS OF MOV
922	003222	000000		HLT		;MOVB INSTRUCTION FAILED
923		000122		N=N+1		
924	003224	000240		SCOPE		
925	003226	112700	000122	MOVB	#122,%0	;MOVE .BYTE 122 TO REGISTER ZERO
926	003232	122700	000122	CMPB	#122,%0	;COMPARE REGISTER ZERO FOR N
927	003236	001401		BEQ	+.4	;TEST RESULTS OF MOV
928	003240	000000		HLT		;MOVB INSTRUCTION FAILED
929		000123		N=N+1		
930	003242	000240		SCOPE		
931	003244	112700	000123	MOVB	#123,%0	;MOVE .BYTE 123 TO REGISTER ZERO
932	003250	122700	000123	CMPB	#123,%0	;COMPARE REGISTER ZERO FOR N

933	003254	001401		BEQ	.+4		;TEST RESULTS OF MOV
934	003256	000000		HLT			;MOVB INSTRUCTION FAILED
935		000124		N=N+1			
936	003260	000240		SCOPE			
937	003262	112700	000124	MOVB	#124,%0		;MOVE .BYTE 124 TO REGISTER ZERO
938	003266	122700	000124	CMPB	#124,%0		;COMPARE REGISTER ZERO FOR N
939	003272	001401		BEQ	.+4		;TEST RESULTS OF MOV
940	003274	000000		HLT			;MOVB INSTRUCTION FAILED
941		000125		N=N+1			
942	003276	000240		SCOPE			
943	003300	112700	000125	MOVB	#125,%0		;MOVE .BYTE 125 TO REGISTER ZERO
944	003304	122700	000125	CMPB	#125,%0		;COMPARE REGISTER ZERO FOR N
945	003310	001401		BEQ	.+4		;TEST RESULTS OF MOV
946	003312	000000		HLT			;MOVB INSTRUCTION FAILED
947		000126		N=N+1			
948	003314	000240		SCOPE			
949	003316	112700	000126	MOVB	#126,%0		;MOVE .BYTE 126 TO REGISTER ZERO
950	003322	122700	000126	CMPB	#126,%0		;COMPARE REGISTER ZERO FOR N
951	003326	001401		BEQ	.+4		;TEST RESULTS OF MOV
952	003330	000000		HLT			;MOVB INSTRUCTION FAILED
953		000127		N=N+1			
954	003332	000240		SCOPE			
955	003334	112700	000127	MOVB	#127,%0		;MOVE .BYTE 127 TO REGISTER ZERO
956	003340	122700	000127	CMPB	#127,%0		;COMPARE REGISTER ZERO FOR N
957	003344	001401		BEQ	.+4		;TEST RESULTS OF MOV
958	003346	000000		HLT			;MOVB INSTRUCTION FAILED
959		000130		N=N+1			
960	003350	000240		SCOPE			
961	003352	112700	000130	MOVB	#130,%0		;MOVE .BYTE 130 TO REGISTER ZERO
962	003356	122700	000130	CMPB	#130,%0		;COMPARE REGISTER ZERO FOR N
963	003362	001401		BEQ	.+4		;TEST RESULTS OF MOV
964	003364	000000		HLT			;MOVB INSTRUCTION FAILED
965		000131		N=N+1			
966	003366	000240		SCOPE			
967	003370	112700	000131	MOVB	#131,%0		;MOVE .BYTE 131 TO REGISTER ZERO
968	003374	122700	000131	CMPB	#131,%0		;COMPARE REGISTER ZERO FOR N
969	003400	001401		BEQ	.+4		;TEST RESULTS OF MOV
970	003402	000000		HLT			;MOVB INSTRUCTION FAILED
971		000132		N=N+1			
972	003404	000240		SCOPE			
973	003406	112700	000132	MOVB	#132,%0		;MOVE .BYTE 132 TO REGISTER ZERO
974	003412	122700	000132	CMPB	#132,%0		;COMPARE REGISTER ZERO FOR N
975	003416	001401		BEQ	.+4		;TEST RESULTS OF MOV
976	003420	000000		HLT			;MOVB INSTRUCTION FAILED
977		000133		N=N+1			
978	003422	000240		SCOPE			
979	003424	112700	000133	MOVB	#133,%0		;MOVE .BYTE 133 TO REGISTER ZERO
980	003430	122700	000133	CMPB	#133,%0		;COMPARE REGISTER ZERO FOR N
981	003434	001401		BEQ	.+4		;TEST RESULTS OF MOV
982	003436	000000		HLT			;MOVB INSTRUCTION FAILED
983		000134		N=N+1			
984	003440	000240		SCOPE			
985	003442	112700	000134	MOVB	#134,%0		;MOVE .BYTE 134 TO REGISTER ZERO
986	003446	122700	000134	CMPB	#134,%0		;COMPARE REGISTER ZERO FOR N
987	003452	001401		BEQ	.+4		;TEST RESULTS OF MOV
988	003454	000000		HLT			;MOVB INSTRUCTION FAILED

989		000135		N=N+1		
990	003456	000240		SCOPE		
991	003460	112700	000135	MOVB	#135,%0	; MOVE .BYTE 135 TO REGISTER ZERO
992	003464	122700	000135	CMPB	#135,%0	; COMPARE REGISTER ZERO FOR N
993	003470	001401		BEQ	+.4	; TEST RESULTS OF MOV
994	003472	000000		HLT		; MOVB INSTRUCTION FAILED
995		000136		N=N+1		
996	003474	000240		SCOPE		
997	003476	112700	000136	MOVB	#136,%0	; MOVE .BYTE 136 TO REGISTER ZERO
998	003502	122700	000136	CMPB	#136,%0	; COMPARE REGISTER ZERO FOR N
999	003506	001401		BEQ	+.4	; TEST RESULTS OF MOV
1000	003510	000000		HLT		; MOVB INSTRUCTION FAILED
1001		000137		N=N+1		
1002	003512	000240		SCOPE		
1003	003514	112700	000137	MOVB	#137,%0	; MOVE .BYTE 137 TO REGISTER ZERO
1004	003520	122700	000137	CMPB	#137,%0	; COMPARE REGISTER ZERO FOR N
1005	003524	001401		BEQ	+.4	; TEST RESULTS OF MOV
1006	003526	000000		HLT		; MOVB INSTRUCTION FAILED
1007		000140		N=N+1		
1008	003530	000240		SCOPE		
1009	003532	112700	000140	MOVB	#140,%0	; MOVE .BYTE 140 TO REGISTER ZERO
1010	003536	122700	000140	CMPB	#140,%0	; COMPARE REGISTER ZERO FOR N
1011	003542	001401		BEQ	+.4	; TEST RESULTS OF MOV
1012	003544	000000		HLT		; MOVB INSTRUCTION FAILED
1013		000141		N=N+1		
1014	003546	000240		SCOPE		
1015	003550	112700	000141	MOVB	#141,%0	; MOVE .BYTE 141 TO REGISTER ZERO
1016	003554	122700	000141	CMPB	#141,%0	; COMPARE REGISTER ZERO FOR N
1017	003560	001401		BEQ	+.4	; TEST RESULTS OF MOV
1018	003562	000000		HLT		; MOVB INSTRUCTION FAILED
1019		000142		N=N+1		
1020	003564	000240		SCOPE		
1021	003566	112700	000142	MOVB	#142,%0	; MOVE .BYTE 142 TO REGISTER ZERO
1022	003572	122700	000142	CMPB	#142,%0	; COMPARE REGISTER ZERO FOR N
1023	003576	001401		BEQ	+.4	; TEST RESULTS OF MOV
1024	003600	000000		HLT		; MOVB INSTRUCTION FAILED
1025		000143		N=N+1		
1026	003602	000240		SCOPE		
1027	003604	112700	000143	MOVB	#143,%0	; MOVE .BYTE 143 TO REGISTER ZERO
1028	003610	122700	000143	CMPB	#143,%0	; COMPARE REGISTER ZERO FOR N
1029	003614	001401		BEQ	+.4	; TEST RESULTS OF MOV
1030	003616	000000		HLT		; MOVB INSTRUCTION FAILED
1031		000144		N=N+1		
1032	003620	000240		SCOPE		
1033	003622	112700	000144	MOVB	#144,%0	; MOVE .BYTE 144 TO REGISTER ZERO
1034	003626	122700	000144	CMPB	#144,%0	; COMPARE REGISTER ZERO FOR N
1035	003632	001401		BEQ	+.4	; TEST RESULTS OF MOV
1036	003634	000000		HLT		; MOVB INSTRUCTION FAILED
1037		000145		N=N+1		
1038	003636	000240		SCOPE		
1039	003640	112700	000145	MOVB	#145,%0	; MOVE .BYTE 145 TO REGISTER ZERO
1040	003644	122700	000145	CMPB	#145,%0	; COMPARE REGISTER ZERO FOR N
1041	003650	001401		BEQ	+.4	; TEST RESULTS OF MOV
1042	003652	000000		HLT		; MOVB INSTRUCTION FAILED
1043		000146		N=N+1		
1044	003654	000240		SCOPE		

1045	003656	112700	000146	MOVB	#146,%0	; MOVE .BYTE 146 TO REGISTER ZERO
1046	003662	122700	000146	CMPB	#146,%0	; COMPARE REGISTER ZERO FOR N
1047	003666	001401		BEQ	+.4	; TEST RESULTS OF MOV
1048	003670	000000		HLT		; MOVB INSTRUCTION FAILED
1049		000147		N=N+1		
1050	003672	000240		SCOPE		
1051	003674	112700	000147	MOVB	#147,%0	; MOVE .BYTE 147 TO REGISTER ZERO
1052	003700	122700	000147	CMPB	#147,%0	; COMPARE REGISTER ZERO FOR N
1053	003704	001401		BEQ	+.4	; TEST RESULTS OF MOV
1054	003706	000000		HLT		; MOVB INSTRUCTION FAILED
1055		000150		N=N+1		
1056	003710	000240		SCOPE		
1057	003712	112700	000150	MOVB	#150,%0	; MOVE .BYTE 150 TO REGISTER ZERO
1058	003716	122700	000150	CMPB	#150,%0	; COMPARE REGISTER ZERO FOR N
1059	003722	001401		BEQ	+.4	; TEST RESULTS OF MOV
1060	003724	000000		HLT		; MOVB INSTRUCTION FAILED
1061		000151		N=N+1		
1062	003726	000240		SCOPE		
1063	003730	112700	000151	MOVB	#151,%0	; MOVE .BYTE 151 TO REGISTER ZERO
1064	003734	122700	000151	CMPB	#151,%0	; COMPARE REGISTER ZERO FOR N
1065	003740	001401		BEQ	+.4	; TEST RESULTS OF MOV
1066	003742	000000		HLT		; MOVB INSTRUCTION FAILED
1067		000152		N=N+1		
1068	003744	000240		SCOPE		
1069	003746	112700	000152	MOVB	#152,%0	; MOVE .BYTE 152 TO REGISTER ZERO
1070	003752	122700	000152	CMPB	#152,%0	; COMPARE REGISTER ZERO FOR N
1071	003756	001401		BEQ	+.4	; TEST RESULTS OF MOV
1072	003760	000000		HLT		; MOVB INSTRUCTION FAILED
1073		000153		N=N+1		
1074	003762	000240		SCOPE		
1075	003764	112700	000153	MOVB	#153,%0	; MOVE .BYTE 153 TO REGISTER ZERO
1076	003770	122700	000153	CMPB	#153,%0	; COMPARE REGISTER ZERO FOR N
1077	003774	001401		BEQ	+.4	; TEST RESULTS OF MOV
1078	003776	000000		HLT		; MOVB INSTRUCTION FAILED
1079		000154		N=N+1		
1080	004000	000240		SCOPE		
1081	004002	112700	000154	MOVB	#154,%0	; MOVE .BYTE 154 TO REGISTER ZERO
1082	004006	122700	000154	CMPB	#154,%0	; COMPARE REGISTER ZERO FOR N
1083	004012	001401		BEQ	+.4	; TEST RESULTS OF MOV
1084	004014	000000		HLT		; MOVB INSTRUCTION FAILED
1085		000155		N=N+1		
1086	004016	000240		SCOPE		
1087	004020	112700	000155	MOVB	#155,%0	; MOVE .BYTE 155 TO REGISTER ZERO
1088	004024	122700	000155	CMPB	#155,%0	; COMPARE REGISTER ZERO FOR N
1089	004030	001401		BEQ	+.4	; TEST RESULTS OF MOV
1090	004032	000000		HLT		; MOVB INSTRUCTION FAILED
1091		000156		N=N+1		
1092	004034	000240		SCOPE		
1093	004036	112700	000156	MOVB	#156,%0	; MOVE .BYTE 156 TO REGISTER ZERO
1094	004042	122700	000156	CMPB	#156,%0	; COMPARE REGISTER ZERO FOR N
1095	004046	001401		BEQ	+.4	; TEST RESULTS OF MOV
1096	004050	000000		HLT		; MOVB INSTRUCTION FAILED
1097		000157		N=N+1		
1098	004052	000240		SCOPE		
1099	004054	112700	000157	MOVB	#157,%0	; MOVE .BYTE 157 TO REGISTER ZERO
1100	004060	122700	000157	CMPB	#157,%0	; COMPARE REGISTER ZERO FOR N

1101	004064	001401		BEQ	+.4		;TEST RESULTS OF MOV
1102	004066	000000		HLT			;MOVB INSTRUCTION FAILED
1103		000160		N=N+1			
1104	004070	000240		SCOPE			
1105	004072	112700	000160	MOVB	#160,%0		;MOVE .BYTE 160 TO REGISTER ZERO
1106	004076	122700	000160	CMPB	#160,%0		;COMPARE REGISTER ZERO FOR N
1107	004102	001401		BEQ	+.4		;TEST RESULTS OF MOV
1108	004104	000000		HLT			;MOVB INSTRUCTION FAILED
1109		000161		N=N+1			
1110	004106	000240		SCOPE			
1111	004110	112700	000161	MOVB	#161,%0		;MOVE .BYTE 161 TO REGISTER ZERO
1112	004114	122700	000161	CMPB	#161,%0		;COMPARE REGISTER ZERO FOR N
1113	004120	001401		BEQ	+.4		;TEST RESULTS OF MOV
1114	004122	000000		HLT			;MOVB INSTRUCTION FAILED
1115		000162		N=N+1			
1116	004124	000240		SCOPE			
1117	004126	112700	000162	MOVB	#162,%0		;MOVE .BYTE 162 TO REGISTER ZERO
1118	004132	122700	000162	CMPB	#162,%0		;COMPARE REGISTER ZERO FOR N
1119	004136	001401		BEQ	+.4		;TEST RESULTS OF MOV
1120	004140	000000		HLT			;MOVB INSTRUCTION FAILED
1121		000163		N=N+1			
1122	004142	000240		SCOPE			
1123	004144	112700	000163	MOVB	#163,%0		;MOVE .BYTE 163 TO REGISTER ZERO
1124	004150	122700	000163	CMPB	#163,%0		;COMPARE REGISTER ZERO FOR N
1125	004154	001401		BEQ	+.4		;TEST RESULTS OF MOV
1126	004156	000000		HLT			;MOVB INSTRUCTION FAILED
1127		000164		N=N+1			
1128	004160	000240		SCOPE			
1129	004162	112700	000164	MOVB	#164,%0		;MOVE .BYTE 164 TO REGISTER ZERO
1130	004166	122700	000164	CMPB	#164,%0		;COMPARE REGISTER ZERO FOR N
1131	004172	001401		BEQ	+.4		;TEST RESULTS OF MOV
1132	004174	000000		HLT			;MOVB INSTRUCTION FAILED
1133		000165		N=N+1			
1134	004176	000240		SCOPE			
1135	004200	112700	000165	MOVB	#165,%0		;MOVE .BYTE 165 TO REGISTER ZERO
1136	004204	122700	000165	CMPB	#165,%0		;COMPARE REGISTER ZERO FOR N
1137	004210	001401		BEQ	+.4		;TEST RESULTS OF MOV
1138	004212	000000		HLT			;MOVB INSTRUCTION FAILED
1139		000166		N=N+1			
1140	004214	000240		SCOPE			
1141	004216	112700	000166	MOVB	#166,%0		;MOVE .BYTE 166 TO REGISTER ZERO
1142	004222	122700	000166	CMPB	#166,%0		;COMPARE REGISTER ZERO FOR N
1143	004226	001401		BEQ	+.4		;TEST RESULTS OF MOV
1144	004230	000000		HLT			;MOVB INSTRUCTION FAILED
1145		000167		N=N+1			
1146	004232	000240		SCOPE			
1147	004234	112700	000167	MOVB	#167,%0		;MOVE .BYTE 167 TO REGISTER ZERO
1148	004240	122700	000167	CMPB	#167,%0		;COMPARE REGISTER ZERO FOR N
1149	004244	001401		BEQ	+.4		;TEST RESULTS OF MOV
1150	004246	000000		HLT			;MOVB INSTRUCTION FAILED
1151		000170		N=N+1			
1152	004250	000240		SCOPE			
1153	004252	112700	000170	MOVB	#170,%0		;MOVE .BYTE 170 TO REGISTER ZERO
1154	004256	122700	000170	CMPB	#170,%0		;COMPARE REGISTER ZERO FOR N
1155	004262	001401		BEQ	+.4		;TEST RESULTS OF MOV
1156	004264	000000		HLT			;MOVB INSTRUCTION FAILED

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1157		000171		N=N+1		
1158	004266	000240		SCOPE		
1159	004270	112700	000171	MOVB	#171,%0	; MOVE .BYTE 171 TO REGISTER ZERO
1160	004274	122700	000171	CMPB	#171,%0	; COMPARE REGISTER ZERO FOR N
1161	004300	001401		BEQ	+.4	; TEST RESULTS OF MOV
1162	004302	000000		HLT		; MOVB INSTRUCTION FAILED
1163		000172		N=N+1		
1164	004304	000240		SCOPE		
1165	004306	112700	000172	MOVB	#172,%0	; MOVE .BYTE 172 TO REGISTER ZERO
1166	004312	122700	000172	CMPB	#172,%0	; COMPARE REGISTER ZERO FOR N
1167	004316	001401		BEQ	+.4	; TEST RESULTS OF MOV
1168	004320	000000		HLT		; MOVB INSTRUCTION FAILED
1169		000173		N=N+1		
1170	004322	000240		SCOPE		
1171	004324	112700	000173	MOVB	#173,%0	; MOVE .BYTE 173 TO REGISTER ZERO
1172	004330	122700	000173	CMPB	#173,%0	; COMPARE REGISTER ZERO FOR N
1173	004334	001401		BEQ	+.4	; TEST RESULTS OF MOV
1174	004336	000000		HLT		; MOVB INSTRUCTION FAILED
1175		000174		N=N+1		
1176	004340	000240		SCOPE		
1177	004342	112700	000174	MOVB	#174,%0	; MOVE .BYTE 174 TO REGISTER ZERO
1178	004346	122700	000174	CMPB	#174,%0	; COMPARE REGISTER ZERO FOR N
1179	004352	001401		BEQ	+.4	; TEST RESULTS OF MOV
1180	004354	000000		HLT		; MOVB INSTRUCTION FAILED
1181		000175		N=N+1		
1182	004356	000240		SCOPE		
1183	004360	112700	000175	MOVB	#175,%0	; MOVE .BYTE 175 TO REGISTER ZERO
1184	004364	122700	000175	CMPB	#175,%0	; COMPARE REGISTER ZERO FOR N
1185	004370	001401		BEQ	+.4	; TEST RESULTS OF MOV
1186	004372	000000		HLT		; MOVB INSTRUCTION FAILED
1187		000176		N=N+1		
1188	004374	000240		SCOPE		
1189	004376	112700	000176	MOVB	#176,%0	; MOVE .BYTE 176 TO REGISTER ZERO
1190	004402	122700	000176	CMPB	#176,%0	; COMPARE REGISTER ZERO FOR N
1191	004406	001401		BEQ	+.4	; TEST RESULTS OF MOV
1192	004410	000000		HLT		; MOVB INSTRUCTION FAILED
1193		000177		N=N+1		
1194	004412	000240		SCOPE		
1195	004414	112700	000177	MOVB	#177,%0	; MOVE .BYTE 177 TO REGISTER ZERO
1196	004420	122700	000177	CMPB	#177,%0	; COMPARE REGISTER ZERO FOR N
1197	004424	001401		BEQ	+.4	; TEST RESULTS OF MOV
1198	004426	000000		HLT		; MOVB INSTRUCTION FAILED
1199		000200		N=N+1		
1200	004430	000240		SCOPE		
1201	004432	112700	000200	MOVB	#200,%0	; MOVE .BYTE 200 TO REGISTER ZERO
1202	004436	122700	000200	CMPB	#200,%0	; COMPARE REGISTER ZERO FOR N
1203	004442	001401		BEQ	+.4	; TEST RESULTS OF MOV
1204	004444	000000		HLT		; MOVB INSTRUCTION FAILED
1205		000201		N=N+1		
1206	004446	000240		SCOPE		
1207	004450	112700	000201	MOVB	#201,%0	; MOVE .BYTE 201 TO REGISTER ZERO
1208	004454	122700	000201	CMPB	#201,%0	; COMPARE REGISTER ZERO FOR N
1209	004460	001401		BEQ	+.4	; TEST RESULTS OF MOV
1210	004462	000000		HLT		; MOVB INSTRUCTION FAILED
1211		000202		N=N+1		
1212	004464	000240		SCOPE		

1213	004466	112700	000202	MOVB	#202,%0	;MOVE .BYTE 202 TO REGISTER ZERO
1214	004472	122700	000202	CMPB	#202,%0	;COMPARE REGISTER ZERO FOR N
1215	004476	001401		BEQ	+.4	;TEST RESULTS OF MOV
1216	004500	000000		HLT		;MOVB INSTRUCTION FAILED
1217		000203		N=N+1		
1218	004502	000240		SCOPE		
1219	004504	112700	000203	MOVB	#203,%0	;MOVE .BYTE 203 TO REGISTER ZERO
1220	004510	122700	000203	CMPB	#203,%0	;COMPARE REGISTER ZERO FOR N
1221	004514	001401		BEQ	+.4	;TEST RESULTS OF MOV
1222	004516	000000		HLT		;MOVB INSTRUCTION FAILED
1223		000204		N=N+1		
1224	004520	000240		SCOPE		
1225	004522	112700	000204	MOVB	#204,%0	;MOVE .BYTE 204 TO REGISTER ZERO
1226	004526	122700	000204	CMPB	#204,%0	;COMPARE REGISTER ZERO FOR N
1227	004532	001401		BEQ	+.4	;TEST RESULTS OF MOV
1228	004534	000000		HLT		;MOVB INSTRUCTION FAILED
1229		000205		N=N+1		
1230	004536	000240		SCOPE		
1231	004540	112700	000205	MOVB	#205,%0	;MOVE .BYTE 205 TO REGISTER ZERO
1232	004544	122700	000205	CMPB	#205,%0	;COMPARE REGISTER ZERO FOR N
1233	004550	001401		BEQ	+.4	;TEST RESULTS OF MOV
1234	004552	000000		HLT		;MOVB INSTRUCTION FAILED
1235		000206		N=N+1		
1236	004554	000240		SCOPE		
1237	004556	112700	000206	MOVB	#206,%0	;MOVE .BYTE 206 TO REGISTER ZERO
1238	004562	122700	000206	CMPB	#206,%0	;COMPARE REGISTER ZERO FOR N
1239	004566	001401		BEQ	+.4	;TEST RESULTS OF MOV
1240	004570	000000		HLT		;MOVB INSTRUCTION FAILED
1241		000207		N=N+1		
1242	004572	000240		SCOPE		
1243	004574	112700	000207	MOVB	#207,%0	;MOVE .BYTE 207 TO REGISTER ZERO
1244	004600	122700	000207	CMPB	#207,%0	;COMPARE REGISTER ZERO FOR N
1245	004604	001401		BEQ	+.4	;TEST RESULTS OF MOV
1246	004606	000000		HLT		;MOVB INSTRUCTION FAILED
1247		000210		N=N+1		
1248	004610	000240		SCOPE		
1249	004612	112700	000210	MOVB	#210,%0	;MOVE .BYTE 210 TO REGISTER ZERO
1250	004616	122700	000210	CMPB	#210,%0	;COMPARE REGISTER ZERO FOR N
1251	004622	001401		BEQ	+.4	;TEST RESULTS OF MOV
1252	004624	000000		HLT		;MOVB INSTRUCTION FAILED
1253		000211		N=N+1		
1254	004626	000240		SCOPE		
1255	004630	112700	000211	MOVB	#211,%0	;MOVE .BYTE 211 TO REGISTER ZERO
1256	004634	122700	000211	CMPB	#211,%0	;COMPARE REGISTER ZERO FOR N
1257	004640	001401		BEQ	+.4	;TEST RESULTS OF MOV
1258	004642	000000		HLT		;MOVB INSTRUCTION FAILED
1259		000212		N=N+1		
1260	004644	000240		SCOPE		
1261	004646	112700	000212	MOVB	#212,%0	;MOVE .BYTE 212 TO REGISTER ZERO
1262	004652	122700	000212	CMPB	#212,%0	;COMPARE REGISTER ZERO FOR N
1263	004656	001401		BEQ	+.4	;TEST RESULTS OF MOV
1264	004660	000000		HLT		;MOVB INSTRUCTION FAILED
1265		000213		N=N+1		
1266	004662	000240		SCOPE		
1267	004664	112700	000213	MOVB	#213,%0	;MOVE .BYTE 213 TO REGISTER ZERO
1268	004670	122700	000213	CMPB	#213,%0	;COMPARE REGISTER ZERO FOR N

1269	004674	001401		BEQ	.+4		:TEST RESULTS OF MOV
1270	004676	000000		HLT			:MOVB INSTRUCTION FAILED
1271		000214		N=N+1			
1272	004700	000240		SCOPE			
1273	004702	112700	000214	MOVB	#214,%0		:MOVE .BYTE 214 TO REGISTER ZERO
1274	004706	122700	000214	CMPB	#214,%0		:COMPARE REGISTER ZERO FOR N
1275	004712	001401		BEQ	.+4		:TEST RESULTS OF MOV
1276	004714	000000		HLT			:MOVB INSTRUCTION FAILED
1277		000215		N=N+1			
1278	004716	000240		SCOPE			
1279	004720	112700	000215	MOVB	#215,%0		:MOVE .BYTE 215 TO REGISTER ZERO
1280	004724	122700	000215	CMPB	#215,%0		:COMPARE REGISTER ZERO FOR N
1281	004730	001401		BEQ	.+4		:TEST RESULTS OF MOV
1282	004732	000000		HLT			:MOVB INSTRUCTION FAILED
1283		000216		N=N+1			
1284	004734	000240		SCOPE			
1285	004736	112700	000216	MOVB	#216,%0		:MOVE .BYTE 216 TO REGISTER ZERO
1286	004742	122700	000216	CMPB	#216,%0		:COMPARE REGISTER ZERO FOR N
1287	004746	001401		BEQ	.+4		:TEST RESULTS OF MOV
1288	004750	000000		HLT			:MOVB INSTRUCTION FAILED
1289		000217		N=N+1			
1290	004752	000240		SCOPE			
1291	004754	112700	000217	MOVB	#217,%0		:MOVE .BYTE 217 TO REGISTER ZERO
1292	004760	122700	000217	CMPB	#217,%0		:COMPARE REGISTER ZERO FOR N
1293	004764	001401		BEQ	.+4		:TEST RESULTS OF MOV
1294	004766	000000		HLT			:MOVB INSTRUCTION FAILED
1295		000220		N=N+1			
1296	004770	000240		SCOPE			
1297	004772	112700	000220	MOVB	#220,%0		:MOVE .BYTE 220 TO REGISTER ZERO
1298	004776	122700	000220	CMPB	#220,%0		:COMPARE REGISTER ZERO FOR N
1299	005002	001401		BEQ	.+4		:TEST RESULTS OF MOV
1300	005004	000000		HLT			:MOVB INSTRUCTION FAILED
1301		000221		N=N+1			
1302	005006	000240		SCOPE			
1303	005010	112700	000221	MOVB	#221,%0		:MOVE .BYTE 221 TO REGISTER ZERO
1304	005014	122700	000221	CMPB	#221,%0		:COMPARE REGISTER ZERO FOR N
1305	005020	001401		BEQ	.+4		:TEST RESULTS OF MOV
1306	005022	000000		HLT			:MOVB INSTRUCTION FAILED
1307		000222		N=N+1			
1308	005024	000240		SCOPE			
1309	005026	112700	000222	MOVB	#222,%0		:MOVE .BYTE 222 TO REGISTER ZERO
1310	005032	122700	000222	CMPB	#222,%0		:COMPARE REGISTER ZERO FOR N
1311	005036	001401		BEQ	.+4		:TEST RESULTS OF MOV
1312	005040	000000		HLT			:MOVB INSTRUCTION FAILED
1313		000223		N=N+1			
1314	005042	000240		SCOPE			
1315	005044	112700	000223	MOVB	#223,%0		:MOVE .BYTE 223 TO REGISTER ZERO
1316	005050	122700	000223	CMPB	#223,%0		:COMPARE REGISTER ZERO FOR N
1317	005054	001401		BEQ	.+4		:TEST RESULTS OF MOV
1318	005056	000000		HLT			:MOVB INSTRUCTION FAILED
1319		000224		N=N+1			
1320	005060	000240		SCOPE			
1321	005062	112700	000224	MOVB	#224,%0		:MOVE .BYTE 224 TO REGISTER ZERO
1322	005066	122700	000224	CMPB	#224,%0		:COMPARE REGISTER ZERO FOR N
1323	005072	001401		BEQ	.+4		:TEST RESULTS OF MOV
1324	005074	000000		HLT			:MOVB INSTRUCTION FAILED

1325		000225		N=N+1		
1326	005076	000240		SCOPE		
1327	005100	112700	000225	MOVB	#225,%0	:MOVE .BYTE 225 TO REGISTER ZERO
1328	005104	122700	000225	CMPB	#225,%0	:COMPARE REGISTER ZERO FOR N
1329	005110	001401		BEQ	.+4	:TEST RESULTS OF MOV
1330	005112	000000		HLT		:MOVB INSTRUCTION FAILED
1331		000226		N=N+1		
1332	005114	000240		SCOPE		
1333	005116	112700	000226	MOVB	#226,%0	:MOVE .BYTE 226 TO REGISTER ZERO
1334	005122	122700	000226	CMPB	#226,%0	:COMPARE REGISTER ZERO FOR N
1335	005126	001401		BEQ	.+4	:TEST RESULTS OF MOV
1336	005130	000000		HLT		:MOVB INSTRUCTION FAILED
1337		000227		N=N+1		
1338	005132	000240		SCOPE		
1339	005134	112700	000227	MOVB	#227,%0	:MOVE .BYTE 227 TO REGISTER ZERO
1340	005140	122700	000227	CMPB	#227,%0	:COMPARE REGISTER ZERO FOR N
1341	005144	001401		BEQ	.+4	:TEST RESULTS OF MOV
1342	005146	000000		HLT		:MOVB INSTRUCTION FAILED
1343		000230		N=N+1		
1344	005150	000240		SCOPE		
1345	005152	112700	000230	MOVB	#230,%0	:MOVE .BYTE 230 TO REGISTER ZERO
1346	005156	122700	000230	CMPB	#230,%0	:COMPARE REGISTER ZERO FOR N
1347	005162	001401		BEQ	.+4	:TEST RESULTS OF MOV
1348	005164	000000		HLT		:MOVB INSTRUCTION FAILED
1349		000231		N=N+1		
1350	005166	000240		SCOPE		
1351	005170	112700	000231	MOVB	#231,%0	:MOVE .BYTE 231 TO REGISTER ZERO
1352	005174	122700	000231	CMPB	#231,%0	:COMPARE REGISTER ZERO FOR N
1353	005200	001401		BEQ	.+4	:TEST RESULTS OF MOV
1354	005202	000000		HLT		:MOVB INSTRUCTION FAILED
1355		000232		N=N+1		
1356	005204	000240		SCOPE		
1357	005206	112700	000232	MOVB	#232,%0	:MOVE .BYTE 232 TO REGISTER ZERO
1358	005212	122700	000232	CMPB	#232,%0	:COMPARE REGISTER ZERO FOR N
1359	005216	001401		BEQ	.+4	:TEST RESULTS OF MOV
1360	005220	000000		HLT		:MOVB INSTRUCTION FAILED
1361		000233		N=N+1		
1362	005222	000240		SCOPE		
1363	005224	112700	000233	MOVB	#233,%0	:MOVE .BYTE 233 TO REGISTER ZERO
1364	005230	122700	000233	CMPB	#233,%0	:COMPARE REGISTER ZERO FOR N
1365	005234	001401		BEQ	.+4	:TEST RESULTS OF MOV
1366	005236	000000		HLT		:MOVB INSTRUCTION FAILED
1367		000234		N=N+1		
1368	005240	000240		SCOPE		
1369	005242	112700	000234	MOVB	#234,%0	:MOVE .BYTE 234 TO REGISTER ZERO
1370	005246	122700	000234	CMPB	#234,%0	:COMPARE REGISTER ZERO FOR N
1371	005252	001401		BEQ	.+4	:TEST RESULTS OF MOV
1372	005254	000000		HLT		:MOVB INSTRUCTION FAILED
1373		000235		N=N+1		
1374	005256	000240		SCOPE		
1375	005260	112700	000235	MOVB	#235,%0	:MOVE .BYTE 235 TO REGISTER ZERO
1376	005264	122700	000235	CMPB	#235,%0	:COMPARE REGISTER ZERO FOR N
1377	005270	001401		BEQ	.+4	:TEST RESULTS OF MOV
1378	005272	000000		HLT		:MOVB INSTRUCTION FAILED
1379		000236		N=N+1		
1380	005274	000240		SCOPE		

1381	005276	112700	000236	MOVB	#236,%0	;MOVE .BYTE 236 TO REGISTER ZERO
1382	005302	122700	000236	CMPB	#236,%0	;COMPARE REGISTER ZERO FOR N
1383	005306	001401		BEQ	+.4	;TEST RESULTS OF MOV
1384	005310	000000		HLT		;MOVB INSTRUCTION FAILED
1385		000237		N=N+1		
1386	005312	000240		SCOPE		
1387	005314	112700	000237	MOVB	#237,%0	;MOVE .BYTE 237 TO REGISTER ZERO
1388	005320	122700	000237	CMPB	#237,%0	;COMPARE REGISTER ZERO FOR N
1389	005324	001401		BEQ	+.4	;TEST RESULTS OF MOV
1390	005326	000000		HLT		;MOVB INSTRUCTION FAILED
1391		000240		N=N+1		
1392	005330	000240		SCOPE		
1393	005332	112700	000240	MOVB	#240,%0	;MOVE .BYTE 240 TO REGISTER ZERO
1394	005336	122700	000240	CMPB	#240,%0	;COMPARE REGISTER ZERO FOR N
1395	005342	001401		BEQ	+.4	;TEST RESULTS OF MOV
1396	005344	000000		HLT		;MOVB INSTRUCTION FAILED
1397		000241		N=N+1		
1398	005346	000240		SCOPE		
1399	005350	112700	000241	MOVB	#241,%0	;MOVE .BYTE 241 TO REGISTER ZERO
1400	005354	122700	000241	CMPB	#241,%0	;COMPARE REGISTER ZERO FOR N
1401	005360	001401		BEQ	+.4	;TEST RESULTS OF MOV
1402	005362	000000		HLT		;MOVB INSTRUCTION FAILED
1403		000242		N=N+1		
1404	005364	000240		SCOPE		
1405	005366	112700	000242	MOVB	#242,%0	;MOVE .BYTE 242 TO REGISTER ZERO
1406	005372	122700	000242	CMPB	#242,%0	;COMPARE REGISTER ZERO FOR N
1407	005376	001401		BEQ	+.4	;TEST RESULTS OF MOV
1408	005400	000000		HLT		;MOVB INSTRUCTION FAILED
1409		000243		N=N+1		
1410	005402	000240		SCOPE		
1411	005404	112700	000243	MOVB	#243,%0	;MOVE .BYTE 243 TO REGISTER ZERO
1412	005410	122700	000243	CMPB	#243,%0	;COMPARE REGISTER ZERO FOR N
1413	005414	001401		BEQ	+.4	;TEST RESULTS OF MOV
1414	005416	000000		HLT		;MOVB INSTRUCTION FAILED
1415		000244		N=N+1		
1416	005420	000240		SCOPE		
1417	005422	112700	000244	MOVB	#244,%0	;MOVE .BYTE 244 TO REGISTER ZERO
1418	005426	122700	000244	CMPB	#244,%0	;COMPARE REGISTER ZERO FOR N
1419	005432	001401		BEQ	+.4	;TEST RESULTS OF MOV
1420	005434	000000		HLT		;MOVB INSTRUCTION FAILED
1421		000245		N=N+1		
1422	005436	000240		SCOPE		
1423	005440	112700	000245	MOVB	#245,%0	;MOVE .BYTE 245 TO REGISTER ZERO
1424	005444	122700	000245	CMPB	#245,%0	;COMPARE REGISTER ZERO FOR N
1425	005450	001401		BEQ	+.4	;TEST RESULTS OF MOV
1426	005452	000000		HLT		;MOVB INSTRUCTION FAILED
1427		000246		N=N+1		
1428	005454	000240		SCOPE		
1429	005456	112700	000246	MOVB	#246,%0	;MOVE .BYTE 246 TO REGISTER ZERO
1430	005462	122700	000246	CMPB	#246,%0	;COMPARE REGISTER ZERO FOR N
1431	005466	001401		BEQ	+.4	;TEST RESULTS OF MOV
1432	005470	000000		HLT		;MOVB INSTRUCTION FAILED
1433		000247		N=N+1		
1434	005472	000240		SCOPE		
1435	005474	112700	000247	MOVB	#247,%0	;MOVE .BYTE 247 TO REGISTER ZERO
1436	005500	122700	000247	CMPB	#247,%0	;COMPARE REGISTER ZERO FOR N

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1437	005504	001401		BEQ	.+4		:TEST RESULTS OF MOV
1438	005506	000000		HLT			:MOVB INSTRUCTION FAILED
1439		000250		N=N+1			
1440	005510	000240		SCOPE			
1441	005512	112700	000250	MOVB	#250,%0		:MOVE .BYTE 250 TO REGISTER ZERO
1442	005516	122700	000250	CMPB	#250,%0		:COMPARE REGISTER ZERO FOR N
1443	005522	001401		BEQ	.+4		:TEST RESULTS OF MOV
1444	005524	000000		HLT			:MOVB INSTRUCTION FAILED
1445		000251		N=N+1			
1446	005526	000240		SCOPE			
1447	005530	112700	000251	MOVB	#251,%0		:MOVE .BYTE 251 TO REGISTER ZERO
1448	005534	122700	000251	CMPB	#251,%0		:COMPARE REGISTER ZERO FOR N
1449	005540	001401		BEQ	.+4		:TEST RESULTS OF MOV
1450	005542	000000		HLT			:MOVB INSTRUCTION FAILED
1451		000252		N=N+1			
1452	005544	000240		SCOPE			
1453	005546	112700	000252	MOVB	#252,%0		:MOVE .BYTE 252 TO REGISTER ZERO
1454	005552	122700	000252	CMPB	#252,%0		:COMPARE REGISTER ZERO FOR N
1455	005556	001401		BEQ	.+4		:TEST RESULTS OF MOV
1456	005560	000000		HLT			:MOVB INSTRUCTION FAILED
1457		000253		N=N+1			
1458	005562	000240		SCOPE			
1459	005564	112700	000253	MOVB	#253,%0		:MOVE .BYTE 253 TO REGISTER ZERO
1460	005570	122700	000253	CMPB	#253,%0		:COMPARE REGISTER ZERO FOR N
1461	005574	001401		BEQ	.+4		:TEST RESULTS OF MOV
1462	005576	000000		HLT			:MOVB INSTRUCTION FAILED
1463		000254		N=N+1			
1464	005600	000240		SCOPE			
1465	005602	112700	000254	MOVB	#254,%0		:MOVE .BYTE 254 TO REGISTER ZERO
1466	005606	122700	000254	CMPB	#254,%0		:COMPARE REGISTER ZERO FOR N
1467	005612	001401		BEQ	.+4		:TEST RESULTS OF MOV
1468	005614	000000		HLT			:MOVB INSTRUCTION FAILED
1469		000255		N=N+1			
1470	005616	000240		SCOPE			
1471	005620	112700	000255	MOVB	#255,%0		:MOVE .BYTE 255 TO REGISTER ZERO
1472	005624	122700	000255	CMPB	#255,%0		:COMPARE REGISTER ZERO FOR N
1473	005630	001401		BEQ	.+4		:TEST RESULTS OF MOV
1474	005632	000000		HLT			:MOVB INSTRUCTION FAILED
1475		000256		N=N+1			
1476	005634	000240		SCOPE			
1477	005636	112700	000256	MOVB	#256,%0		:MOVE .BYTE 256 TO REGISTER ZERO
1478	005642	122700	000256	CMPB	#256,%0		:COMPARE REGISTER ZERO FOR N
1479	005646	001401		BEQ	.+4		:TEST RESULTS OF MOV
1480	005650	000000		HLT			:MOVB INSTRUCTION FAILED
1481		000257		N=N+1			
1482	005652	000240		SCOPE			
1483	005654	112700	000257	MOVB	#257,%0		:MOVE .BYTE 257 TO REGISTER ZERO
1484	005660	122700	000257	CMPB	#257,%0		:COMPARE REGISTER ZERO FOR N
1485	005664	001401		BEQ	.+4		:TEST RESULTS OF MOV
1486	005666	000000		HLT			:MOVB INSTRUCTION FAILED
1487		000260		N=N+1			
1488	005670	000240		SCOPE			
1489	005672	112700	000260	MOVB	#260,%0		:MOVE .BYTE 260 TO REGISTER ZERO
1490	005676	122700	000260	CMPB	#260,%0		:COMPARE REGISTER ZERO FOR N
1491	005702	001401		BEQ	.+4		:TEST RESULTS OF MOV
1492	005704	000000		HLT			:MOVB INSTRUCTION FAILED

1493		000261		N=N+1		
1494	005706	000240		SCOPE		
1495	005710	112700	000261	MOVB	#261,%0	; MOVE .BYTE 261 TO REGISTER ZERO
1496	005714	122700	000261	CMPB	#261,%0	; COMPARE REGISTER ZERO FOR N
1497	005720	001401		BEQ	.+4	; TEST RESULTS OF MOV
1498	005722	000000		HLT		; MOVB INSTRUCTION FAILED
1499		000262		N=N+1		
1500	005724	000240		SCOPE		
1501	005726	112700	000262	MOVB	#262,%0	; MOVE .BYTE 262 TO REGISTER ZERO
1502	005732	122700	000262	CMPB	#262,%0	; COMPARE REGISTER ZERO FOR N
1503	005736	001401		BEQ	.+4	; TEST RESULTS OF MOV
1504	005740	000000		HLT		; MOVB INSTRUCTION FAILED
1505		000263		N=N+1		
1506	005742	000240		SCOPE		
1507	005744	112700	000263	MOVB	#263,%0	; MOVE .BYTE 263 TO REGISTER ZERO
1508	005750	122700	000263	CMPB	#263,%0	; COMPARE REGISTER ZERO FOR N
1509	005754	001401		BEQ	.+4	; TEST RESULTS OF MOV
1510	005756	000000		HLT		; MOVB INSTRUCTION FAILED
1511		000264		N=N+1		
1512	005760	000240		SCOPE		
1513	005762	112700	000264	MOVB	#264,%0	; MOVE .BYTE 264 TO REGISTER ZERO
1514	005766	122700	000264	CMPB	#264,%0	; COMPARE REGISTER ZERO FOR N
1515	005772	001401		BEQ	.+4	; TEST RESULTS OF MOV
1516	005774	000000		HLT		; MOVB INSTRUCTION FAILED
1517		000265		N=N+1		
1518	005776	000240		SCOPE		
1519	006000	112700	000265	MOVB	#265,%0	; MOVE .BYTE 265 TO REGISTER ZERO
1520	006004	122700	000265	CMPB	#265,%0	; COMPARE REGISTER ZERO FOR N
1521	006010	001401		BEQ	.+4	; TEST RESULTS OF MOV
1522	006012	000000		HLT		; MOVB INSTRUCTION FAILED
1523		000266		N=N+1		
1524	006014	000240		SCOPE		
1525	006016	112700	000266	MOVB	#266,%0	; MOVE .BYTE 266 TO REGISTER ZERO
1526	006022	122700	000266	CMPB	#266,%0	; COMPARE REGISTER ZERO FOR N
1527	006026	001401		BEQ	.+4	; TEST RESULTS OF MOV
1528	006030	000000		HLT		; MOVB INSTRUCTION FAILED
1529		000267		N=N+1		
1530	006032	000240		SCOPE		
1531	006034	112700	000267	MOVB	#267,%0	; MOVE .BYTE 267 TO REGISTER ZERO
1532	006040	122700	000267	CMPB	#267,%0	; COMPARE REGISTER ZERO FOR N
1533	006044	001401		BEQ	.+4	; TEST RESULTS OF MOV
1534	006046	000000		HLT		; MOVB INSTRUCTION FAILED
1535		000270		N=N+1		
1536	006050	000240		SCOPE		
1537	006052	112700	000270	MOVB	#270,%0	; MOVE .BYTE 270 TO REGISTER ZERO
1538	006056	122700	000270	CMPB	#270,%0	; COMPARE REGISTER ZERO FOR N
1539	006062	001401		BEQ	.+4	; TEST RESULTS OF MOV
1540	006064	000000		HLT		; MOVB INSTRUCTION FAILED
1541		000271		N=N+1		
1542	006066	000240		SCOPE		
1543	006070	112700	000271	MOVB	#271,%0	; MOVE .BYTE 271 TO REGISTER ZERO
1544	006074	122700	000271	CMPB	#271,%0	; COMPARE REGISTER ZERO FOR N
1545	006100	001401		BEQ	.+4	; TEST RESULTS OF MOV
1546	006102	000000		HLT		; MOVB INSTRUCTION FAILED
1547		000272		N=N+1		
1548	006104	000240		SCOPE		

1549	006106	112700	000272	MOVB	#272,%0	;MOVE .BYTE 272 TO REGISTER ZERO
1550	006112	122700	000272	CMPB	#272,%0	;COMPARE REGISTER ZERO FOR N
1551	006116	001401		BEQ	+.4	;TEST RESULTS OF MOV
1552	006120	000000		HLT		;MOVB INSTRUCTION FAILED
1553		000273		N=N+1		
1554	006122	000240		SCOPE		
1555	006124	112700	000273	MOVB	#273,%0	;MOVE .BYTE 273 TO REGISTER ZERO
1556	006130	122700	000273	CMPB	#273,%0	;COMPARE REGISTER ZERO FOR N
1557	006134	001401		BEQ	+.4	;TEST RESULTS OF MOV
1558	006136	000000		HLT		;MOVB INSTRUCTION FAILED
1559		000274		N=N+1		
1560	006140	000240		SCOPE		
1561	006142	112700	000274	MOVB	#274,%0	;MOVE .BYTE 274 TO REGISTER ZERO
1562	006146	122700	000274	CMPB	#274,%0	;COMPARE REGISTER ZERO FOR N
1563	006152	001401		BEQ	+.4	;TEST RESULTS OF MOV
1564	006154	000000		HLT		;MOVB INSTRUCTION FAILED
1565		000275		N=N+1		
1566	006156	000240		SCOPE		
1567	006160	112700	000275	MOVB	#275,%0	;MOVE .BYTE 275 TO REGISTER ZERO
1568	006164	122700	000275	CMPB	#275,%0	;COMPARE REGISTER ZERO FOR N
1569	006170	001401		BEQ	+.4	;TEST RESULTS OF MOV
1570	006172	000000		HLT		;MOVB INSTRUCTION FAILED
1571		000276		N=N+1		
1572	006174	000240		SCOPE		
1573	006176	112700	000276	MOVB	#276,%0	;MOVE .BYTE 276 TO REGISTER ZERO
1574	006202	122700	000276	CMPB	#276,%0	;COMPARE REGISTER ZERO FOR N
1575	006206	001401		BEQ	+.4	;TEST RESULTS OF MOV
1576	006210	000000		HLT		;MOVB INSTRUCTION FAILED
1577		000277		N=N+1		
1578	006212	000240		SCOPE		
1579	006214	112700	000277	MOVB	#277,%0	;MOVE .BYTE 277 TO REGISTER ZERO
1580	006220	122700	000277	CMPB	#277,%0	;COMPARE REGISTER ZERO FOR N
1581	006224	001401		BEQ	+.4	;TEST RESULTS OF MOV
1582	006226	000000		HLT		;MOVB INSTRUCTION FAILED
1583		000300		N=N+1		
1584	006230	000240		SCOPE		
1585	006232	112700	000300	MOVB	#300,%0	;MOVE .BYTE 300 TO REGISTER ZERO
1586	006236	122700	000300	CMPB	#300,%0	;COMPARE REGISTER ZERO FOR N
1587	006242	001401		BEQ	+.4	;TEST RESULTS OF MOV
1588	006244	000000		HLT		;MOVB INSTRUCTION FAILED
1589		000301		N=N+1		
1590	006246	000240		SCOPE		
1591	006250	112700	000301	MOVB	#301,%0	;MOVE .BYTE 301 TO REGISTER ZERO
1592	006254	122700	000301	CMPB	#301,%0	;COMPARE REGISTER ZERO FOR N
1593	006260	001401		BEQ	+.4	;TEST RESULTS OF MOV
1594	006262	000000		HLT		;MOVB INSTRUCTION FAILED
1595		000302		N=N+1		
1596	006264	000240		SCOPE		
1597	006266	112700	000302	MOVB	#302,%0	;MOVE .BYTE 302 TO REGISTER ZERO
1598	006272	122700	000302	CMPB	#302,%0	;COMPARE REGISTER ZERO FOR N
1599	006276	001401		BEQ	+.4	;TEST RESULTS OF MOV
1600	006300	000000		HLT		;MOVB INSTRUCTION FAILED
1601		000303		N=N+1		
1602	006302	000240		SCOPE		
1603	006304	112700	000303	MOVB	#303,%0	;MOVE .BYTE 303 TO REGISTER ZERO
1604	006310	122700	000303	CMPB	#303,%0	;COMPARE REGISTER ZERO FOR N

1605	006314	001401		BEQ	.+4		;TEST RESULTS OF MOV
1606	006316	000000		HLT			;MOVB INSTRUCTION FAILED
1607		000304		N=N+1			
1608	006320	000240		SCOPE			
1609	006322	112700	000304	MOVB	#304,%0		;MOVE .BYTE 304 TO REGISTER ZERO
1610	006326	122700	000304	CMPB	#304,%0		;COMPARE REGISTER ZERO FOR N
1611	006332	001401		BEQ	.+4		;TEST RESULTS OF MOV
1612	006334	000000		HLT			;MOVB INSTRUCTION FAILED
1613		000305		N=N+1			
1614	006336	000240		SCOPE			
1615	006340	112700	000305	MOVB	#305,%0		;MOVE .BYTE 305 TO REGISTER ZERO
1616	006344	122700	000305	CMPB	#305,%0		;COMPARE REGISTER ZERO FOR N
1617	006350	001401		BEQ	.+4		;TEST RESULTS OF MOV
1618	006352	000000		HLT			;MOVB INSTRUCTION FAILED
1619		000306		N=N+1			
1620	006354	000240		SCOPE			
1621	006356	112700	000306	MOVB	#306,%0		;MOVE .BYTE 306 TO REGISTER ZERO
1622	006362	122700	000306	CMPB	#306,%0		;COMPARE REGISTER ZERO FOR N
1623	006366	001401		BEQ	.+4		;TEST RESULTS OF MOV
1624	006370	000000		HLT			;MOVB INSTRUCTION FAILED
1625		000307		N=N+1			
1626	006372	000240		SCOPE			
1627	006374	112700	000307	MOVB	#307,%0		;MOVE .BYTE 307 TO REGISTER ZERO
1628	006400	122700	000307	CMPB	#307,%0		;COMPARE REGISTER ZERO FOR N
1629	006404	001401		BEQ	.+4		;TEST RESULTS OF MOV
1630	006406	000000		HLT			;MOVB INSTRUCTION FAILED
1631		000310		N=N+1			
1632	006410	000240		SCOPE			
1633	006412	112700	000310	MOVB	#310,%0		;MOVE .BYTE 310 TO REGISTER ZERO
1634	006416	122700	000310	CMPB	#310,%0		;COMPARE REGISTER ZERO FOR N
1635	006422	001401		BEQ	.+4		;TEST RESULTS OF MOV
1636	006424	000000		HLT			;MOVB INSTRUCTION FAILED
1637		000311		N=N+1			
1638	006426	000240		SCOPE			
1639	006430	112700	000311	MOVB	#311,%0		;MOVE .BYTE 311 TO REGISTER ZERO
1640	006434	122700	000311	CMPB	#311,%0		;COMPARE REGISTER ZERO FOR N
1641	006440	001401		BEQ	.+4		;TEST RESULTS OF MOV
1642	006442	000000		HLT			;MOVB INSTRUCTION FAILED
1643		000312		N=N+1			
1644	006444	000240		SCOPE			
1645	006446	112700	000312	MOVB	#312,%0		;MOVE .BYTE 312 TO REGISTER ZERO
1646	006452	122700	000312	CMPB	#312,%0		;COMPARE REGISTER ZERO FOR N
1647	006456	001401		BEQ	.+4		;TEST RESULTS OF MOV
1648	006460	000000		HLT			;MOVB INSTRUCTION FAILED
1649		000313		N=N+1			
1650	006462	000240		SCOPE			
1651	006464	112700	000313	MOVB	#313,%0		;MOVE .BYTE 313 TO REGISTER ZERO
1652	006470	122700	000313	CMPB	#313,%0		;COMPARE REGISTER ZERO FOR N
1653	006474	001401		BEQ	.+4		;TEST RESULTS OF MOV
1654	006476	000000		HLT			;MOVB INSTRUCTION FAILED
1655		000314		N=N+1			
1656	006500	000240		SCOPE			
1657	006502	112700	000314	MOVB	#314,%0		;MOVE .BYTE 314 TO REGISTER ZERO
1658	006506	122700	000314	CMPB	#314,%0		;COMPARE REGISTER ZERO FOR N
1659	006512	001401		BEQ	.+4		;TEST RESULTS OF MOV
1660	006514	000000		HLT			;MOVB INSTRUCTION FAILED

1661		000315		N=N+1		
1662	006516	000240		SCOPE		
1663	006520	112700	000315	MOVB	#315,%0	;MOVE .BYTE 315 TO REGISTER ZERO
1664	006524	122700	000315	CMPB	#315,%0	;COMPARE REGISTER ZERO FOR N
1665	006530	001401		BEQ	+.4	;TEST RESULTS OF MOV
1666	006532	000000		HLT		;MOVB INSTRUCTION FAILED
1667		000316		N=N+1		
1668	006534	000240		SCOPE		
1669	006536	112700	000316	MOVB	#316,%0	;MOVE .BYTE 316 TO REGISTER ZERO
1670	006542	122700	000316	CMPB	#316,%0	;COMPARE REGISTER ZERO FOR N
1671	006546	001401		BEQ	+.4	;TEST RESULTS OF MOV
1672	006550	000000		HLT		;MOVB INSTRUCTION FAILED
1673		000317		N=N+1		
1674	006552	000240		SCOPE		
1675	006554	112700	000317	MOVB	#317,%0	;MOVE .BYTE 317 TO REGISTER ZERO
1676	006560	122700	000317	CMPB	#317,%0	;COMPARE REGISTER ZERO FOR N
1677	006564	001401		BEQ	+.4	;TEST RESULTS OF MOV
1678	006566	000000		HLT		;MOVB INSTRUCTION FAILED
1679		000320		N=N+1		
1680	006570	000240		SCOPE		
1681	006572	112700	000320	MOVB	#320,%0	;MOVE .BYTE 320 TO REGISTER ZERO
1682	006576	122700	000320	CMPB	#320,%0	;COMPARE REGISTER ZERO FOR N
1683	006602	001401		BEQ	+.4	;TEST RESULTS OF MOV
1684	006604	000000		HLT		;MOVB INSTRUCTION FAILED
1685		000321		N=N+1		
1686	006606	000240		SCOPE		
1687	006610	112700	000321	MOVB	#321,%0	;MOVE .BYTE 321 TO REGISTER ZERO
1688	006614	122700	000321	CMPB	#321,%0	;COMPARE REGISTER ZERO FOR N
1689	006620	001401		BEQ	+.4	;TEST RESULTS OF MOV
1690	006622	000000		HLT		;MOVB INSTRUCTION FAILED
1691		000322		N=N+1		
1692	006624	000240		SCOPE		
1693	006626	112700	000322	MOVB	#322,%0	;MOVE .BYTE 322 TO REGISTER ZERO
1694	006632	122700	000322	CMPB	#322,%0	;COMPARE REGISTER ZERO FOR N
1695	006636	001401		BEQ	+.4	;TEST RESULTS OF MOV
1696	006640	000000		HLT		;MOVB INSTRUCTION FAILED
1697		000323		N=N+1		
1698	006642	000240		SCOPE		
1699	006644	112700	000323	MOVB	#323,%0	;MOVE .BYTE 323 TO REGISTER ZERO
1700	006650	122700	000323	CMPB	#323,%0	;COMPARE REGISTER ZERO FOR N
1701	006654	001401		BEQ	+.4	;TEST RESULTS OF MOV
1702	006656	000000		HLT		;MOVB INSTRUCTION FAILED
1703		000324		N=N+1		
1704	006660	000240		SCOPE		
1705	006662	112700	000324	MOVB	#324,%0	;MOVE .BYTE 324 TO REGISTER ZERO
1706	006666	122700	000324	CMPB	#324,%0	;COMPARE REGISTER ZERO FOR N
1707	006672	001401		BEQ	+.4	;TEST RESULTS OF MOV
1708	006674	000000		HLT		;MOVB INSTRUCTION FAILED
1709		000325		N=N+1		
1710	006676	000240		SCOPE		
1711	006700	112700	000325	MOVB	#325,%0	;MOVE .BYTE 325 TO REGISTER ZERO
1712	006704	122700	000325	CMPB	#325,%0	;COMPARE REGISTER ZERO FOR N
1713	006710	001401		BEQ	+.4	;TEST RESULTS OF MOV
1714	006712	000000		HLT		;MOVB INSTRUCTION FAILED
1715		000326		N=N+1		
1716	006714	000240		SCOPE		

1717	005716	112700	000326	MOVB	#326,%0	;MOVE .BYTE 326 TO REGISTER ZERO
1718	006722	122700	000326	CMPB	#326,%0	;COMPARE REGISTER ZERO FOR N
1719	006726	001401		BEQ	+.4	;TEST RESULTS OF MOV
1720	006730	000000		HLT		;MOVB INSTRUCTION FAILED
1721		000327		N=N+1		
1722	006732	000240		SCOPE		
1723	006734	112700	000327	MOVB	#327,%0	;MOVE .BYTE 327 TO REGISTER ZERO
1724	006740	122700	000327	CMPB	#327,%0	;COMPARE REGISTER ZERO FOR N
1725	006744	001401		BEQ	+.4	;TEST RESULTS OF MOV
1726	006746	000000		HLT		;MOVB INSTRUCTION FAILED
1727		000330		N=N+1		
1728	006750	000240		SCOPE		
1729	006752	112700	000330	MOVB	#330,%0	;MOVE .BYTE 330 TO REGISTER ZERO
1730	006756	122700	000330	CMPB	#330,%0	;COMPARE REGISTER ZERO FOR N
1731	006762	001401		BEQ	+.4	;TEST RESULTS OF MOV
1732	006764	000000		HLT		;MOVB INSTRUCTION FAILED
1733		000331		N=N+1		
1734	006766	000240		SCOPE		
1735	006770	112700	000331	MOVB	#331,%0	;MOVE .BYTE 331 TO REGISTER ZERO
1736	006774	122700	000331	CMPB	#331,%0	;COMPARE REGISTER ZERO FOR N
1737	007000	001401		BEQ	+.4	;TEST RESULTS OF MOV
1738	007002	000000		HLT		;MOVB INSTRUCTION FAILED
1739		000332		N=N+1		
1740	007004	000240		SCOPE		
1741	007006	112700	000332	MOVB	#332,%0	;MOVE .BYTE 332 TO REGISTER ZERO
1742	007012	122700	000332	CMPB	#332,%0	;COMPARE REGISTER ZERO FOR N
1743	007016	001401		BEQ	+.4	;TEST RESULTS OF MOV
1744	007020	000000		HLT		;MOVB INSTRUCTION FAILED
1745		000333		N=N+1		
1746	007022	000240		SCOPE		
1747	007024	112700	000333	MOVB	#333,%0	;MOVE .BYTE 333 TO REGISTER ZERO
1748	007030	122700	000333	CMPB	#333,%0	;COMPARE REGISTER ZERO FOR N
1749	007034	001401		BEQ	+.4	;TEST RESULTS OF MOV
1750	007036	000000		HLT		;MOVB INSTRUCTION FAILED
1751		000334		N=N+1		
1752	007040	000240		SCOPE		
1753	007042	112700	000334	MOVB	#334,%0	;MOVE .BYTE 334 TO REGISTER ZERO
1754	007046	122700	000334	CMPB	#334,%0	;COMPARE REGISTER ZERO FOR N
1755	007052	001401		BEQ	+.4	;TEST RESULTS OF MOV
1756	007054	000000		HLT		;MOVB INSTRUCTION FAILED
1757		000335		N=N+1		
1758	007056	000240		SCOPE		
1759	007060	112700	000335	MOVB	#335,%0	;MOVE .BYTE 335 TO REGISTER ZERO
1760	007064	122700	000335	CMPB	#335,%0	;COMPARE REGISTER ZERO FOR N
1761	007070	001401		BEQ	+.4	;TEST RESULTS OF MOV
1762	007072	000000		HLT		;MOVB INSTRUCTION FAILED
1763		000336		N=N+1		
1764	007074	000240		SCOPE		
1765	007076	112700	000336	MOVB	#336,%0	;MOVE .BYTE 336 TO REGISTER ZERO
1766	007102	122700	000336	CMPB	#336,%0	;COMPARE REGISTER ZERO FOR N
1767	007106	001401		BEQ	+.4	;TEST RESULTS OF MOV
1768	007110	000000		HLT		;MOVB INSTRUCTION FAILED
1769		000337		N=N+1		
1770	007112	000240		SCOPE		
1771	007114	112700	000337	MOVB	#337,%0	;MOVE .BYTE 337 TO REGISTER ZERO
1772	007120	122700	000337	CMPB	#337,%0	;COMPARE REGISTER ZERO FOR N

1773	007124	001401		BEQ	.+4		;TEST RESULTS OF MOV
1774	007126	000000		HLT			;MOVB INSTRUCTION FAILED
1775		000340		N=N+1			
1776	007130	000240		SCOPE			
1777	007132	112700	000340	MOVB	#340,%0		;MOVE .BYTE 340 TO REGISTER ZERO
1778	007136	122700	000340	CMPB	#340,%0		;COMPARE REGISTER ZERO FOR N
1779	007142	001401		BEQ	.+4		;TEST RESULTS OF MOV
1780	007144	000000		HLT			;MOVB INSTRUCTION FAILED
1781		000341		N=N+1			
1782	007146	000240		SCOPE			
1783	007150	112700	000341	MOVB	#341,%0		;MOVE .BYTE 341 TO REGISTER ZERO
1784	007154	122700	000341	CMPB	#341,%0		;COMPARE REGISTER ZERO FOR N
1785	007160	001401		BEQ	.+4		;TEST RESULTS OF MOV
1786	007162	000000		HLT			;MOVB INSTR ICTION FAILED
1787		000342		N=N+1			
1788	007164	000240		SCOPE			
1789	007166	112700	000342	MOVB	#342,%0		;MOVE .BYTE 342 TO REGISTER ZERO
1790	007172	122700	000342	CMPB	#342,%0		;COMPARE REGISTER ZERO FOR N
1791	007176	001401		BEQ	.+4		;TEST RESULTS OF MOV
1792	007200	000000		HLT			;MOVB INSTRUCTION FAILED
1793		000343		N=N+1			
1794	007202	000240		SCOPE			
1795	007204	112700	000343	MOVB	#343,%0		;MOVE .BYTE 343 TO REGISTER ZERO
1796	007210	122700	000343	CMPB	#343,%0		;COMPARE REGISTER ZERO FOR N
1797	007214	001401		BEQ	.+4		;TEST RESULTS OF MOV
1798	007216	000000		HLT			;MOVB INSTRUCTION FAILED
1799		000344		N=N+1			
1800	007220	000240		SCOPE			
1801	007222	112700	000344	MOVB	#344,%0		;MOVE .BYTE 344 TO REGISTER ZERO
1802	007226	122700	000344	CMPB	#344,%0		;COMPARE REGISTER ZERO FOR N
1803	007232	001401		BEQ	.+4		;TEST RESULTS OF MOV
1804	007234	000000		HLT			;MOVB INSTRUCTION FAILED
1805		000345		N=N+1			
1806	007236	000240		SCOPE			
1807	007240	112700	000345	MOVB	#345,%0		;MOVE .BYTE 345 TO REGISTER ZERO
1808	007244	122700	000345	CMPB	#345,%0		;COMPARE REGISTER ZERO FOR N
1809	007250	001401		BEQ	.+4		;TEST RESULTS OF MOV
1810	007252	000000		HLT			;MOVB INSTRUCTION FAILED
1811		000346		N=N+1			
1812	007254	000240		SCOPE			
1813	007256	112700	000346	MOVB	#346,%0		;MOVE .BYTE 346 TO REGISTER ZERO
1814	007262	122700	000346	CMPB	#346,%0		;COMPARE REGISTER ZERO FOR N
1815	007266	001401		BEQ	.+4		;TEST RESULTS OF MOV
1816	007270	000000		HLT			;MOVB INSTRUCTION FAILED
1817		000347		N=N+1			
1818	007272	000240		SCOPE			
1819	007274	112700	000347	MOVB	#347,%0		;MOVE .BYTE 347 TO REGISTER ZERO
1820	007300	122700	000347	CMPB	#347,%0		;COMPARE REGISTER ZERO FOR N
1821	007304	001401		BEQ	.+4		;TEST RESULTS OF MOV
1822	007306	000000		HLT			;MOVB INSTRUCTION FAILED
1823		000350		N=N+1			
1824	007310	000240		SCOPE			
1825	007312	112700	000350	MOVB	#350,%0		;MOVE .BYTE 350 TO REGISTER ZERO
1826	007316	122700	000350	CMPB	#350,%0		;COMPARE REGISTER ZERO FOR N
1827	007322	001401		BEQ	.+4		;TEST RESULTS OF MOV
1828	007324	000000		HLT			;MOVB INSTRUCTION FAILED

1829		000351		N=N+1		
1830	007326	000240		SCOPE		
1831	007330	112700	000351	MOVB	#351,%0	; MOVE .BYTE 351 TO REGISTER ZERO
1832	007334	122700	000351	CMPB	#351,%0	; COMPARE REGISTER ZERO FOR N
1833	007340	001401		BEQ	+.4	; TEST RESULTS OF MOV
1834	007342	000000		HLT		; MOVB INSTRUCTION FAILED
1835		000352		N=N+1		
1836	007344	000240		SCOPE		
1837	007346	112700	000352	MOVB	#352,%0	; MOVE .BYTE 352 TO REGISTER ZERO
1838	007352	122700	000352	CMPB	#352,%0	; COMPARE REGISTER ZERO FOR N
1839	007356	001401		BEQ	+.4	; TEST RESULTS OF MOV
1840	007360	000000		HLT		; MOVB INSTRUCTION FAILED
1841		000353		N=N+1		
1842	007362	000240		SCOPE		
1843	007364	112700	000353	MOVB	#353,%0	; MOVE .BYTE 353 TO REGISTER ZERO
1844	007370	122700	000353	CMPB	#353,%0	; COMPARE REGISTER ZERO FOR N
1845	007374	001401		BEQ	+.4	; TEST RESULTS OF MOV
1846	007376	000000		HLT		; MOVB INSTRUCTION FAILED
1847		000354		N=N+1		
1848	007400	000240		SCOPE		
1849	007402	112700	000354	MOVB	#354,%0	; MOVE .BYTE 354 TO REGISTER ZERO
1850	007406	122700	000354	CMPB	#354,%0	; COMPARE REGISTER ZERO FOR N
1851	007412	001401		BEQ	+.4	; TEST RESULTS OF MOV
1852	007414	000000		HLT		; MOVB INSTRUCTION FAILED
1853		000355		N=N+1		
1854	007416	000240		SCOPE		
1855	007420	112700	000355	MOVB	#355,%0	; MOVE .BYTE 355 TO REGISTER ZERO
1856	007424	122700	000355	CMPB	#355,%0	; COMPARE REGISTER ZERO FOR N
1857	007430	001401		BEQ	+.4	; TEST RESULTS OF MOV
1858	007432	000000		HLT		; MOVB INSTRUCTION FAILED
1859		000356		N=N+1		
1860	007434	000240		SCOPE		
1861	007436	112700	000356	MOVB	#356,%0	; MOVE .BYTE 356 TO REGISTER ZERO
1862	007442	122700	000356	CMPB	#356,%0	; COMPARE REGISTER ZERO FOR N
1863	007446	001401		BEQ	+.4	; TEST RESULTS OF MOV
1864	007450	000000		HLT		; MOVB INSTRUCTION FAILED
1865		000357		N=N+1		
1866	007452	000240		SCOPE		
1867	007454	112700	000357	MOVB	#357,%0	; MOVE .BYTE 357 TO REGISTER ZERO
1868	007460	122700	000357	CMPB	#357,%0	; COMPARE REGISTER ZERO FOR N
1869	007464	001401		BEQ	+.4	; TEST RESULTS OF MOV
1870	007466	000000		HLT		; MOVB INSTRUCTION FAILED
1871		000360		N=N+1		
1872	007470	000240		SCOPE		
1873	007472	112700	000360	MOVB	#360,%0	; MOVE .BYTE 360 TO REGISTER ZERO
1874	007476	122700	000360	CMPB	#360,%0	; COMPARE REGISTER ZERO FOR N
1875	007502	001401		BEQ	+.4	; TEST RESULTS OF MOV
1876	007504	000000		HLT		; MOVB INSTRUCTION FAILED
1877		000361		N=N+1		
1878	007506	000240		SCOPE		
1879	007510	112700	000361	MOVB	#361,%0	; MOVE .BYTE 361 TO REGISTER ZERO
1880	007514	122700	000361	CMPB	#361,%0	; COMPARE REGISTER ZERO FOR N
1881	007520	001401		BEQ	+.4	; TEST RESULTS OF MOV
1882	007522	000000		HLT		; MOVB INSTRUCTION FAILED
1883		000362		N=N+1		
1884	007524	000240		SCOPE		

1885	007526	112700	000362	MOVB	#362,%0	;MOVE .BYTE 362 TO REGISTER ZERO
1886	007532	122700	000362	CMPB	#362,%0	;COMPARE REGISTER ZERO FOR N
1887	007536	001401		BEQ	.+4	;TEST RESULTS OF MOV
1888	007540	000000		HLT		;MOVB INSTRUCTION FAILED
1889		000363		N=N+1		
1890	007542	000240		SCOPE		
1891	007544	112700	000363	MOVB	#363,%0	;MOVE .BYTE 363 TO REGISTER ZERO
1892	007550	122700	000363	CMPB	#363,%0	;COMPARE REGISTER ZERO FOR N
1893	007554	001401		BEQ	.+4	;TEST RESULTS OF MOV
1894	007556	000000		HLT		;MOVB INSTRUCTION FAILED
1895		000364		N=N+1		
1896	007560	000240		SCOPE		
1897	007562	112700	000364	MOVB	#364,%0	;MOVE .BYTE 364 TO REGISTER ZERO
1898	007566	122700	000364	CMPB	#364,%0	;COMPARE REGISTER ZERO FOR N
1899	007572	001401		BEQ	.+4	;TEST RESULTS OF MOV
1900	007574	000000		HLT		;MOVB INSTRUCTION FAILED
1901		000365		N=N+1		
1902	007576	000240		SCOPE		
1903	007600	112700	000365	MOVB	#365,%0	;MOVE .BYTE 365 TO REGISTER ZERO
1904	007604	122700	000365	CMPB	#365,%0	;COMPARE REGISTER ZERO FOR N
1905	007610	001401		BEQ	.+4	;TEST RESULTS OF MOV
1906	007612	000000		HLT		;MOVB INSTRUCTION FAILED
1907		000366		N=N+1		
1908	007614	000240		SCOPE		
1909	007616	112700	000366	MOVB	#366,%0	;MOVE .BYTE 366 TO REGISTER ZERO
1910	007622	122700	000366	CMPB	#366,%0	;COMPARE REGISTER ZERO FOR N
1911	007626	001401		BEQ	.+4	;TEST RESULTS OF MOV
1912	007630	000000		HLT		;MOVB INSTRUCTION FAILED
1913		000367		N=N+1		
1914	007632	000240		SCOPE		
1915	007634	112700	000367	MOVB	#367,%0	;MOVE .BYTE 367 TO REGISTER ZERO
1916	007640	122700	000367	CMPB	#367,%0	;COMPARE REGISTER ZERO FOR N
1917	007644	001401		BEQ	.+4	;TEST RESULTS OF MOV
1918	007646	000000		HLT		;MOVB INSTRUCTION FAILED
1919		000370		N=N+1		
1920	007650	000240		SCOPE		
1921	007652	112700	000370	MOVB	#370,%0	;MOVE .BYTE 370 TO REGISTER ZERO
1922	007656	122700	000370	CMPB	#370,%0	;COMPARE REGISTER ZERO FOR N
1923	007662	001401		BEQ	.+4	;TEST RESULTS OF MOV
1924	007664	000000		HLT		;MOVB INSTRUCTION FAILED
1925		000371		N=N+1		
1926	007666	000240		SCOPE		
1927	007670	112700	000371	MOVB	#371,%0	;MOVE .BYTE 371 TO REGISTER ZERO
1928	007674	122700	000371	CMPB	#371,%0	;COMPARE REGISTER ZERO FOR N
1929	007700	001401		BEQ	.+4	;TEST RESULTS OF MOV
1930	007702	000000		HLT		;MOVB INSTRUCTION FAILED
1931		000372		N=N+1		
1932	007704	000240		SCOPE		
1933	007706	112700	000372	MOVB	#372,%0	;MOVE .BYTE 372 TO REGISTER ZERO
1934	007712	122700	000372	CMPB	#372,%0	;COMPARE REGISTER ZERO FOR N
1935	007716	001401		BEQ	.+4	;TEST RESULTS OF MOV
1936	007720	000000		HLT		;MOVB INSTRUCTION FAILED
1937		000373		N=N+1		
1938	007722	000240		SCOPE		
1939	007724	112700	000373	MOVB	#373,%0	;MOVE .BYTE 373 TO REGISTER ZERO
1940	007730	122700	000373	CMPB	#373,%0	;COMPARE REGISTER ZERO FOR N

1941	007734	001401		BEQ	.+4		;TEST RESULTS OF MOV
1942	007736	000000		HLT			;MOVB INSTRUCTION FAILED
1943		000374		N=N+1			
1944	007740	000240		SCOPE			
1945	007742	112700	000374	MOVB	#374,%0		;MOVE .BYTE 374 TO REGISTER ZERO
1946	007746	122700	000374	CMPB	#374,%0		;COMPARE REGISTER ZERO FOR N
1947	007752	001401		BEQ	.+4		;TEST RESULTS OF MOV
1948	007754	000000		HLT			;MOVB INSTRUCTION FAILED
1949		000375		N=N+1			
1950	007756	000240		SCOPE			
1951	007760	112700	000375	MOVB	#375,%0		;MOVE .BYTE 375 TO REGISTER ZERO
1952	007764	122700	000375	CMPB	#375,%0		;COMPARE REGISTER ZERO FOR N
1953	007770	001401		BEQ	.+4		;TEST RESULTS OF MOV
1954	007772	000000		HLT			;MOVB INSTRUCTION FAILED
1955		000376		N=N+1			
1956	007774	000240		SCOPE			
1957	007776	112700	000376	MOVB	#376,%0		;MOVE .BYTE 376 TO REGISTER ZERO
1958	010002	122700	000376	CMPB	#376,%0		;COMPARE REGISTER ZERO FOR N
1959	010006	001401		BEQ	.+4		;TEST RESULTS OF MOV
1960	010010	000000		HLT			;MOVB INSTRUCTION FAILED
1961		000377		N=N+1			
1962		000000		N=0			
1963	010012	012700	000000	MOV	#0,%0		;MOVE 0 TO REGISTER ZERO
1964	010016	022700	000000	CMP	#0,%0		;COMPARE REGISTER ZERO FOR 0
1965	010022	001401		BEQ	.+4		;TEST RESULTS OF MOV
1966	010024	000000		HLT			;MOV INSTRUCTION FAILED
1967		000401		N=N+401			
1968	010026	012700	000401	MOV	#401,%0		;MOVE 401 TO REGISTER ZERO
1969	010032	022700	000401	CMP	#401,%0		;COMPARE REGISTER ZERO FOR 401
1970	010036	001401		BEQ	.+4		;TEST RESULTS OF MOV
1971	010040	000000		HLT			;MOV INSTRUCTION FAILED
1972		001002		N=N+401			
1973	010042	012700	001002	MOV	#1002,%0		;MOVE 1002 TO REGISTER ZERO
1974	010046	022700	001002	CMP	#1002,%0		;COMPARE REGISTER ZERO FOR 1002
1975	010052	001401		BEQ	.+4		;TEST RESULTS OF MOV
1976	010054	000000		HLT			;MOV INSTRUCTION FAILED
1977		001403		N=N+401			
1978	010056	012700	001403	MOV	#1403,%0		;MOVE 1403 TO REGISTER ZERO
1979	010062	022700	001403	CMP	#1403,%0		;COMPARE REGISTER ZERO FOR 1403
1980	010066	001401		BEQ	.+4		;TEST RESULTS OF MOV
1981	010070	000000		HLT			;MOV INSTRUCTION FAILED
1982		002004		N=N+401			
1983	010072	012700	002004	MOV	#2004,%0		;MOVE 2004 TO REGISTER ZERO
1984	010076	022700	002004	CMP	#2004,%0		;COMPARE REGISTER ZERO FOR 2004
1985	010102	001401		BEQ	.+4		;TEST RESULTS OF MOV
1986	010104	000000		HLT			;MOV INSTRUCTION FAILED
1987		002405		N=N+401			
1988	010106	012700	002405	MOV	#2405,%0		;MOVE 2405 TO REGISTER ZERO
1989	010112	022700	002405	CMP	#2405,%0		;COMPARE REGISTER ZERO FOR 2405
1990	010116	001401		BEQ	.+4		;TEST RESULTS OF MOV
1991	010120	000000		HLT			;MOV INSTRUCTION FAILED
1992		003006		N=N+401			
1993	010122	012700	003006	MOV	#3006,%0		;MOVE 3006 TO REGISTER ZERO
1994	010126	022700	003006	CMP	#3006,%0		;COMPARE REGISTER ZERO FOR 3006
1995	010132	001401		BEQ	.+4		;TEST RESULTS OF MOV
1996	010134	000000		HLT			;MOV INSTRUCTION FAILED

1997		003407		N=N+401		
1998	010136	012700	003407	MOV	#3407,%0	;MOVE 3407 TO REGISTER ZERO
1999	010142	022700	003407	CMP	#3407,%0	;COMPARE REGISTER ZERO FOR 3407
2000	010146	001401		BEQ	+.4	;TEST RESULTS OF MOV
2001	010150	000000		HLT		;MOV INSTRUCTION FAILED
2002		004010		N=N+401		
2003	010152	012700	004010	MOV	#4010,%0	;MOVE 4010 TO REGISTER ZERO
2004	010156	022700	004010	CMP	#4010,%0	;COMPARE REGISTER ZERO FOR 4010
2005	010162	001401		BEQ	+.4	;TEST RESULTS OF MOV
2006	010164	000000		HLT		;MOV INSTRUCTION FAILED
2007		004411		N=N+401		
2008	010166	012700	004411	MOV	#4411,%0	;MOVE 4411 TO REGISTER ZERO
2009	010172	022700	004411	CMP	#4411,%0	;COMPARE REGISTER ZERO FOR 4411
2010	010176	001401		BEQ	+.4	;TEST RESULTS OF MOV
2011	010200	000000		HLT		;MOV INSTRUCTION FAILED
2012		005012		N=N+401		
2013	010202	012700	005012	MOV	#5012,%0	;MOVE 5012 TO REGISTER ZERO
2014	010206	022700	005012	CMP	#5012,%0	;COMPARE REGISTER ZERO FOR 5012
2015	010212	001401		BEQ	+.4	;TEST RESULTS OF MOV
2016	010214	000000		HLT		;MOV INSTRUCTION FAILED
2017		005413		N=N+401		
2018	010216	012700	005413	MOV	#5413,%0	;MOVE 5413 TO REGISTER ZERO
2019	010222	022700	005413	CMP	#5413,%0	;COMPARE REGISTER ZERO FOR 5413
2020	010226	001401		BEQ	+.4	;TEST RESULTS OF MOV
2021	010230	000000		HLT		;MOV INSTRUCTION FAILED
2022		006014		N=N+401		
2023	010232	012700	006014	MOV	#6014,%0	;MOVE 6014 TO REGISTER ZERO
2024	010236	022700	006014	CMP	#6014,%0	;COMPARE REGISTER ZERO FOR 6014
2025	010242	001401		BEQ	+.4	;TEST RESULTS OF MOV
2026	010244	000000		HLT		;MOV INSTRUCTION FAILED
2027		006415		N=N+401		
2028	010246	012700	006415	MOV	#6415,%0	;MOVE 6415 TO REGISTER ZERO
2029	010252	022700	006415	CMP	#6415,%0	;COMPARE REGISTER ZERO FOR 6415
2030	010256	001401		BEQ	+.4	;TEST RESULTS OF MOV
2031	010260	000000		HLT		;MOV INSTRUCTION FAILED
2032		007016		N=N+401		
2033	010262	012700	007016	MOV	#7016,%0	;MOVE 7016 TO REGISTER ZERO
2034	010266	022700	007016	CMP	#7016,%0	;COMPARE REGISTER ZERO FOR 7016
2035	010272	001401		BEQ	+.4	;TEST RESULTS OF MOV
2036	010274	000000		HLT		;MOV INSTRUCTION FAILED
2037		007417		N=N+401		
2038	010276	012700	007417	MOV	#7417,%0	;MOVE 7417 TO REGISTER ZERO
2039	010302	022700	007417	CMP	#7417,%0	;COMPARE REGISTER ZERO FOR 7417
2040	010306	001401		BEQ	+.4	;TEST RESULTS OF MOV
2041	010310	000000		HLT		;MOV INSTRUCTION FAILED
2042		010020		N=N+401		
2043	010312	012700	010020	MOV	#10020,%0	;MOVE 10020 TO REGISTER ZERO
2044	010316	022700	010020	CMP	#10020,%0	;COMPARE REGISTER ZERO FOR 10020
2045	010322	001401		BEQ	+.4	;TEST RESULTS OF MOV
2046	010324	000000		HLT		;MOV INSTRUCTION FAILED
2047		010421		N=N+401		
2048	010326	012700	010421	MOV	#10421,%0	;MOVE 10421 TO REGISTER ZERO
2049	010332	022700	010421	CMP	#10421,%0	;COMPARE REGISTER ZERO FOR 10421
2050	010336	001401		BEQ	+.4	;TEST RESULTS OF MOV
2051	010340	000000		HLT		;MOV INSTRUCTION FAILED
2052		011022		N=N+401		

2053	010342	012700	011022	MOV	#11022,%0	;MOVE 11022 TO REGISTER ZERO
2054	010346	022700	011022	CMP	#11022,%0	;COMPARE REGISTER ZERO FOR 11022
2055	010352	001401		BEQ	+.4	;TEST RESULTS OF MOV
2056	010354	000000		HLT		;MOV INSTRUCTION FAILED
2057		011423		N=N+401		
2058	010356	012700	011423	MOV	#11423,%0	;MOVE 11423 TO REGISTER ZERO
2059	010362	022700	011423	CMP	#11423,%0	;COMPARE REGISTER ZERO FOR 11423
2060	010366	001401		BEQ	+.4	;TEST RESULTS OF MOV
2061	010370	000000		HLT		;MOV INSTRUCTION FAILED
2062		012024		N=N+401		
2063	010372	012700	012024	MOV	#12024,%0	;MOVE 12024 TO REGISTER ZERO
2064	010376	022700	012024	CMP	#12024,%0	;COMPARE REGISTER ZERO FOR 12024
2065	010402	001401		BEQ	+.4	;TEST RESULTS OF MOV
2066	010404	000000		HLT		;MOV INSTRUCTION FAILED
2067		012425		N=N+401		
2068	010406	012700	012425	MOV	#12425,%0	;MOVE 12425 TO REGISTER ZERO
2069	010412	022700	012425	CMP	#12425,%0	;COMPARE REGISTER ZERO FOR 12425
2070	010416	001401		BEQ	+.4	;TEST RESULTS OF MOV
2071	010420	000000		HLT		;MOV INSTRUCTION FAILED
2072		013026		N=N+401		
2073	010422	012700	013026	MOV	#13026,%0	;MOVE 13026 TO REGISTER ZERO
2074	010426	022700	013026	CMP	#13026,%0	;COMPARE REGISTER ZERO FOR 13026
2075	010432	001401		BEQ	+.4	;TEST RESULTS OF MOV
2076	010434	000000		HLT		;MOV INSTRUCTION FAILED
2077		013427		N=N+401		
2078	010436	012700	013427	MOV	#13427,%0	;MOVE 13427 TO REGISTER ZERO
2079	010442	022700	013427	CMP	#13427,%0	;COMPARE REGISTER ZERO FOR 13427
2080	010446	001401		BEQ	+.4	;TEST RESULTS OF MOV
2081	010450	000000		HLT		;MOV INSTRUCTION FAILED
2082		014030		N=N+401		
2083	010452	012700	014030	MOV	#14030,%0	;MOVE 14030 TO REGISTER ZERO
2084	010456	022700	014030	CMP	#14030,%0	;COMPARE REGISTER ZERO FOR 14030
2085	010462	001401		BEQ	+.4	;TEST RESULTS OF MOV
2086	010464	000000		HLT		;MOV INSTRUCTION FAILED
2087		014431		N=N+401		
2088	010466	012700	014431	MOV	#14431,%0	;MOVE 14431 TO REGISTER ZERO
2089	010472	022700	014431	CMP	#14431,%0	;COMPARE REGISTER ZERO FOR 14431
2090	010476	001401		BEQ	+.4	;TEST RESULTS OF MOV
2091	010500	000000		HLT		;MOV INSTRUCTION FAILED
2092		015032		N=N+401		
2093	010502	012700	015032	MOV	#15032,%0	;MOVE 15032 TO REGISTER ZERO
2094	010506	022700	015032	CMP	#15032,%0	;COMPARE REGISTER ZERO FOR 15032
2095	010512	001401		BEQ	+.4	;TEST RESULTS OF MOV
2096	010514	000000		HLT		;MOV INSTRUCTION FAILED
2097		015433		N=N+401		
2098	010516	012700	015433	MOV	#15433,%0	;MOVE 15433 TO REGISTER ZERO
2099	010522	022700	015433	CMP	#15433,%0	;COMPARE REGISTER ZERO FOR 15433
2100	010526	001401		BEQ	+.4	;TEST RESULTS OF MOV
2101	010530	000000		HLT		;MOV INSTRUCTION FAILED
2102		016034		N=N+401		
2103	010532	012700	016034	MOV	#16034,%0	;MOVE 16034 TO REGISTER ZERO
2104	010536	022700	016034	CMP	#16034,%0	;COMPARE REGISTER ZERO FOR 16034
2105	010542	001401		BEQ	+.4	;TEST RESULTS OF MOV
2106	010544	000000		HLT		;MOV INSTRUCTION FAILED
2107		016435		N=N+401		
2108	010546	012700	016435	MOV	#16435,%0	;MOVE 16435 TO REGISTER ZERO

2109	010552	022700	016435	CMP	#16435,%0	:COMPARE REGISTER ZERO FOR 16435
2110	010556	001401		BEQ	+.4	:TEST RESULTS OF MOV
2111	010560	000000		HLT		:MOV INSTRUCTION FAILED
2112		017036		N=N+401		
2113	010562	012700	017036	MOV	#17036,%0	:MOVE 17036 TO REGISTER ZERO
2114	010566	022700	017036	CMP	#17036,%0	:COMPARE REGISTER ZERO FOR 17036
2115	010572	001401		BEQ	+.4	:TEST RESULTS OF MOV
2116	010574	000000		HLT		:MOV INSTRUCTION FAILED
2117		017437		N=N+401		
2118	010576	012700	017437	MOV	#17437,%0	:MOVE 17437 TO REGISTER ZERO
2119	010602	022700	017437	CMP	#17437,%0	:COMPARE REGISTER ZERO FOR 17437
2120	010606	001401		BEQ	+.4	:TEST RESULTS OF MOV
2121	010610	000000		HLT		:MOV INSTRUCTION FAILED
2122		020040		N=N+401		
2123	010612	012700	020040	MOV	#20040,%0	:MOVE 20040 TO REGISTER ZERO
2124	010616	022700	020040	CMP	#20040,%0	:COMPARE REGISTER ZERO FOR 20040
2125	010622	001401		BEQ	+.4	:TEST RESULTS OF MOV
2126	010624	000000		HLT		:MOV INSTRUCTION FAILED
2127		020441		N=N+401		
2128	010626	012700	020441	MOV	#20441,%0	:MOVE 20441 TO REGISTER ZERO
2129	010632	022700	020441	CMP	#20441,%0	:COMPARE REGISTER ZERO FOR 20441
2130	010636	001401		BEQ	+.4	:TEST RESULTS OF MOV
2131	010640	000000		HLT		:MOV INSTRUCTION FAILED
2132		021042		N=N+401		
2133	010642	012700	021042	MOV	#21042,%0	:MOVE 21042 TO REGISTER ZERO
2134	010646	022700	021042	CMP	#21042,%0	:COMPARE REGISTER ZERO FOR 21042
2135	010652	001401		BEQ	+.4	:TEST RESULTS OF MOV
2136	010654	000000		HLT		:MOV INSTRUCTION FAILED
2137		021443		N=N+401		
2138	010656	012700	021443	MOV	#21443,%0	:MOVE 21443 TO REGISTER ZERO
2139	010662	022700	021443	CMP	#21443,%0	:COMPARE REGISTER ZERO FOR 21443
2140	010666	001401		BEQ	+.4	:TEST RESULTS OF MOV
2141	010670	000000		HLT		:MOV INSTRUCTION FAILED
2142		022044		N=N+401		
2143	010672	012700	022044	MOV	#22044,%0	:MOVE 22044 TO REGISTER ZERO
2144	010676	022700	022044	CMP	#22044,%0	:COMPARE REGISTER ZERO FOR 22044
2145	010702	001401		BEQ	+.4	:TEST RESULTS OF MOV
2146	010704	000000		HLT		:MOV INSTRUCTION FAILED
2147		022445		N=N+401		
2148	010706	012700	022445	MOV	#22445,%0	:MOVE 22445 TO REGISTER ZERO
2149	010712	022700	022445	CMP	#22445,%0	:COMPARE REGISTER ZERO FOR 22445
2150	010716	001401		BEQ	+.4	:TEST RESULTS OF MOV
2151	010720	000000		HLT		:MOV INSTRUCTION FAILED
2152		023046		N=N+401		
2153	010722	012700	023046	MOV	#23046,%0	:MOVE 23046 TO REGISTER ZERO
2154	010726	022700	023046	CMP	#23046,%0	:COMPARE REGISTER ZERO FOR 23046
2155	010732	001401		BEQ	+.4	:TEST RESULTS OF MOV
2156	010734	000000		HLT		:MOV INSTRUCTION FAILED
2157		023447		N=N+401		
2158	010736	012700	023447	MOV	#23447,%0	:MOVE 23447 TO REGISTER ZERO
2159	010742	022700	023447	CMP	#23447,%0	:COMPARE REGISTER ZERO FOR 23447
2160	010746	001401		BEQ	+.4	:TEST RESULTS OF MOV
2161	010750	000000		HLT		:MOV INSTRUCTION FAILED
2162		024050		N=N+401		
2163	010752	012700	024050	MOV	#24050,%0	:MOVE 24050 TO REGISTER ZERO
2164	010756	022700	024050	CMP	#24050,%0	:COMPARE REGISTER ZERO FOR 24050

2165	010762	001401		BEQ	.+4		;TEST RESULTS OF MOV
2166	010764	000000		HLT			;MOV INSTRUCTION FAILED
2167		024451		N=N+401			
2168	010766	012700	024451	MOV	#24451,%0		;MOVE 24451 TO REGISTER ZERO
2169	010772	022700	024451	CMP	#24451,%0		;COMPARE REGISTER ZERO FOR 24451
2170	010776	001401		BEQ	.+4		;TEST RESULTS OF MOV
2171	011000	000000		HLT			;MOV INSTRUCTION FAILED
2172		025052		N=N+401			
2173	011002	012700	025052	MOV	#25052,%0		;MOVE 25052 TO REGISTER ZERO
2174	011006	022700	025052	CMP	#25052,%0		;COMPARE REGISTER ZERO FOR 25052
2175	011012	001401		BEQ	.+4		;TEST RESULTS OF MOV
2176	011014	000000		HLT			;MOV INSTRUCTION FAILED
2177		025453		N=N+401			
2178	011016	012700	025453	MOV	#25453,%0		;MOVE 25453 TO REGISTER ZERO
2179	011022	022700	025453	CMP	#25453,%0		;COMPARE REGISTER ZERO FOR 25453
2180	011026	001401		BEQ	.+4		;TEST RESULTS OF MOV
2181	011030	000000		HLT			;MOV INSTRUCTION FAILED
2182		026054		N=N+401			
2183	011032	012700	026054	MOV	#26054,%0		;MOVE 26054 TO REGISTER ZERO
2184	011036	022700	026054	CMP	#26054,%0		;COMPARE REGISTER ZERO FOR 26054
2185	011042	001401		BEQ	.+4		;TEST RESULTS OF MOV
2186	011044	000000		HLT			;MOV INSTRUCTION FAILED
2187		026455		N=N+401			
2188	011046	012700	026455	MOV	#26455,%0		;MOVE 26455 TO REGISTER ZERO
2189	011052	022700	026455	CMP	#26455,%0		;COMPARE REGISTER ZERO FOR 26455
2190	011056	001401		BEQ	.+4		;TEST RESULTS OF MOV
2191	011060	000000		HLT			;MOV INSTRUCTION FAILED
2192		027056		N=N+401			
2193	011062	012700	027056	MOV	#27056,%0		;MOVE 27056 TO REGISTER ZERO
2194	011066	022700	027056	CMP	#27056,%0		;COMPARE REGISTER ZERO FOR 27056
2195	011072	001401		BEQ	.+4		;TEST RESULTS OF MOV
2196	011074	000000		HLT			;MOV INSTRUCTION FAILED
2197		027457		N=N+401			
2198	011076	012700	027457	MOV	#27457,%0		;MOVE 27457 TO REGISTER ZERO
2199	011102	022700	027457	CMP	#27457,%0		;COMPARE REGISTER ZERO FOR 27457
2200	011106	001401		BEQ	.+4		;TEST RESULTS OF MOV
2201	011110	000000		HLT			;MOV INSTRUCTION FAILED
2202		030060		N=N+401			
2203	011112	012700	030060	MOV	#30060,%0		;MOVE 30060 TO REGISTER ZERO
2204	011116	022700	030060	CMP	#30060,%0		;COMPARE REGISTER ZERO FOR 30060
2205	011122	001401		BEQ	.+4		;TEST RESULTS OF MOV
2206	011124	000000		HLT			;MOV INSTRUCTION FAILED
2207		030461		N=N+401			
2208	011126	012700	030461	MOV	#30461,%0		;MOVE 30461 TO REGISTER ZERO
2209	011132	022700	030461	CMP	#30461,%0		;COMPARE REGISTER ZERO FOR 30461
2210	011136	001401		BEQ	.+4		;TEST RESULTS OF MOV
2211	011140	000000		HLT			;MOV INSTRUCTION FAILED
2212		031062		N=N+401			
2213	011142	012700	031062	MOV	#31062,%0		;MOVE 31062 TO REGISTER ZERO
2214	011146	022700	031062	CMP	#31062,%0		;COMPARE REGISTER ZERO FOR 31062
2215	011152	001401		BEQ	.+4		;TEST RESULTS OF MOV
2216	011154	000000		HLT			;MOV INSTRUCTION FAILED
2217		031463		N=N+401			
2218	011156	012700	031463	MOV	#31463,%0		;MOVE 31463 TO REGISTER ZERO
2219	011162	022700	031463	CMP	#31463,%0		;COMPARE REGISTER ZERO FOR 31463
2220	011166	001401		BEQ	.+4		;TEST RESULTS OF MOV

2221	011170	000000		HLT		;MOV INSTRUCTION FAILED
2222		032064		N=N+401		
2223	011172	012700	032064	MOV	#32064,%0	;MOVE 32064 TO REGISTER ZERO
2224	011176	022700	032064	CMP	#32064,%0	;COMPARE REGISTER ZERO FOR 32064
2225	011202	001401		BEQ	.+4	;TEST RESULTS OF MOV
2226	011204	000000		HLT		;MOV INSTRUCTION FAILED
2227		032465		N=N+401		
2228	011206	012700	032465	MOV	#32465,%0	;MOVE 32465 TO REGISTER ZERO
2229	011212	022700	032465	CMP	#32465,%0	;COMPARE REGISTER ZERO FOR 32465
2230	011216	001401		BEQ	.+4	;TEST RESULTS OF MOV
2231	011220	000000		HLT		;MOV INSTRUCTION FAILED
2232		033066		N=N+401		
2233	011222	012700	033066	MOV	#33066,%0	;MOVE 33066 TO REGISTER ZERO
2234	011226	022700	033066	CMP	#33066,%0	;COMPARE REGISTER ZERO FOR 33066
2235	011232	001401		BEQ	.+4	;TEST RESULTS OF MOV
2236	011234	000000		HLT		;MOV INSTRUCTION FAILED
2237		033467		N=N+401		
2238	011236	012700	033467	MOV	#33467,%0	;MOVE 33467 TO REGISTER ZERO
2239	011242	022700	033467	CMP	#33467,%0	;COMPARE REGISTER ZERO FOR 33467
2240	011246	001401		BEQ	.+4	;TEST RESULTS OF MOV
2241	011250	000000		HLT		;MOV INSTRUCTION FAILED
2242		034070		N=N+401		
2243	011252	012700	034070	MOV	#34070,%0	;MOVE 34070 TO REGISTER ZERO
2244	011256	022700	034070	CMP	#34070,%0	;COMPARE REGISTER ZERO FOR 34070
2245	011262	001401		BEQ	.+4	;TEST RESULTS OF MOV
2246	011264	000000		HLT		;MOV INSTRUCTION FAILED
2247		034471		N=N+401		
2248	011266	012700	034471	MOV	#34471,%0	;MOVE 34471 TO REGISTER ZERO
2249	011272	022700	034471	CMP	#34471,%0	;COMPARE REGISTER ZERO FOR 34471
2250	011276	001401		BEQ	.+4	;TEST RESULTS OF MOV
2251	011300	000000		HLT		;MOV INSTRUCTION FAILED
2252		035072		N=N+401		
2253	011302	012700	035072	MOV	#35072,%0	;MOVE 35072 TO REGISTER ZERO
2254	011306	022700	035072	CMP	#35072,%0	;COMPARE REGISTER ZERO FOR 35072
2255	011312	001401		BEQ	.+4	;TEST RESULTS OF MOV
2256	011314	000000		HLT		;MOV INSTRUCTION FAILED
2257		035473		N=N+401		
2258	011316	012700	035473	MOV	#35473,%0	;MOVE 35473 TO REGISTER ZERO
2259	011322	022700	035473	CMP	#35473,%0	;COMPARE REGISTER ZERO FOR 35473
2260	011326	001401		BEQ	.+4	;TEST RESULTS OF MOV
2261	011330	000000		HLT		;MOV INSTRUCTION FAILED
2262		036074		N=N+401		
2263	011332	012700	036074	MOV	#36074,%0	;MOVE 36074 TO REGISTER ZERO
2264	011336	022700	036074	CMP	#36074,%0	;COMPARE REGISTER ZERO FOR 36074
2265	011342	001401		BEQ	.+4	;TEST RESULTS OF MOV
2266	011344	000000		HLT		;MOV INSTRUCTION FAILED
2267		036475		N=N+401		
2268	011346	012700	036475	MOV	#36475,%0	;MOVE 36475 TO REGISTER ZERO
2269	011352	022700	036475	CMP	#36475,%0	;COMPARE REGISTER ZERO FOR 36475
2270	011356	001401		BEQ	.+4	;TEST RESULTS OF MOV
2271	011360	000000		HLT		;MOV INSTRUCTION FAILED
2272		037076		N=N+401		
2273	011362	012700	037076	MOV	#37076,%0	;MOVE 37076 TO REGISTER ZERO
2274	011366	022700	037076	CMP	#37076,%0	;COMPARE REGISTER ZERO FOR 37076
2275	011372	001401		BEQ	.+4	;TEST RESULTS OF MOV
2276	011374	000000		HLT		;MOV INSTRUCTION FAILED

2277		037477		N=N+401		
2278	011376	012700	037477	MOV	#37477,%0	;MOVE 37477 TO REGISTER ZERO
2279	011402	022700	037477	CMP	#37477,%0	;COMPARE REGISTER ZERO FOR 37477
2280	011406	001401		BEQ	+.4	;TEST RESULTS OF MOV
2281	011410	000000		HLT		;MOV INSTRUCTION FAILED
2282		040100		N=N+401		
2283	011412	012700	040100	MOV	#40100,%0	;MOVE 40100 TO REGISTER ZERO
2284	011416	022700	040100	CMP	#40100,%0	;COMPARE REGISTER ZERO FOR 40100
2285	011422	001401		BEQ	+.4	;TEST RESULTS OF MOV
2286	011424	000000		HLT		;MOV INSTRUCTION FAILED
2287		040501		N=N+401		
2288	011426	012700	040501	MOV	#40501,%0	;MOVE 40501 TO REGISTER ZERO
2289	011432	022700	040501	CMP	#40501,%0	;COMPARE REGISTER ZERO FOR 40501
2290	011436	001401		BEQ	+.4	;TEST RESULTS OF MOV
2291	011440	000000		HLT		;MOV INSTRUCTION FAILED
2292		041102		N=N+401		
2293	011442	012700	041102	MOV	#41102,%0	;MOVE 41102 TO REGISTER ZERO
2294	011446	022700	041102	CMP	#41102,%0	;COMPARE REGISTER ZERO FOR 41102
2295	011452	001401		BEQ	+.4	;TEST RESULTS OF MOV
2296	011454	000000		HLT		;MOV INSTRUCTION FAILED
2297		041503		N=N+401		
2298	011456	012700	041503	MOV	#41503,%0	;MOVE 41503 TO REGISTER ZERO
2299	011462	022700	041503	CMP	#41503,%0	;COMPARE REGISTER ZERO FOR 41503
2300	011466	001401		BEQ	+.4	;TEST RESULTS OF MOV
2301	011470	000000		HLT		;MOV INSTRUCTION FAILED
2302		042104		N=N+401		
2303	011472	012700	042104	MOV	#42104,%0	;MOVE 42104 TO REGISTER ZERO
2304	011476	022700	042104	CMP	#42104,%0	;COMPARE REGISTER ZERO FOR 42104
2305	011502	001401		BEQ	+.4	;TEST RESULTS OF MOV
2306	011504	000000		HLT		;MOV INSTRUCTION FAILED
2307		042505		N=N+401		
2308	011506	012700	042505	MOV	#42505,%0	;MOVE 42505 TO REGISTER ZERO
2309	011512	022700	042505	CMP	#42505,%0	;COMPARE REGISTER ZERO FOR 42505
2310	011516	001401		BEQ	+.4	;TEST RESULTS OF MOV
2311	011520	000000		HLT		;MOV INSTRUCTION FAILED
2312		043106		N=N+401		
2313	011522	012700	043106	MOV	#43106,%0	;MOVE 43106 TO REGISTER ZERO
2314	011526	022700	043106	CMP	#43106,%0	;COMPARE REGISTER ZERO FOR 43106
2315	011532	001401		BEQ	+.4	;TEST RESULTS OF MOV
2316	011534	000000		HLT		;MOV INSTRUCTION FAILED
2317		043507		N=N+401		
2318	011536	012700	043507	MOV	#43507,%0	;MOVE 43507 TO REGISTER ZERO
2319	011542	022700	043507	CMP	#43507,%0	;COMPARE REGISTER ZERO FOR 43507
2320	011546	001401		BEQ	+.4	;TEST RESULTS OF MOV
2321	011550	000000		HLT		;MOV INSTRUCTION FAILED
2322		044110		N=N+401		
2323	011552	012700	044110	MOV	#44110,%0	;MOVE 44110 TO REGISTER ZERO
2324	011556	022700	044110	CMP	#44110,%0	;COMPARE REGISTER ZERO FOR 44110
2325	011562	001401		BEQ	+.4	;TEST RESULTS OF MOV
2326	011564	000000		HLT		;MOV INSTRUCTION FAILED
2327		044511		N=N+401		
2328	011566	012700	044511	MOV	#44511,%0	;MOVE 44511 TO REGISTER ZERO
2329	011572	022700	044511	CMP	#44511,%0	;COMPARE REGISTER ZERO FOR 44511
2330	011576	001401		BEQ	+.4	;TEST RESULTS OF MOV
2331	011600	000000		HLT		;MOV INSTRUCTION FAILED
2332		045112		N=N+401		

2333	011602	012700	045112	MOV	#45112,%0	;MOVE 45112 TO REGISTER ZERO
2334	011606	022700	045112	CMP	#45112,%0	;COMPARE REGISTER ZERO FOR 45112
2335	011612	001401		BEQ	+.4	;TEST RESULTS OF MOV
2336	011614	000000		HLT		;MOV INSTRUCTION FAILED
2337		045513		N=N+401		
2338	011616	012700	045513	MOV	#45513,%0	;MOVE 45513 TO REGISTER ZERO
2339	011622	022700	045513	CMP	#45513,%0	;COMPARE REGISTER ZERO FOR 45513
2340	011626	001401		BEQ	+.4	;TEST RESULTS OF MOV
2341	011630	000000		HLT		;MOV INSTRUCTION FAILED
2342		046114		N=N+401		
2343	011632	012700	046114	MOV	#46114,%0	;MOVE 46114 TO REGISTER ZERO
2344	011636	022700	046114	CMP	#46114,%0	;COMPARE REGISTER ZERO FOR 46114
2345	011642	001401		BEQ	+.4	;TEST RESULTS OF MOV
2346	011644	000000		HLT		;MOV INSTRUCTION FAILED
2347		046515		N=N+401		
2348	011646	012700	046515	MOV	#46515,%0	;MOVE 46515 TO REGISTER ZERO
2349	011652	022700	046515	CMP	#46515,%0	;COMPARE REGISTER ZERO FOR 46515
2350	011656	001401		BEQ	+.4	;TEST RESULTS OF MOV
2351	011660	000000		HLT		;MOV INSTRUCTION FAILED
2352		047116		N=N+401		
2353	011662	012700	047116	MOV	#47116,%0	;MOVE 47116 TO REGISTER ZERO
2354	011666	022700	047116	CMP	#47116,%0	;COMPARE REGISTER ZERO FOR 47116
2355	011672	001401		BEQ	+.4	;TEST RESULTS OF MOV
2356	011674	000000		HLT		;MOV INSTRUCTION FAILED
2357		047517		N=N+401		
2358	011676	012700	047517	MOV	#47517,%0	;MOVE 47517 TO REGISTER ZERO
2359	011702	022700	047517	CMP	#47517,%0	;COMPARE REGISTER ZERO FOR 47517
2360	011706	001401		BEQ	+.4	;TEST RESULTS OF MOV
2361	011710	000000		HLT		;MOV INSTRUCTION FAILED
2362		050120		N=N+401		
2363	011712	012700	050120	MOV	#50120,%0	;MOVE 50120 TO REGISTER ZERO
2364	011716	022700	050120	CMP	#50120,%0	;COMPARE REGISTER ZERO FOR 50120
2365	011722	001401		BEQ	+.4	;TEST RESULTS OF MOV
2366	011724	000000		HLT		;MOV INSTRUCTION FAILED
2367		050521		N=N+401		
2368	011726	012700	050521	MOV	#50521,%0	;MOVE 50521 TO REGISTER ZERO
2369	011732	022700	050521	CMP	#50521,%0	;COMPARE REGISTER ZERO FOR 50521
2370	011736	001401		BEQ	+.4	;TEST RESULTS OF MOV
2371	011740	000000		HLT		;MOV INSTRUCTION FAILED
2372		051122		N=N+401		
2373	011742	012700	051122	MOV	#51122,%0	;MOVE 51122 TO REGISTER ZERO
2374	011746	022700	051122	CMP	#51122,%0	;COMPARE REGISTER ZERO FOR 51122
2375	011752	001401		BEQ	+.4	;TEST RESULTS OF MOV
2376	011754	000000		HLT		;MOV INSTRUCTION FAILED
2377		051523		N=N+401		
2378	011756	012700	051523	MOV	#51523,%0	;MOVE 51523 TO REGISTER ZERO
2379	011762	022700	051523	CMP	#51523,%0	;COMPARE REGISTER ZERO FOR 51523
2380	011766	001401		BEQ	+.4	;TEST RESULTS OF MOV
2381	011770	000000		HLT		;MOV INSTRUCTION FAILED
2382		052124		N=N+401		
2383	011772	012700	052124	MOV	#52124,%0	;MOVE 52124 TO REGISTER ZERO
2384	011776	022700	052124	CMP	#52124,%0	;COMPARE REGISTER ZERO FOR 52124
2385	012002	001401		BEQ	+.4	;TEST RESULTS OF MOV
2386	012004	000000		HLT		;MOV INSTRUCTION FAILED
2387		052525		N=N+401		
2388	012006	012700	052525	MOV	#52525,%0	;MOVE 52525 TO REGISTER ZERO

2389	012012	022700	052525	CMP	#52525,%0		;COMPARE REGISTER ZERO FOR 52525
2390	012016	001401		BEQ	.+4		;TEST RESULTS OF MOV
2391	012020	000000		HLT			;MOV INSTRUCTION FAILED
2392		053126		N=N+401			
2393	012022	012700	053126	MOV	#53126,%0		;MOVE 53126 TO REGISTER ZERO
2394	012026	022700	053126	CMP	#53126,%0		;COMPARE REGISTER ZERO FOR 53126
2395	012032	001401		BEQ	.+4		;TEST RESULTS OF MOV
2396	012034	000000		HLT			;MOV INSTRUCTION FAILED
2397		053527		N=N+401			
2398	012036	012700	053527	MOV	#53527,%0		;MOVE 53527 TO REGISTER ZERO
2399	012042	022700	053527	CMP	#53527,%0		;COMPARE REGISTER ZERO FOR 53527
2400	012046	001401		BEQ	.+4		;TEST RESULTS OF MOV
2401	012050	000000		HLT			;MOV INSTRUCTION FAILED
2402		054130		N=N+401			
2403	012052	012700	054130	MOV	#54130,%0		;MOVE 54130 TO REGISTER ZERO
2404	012056	022700	054130	CMP	#54130,%0		;COMPARE REGISTER ZERO FOR 54130
2405	012062	001401		BEQ	.+4		;TEST RESULTS OF MOV
2406	012064	000000		HLT			;MOV INSTRUCTION FAILED
2407		054531		N=N+401			
2408	012066	012700	054531	MOV	#54531,%0		;MOVE 54531 TO REGISTER ZERO
2409	012072	022700	054531	CMP	#54531,%0		;COMPARE REGISTER ZERO FOR 54531
2410	012076	001401		BEQ	.+4		;TEST RESULTS OF MOV
2411	012100	000000		HLT			;MOV INSTRUCTION FAILED
2412		055132		N=N+401			
2413	012102	012700	055132	MOV	#55132,%0		;MOVE 55132 TO REGISTER ZERO
2414	012106	022700	055132	CMP	#55132,%0		;COMPARE REGISTER ZERO FOR 55132
2415	012112	001401		BEQ	.+4		;TEST RESULTS OF MOV
2416	012114	000000		HLT			;MOV INSTRUCTION FAILED
2417		055533		N=N+401			
2418	012116	012700	055533	MOV	#55533,%0		;MOVE 55533 TO REGISTER ZERO
2419	012122	022700	055533	CMP	#55533,%0		;COMPARE REGISTER ZERO FOR 55533
2420	012126	001401		BEQ	.+4		;TEST RESULTS OF MOV
2421	012130	000000		HLT			;MOV INSTRUCTION FAILED
2422		056134		N=N+401			
2423	012132	012700	056134	MOV	#56134,%0		;MOVE 56134 TO REGISTER ZERO
2424	012136	022700	056134	CMP	#56134,%0		;COMPARE REGISTER ZERO FOR 56134
2425	012142	001401		BEQ	.+4		;TEST RESULTS OF MOV
2426	012144	000000		HLT			;MOV INSTRUCTION FAILED
2427		056535		N=N+401			
2428	012146	012700	056535	MOV	#56535,%0		;MOVE 56535 TO REGISTER ZERO
2429	012152	022700	056535	CMP	#56535,%0		;COMPARE REGISTER ZERO FOR 56535
2430	012156	001401		BEQ	.+4		;TEST RESULTS OF MOV
2431	012160	000000		HLT			;MOV INSTRUCTION FAILED
2432		057136		N=N+401			
2433	012162	012700	057136	MOV	#57136,%0		;MOVE 57136 TO REGISTER ZERO
2434	012166	022700	057136	CMP	#57136,%0		;COMPARE REGISTER ZERO FOR 57136
2435	012172	001401		BEQ	.+4		;TEST RESULTS OF MOV
2436	012174	000000		HLT			;MOV INSTRUCTION FAILED
2437		057537		N=N+401			
2438	012176	012700	057537	MOV	#57537,%0		;MOVE 57537 TO REGISTER ZERO
2439	012202	022700	057537	CMP	#57537,%0		;COMPARE REGISTER ZERO FOR 57537
2440	012206	001401		BEQ	.+4		;TEST RESULTS OF MOV
2441	012210	000000		HLT			;MOV INSTRUCTION FAILED
2442		060140		N=N+401			
2443	012212	012700	060140	MOV	#60140,%0		;MOVE 60140 TO REGISTER ZERO
2444	012216	022700	060140	CMP	#60140,%0		;COMPARE REGISTER ZERO FOR 60140

2445	012222	001401		BEQ	+.4		;TEST RESULTS OF MOV
2446	012224	000000		HLT			;MOV INSTRUCTION FAILED
2447		060541		N=N+401			
2448	012226	012700	060541	MOV	#60541,%0		;MOVE 60541 TO REGISTER ZERO
2449	012232	022700	060541	CMP	#60541,%0		;COMPARE REGISTER ZERO FOR 60541
2450	012236	001401		BEQ	+.4		;TEST RESULTS OF MOV
2451	012240	000000		HLT			;MOV INSTRUCTION FAILED
2452		061142		N=N+401			
2453	012242	012700	061142	MOV	#61142,%0		;MOVE 61142 TO REGISTER ZERO
2454	012246	022700	061142	CMP	#61142,%0		;COMPARE REGISTER ZERO FOR 61142
2455	012252	001401		BEQ	+.4		;TEST RESULTS OF MOV
2456	012254	000000		HLT			;MOV INSTRUCTION FAILED
2457		061543		N=N+401			
2458	012256	012700	061543	MOV	#61543,%0		;MOVE 61543 TO REGISTER ZERO
2459	012262	022700	061543	CMP	#61543,%0		;COMPARE REGISTER ZERO FOR 61543
2460	012266	001401		BEQ	+.4		;TEST RESULTS OF MOV
2461	012270	000000		HLT			;MOV INSTRUCTION FAILED
2462		062144		N=N+401			
2463	012272	012700	062144	MOV	#62144,%0		;MOVE 62144 TO REGISTER ZERO
2464	012276	022700	062144	CMP	#62144,%0		;COMPARE REGISTER ZERO FOR 62144
2465	012302	001401		BEQ	+.4		;TEST RESULTS OF MOV
2466	012304	000000		HLT			;MOV INSTRUCTION FAILED
2467		062545		N=N+401			
2468	012306	012700	062545	MOV	#62545,%0		;MOVE 62545 TO REGISTER ZERO
2469	012312	022700	062545	CMP	#62545,%0		;COMPARE REGISTER ZERO FOR 62545
2470	012316	001401		BEQ	+.4		;TEST RESULTS OF MOV
2471	012320	000000		HLT			;MOV INSTRUCTION FAILED
2472		063146		N=N+401			
2473	012322	012700	063146	MOV	#63146,%0		;MOVE 63146 TO REGISTER ZERO
2474	012326	022700	063146	CMP	#63146,%0		;COMPARE REGISTER ZERO FOR 63146
2475	012332	001401		BEQ	+.4		;TEST RESULTS OF MOV
2476	012334	000000		HLT			;MOV INSTRUCTION FAILED
2477		063547		N=N+401			
2478	012336	012700	063547	MOV	#63547,%0		;MOVE 63547 TO REGISTER ZERO
2479	012342	022700	063547	CMP	#63547,%0		;COMPARE REGISTER ZERO FOR 63547
2480	012346	001401		BEQ	+.4		;TEST RESULTS OF MOV
2481	012350	000000		HLT			;MOV INSTRUCTION FAILED
2482		064150		N=N+401			
2483	012352	012700	064150	MOV	#64150,%0		;MOVE 64150 TO REGISTER ZERO
2484	012356	022700	064150	CMP	#64150,%0		;COMPARE REGISTER ZERO FOR 64150
2485	012362	001401		BEQ	+.4		;TEST RESULTS OF MOV
2486	012364	000000		HLT			;MOV INSTRUCTION FAILED
2487		064551		N=N+401			
2488	012366	012700	064551	MOV	#64551,%0		;MOVE 64551 TO REGISTER ZERO
2489	012372	022700	064551	CMP	#64551,%0		;COMPARE REGISTER ZERO FOR 64551
2490	012376	001401		BEQ	+.4		;TEST RESULTS OF MOV
2491	012400	000000		HLT			;MOV INSTRUCTION FAILED
2492		065152		N=N+401			
2493	012402	012700	065152	MOV	#65152,%0		;MOVE 65152 TO REGISTER ZERO
2494	012406	022700	065152	CMP	#65152,%0		;COMPARE REGISTER ZERO FOR 65152
2495	012412	001401		BEQ	+.4		;TEST RESULTS OF MOV
2496	012414	000000		HLT			;MOV INSTRUCTION FAILED
2497		065553		N=N+401			
2498	012416	012700	065553	MOV	#65553,%0		;MOVE 65553 TO REGISTER ZERO
2499	012422	022700	065553	CMP	#65553,%0		;COMPARE REGISTER ZERO FOR 65553
2500	012426	001401		BEQ	+.4		;TEST RESULTS OF MOV

2501	012430	000000		HLT		;MOV INSTRUCTION FAILED
2502		066154		N=N+401		
2503	012432	012700	066154	MOV	#66154,%0	;MOVE 66154 TO REGISTER ZERO
2504	012436	022700	066154	CMP	#66154,%0	;COMPARE REGISTER ZERO FOR 66154
2505	012442	001401		BEQ	+.4	;TEST RESULTS OF MOV
2506	012444	000000		HLT		;MOV INSTRUCTION FAILED
2507		066555		N=N+401		
2508	012446	012700	066555	MOV	#66555,%0	;MOVE 66555 TO REGISTER ZERO
2509	012452	022700	066555	CMP	#66555,%0	;COMPARE REGISTER ZERO FOR 66555
2510	012456	001401		BEQ	+.4	;TEST RESULTS OF MOV
2511	012460	000000		HLT		;MOV INSTRUCTION FAILED
2512		067156		N=N+401		
2513	012462	012700	067156	MOV	#67156,%0	;MOVE 67156 TO REGISTER ZERO
2514	012466	022700	067156	CMP	#67156,%0	;COMPARE REGISTER ZERO FOR 67156
2515	012472	001401		BEQ	+.4	;TEST RESULTS OF MOV
2516	012474	000000		HLT		;MOV INSTRUCTION FAILED
2517		067557		N=N+401		
2518	012476	012700	067557	MOV	#67557,%0	;MOVE 67557 TO REGISTER ZERO
2519	012502	022700	067557	CMP	#67557,%0	;COMPARE REGISTER ZERO FOR 67557
2520	012506	001401		BEQ	+.4	;TEST RESULTS OF MOV
2521	012510	000000		HLT		;MOV INSTRUCTION FAILED
2522		070160		N=N+401		
2523	012512	012700	070160	MOV	#70160,%0	;MOVE 70160 TO REGISTER ZERO
2524	012516	022700	070160	CMP	#70160,%0	;COMPARE REGISTER ZERO FOR 70160
2525	012522	001401		BEQ	+.4	;TEST RESULTS OF MOV
2526	012524	000000		HLT		;MOV INSTRUCTION FAILED
2527		070561		N=N+401		
2528	012526	012700	070561	MOV	#70561,%0	;MOVE 70561 TO REGISTER ZERO
2529	012532	022700	070561	CMP	#70561,%0	;COMPARE REGISTER ZERO FOR 70561
2530	012536	001401		BEQ	+.4	;TEST RESULTS OF MOV
2531	012540	000000		HLT		;MOV INSTRUCTION FAILED
2532		071162		N=N+401		
2533	012542	012700	071162	MOV	#71162,%0	;MOVE 71162 TO REGISTER ZERO
2534	012546	022700	071162	CMP	#71162,%0	;COMPARE REGISTER ZERO FOR 71162
2535	012552	001401		BEQ	+.4	;TEST RESULTS OF MOV
2536	012554	000000		HLT		;MOV INSTRUCTION FAILED
2537		071563		N=N+401		
2538	012556	012700	071563	MOV	#71563,%0	;MOVE 71563 TO REGISTER ZERO
2539	012562	022700	071563	CMP	#71563,%0	;COMPARE REGISTER ZERO FOR 71563
2540	012566	001401		BEQ	+.4	;TEST RESULTS OF MOV
2541	012570	000000		HLT		;MOV INSTRUCTION FAILED
2542		072164		N=N+401		
2543	012572	012700	072164	MOV	#72164,%0	;MOVE 72164 TO REGISTER ZERO
2544	012576	022700	072164	CMP	#72164,%0	;COMPARE REGISTER ZERO FOR 72164
2545	012602	001401		BEQ	+.4	;TEST RESULTS OF MOV
2546	012604	000000		HLT		;MOV INSTRUCTION FAILED
2547		072565		N=N+401		
2548	012606	012700	072565	MOV	#72565,%0	;MOVE 72565 TO REGISTER ZERO
2549	012612	022700	072565	CMP	#72565,%0	;COMPARE REGISTER ZERO FOR 72565
2550	012616	001401		BEQ	+.4	;TEST RESULTS OF MOV
2551	012620	000000		HLT		;MOV INSTRUCTION FAILED
2552		073166		N=N+401		
2553	012622	012700	073166	MOV	#73166,%0	;MOVE 73166 TO REGISTER ZERO
2554	012626	022700	073166	CMP	#73166,%0	;COMPARE REGISTER ZERO FOR 73166
2555	012632	001401		BEQ	+.4	;TEST RESULTS OF MOV
2556	012634	000000		HLT		;MOV INSTRUCTION FAILED

2557		073567		N=N+401		
2558	012636	012700	073567	MOV	#73567,%0	;MOVE 73567 TO REGISTER ZERO
2559	012642	022700	073567	CMP	#73567,%0	;COMPARE REGISTER ZERO FOR 73567
2560	012646	001401		BEQ	+.4	;TEST RESULTS OF MOV
2561	012650	000000		HLT		;MOV INSTRUCTION FAILED
2562		074170		N=N+401		
2563	012652	012700	074170	MOV	#74170,%0	;MOVE 74170 TO REGISTER ZERO
2564	012656	022700	074170	CMP	#74170,%0	;COMPARE REGISTER ZERO FOR 74170
2565	012662	001401		BEQ	+.4	;TEST RESULTS OF MOV
2566	012664	000000		HLT		;MOV INSTRUCTION FAILED
2567		074571		N=N+401		
2568	012666	012700	074571	MOV	#74571,%0	;MOVE 74571 TO REGISTER ZERO
2569	012672	022700	074571	CMP	#74571,%0	;COMPARE REGISTER ZERO FOR 74571
2570	012676	001401		BEQ	+.4	;TEST RESULTS OF MOV
2571	012700	000000		HLT		;MOV INSTRUCTION FAILED
2572		075172		N=N+401		
2573	012702	012700	075172	MOV	#75172,%0	;MOVE 75172 TO REGISTER ZERO
2574	012706	022700	075172	CMP	#75172,%0	;COMPARE REGISTER ZERO FOR 75172
2575	012712	001401		BEQ	+.4	;TEST RESULTS OF MOV
2576	012714	000000		HLT		;MOV INSTRUCTION FAILED
2577		075573		N=N+401		
2578	012716	012700	075573	MOV	#75573,%0	;MOVE 75573 TO REGISTER ZERO
2579	012722	022700	075573	CMP	#75573,%0	;COMPARE REGISTER ZERO FOR 75573
2580	012726	001401		BEQ	+.4	;TEST RESULTS OF MOV
2581	012730	000000		HLT		;MOV INSTRUCTION FAILED
2582		076174		N=N+401		
2583	012732	012700	076174	MOV	#76174,%0	;MOVE 76174 TO REGISTER ZERO
2584	012736	022700	076174	CMP	#76174,%0	;COMPARE REGISTER ZERO FOR 76174
2585	012742	001401		BEQ	+.4	;TEST RESULTS OF MOV
2586	012744	000000		HLT		;MOV INSTRUCTION FAILED
2587		076575		N=N+401		
2588	012746	012700	076575	MOV	#76575,%0	;MOVE 76575 TO REGISTER ZERO
2589	012752	022700	076575	CMP	#76575,%0	;COMPARE REGISTER ZERO FOR 76575
2590	012756	001401		BEQ	+.4	;TEST RESULTS OF MOV
2591	012760	000000		HLT		;MOV INSTRUCTION FAILED
2592		077176		N=N+401		
2593	012762	012700	077176	MOV	#77176,%0	;MOVE 77176 TO REGISTER ZERO
2594	012766	022700	077176	CMP	#77176,%0	;COMPARE REGISTER ZERO FOR 77176
2595	012772	001401		BEQ	+.4	;TEST RESULTS OF MOV
2596	012774	000000		HLT		;MOV INSTRUCTION FAILED
2597		077577		N=N+401		
2598	012776	012700	077577	MOV	#77577,%0	;MOVE 77577 TO REGISTER ZERO
2599	013002	022700	077577	CMP	#77577,%0	;COMPARE REGISTER ZERO FOR 77577
2600	013006	001401		BEQ	+.4	;TEST RESULTS OF MOV
2601	013010	000000		HLT		;MOV INSTRUCTION FAILED
2602		100200		N=N+401		
2603						
2604	013012	010700		MOV	%7,%0	
2605	013014	022700	013014	CMP	%,%0	
2606	013020	001401		BEQ	+.4	
2607	013022	000000		HLT		
2608	013024	110700		MOVB	%7,%0	
2609	013026	122700	013026	CMPB	%,%0	
2610	013032	001401		BEQ	+.4	
2611	013034	000000		HLT		
2612	013036	016700	000410	MOV	VALUE,%0	

2613	013042	022700	123456		CMP	#123456,%0	
2614	013046	001401			BEQ	.+4	
2615	013050	000000			HLT		
2616	013052	116700	000374		MOVB	VALUE,%0	
2617	013056	122700	000456		CMPB	#456,%0	
2618	013062	001401			BEQ	.+4	
2619	013064	000000			HLT		
2620	013066	116700	000361		MOVB	VALUE+1,%0	
2621	013072	122700	000247		CMPB	#247,%0	
2622	013076	001401			BEQ	.+4	
2623	013100	000000			HLT		
2624							
2625	013102	005067	000346		CLR	SVAL	
2626	013106	112767	000456	000340	MOVB	#456,SVAL	;MOVE #456 TO SVAL
2627	013114	126767	000332	000332	CMPB	VALUE,SVAL	;COMPARE VALUE TO SVAL
2628	013122	001401			BEQ	.+4	;TEST FOR EQUAL
2629	013124	000000			HLT		;MOVE INSTRUCTION FAILED
2630	013126	000240			SCOPE		
2631	013130	005067	000320		CLR	SVAL	
2632	013134	116767	000313	000313	MOVB	VALUE+1,SVAL+1	;MOVE VALUE+1 TO SVAL+1
2633	013142	126727	000307	000247	CMPB	SVAL+1,#247	;COMPARE SVAL+1 TO #247
2634	013150	001401			BEQ	.+4	;TEST FOR EQUAL
2635	013152	000000			HLT		;MOVE INSTRUCTION FAILED
2636	013154	000240			SCOPE		
2637	013156	005067	000272		CLR	SVAL	
2638	013162	116767	000265	000264	MOVB	VALUE+1,SVAL	;MOVE VALUE+1 TO SVAL
2639	013170	122767	000247	000256	CMPB	#247,SVAL	;COMPARE #247 TO SVAL
2640	013176	001401			BEQ	.+4	;TEST FOR EQUAL
2641	013200	000000			HLT		;MOVE INSTRUCTION FAILED
2642	013202	000240			SCOPE		
2643	013204	005067	000244		CLR	SVAL	
2644	013210	116767	000236	000237	MOVB	VALUE,SVAL+1	;MOVE VALUE TO SVAL+1
2645	013216	122767	027456	000231	CMPB	#027456,SVAL+1	;COMPARE #027456 TO SVAL+1
2646	013224	001401			BEQ	.+4	;TEST FOR EQUAL
2647	013226	000000			HLT		;MOVE INSTRUCTION FAILED
2648	013230	000240			SCOPE		
2649	013232	005067	000216		CLR	SVAL	
2650	013236	116700	000211		MOVB	VALUE+1,%0	;MOVE VALUE+1 TO %0
2651	013242	122700	000247		CMPB	#247,%0	;COMPARE #247 TO %0
2652	013246	001401			BEQ	.+4	;TEST FOR EQUAL
2653	013250	000000			HLT		;MOVE INSTRUCTION FAILED
2654	013252	000240			SCOPE		
2655	013254	005067	000174		CLR	SVAL	
2656	013260	116700	000166		MOVB	VALUE,%0	;MOVE VALUE TO %0
2657	013264	122700	000456		CMPB	#456,%0	;COMPARE #456 TO %0
2658	013270	001401			BEQ	.+4	;TEST FOR EQUAL
2659	013272	000000			HLT		;MOVE INSTRUCTION FAILED
2660	013274	000240			SCOPE		
2661							
2662	013276	012767	177777	000150	MOV	#-1,SVAL	
2663	013304	012700	123456		MOV	#123456,%0	
2664	013310	110067	000140		MOVB	%0,SVAL	;REG TO LO .BYTE
2665	013314	022767	177456	000132	CMP	#177456,SVAL	
2666	013322	001401			BEQ	.+4	
2667	013324	000000			HLT		;MOVB REG TO LOW .BYTE FAILED
2668	013326	000240			SCOPE		

2669							
2670	013330	012767	177777	000116		MOV	#-1,SVAL
2671	013336	012700	123456			MOV	#123456,%0
2672	013342	110067	000107			MOVB	%0,SVAL+1
2673	013346	022767	027377	000100		CMP	#027377,SVAL
2674	013354	001401				BEQ	+.4
2675	013356	000000				HLT	
2676	013360	000240				SCOPE	
2677							
2678							
2679	013362	012700	177777				
2680	013366	112700	000177			MOV	#-1,%0
2681	013372	020027	000177			MOVB	#177,%0
2682	013376	001401				CMP	%0,#177
2683	013400	000000				BEQ	+.4
2684	013402	000240				HLT	
2685						SCOPE	
2686	013404	005000					
2687	013406	112700	000377			CLR	%0
2688	013412	020027	177777			MOVB	#377,%0
2689	013416	001401				CMP	%0,#-1
2690	013420	000000				BEQ	+.4
2691	013422	000240				HLT	
2692	013424	005267	000026			SCOPE	
2693	013430	001006				INC	LOOP
2694	013432	012737	000207	177566		BNE	FIN
2695	013440	105737	177564			MOV	#207,@#177566
2696	013444	100375				TSTB	@#177564
2697	013446	000167	164526			BPL	-4
2698	013452	123456				JMP	200
2699	013454	000000					
2700	013456	000000					
2701		000001					

;TEST FOR ZERO SIGN FILL

;REGISTER TO HIGH .BYTE

;MOVB REG TO HIGH .BYTE FAILED

;%0=ALL ONES
 ;%0 SHOULD =000177

;UPPER BYTE OF %0 NOT ZERO

;REGISTER ZERO=ZERO
 ;REGISTER ZERO=ALL ONES

;SIGN FILL OF 1'S FAILED

FIN:
 VALUE: 123456
 SVAL: 0
 LOOP: 0
 .END

;TEMP STORAGE

1212	1218	1224	1230	1236	1242	1248	1254	1260	1266	1272	1278	1284
1290	1296	1302	1308	1314	1320	1326	1332	1338	1344	1350	1356	1362
1368	1374	1380	1386	1392	1398	1404	1410	1416	1422	1428	1434	1440
1446	1452	1458	1464	1470	1476	1482	1488	1494	1500	1506	1512	1518
1524	1530	1536	1542	1548	1554	1560	1566	1572	1578	1584	1590	1596
1602	1608	1614	1620	1626	1632	1638	1644	1650	1656	1662	1668	1674
1680	1686	1692	1698	1704	1710	1716	1722	1728	1734	1740	1746	1752
1758	1764	1770	1776	1782	1788	1794	1800	1806	1812	1818	1824	1830
1836	1842	1848	1854	1860	1866	1872	1878	1884	1890	1896	1902	1908
1914	1920	1926	1932	1938	1944	1950	1956	2630	2636	2642	2648	2654
2660	2668	2676	2684	2691	2633	2637*	2638*	2639	2643*	2644*	2645	2649*
2625*	2626*	2627	2631*	2632*	2672*	2673	2699*					
2655*	2662*	2664*	2665	2670*	2638	2644	2650	2656	2698*			
2612	2616	2620	2627	2632	285	289	294	299	304	309	314	319
258*	267	271	276	281	344	349	354	359	364	369	374	379
324	329	334	339	344	416	422	428	435	441	447	453	459
392	398	404	410	416	495	501	507	513	519	525	531	537
471	477	483	489	495	573	579	585	591	597	603	609	615
549	555	561	567	573	651	657	663	669	675	681	687	693
627	633	639	645	651	729	735	741	747	753	759	765	771
705	711	717	723	729	801	807	813	819	825	831	837	843
783	789	795	801	807	885	891	897	903	909	915	921	927
861	867	873	879	885	957	963	969	975	981	987	993	999
939	945	951	957	963	1041	1047	1053	1059	1065	1071	1077	1083
1017	1023	1029	1035	1041	1119	1125	1131	1137	1143	1149	1155	1161
1095	1101	1107	1113	1119	1197	1203	1209	1215	1221	1227	1233	1239
1173	1179	1185	1191	1197	1275	1281	1287	1293	1299	1305	1311	1317
1251	1257	1263	1269	1275	1353	1359	1365	1371	1377	1383	1389	1395
1329	1335	1341	1347	1353	1431	1437	1443	1449	1455	1461	1467	1473
1407	1413	1419	1425	1431	1509	1515	1521	1527	1533	1539	1545	1551
1485	1491	1497	1503	1509	1587	1593	1599	1605	1611	1617	1623	1629
1563	1569	1575	1581	1587	1665	1671	1677	1683	1689	1695	1701	1707
1641	1647	1653	1659	1665	1743	1749	1755	1761	1767	1773	1779	1785
1719	1725	1731	1737	1743	1821	1827	1833	1839	1845	1851	1857	1863
1797	1803	1809	1815	1821	1899	1905	1911	1917	1923	1929	1935	1941
1875	1881	1887	1893	1899	1975	1980	1985	1990	1995	2000	2005	2010
1953	1959	1965	1970	1975	2040	2045	2050	2055	2060	2065	2070	2075
2020	2025	2030	2035	2040	2100	2105	2110	2115	2120	2125	2130	2135
2085	2090	2095	2100	2105	2175	2180	2185	2190	2195	2200	2205	2210
2150	2155	2160	2165	2170	2235	2240	2245	2250	2255	2260	2265	2270
2215	2220	2225	2230	2235	2295	2300	2305	2310	2315	2320	2325	2330
2280	2285	2290	2295	2300	2365	2370	2375	2380	2385	2390	2395	2400
2345	2350	2355	2360	2365	2425	2430	2435	2440	2445	2450	2455	2460
2410	2415	2420	2425	2430	2490	2495	2500	2505	2510	2515	2520	2525
2475	2480	2485	2490	2495	2555	2560	2565	2570	2575	2580	2585	2590
2540	2545	2550	2555	2560	2610	2614	2618	2622	2628	2634	2640	2646
2605	2606	2609	2610	2614	2689	2696						
2666	2674	2682	2689	2696								

SVAL 013454
VALUE = 013452
 = 013460

.EQUAT	1#
.HEADE	1#
.KT11	1#
.SETUP	1#
.SWRHI	1#
.SACTI	1#
.SAPT8	1#
.SAPTH	1#
.SAPTY	1#
.SASTA	1#
.SCATC	1#
.SCMTA	1#
.SDB2D	1#
.SDB20	1#
.SDIV	1#
.SEOP	1#
.SERRO	1#
.SERRT	1#
.SMULT	1#
.SPOWE	1#
.SRAND	1#
.SRDDE	1#
.SRDOC	1#
.SREAD	1#
.SR2AZ	1#
.SSAVE	1#
.SSB2D	1#
.SSB20	1#
.SSCOP	1#
.SSIZE	1#
.SSUPR	1#
.STRAP	1#
.STYPB	1#
.STYPD	1#
.STYPE	1#
.STYPO	1#
.S40CA	1#
.1170	1#

	1532	1538	1544	1550	1556	1562	1568	1574	1580	1586	1592	1598	1604	1610	1616
	1622	1628	1634	1640	1646	1652	1658	1664	1670	1676	1682	1688	1694	1700	1706
	1712	1718	1724	1730	1736	1742	1748	1754	1760	1766	1772	1778	1784	1790	1796
	1802	1808	1814	1820	1826	1832	1838	1844	1850	1856	1862	1868	1874	1880	1886
	1892	1898	1904	1910	1916	1922	1928	1934	1940	1946	1952	1958	2609	2617	2621
	2627	2633	2639	2645	2651	2657									
HALT	259														
INC	2692														
JMP	2697														
MOV	265	269	283	287	302	307	312	317	322	327	332	337	342	347	352
	357	362	367	372	377	1963	1968	1973	1978	1983	1988	1993	1998	2003	2008
	2013	2018	2023	2028	2033	2038	2043	2048	2053	2058	2063	2068	2073	2078	2083
	2088	2093	2098	2103	2108	2113	2118	2123	2128	2133	2138	2143	2148	2153	2158
	2163	2168	2173	2178	2183	2188	2193	2198	2203	2208	2213	2218	2223	2228	2233
	2238	2243	2248	2253	2258	2263	2268	2273	2278	2283	2288	2293	2298	2303	2308
	2313	2318	2323	2328	2333	2338	2343	2348	2353	2358	2363	2368	2373	2378	2383
	2388	2393	2398	2403	2408	2413	2418	2423	2428	2433	2438	2443	2448	2453	2458
	2463	2468	2473	2478	2483	2488	2493	2498	2503	2508	2513	2518	2523	2528	2533
	2538	2543	2548	2553	2558	2563	2568	2573	2578	2583	2588	2593	2598	2604	2612
	2662	2663	2670	2671	2679	2694									
MOVE	274	279	292	297	384	390	396	402	408	414	420	426	433	439	445
	451	457	463	469	475	481	487	493	499	505	511	517	523	529	535
	541	547	553	559	565	571	577	583	589	595	601	607	613	619	625
	631	637	643	649	655	661	667	673	679	685	691	697	703	709	715
	721	727	733	739	745	751	757	763	769	775	781	787	793	799	805
	811	817	823	829	835	841	847	853	859	865	871	877	883	889	895
	901	907	913	919	925	931	937	943	949	955	961	967	973	979	985
	991	997	1003	1009	1015	1021	1027	1033	1039	1045	1051	1057	1063	1069	1075
	1081	1087	1093	1099	1105	1111	1117	1123	1129	1135	1141	1147	1153	1159	1165
	1171	1177	1183	1189	1195	1201	1207	1213	1219	1225	1231	1237	1243	1249	1255
	1261	1267	1273	1279	1285	1291	1297	1303	1309	1315	1321	1327	1333	1339	1345
	1351	1357	1363	1369	1375	1381	1387	1393	1399	1405	1411	1417	1423	1429	1435
	1441	1447	1453	1459	1465	1471	1477	1483	1489	1495	1501	1507	1513	1519	1525
	1531	1537	1543	1549	1555	1561	1567	1573	1579	1585	1591	1597	1603	1609	1615
	1621	1627	1633	1639	1645	1651	1657	1663	1669	1675	1681	1687	1693	1699	1705
	1711	1717	1723	1729	1735	1741	1747	1753	1759	1765	1771	1777	1783	1789	1795
	1801	1807	1813	1819	1825	1831	1837	1843	1849	1855	1861	1867	1873	1879	1885
	1891	1897	1903	1909	1915	1921	1927	1933	1939	1945	1951	1957	2608	2616	2620
	2626	2632	2638	2644	2650	2656	2664	2672	2680	2687					
TSTB	2695														
.ENABL	1														
.END	2701														
.LIST	1														
.MACR	263	264	2603	2624											
.MACRO	1														
.NLIST	1														
.REM	1														
.REPT	302	383	432	1963											

ERRORS DETECTED: 0
 DEFAULT GLOBALS GENERATED: 0

*,DOHA.SEG/SOL/CRF/PAGNUM/NL:TOC=SYSMAC.CO,DOHA.P11

.MAIN. MACY11 27(732) 01-OCT-75 10:19 PAGE 64
DDHA.P11 CROSS REFERENCE TABLE -- PERMANENT SYMBOLS

RUN-TIME: 29 40 3 SECONDS
RUN-TIME RATIO: 185/73=2.5
CORE USED: 32K (63 PAGES)

