

LPA/AD11-K

DIAGNOSTIC TEST
MD-11-DRLPK-A

EP-DRLPK-A-DL
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digital
MADE IN USA



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EOF1DRLPKASEQ411

00010000 780223

IDENTIFICATION

ECHDR1DRLPKASEQ

00010000

780223
SEQ 0001

Product Code: MAINDEC-11-DRLPK-A-D
Product Name: LPA/AD11-K DIAGNOSTIC TEST
Date: JAN 1978
Maintainer: Diagnostic Group

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1.0 ABSTRACT

This diagnostic has two starting addresses: 200 for standard tolerances and 210 for tighter option test area tolerances.

This diagnostic tests the AD11K with or without a wraparound module (G5036).

When starting the diagnostic, a set of tests is listed and this statement is printed out: "Type the letter and carriage return of the desired test:". The following chart indicates which letter corresponds to which test:

- W: The entire Wraparound test (requires G5036 module)
 - a. Analog subtests
 - b. Noise test
 - c. Interchannel Settling test
 - d. Differential Linearity and Relative Accuracy test
- C: Calibration test only
- N: Noise test only
- S: Interchannel Settling only
- L: Logic Subtests only
- A: Auto test (requires G5036 module)
 - A. Logic subtests
 - B. Analog subtests
 - C. Noise Test
 - D. Interchannel Settling Test
 - E. Differential Linearity and Relative Accuracy Test

THIS PROGRAM IS A MODIFIED VERSION OF "MD-11-DZADL-B" IT WAS MODIFIED TO ENABLE THE OPERATOR TO CHECK OUT THE AD 11K OPTION WHEN IT IS ON THE LPA11-KX I/O BUS. NO RECABLING IS NEEDED. SOME TEST DONE IN THE ORIGINAL DIAGNOSTIC SUCH AS ARBITRATION TEST, WERE DELETED AS THEY COULD NOT BE CHECKED. IF THIS DIAGNOSTIC DOESN'T FIND A SUSPECTED PROBLEM, YOU MAY HAVE TO RUN "MD-11-DZADL-B" YOU SHOULD RUN "MD-11-DALPA" BEFORE RUNNING THIS DIAGNOSTIC. PLEASE READ SECTION 10.

2.0 REQUIREMENTS

2.1 Equipment

PDP-11 family computer with 8K of memory
Teletype
AD11K Module
VT55 Terminal supported for graphic output
G5036 Wraparound Module

2.2 Storage

This program uses all 8K of memory and is not "chainable" on an 8K CPU. The program is "chainable" on 12K or greater. The program will destroy "absolute loader" on an 8K CPU, if "W" or "A" is selected.

3.0 LOADING PROCEDURE

Procedure for loading normal binary tapes should be followed.

4.0 STARTING PROCEDURE

4.1 Control Switch Settings

Standard PDP-11 Format

SW15=1	Halt on error
SW14=1	Loop on test
SW13=1	Inhibit error typeouts
SW12=1	Halt for VTSS display
SW11=1	Inhibit iterations
SW10=1	Bell on error
SW9 =1	Loop on error
SW8 =1	Loop on test in SWR <7:0>

200 is the starting address of the diagnostic for standard tolerances. 204 is the restart address. 210 is the starting address of the diagnostic for the option test area's tighter tolerances.

5.0 OPERATING PROCEDURE

Start the diagnostic at 200 or 210. The program heading and the list of tests available, will be printed out followed by a message "Type the letter and carriage return for the desired test:". Then type the letter you want, according to the table listed and hit carriage return.

Two control characters, $\uparrow A$ and $\uparrow C$, are set aside for interrupting a test and transferring control to either the beginning of the diagnostic ($\uparrow C$) or to the beginning of the specific test which was in progress ($\uparrow A$). During the logic tests while a reset is being performed, $\uparrow C$ or $\uparrow A$ will not be executed until after the reset has been completed, therefore hit $\uparrow C$ or $\uparrow A$ until it is successful.

For machines without a hardware switch register, location SWREG (176) is used as a software switch register. To modify the contents of SWREG, type $\uparrow G$. The program responds with the current contents of SWREG and a slash. Type the desired new contents of SWREG followed by a carriage return.

If "W" is typed, the program will type "xz AD11K's FOUND". Where xz is the number of AD11K's in octal. If the number is greater than 1, the test will be run successively on each AD11K. The program will run through the logic subtests, the Noise test on 8 edges, the Interchannel Settling test on 8 edges, and the Differential Linearity and Relative Accuracy test. A G5036 wraparound module is required. The program supports AD11K expansion beyond 16 channels. To run this test on a group of channels other than 0-17, load 20, 40, or 60 into location BASECH (1336) for channels 20-37, 40-57, 60-77.

If "C" is typed, the program will run the calibration test and will loop on that test until the operator halts it. If a certain AD11K is to be tested, its status register address must be loaded into SBASE (1250), and its vector address must be loaded into the low byte of SVECT1 (1244) (the high byte containing the priority).

If "N" is typed, the program will run the Noise test tagged "BEGINN" and will loop on this test until the operator halts it. If a certain AD11K is to be tested its status register address must be loaded into SBASE (1250), and its vector address must be loaded into the low byte of SVECT1 (1244) (the high byte containing the priority).

If "S" is typed, the program will run the Interchannel Settling test tagged "BEGINS" and will loop on this test until the operator halts it. At the beginning of this test, the operator must respond to the statements asking for the "FROM" channel and the "TO" channel by typing in the channel value in octal and hitting carriage return. If a certain AD11K is to be tested its status register address must be loaded into \$BASE (1250), and its vector address must be loaded into \$VECT1 (1244) (the high byte containing the priority).

If "A" is typed, the program will execute the logic tests, analog tests, noise, settle and differential linearity. At the beginning of the test the program will type "XX AD11K's Found". Where XX IS THE NUMBER OF AD11K's in octal. If the number is greater than 1, the test will be run successively on each AD11K. The program supports AD11K expansion beyond 16 channels. To run this test on a group of channels other than 0-17, load 20, 40, or 60 into location BASECH (1336) for channels 20-37, 40-57, 60-77.

If "L" is typed, the program will execute the logic tests, printing "END PASS" when it has completed an entire pass. At the beginning of the test the program will type "XX AD11K's Found". Where XX is the number of AD11K's in octal. If the number is greater than 1, the test will be run successively on each AD11K.

6.0 ERRORS

This program uses the Diagnostic "SYSMAC" package for error reporting and typeout. The error information consists of the following:

ERRPC: Location at which an error was detected.
 STREG: Address of the status register.
 ADBUFF: Address of the buffer
 CHANL: Channel value
 NOMINAL: Expected correct data
 TOLERANCE: The acceptable deviation from the nominal
 ACTUAL: Actual data
 EXPECTED: Expected correct data

7.0 MISCELLANEOUS

7.1 Execution Time

Execution time for each of the tests is:

Calibration:	8 conversions/5 seconds @ 110 baud
Wraparound Test:	17 minutes first pass; 35 minutes for successive passes
Settling Test:	1 minute
Noise Test:	1 minute
Logic Test:	1 minute
Auto Test:	18 minutes first pass, 36 minutes for successive passes

7.2 Status Register and Vector Addresses and Priority

When testing more than one AD11K, the difference in addresses is presently 40 for bus address and vector address. These values are in VADR (bus address) (1332) and VVCT (vector address) (1334). The first AD11K's status register address must be in \$BASE (1250), its vector address must be in the low byte of \$VECT1 (1244), and the priority must be in the high byte of \$VECT1.

7.3 AD11K Priority

If AD11K is set for a priority other than 6, the high byte of \$VECT1 (1244) must be adjusted accordingly (the low byte containing the vector address). If more than one AD11K is being tested, all must be set at the same priority.

7.4 Switch Register

If a hardware switch register is present and the operator desires to use a software switch register and the tG feature; it is necessary to load the starting address, set the hardware switch register to all ones (-1), and hit start. The program will then run with the software switch register.

7.5 VT55 Graphic Output

The screen display may be halted for examination by setting bit 12. And then just hit continue to complete the program's execution.

7.6 USER LINK TO I/O DEVICE

A SPECIAL USER LINK HAS BEEN PROVIDED IN ORDER FOR THE OPERATOR TO EXAMINE OR MODIFY LOCATIONS ON THE LPA11-KX I/O BUS. (NOTE: THIS CANNOT BE DONE DIRECTLY.)

PROCEDURE:

- 1) START THE PROCESSOR AT LOCATION \$UTK:
- 2) THE DIALOG TO EXAMINE A LOCATION IS AS FOLLOWS:

```

E OR D      "E"
DEVICE ADDRS= "OCTAL ADDRS"
XXXXXX
  
```

WHERE XXXXXX IS THE CONTENTS OF THE SPECIFIED LOC.

- 3) THE DIALOG TO MODIFY A LOCATION IS AS FOLLOWS:

```

E OR D      "D"
DATA=       "DATA TO BE DEPOSITED"
  
```

- 4) THE PROGRAM WILL STAY IN THIS LOOP UNTIL THE OPERATOR IS FINISHED. AT THIS TIME THE PROCESSOR SHOULD BE HALTED.

NOTE: THE OPERATORS RESPONSE IS ENCLOSED IN QUOTES.

8.0 RESTRICTIONS

- 8.1 A G5036 wraparound module must be present when running the auto test and the wraparound test.

Switch on G5036 must be in '0' position.

The wraparound (G5036) module must be connected as follows:
AD11K TO BC08R CONNECTION A-A, VV-VV
BC08R TO G5036 CONNECTION "UPSIDE-DOWN" A-VV, VV-A

9.0 PROGRAM DESCRIPTION

9.1 Logic Tests

These 14 logic subtests run sequentially without further operator intervention after he/she has typed in the number of AD11K's to be tested. Its purpose is to check that each of the mux bits can be loaded and properly read back; that initialize clears the external start enable bit, the done bit, the interrupt enable bit, the overflow bit, the error flag, and the A/D start bit. It also checks that the A/D done flag sets at end of conversion and clears when the converted value is read. It checks the interrupt logic and the correct setting of the error flag.

9.2 Calibration Test

This test begins when the operator types "C", it then loads the channel from the switch register bits 0-7 and does a conversion on that channel. If SWR bit 13 is down, it prints out the converted value on the teletype; otherwise, if SWR bit 13 is up, it puts the converted value in the display register. The operator may change the channel at any time during the test, however the new values from the new channel will not be printed until the next line of 8 values is printed. The 8 values on each line correspond to only one channel.

9.3 Differential Linearity

This test is to determine if a change in the input voltage represents a similar change in the resulting converted binary value.

9.4 Settling Test

The purpose of this test is to check that the time needed to settle and correctly report a new input value after switching channels does not exceed the expected amount of time for such a change.

9.5 Noise Test

This test measures the internal short-term repeatability noise within the A/D. RMS noise equals 1 standard deviation of the Gaussian curve, PEAK noise equals 2.3 standard deviation of the Gaussian curve.

9.6 Analog Tests

These 11 subtests check the channels and their output.

10. LPA11 (SYSTEM) DIAGNOSTIC SUMMARY

DIAGNOSTICS FOR THE LPA11 ARE WRITTEN AT THREE LEVELS: (1) TOTAL PDP-11 SYSTEM, (2) LPA11 SYSTEM; AND, (3) LPA11 OPTIONS.

LEVEL 1 IS DESIGNED TO ISOLATE A FAILURE TO THE LPA11 SYSTEM. ALL OPTIONS ON THE PDP-11 ARE EXERCISED.

LEVEL 2 DIAGNOSTICS ISOLATE A FAILURE TO THE INDIVIDUAL OPTION WITHIN THE LPA11. THE LEVEL 2 DIAGNOSTIC IS MD-11-DRLPA. WHEN

K01

THE USER RUNS DRLPA HE CAN GENERALLY TELL WHICH OPTION DIAGNOSTIC (LEVEL 3) TO RUN NEXT. M8254 AND M8200-YC ERRORS MAY "LOOK" ALIKE AND DRLPA MAY NOT BE ABLE TO DISTINGUISH BETWEEN THEM. ARBITRATION ERRORS WILL NOT BE DETECTED BY THIS DIAGNOSTIC.

SEQ 0011

LEVEL THREE DIAGNOSTICS AID IN DETERMINING IF THE ERROR WAS IN FACT ON THE OPTION THE DRLPA SPECIFIED. THE USER MAY "LOOP" ON THE ERROR. WITHIN LEVEL THREE, THERE ARE TWO GROUPS OF DIAGNOSTICS. THE FIRST GROUP REQUIRES NO "EXTRA" WORK BY THE USER IN ORDER TO RUN. GROUP "A" DIAGNOSTICS DO NOT CHECK ARBITRATION, AND REQUIRE EXTRA TIME FOR EXECUTION. THE SECOND GROUP (GROUP "B") REQUIRES THAT THE USER RECONFIGURE THE PDP-11 SYSTEM. THIS RECONFIGURATION INVOLVES CABLING THE UNIBUS TO THE LPA'S I/O BUS.

THE DIAGNOSTIC FOR THE M8254 FALLS INTO THE GROUP "B" CATEGORY.

THE LPA11-KX DIAGNOSTIC KIT WILL INCLUDE:

SEQ 0012

<u>OPTION</u>	<u>GROUP</u>	<u>DIAG. #</u>	<u>DIAG. TITLE</u>
LPA11-KX	LEVEL 2	MD-11-DRLPA	LPA11-K SYSTEM DIAG.
M8254	"B"	MD-11-DRLPN	M8254 (IPBM) DIAG.
AA11-K	A	MD-11-DRLPB	AA11-K DIAG.
	B	MD-11-DZAAC	AA11-K DIAG.
AR11	A	MD-11-DRLPC	LPA/AR11 DIAG. #1
	A	MD-11-DRLPD	LPA/AR11 DIAG. #2
	A	MD-11-DRLPE	LPA/AR11 DIAG. #3
	B	MD-11-DZARA	AR11 DIAG. #1
	B	MD-11-DZARB	AR11 DIAG. #2
	B	MD-11-DZARC	AR11 DIAG. #3
DR11-K	A	MD-11-DRLPF	LPA/DR11-K DIAG.
	B	MD-11-DZDRG	DR11-K DIAG.
KW11-K	A	MD-11-DRLPG	LPA/KW11-K DIAG.
	B	MD-11-DZKWK	KW11-K DIAG.
LPS11	A	MD-11-DRLPH	LPA/LPS11 DIAG. #1
	A	MD-11-DRLPI	LPA/LPS11 DIAG. #2
	A	MD-11-DRLPJ	LPA/LPS11 DIAG. #3
	B	MD-11-DZLPC	LPS11 DIAG. #1
	B	MD-11-DZLPD	LPS11 DIAG. #2
	B	MD-11-DZLPI	LPS11 DIAG. #3
AD11-K	A	MD-11-DRLPK	LPA/AD11-K DIAG.
	B	MD-11-DZADL	AD11-K DIAG.
M8200-YC	B	MD-11-DZLPL	LPA/M8200-YC BASIC MICRO-CPU R/W TEST
	B	MD-11-DZLPM	LPA/M8200-YC JMP+ROM READ TEST

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666	T4 LOAD AND READ BACK EXTERNAL START ENABLE BIT4
674	T5 LOAD AND READ BACK ERROR FLAG BIT15
682	T6 TEST DONE FLAG SETS AND BIT0 CLEARS ON END OF CONV.
709	T7 TEST A/D DONE FLAG CLEARS WHEN READ CONVERTED VALUE
728	T10 TEST ERROR FLAG SETS IF 2ND CONVERSION ENDS BEFORE READING BUFFER
770	WRAPAROUND TEST SECTION
772	T11 TEST CH14 GROUND
791	T12 TEST CONVERSION FROM EXT. START
820	T13 TEST CH0 GROUND
832	T14 TEST CH1 GROUND
845	T15 TEST CH2 +1 VOLT
859	T16 TEST CH3 +2.5 VOLTS
872	T17 TEST CH4 -2.5 VOLTS
884	T20 TEST VERNIER OFFSET DAC ON CH12
937	T21 TEST CH13 +2.5 VOLTS
949	T22 TEST CH17 +4V
961	T23 OFFSET ON CH0
992	T24 NOISE TEST ON 8 EDGES
1006	T25 SETTLE TEST ON 8 EDGES
1019	T26 DIFFERENTIAL LINEARITY AND RELATIVE ACCURACY TEST
1034	CALIBRATION TEST
1089	LOGIC TEST SECTION
1099	AUTO TEST
1117	WRAPAROUND TEST
1128	DETERMINE IF MORE AD11K'S TO BE TESTED
1167	NOISE TEST, 1 EDGE
1177	INTERCHANNEL SETTLING TEST, 1 EDGE
1906	END OF PASS ROUTINE
1943	ASCII MESSAGES
2229	TTY INPUT ROUTINE
2303	READ AN OCTAL NUMBER FROM THE TTY
2341	SCOPE HANDLER ROUTINE
2405	ERROR HANDLER ROUTINE
2457	ERROR MESSAGE TYPEOUT ROUTINE
2504	TYPE ROUTINE
2583	APT COMMUNICATIONS ROUTINE

NO1

MAINDEC-11-DRLPKA MACY11 27(654) 15-DEC-77 08:40
DRLPK.P11 TABLE OF CONTENTS

SEQ 0014

3234 BINARY TO OCTAL (ASCII) AND TYPE
3311 TRAP DECODER
3334 TRAP TABLE
3351 POWER DOWN AND UP ROUTINES

.REM [

LPA.MAC

WELCOME, THIS DIAGNOSTIC IS ONE IN A SERIES OF DIAGNOSTIC
DESIGNED IN ORDER TO AID YOU IN TESTING THE LPA-11XX OPTION.
I HOPE THAT YOU HAVE READ THE DOCUMENTATION SECTION OF THIS
DIAGNOSTIC. IF YOU HAVE, YOU KNOW ABOUT ALL OF THE DIAGNOSTICS
THAT ARE AVAILABLE FOR TESTING THE LPA SYSTEM.

GOOD LUCK !

[
.GLOBL DRLPX2

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```
.TITLE MAINDEC-11-DRLPKA
;*COPYRIGHT (C) 1976
;*DIGITAL EQUIPMENT CORP.
;*MAYNARD, MASS. 01754
;*
;*PROGRAM BY VERA BREUER
;*
;*THIS PROGRAM WAS ASSEMBLED USING THE PDP-11 MAINDEC SYSMAC
;*PACKAGE (MAINDEC-11-DZQAC-C2), SEPT 14, 1976.
;*
.SBTTL BASIC DEFINITIONS

;*INITIAL ADDRESS OF THE STACK POINTER *** 1100 ***
STACK= 1100
.EQUIV EMT,ERROR          ;;BASIC DEFINITION OF ERROR CALL
.EQUIV IOT,SCOPE          ;;BASIC DEFINITION OF SCOPE CALL

;*MISCELLANEOUS DEFINITIONS
HT= 11                    ;;CODE FOR HORIZONTAL TAB
LF= 12                    ;;CODE FOR LINE FEED
CR= 15                    ;;CODE FOR CARRIAGE RETURN
CRLF= 200                 ;;CODE FOR CARRIAGE RETURN-LINE FEED
PS= 177776               ;;PROCESSOR STATUS WORD
.EQUIV PS,PSW
STKLMT= 177774           ;;STACK LIMIT REGISTER
PIRQ= 177772            ;;PROGRAM INTERRUPT REQUEST REGISTER
DSWR= 177570           ;;HARDWARE SWITCH REGISTER
DDISP= 177570          ;;HARDWARE DISPLAY REGISTER

;*GENERAL PURPOSE REGISTER DEFINITIONS
R0= %0                   ;;GENERAL REGISTER
R1= %1                   ;;GENERAL REGISTER
R2= %2                   ;;GENERAL REGISTER
R3= %3                   ;;GENERAL REGISTER
R4= %4                   ;;GENERAL REGISTER
R5= %5                   ;;GENERAL REGISTER
R6= %6                   ;;GENERAL REGISTER
R7= %7                   ;;GENERAL REGISTER
SP= %6                   ;;STACK POINTER
PC= %7                   ;;PROGRAM COUNTER

;*PRIORITY LEVEL DEFINITIONS
PRO= 0                   ;;PRIORITY LEVEL 0
PR1= 40                  ;;PRIORITY LEVEL 1
PR2= 100                 ;;PRIORITY LEVEL 2
PR3= 140                 ;;PRIORITY LEVEL 3
PR4= 200                 ;;PRIORITY LEVEL 4
PR5= 240                 ;;PRIORITY LEVEL 5
PR6= 300                 ;;PRIORITY LEVEL 6
PR7= 340                 ;;PRIORITY LEVEL 7

;*"SWITCH REGISTER" SWITCH DEFINITIONS
SW15= 100000
SW14= 40000
```

84	020000	SW13=	20000
85	010000	SW12=	10000
86	004000	SW11=	4000
87	002000	SW10=	2000
88	001000	SW09=	1000
89	000400	SW08=	400
90	000200	SW07=	200
91	000100	SW06=	100
92	000040	SW05=	40
93	000020	SW04=	20
94	000010	SW03=	10
95	000004	SW02=	4
96	000002	SW01=	2
97	000001	SW00=	1
98		.EQUIV	SW09,SW9
99		.EQUIV	SW08,SW8
100		.EQUIV	SW07,SW7
101		.EQUIV	SW06,SW6
102		.EQUIV	SW05,SW5
103		.EQUIV	SW04,SW4
104		.EQUIV	SW03,SW3
105		.EQUIV	SW02,SW2
106		.EQUIV	SW01,SW1
107		.EQUIV	SW00,SW0

108		.*DATA BIT DEFINITIONS (BIT00 TO BIT15)	
109		BIT15=	100000
110	100000	BIT14=	40000
111	040000	BIT13=	20000
112	020000	BIT12=	10000
113	010000	BIT11=	4000
114	004000	BIT10=	2000
115	002000	BIT09=	1000
116	001000	BIT08=	400
117	000400	BIT07=	200
118	000200	BIT06=	100
119	000100	BIT05=	40
120	000040	BIT04=	20
121	000020	BIT03=	10
122	000010	BIT02=	4
123	000004	BIT01=	2
124	000002	BIT00=	1
125	000001	.EQUIV	BIT09,BIT9
126		.EQUIV	BIT08,BIT8
127		.EQUIV	BIT07,BIT7
128		.EQUIV	BIT06,BIT6
129		.EQUIV	BIT05,BIT5
130		.EQUIV	BIT04,BIT4
131		.EQUIV	BIT03,BIT3
132		.EQUIV	BIT02,BIT2
133		.EQUIV	BIT01,BIT1
134		.EQUIV	BIT00,BIT0
135		.*BASIC "CPU" TRAP VECTOR ADDRESSES	
136			
137			


```

138      000004      ERRVEC= 4      ;; TIME OUT AND OTHER ERRORS
139      000010      RESVEC= 10     ;; RESERVED AND ILLEGAL INSTRUCTIONS
140      000014      TBITVEC=14     ;; "T" BIT
141      000014      TRTVEC= 14     ;; TRACE TRAP
142      000014      BPTVEC= 14     ;; BREAKPOINT TRAP (BPT)
143      000020      IOTVEC= 20     ;; INPUT/OUTPUT TRAP (IOT) **SCOPE**
144      000024      PWRVEC= 24     ;; POWER FAIL
145      000030      EMTVEC= 30     ;; EMULATOR TRAP (EMT) **ERROR**
146      000034      TRAPVEC=34     ;; "TRAP" TRAP
147      000060      TKVEC= 60      ;; TTY KEYBOARD VECTOR
148      000064      TPVEC= 64      ;; TTY PRINTER VECTOR
149      000240      PIRQVEC=240    ;; PROGRAM INTERRUPT REQUEST VECTOR
150      .SBTTL      OPERATIONAL SWITCH SETTINGS
151      ;*
152      ;*          SWITCH          USE
153      ;*          -----          -----
154      ;*          15              HALT ON ERROR
155      ;*          14              LOOP ON TEST
156      ;*          13              INHIBIT ERROR TYPEOUTS
157      ;*          12              HALT FOR VTSS DISPLAY
158      ;*          11              INHIBIT ITERATIONS
159      ;*          10              BELL ON ERROR
160      ;*          9               LOOP ON ERROR
161      ;*          8               LOOP ON TEST IN SWR<7:0>
162      170400      ABASE= 170400
163      140340      AVECT1= 140340
164      000300      APRIOR= 300
165
166
167
168
169
170
171
172
173
174      .SBTTL      TRAP CATCHER
175
176      000000      .=0
177      ;*ALL UNUSED LOCATIONS FROM 4 - 776 CONTAIN A ".+2,HALT"
178      ;*SEQUENCE TO CATCH ILLEGAL TRAPS AND INTERRUPTS
179      ;*LOCATION 0 CONTAINS 0 TO CATCH IMPROPERLY LOADED VECTORS
180      000174      000174      .=174
181      000174      000000      DISPREG: .WORD 0      ;; SOFTWARE DISPLAY REGISTER
182      000176      000000      SWREG: .WORD 0      ;; SOFTWARE SWITCH REGISTER
183      .SBTTL      STARTING ADDRESS(ES)
184      000200      000137      001714      JMP @#BEGIN ;; JUMP TO STARTING ADDRESS OF PROGRAM
185      000204      000137      002404      JMP @#BEG2   ;RESTART ADDRESS
186      000210      000137      001722      JMP @#BEGIN2 ;START ADDRESS FOR OPTION TEST AREA
  
```

```

187 .SBTTL ACT11 HOOKS
188
189 ;:*****
190 ;HOOKS REQUIRED BY ACT11
191 $SVPC=. ;SAVE PC
192 .=46
193 000046 012074 $ENDAD ;;1)SET LOC.46 TO ADDRESS OF $ENDAD IN .SEOp
194 000052 000052 .=52
195 000052 000000 .WORD 0 ;;2)SET LOC.52 TO ZERO
196 000214 000214 .=$SVPC ;; RESTORE PC
197 .=1000
198 .SBTTL APT PARAMETER BLOCK
199
200 ;:*****
201 ;SET LOCATIONS 24 AND 44 AS REQUIRED FOR APT
202 ;:*****
203 001000 .SX=. ;;SAVE CURRENT LOCATION
204 000024 000024 .=24 ;;SET POWER FAIL TO POINT TO START OF PROGRAM
205 000024 000200 200 ;;FOR APT START UP
206 000044 000044 .=44 ;;POINT TO APT INDIRECT ADDRESS PNTR.
207 000044 001000 $APTHDR ;;POINT TO APT HEADER BLOCK
208 001000 001000 .=$X ;;RESET LOCATION COUNTER
209 ;:*****
210 ;SETUP APT PARAMETER BLOCK AS DEFINED IN THE APT-PDP11 DIAGNOSTIC
211 ;INTERFACE SPEC.
212
213 $APTHD:
214 001000 000000 $HIBTS: .WORD 0 ;;TWO HIGH BITS OF 18 BIT MAILBOX ADDR.
215 001002 001174 $MBADR: .WORD $MAIL ;;ADDRESS OF APT MAILBOX (BITS 0-15)
216 001004 002260 $TSTM: .WORD 1200. ;;RUN TIM OF LONGEST TEST
217 001006 000764 $PASTM: .WORD 500. ;;RUN TIME IN SECS. OF 1ST PASS ON 1 UNIT (QUICK VERIFY)
218 001010 003244 $UNITM: .WORD 1700. ;;ADDITIONAL RUN TIME (SECS) OF A PASS FOR EACH ADDITIONAL UNIT
219 001012 000031 .WORD $ETEND-$MAIL/2 ;;LENGTH MAILBOX-ETABLE(WORDS)

```

220
221
222
223
224
225
226
227 001100
228 001100 000000
229 001102 000
230 001103 000
231 001104 000000
232 001106 000000
233 001110 000000
234 001112 000000
235 001114 000
236 001115 001
237 001116 000000
238 001120 000000
239 001122 000000
240 001124 000000
241 001126 000000
242 001130 000000
243 001132 000000
244 001134 000
245 001135 000
246 001136 000000
247 001140 177570
248 001142 177570
249 001144 177560
250 001146 177562
251 001150 177564
252 001152 177566
253 001154 000
254 001155 002
255 001156 012
256 001157 000
257 001160 000000
258 001162 000000
259 001164 177607 000377
260 001170 077
261 001171 015
262 001172 000012
263
264
265
266
267
268 001174
269 001174 000000
270 001176 000000
271 001200 000000
272 001202 000000
273 001204 000000

.SBTTL COMMON TAGS

; THIS TABLE CONTAINS VARIOUS COMMON STORAGE LOCATIONS
; USED IN THE PROGRAM.

SCMTAG: . =1100 ; ; START OF COMMON TAGS
 .WORD 0
 STSNM: .BYTE 0 ; ; CONTAINS THE TEST NUMBER
 SERFLG: .BYTE 0 ; ; CONTAINS ERROR FLAG
 SICNT: .WORD 0 ; ; CONTAINS SUBTEST ITERATION COUNT
 SLPADR: .WORD 0 ; ; CONTAINS SCOPE LOOP ADDRESS
 SLPERR: .WORD 0 ; ; CONTAINS SCOPE RETURN FOR ERRORS
 SERTTL: .WORD 0 ; ; CONTAINS TOTAL ERRORS DETECTED
 SITMB: .BYTE 0 ; ; CONTAINS ITEM CONTROL BYTE
 SERMAX: .BYTE 1 ; ; CONTAINS MAX. ERRORS PER TEST
 SERPC: .WORD 0 ; ; CONTAINS PC OF LAST ERROR INSTRUCTION
 SGDADR: .WORD 0 ; ; CONTAINS ADDRESS OF 'GOOD' DATA
 SBDADR: .WORD 0 ; ; CONTAINS ADDRESS OF 'BAD' DATA
 SGDDAT: .WORD 0 ; ; CONTAINS 'GOOD' DATA
 SBDDAT: .WORD 0 ; ; CONTAINS 'BAD' DATA
 .WORD 0 ; ; RESERVED--NOT TO BE USED
 .WORD 0
 SAUTOB: .BYTE 0 ; ; AUTOMATIC MODE INDICATOR
 SINTAG: .BYTE 0 ; ; INTERRUPT MODE INDICATOR
 .WORD 0
 SWR: .WORD DSWR ; ; ADDRESS OF SWITCH REGISTER
 DISPLAY: .WORD DDISP ; ; ADDRESS OF DISPLAY REGISTER
 STKS: 177560 ; ; TTY KBD STATUS
 STKB: 177562 ; ; TTY KBD BUFFER
 STPS: 177564 ; ; TTY PRINTER STATUS REG. ADDRESS
 STPB: 177566 ; ; TTY PRINTER BUFFER REG. ADDRESS
 \$NULL: .BYTE 0 ; ; CONTAINS NULL CHARACTER FOR FILLS
 \$FILLS: .BYTE 2 ; ; CONTAINS # OF FILLER CHARACTERS REQUIRED
 \$FILLC: .BYTE 12 ; ; INSERT FILL CHARS. AFTER A "LINE FEED"
 \$TPFLG: .BYTE 0 ; ; "TERMINAL AVAILABLE" FLAG (BIT<07>=0=YES)
 \$TIMES: 0 ; ; MAX. NUMBER OF ITERATIONS
 \$ESCAPE: 0 ; ; ESCAPE ON ERROR ADDRESS
 \$BELL: .ASCIZ <207><377><377> ; ; CODE FOR BELL
 \$QUES: .ASCII /?/ ; ; QUESTION MARK
 \$CRLF: .ASCII <15> ; ; CARRIAGE RETURN
 \$LF: .ASCIZ <12> ; ; LINE FEED

.SBTTL APT MAILBOX-ETABLE

 .EVEN
 \$MAIL: ; ; APT MAILBOX
 \$MSGTY: .WORD AMSGTY ; ; MESSAGE TYPE CODE
 \$FATAL: .WORD AFATAL ; ; FATAL ERROR NUMBER
 \$TESTN: .WORD ATESTN ; ; TEST NUMBER
 \$PASS: .WORD APASS ; ; PASS COUNT
 \$DEVCT: .WORD ADEVCT ; ; DEVICE COUNT


```

274 001206 000000
275 001210 000000
276 001212 000000
277 001214
278 001214 000
279 001215 000
280 001216 000000
281 001220 000000
282 001222 000000
283
284
285
286
287
288
289 001224 000
290 001225 000
291
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295 001226 000000
296
297 001230 000
298 001231 000
299 001232 000000
300 001234 000
301 001235 000
302 001236 000000
303 001240 000
304 001241 000
305 001242 000000
306 001244 140340
307 001246 000000
308 001250 170400
309 001252 000000
310 001254 000000
311 001256
312

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$UNIT: .WORD AUNIT
$MSGAD: .WORD AMSGAD
$MSGLG: .WORD AMSGLG
$ETABLE:
$ENV: .BYTE AENV
$ENVM: .BYTE AENVM
$SWREG: .WORD ASWREG
$USWR: .WORD AUSWR
$CPUOP: .WORD ACPUOP
*
*
*
*
*
*
*
*
$MAMS1: .BYTE AMAMS1
$MTYP1: .BYTE AMTYP1
*
*
*
*
$MADR1: .WORD AMADR1
*
$MAMS2: .BYTE AMAMS2
$MTYP2: .BYTE AMTYP2
$MADR2: .WORD AMADR2
$MAMS3: .BYTE AMAMS3
$MTYP3: .BYTE AMTYP3
$MADR3: .WORD AMADR3
$MAMS4: .BYTE AMAMS4
$MTYP4: .BYTE AMTYP4
$MADR4: .WORD AMADR4
$VECT1: .WORD AVECT1
$VECT2: .WORD AVECT2
$BASE: .WORD ABASE
$DEVN: .WORD ADEVN
$CDW1: .WORD ACDW1
$ETEND:
.MEXIT

```

```

I/O UNIT NUMBER
MESSAGE ADDRESS
MESSAGE LENGTH
APT ENVIRONMENT TABLE
ENVIRONMENT BYTE
ENVIRONMENT MODE BITS
APT SWITCH REGISTER
USER SWITCHES
CPU TYPE, OPTIONS
BITS 15-11=CPU TYPE
11/04=01,11/05=02,11/20=03,11/40=04,11/45=05
11/70=06,PDQ=07,Q=10
BIT 10=REAL TIME CLOCK
BIT 9=FLOATING POINT PROCESSOR
BIT 8=MEMORY MANAGEMENT
HIGH ADDRESS, M.S. BYTE
MEM. TYPE, BLK#1
MEM. TYPE BYTE -- (HIGH BYTE)
900 NSEC CORE=001
300 NSEC BIPOLAR=002
500 NSEC MOS=003
HIGH ADDRESS, BLK#1
MEM. LAST ADDR.=3 BYTES, THIS WORD AND LOW OF "TYPE" ABOVE
HIGH ADDRESS, M.S. BYTE
MEM. TYPE, BLK#2
MEM. LAST ADDRESS, BLK#2
HIGH ADDRESS, M.S. BYTE
MEM. TYPE, BLK#3
MEM. LAST ADDRESS, BLK#3
HIGH ADDRESS, M.S. BYTE
MEM. TYPE, BLK#4
MEM. LAST ADDRESS, BLK#4
INTERRUPT VECTOR#1, BUS PRIORITY#1
INTERRUPT VECTOR#2, BUS PRIORITY#2
BASE ADDRESS OF EQUIPMENT UNDER TEST
DEVICE MAP
CONTROLLER DESCRIPTION WORD#1

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.SBTTL ERROR POINTER TABLE

;*THIS TABLE CONTAINS THE INFORMATION FOR EACH ERROR THAT CAN OCCUR.
;*THE INFORMATION IS OBTAINED BY USING THE INDEX NUMBER FOUND IN
;*LOCATION \$ITEMB. THIS NUMBER INDICATES WHICH ITEM IN THE TABLE IS PERTINENT.
;*NOTE1: IF \$ITEMB IS 0 THE ONLY PERTINENT DATA IS (\$ERRPC).
;*NOTE2: EACH ITEM IN THE TABLE CONTAINS 4 POINTERS EXPLAINED AS FOLLOWS:

;* EM ::POINTS TO THE ERROR MESSAGE
;* DH ::POINTS TO THE DATA HEADER
;* DT ::POINTS TO THE DATA
;* DF ::POINTS TO THE DATA FORMAT

001256

\$ERRTB:

; ITEM 1
EM1 :STATUS REG. ERROR
DH1 :ERRPC STREG EXPECTED ACTUAL
DT1 :\$ERRPC, STREG, \$GDDAT, \$BDDAT
DF1

001256 014253
001260 014413
001262 014576
001264 014636

; ITEM 2
EM2 :FAILED TO INTERRUPT
DH3 :ERRPC STREG ACTUAL
DT3 :\$ERRPC, STREG, \$BDDAT
DF1

001266 014301
001270 014534
001272 014626
001274 014636

; ITEM 3
EM3 :UNEXPECTED INTERRUPT
DH3 :ERRPC STREG
DT3 :\$ERRPC, STREG
DF1

001276 014331
001300 014534
001302 014626
001304 014636

; ITEM 4
EM4 :ERROR ON A/D CHANNEL
DH2 :ERRPC STREG CHAN NOMINAL TOL ACTUAL
DT2 :\$ERRPC, STREG, CHANL, \$GDDAT, SPREAD, \$BDDAT
DF1

001306 014362
001310 014451
001312 014610
001314 014636

357			.SBTTL	MISCELLANEOUS, TEMPORARY, AND STORAGE LOCATIONS	
358	001316	170400	STREG:	ABASE	; ADDRESS OF STATUS REGISTER
359	001320	170402	ADBUFF:	ABASE+2	; ADDRESS OF A/D BUFFER
360	001322	000300	BASEBR:	APRIOR	; INTERRUPT PRIORITY LEVEL
361	001324	140342	VECTR1:	AVECT1+2	
362	001326	000040	VADR:	40	; INCREMENT FOR BUS ADDRESS
363	001330	000040	VVCT:	40	; INCREMENT FOR VECTOR ADDRESS
364	001332	000000	BASECH:	0	; BASE CHANNEL
365	001334	000060	KBVECT:	60	
366	001336	000000	WIDE:	0	; NO. OF WIDE STATES
367	001340	000000	NARROW:	0	; NO. OF NARROW STATES
368	001342	000000	FIRST:	0	
369	001344	000000	SKIPST:	0	; NO. OF SKIPPED STATES
370	001346	000000	TEMP:	0	; WORK AREA
371	001350	000000	CH1:	0	; FIRST CHANNEL
372	001352	000300	CH2:	0	; SECOND CHANNEL
373	001354	000000	NBEXT:	0	; NO. OF AD11K'S TO BE TESTED
374	001356	000000	NMBEXT:	0	; NO. OF AD11K'S TO BE TESTED
375	001360	000000	DUMMY:	0	; DUMMY CHANNEL
376	001362	000000	CHANL:	0	; CHANNEL VALUE
377	001364	000000	TADDR:	0	; TEST ADDRESS
378	001366	000000	RNA:	0	; RANDOM
379	001370	000000	RNB:	0	; NUMBER
380	001372	000000	RNC:	0	; VALUES
381	001374	000000	RMS:	0	; RMS NOISE VALUE
382	001376	000000	PEAK:	0	; PEAK NOISE VALUE
383	001400	000000	FLAG:	0	; VTSS FLAG
384	001402	000000	SPREAD:	0	; DEVIATION FROM THE NOMINAL
385	001404	000000	DAC:	0	; SAR VALUE
386	001406	000000	DELAY:	0	; TIME DELAY COUNTER
387	001410	000000	EDGE:	0	; EDGE VALUE
388	001412	000000	BITPNT:	0	
389	001414	000000	MIN:	0	; MIN VALUE
390	001416	000000	WFTST:	0	; OPTION TEST AREA FLAG
391	001420	000000	MAX:	0	; MAX VALUE
392	001422	000000	PERCNT:	0	; PERCENT FOR SAR ROUTINE
393	001424	000000	OUT:	0	
394	001426	000000	MYTEMP:	0	
395	001430	000000	EDINT:	0	
396	001432	000000	STEMP1:	0	
397	001434	000000	STEMP2:	0	

ADDRESS OF KMC-11 OF LPA-11 THE ADDR FOR KMADO MAY BE
 CHANGED BY THE USER TO REFLECT
 A DIFFERENT KMC-11 ADDR. THE
 REST OF THE ADDRESSES WILL
 BE CHANGED BY THE PROGRAM.

406	001436		LPCI:		
407	001436	170460	KMADO:	.WORD 170460	; BASE KMC ADDR. MAY BE PATCHED BY USER.
408					
409	001440		LPMR:		
410	001440	170461	KMAD1:	.WORD 170460+1	; > DO NOT <; KMC-CSR ADDR

411	001442				LPC0:				
412	001442	170462			KMAD2:	.WORD	170460+2	; >PATCH	< ;
413	001444				LPS0:				
414	001444	170463			KMAD3:	.WORD	170460+3	; >THIS AREA	<
415	001446				LPADL:				
416	001446	170464			KMAD4:	.WORD	170460+4	;	
417	001450				LPADH:				
418	001450	170465			KMAD5:	.WORD	170460+5	; >DO NOT	<
419	001452				LPMS1:				
420	001452	170466			KMAD6:	.WORD	170460+6	; >PATCH	<
421	001454				LPMS2:				
422	001454	170467			KMAD7:	.WORD	170460+7	; >THIS AREA	<
423									
424	001456	000340			VECTOR:	.WORD	AVECT1&777	; BASE VECTOR OF KMC	
425	001460	000344			VECTPS:	.WORD	4+AVECT1&777	; VECOTR ADDR.+2	
426									
427	001462	000004			VERSN:	.WORD	4	; CURRENT VERSION NUMBER OF MICROCODE.	
428									
429	001464	000000			.DVLS:	.WORD	0	; /DEVICE LIST OF I/O ADDR. DEFINED	
430	001466	000020				.BLKW	16.	; /BY INIT.	
431									
432									
433	001526				UNEXP:				
434	001526	012737	001542	001162	MOV	#1\$, \$ESCAPE		; ;ESCAPE TO 1\$ ON ERROR	
435	001534	005237	001103		INC	\$ERFLG			
436	001540	104003			ERROR	3			
437	001542	005037	001162		1\$:	CLR	\$ESCAPE	; RETURN ESCAPE TO NORMAL	
438	001546	000002			RTI			; UNEXPECTED INTERRUPT	

```

439          .SBTTL          CONTROL A AND C DECODERS
440          ISERV:        MOV      RO, -(SP)          ;SAVE RO
441          001550 010046          MOV      @STKB, RO      ;GET CHARACTER
442          001552 017700 177370    BIC      #177600, RO
443          001556 042700 177600    CMPB     RO, #3          ;IS IT ↑C?
444          001562 120027 000003    BNE     1$
445          001566 001010          TYPE     ,CMMSG        ;ECHO CHARACTER
446          001570 104401 012244    MOV      @STACK, SP
447          001574 012706 001100    JSR     PC, RST        ;RESET & SET INTRPT. EN.
448          001600 004737 011362    JMP     BEG2
449          001604 000137 002404    1$:     CMPB     RO, #1          ;IS IT ↑A?
450          001610 120027 000001    BNE     2$
451          001614 001010          TYPE     ,AMSG        ;ECHO CHARACTER
452          001616 104401 012237    MOV      @STACK, SP
453          001622 012706 001100    JSR     PC, RST        ;RESET & SET INTRPT. EN.
454          001626 004737 011362    JMP     @TADDR        ;RETURN TO TEST
455          001632 000177 177526    2$:     CMPB     RO, #7          ;IS IT ↑G?
456          001636 120027 000007    BNE     NONE
457          001642 001021          CMP      SWR, #177570 ;HARDWARE SWREG?
458          001644 023727 001140 177570 BNE     NONE
459          001652 001415          TYPE     ,GMSG        ;ECHO CHARACTER
460          001654 104401 012251    MOV      @SWR, -(SP)  ;;SAVE @SWR FOR TYPEOUT
461          001660 017746 177254    ;;TYPE SWREG
462          001664 104403          ;;GO TYPE--OCTAL ASCII
463          001666 006          ;;TYPE 6 DIGITS
464          001667 001          ;;TYPE LEADING ZEROS
465          001670 104401 012431    RDOCT   ,SLASH
466          001674 104407          MOV      (SP)+, @SWR  ;READ NEW VALUE
467          001676 012677 177236    MOV      (SP)+, RO   ;LOAD NEW SWREG VALUE
468          001702 012600          POPRO:  RTI
469          001704 000002          RETURN:
470          001706 104401 012235    NONE:   TYPE     ,QUEST ;TYPE "?"
471          001712 000773          BR      POPRO

```

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472 .SBTTL INITIAL START-UP,HOUSEKEEPING, AND DIALOGUE
473 BEGIN: CLR WFTST
474 BR RBEG
475 BEGIN2: MOV #1,WFTST
476 RBEG: RESET
477 .SBTTL INITIALIZE THE COMMON TAGS
478 ;;CLEAR THE COMMON TAGS ($CMTAG) AREA
479 MOV #SCMTAG,R6 ;;FIRST LOCATION TO BE CLEARED
480 CLR (R6)+ ;;CLEAR MEMORY LOCATION
481 CMP #SWR,R6 ;;DONE?
482 BNE -6 ;;LOOP BACK IF NO
483 MOV #STACK,SP ;;SETUP THE STACK POINTER
484 ;;INITIALIZE A FEW VECTORS
485 MOV #SCOPE,@IOTVEC ;;IOT VECTOR FOR SCOPE ROUTINE
486 MOV #340,@IOTVEC+2 ;;LEVEL 7
487 MOV #ERROR,@EMTVEC ;;EMT VECTOR FOR ERROR ROUTINE
488 MOV #340,@EMTVEC+2 ;;LEVEL 7
489 MOV #TRAP,@TRAPVEC ;;TRAP VECTOR FOR TRAP CALLS
490 MOV #340,@TRAPVEC+2 ;;LEVEL 7
491 MOV #SPWRDN,@PWRVEC ;;POWER FAILURE VECTOR
492 MOV #340,@PWRVEC+2 ;;LEVEL 7
493 MOV SENDCT,SEOPCT ;;SETUP END-OF-PROGRAM COUNTER
494 CLR STIMES ;;INITIALIZE NUMBER OF ITERATIONS
495 CLR $ESCAPE ;;CLEAR THE ESCAPE ON ERROR ADDRESS
496 MOV #1,$SERMAX ;;ALLOW ONE ERROR PER TEST
497 MOV #,$SLPADR ;;INITIALIZE THE LOOP ADDRESS FOR SCOPE
498 MOV #,$SLPERR ;;SETUP THE ERROR LOOP ADDRESS
499 ;;SIZE FOR A HARDWARE SWITCH REGISTER. IF NOT FOUND OR IT IS
500 ;;EQUAL TO A "-1" SETUP FOR A SOFTWARE SWITCH REGISTER.
501 MOV @ERRVEC,-(SP) ;;SAVE ERROR VECTOR
502 MOV #64,$ERRVEC ;;SET UP ERROR VECTOR
503 MOV #DSWR,SWR ;;SETUP FOR A HARDWARE SWICH REGISTER
504 MOV #DDISP,DISPLAY ;;AND A HARDWARE DISPLAY REGISTER
505 CMP #-1,$SWR ;;TRY TO REFERENCE HARDWARE SWR
506 BNE 66$ ;;BRANCH IF NO TIMEOUT TRAP OCCURRED
507 ;;AND THE HARDWARE SWR IS NOT = -1
508 BR 65$ ;;BRANCH IF NO TIMEOUT
509 MOV #65$,(SP) ;;SET UP FOR TRAP RETURN
510 RTI
511 MOV #SWREG,SWR ;;POINT TO SOFTWARE SWR
512 MOV #DISPREG,DISPLAY
513 MOV (SP)+,@ERRVEC ;;RESTORE ERROR VECTOR
514
515 CLR $PASS ;;CLEAR PASS COUNT
516 BITB #APTSIZE,$ENVM ;;TEST USER SIZE UNDER APT
517 BEQ 67$ ;;YES,USE NON-APT SWITCH
518 MOV #SSWREG,SWR ;;NO,USE APT SWITCH REGISTER
519
520
521
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525

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;; THIS SECTION OF CODE HANDLES INITIALIZING LPA-11 FUNCTIONS

526	002200	010046		MOV	RO, -(SP)	
527	002202	010146		MOV	R1, -(SP)	
528	002204	013700	001436	MOV	KMAD0, RC	; GET KMC-11 ADDRESS.
529	002210	012701	001440	MOV	#KMAD1, R1	; GET ADDR. OF ADDR. LIST.
530						
531	002214	005200		68\$: INC	RO	; UPDATE ADDR.
532	002216	010021		MOV	RO, (1)+	; WRITE ADDR.
533	002220	020127	001456	CMP	R1, #KMAD7+2	; DONE ALL ADDRESSES?
534	002224	001373		BNE	68\$; NO - DO NEXT ADDR.
535	002226	005037	001464	CLR	. DVLS	; CLR ADDR. LIST.
536	002232	012601		MOV	(SP)+, R1	
537	002234	012600		MOV	(SP)+, RO	

```

538 002236 005037 001400 CLR FLAG ;CLEAR VT55 FLAG
539 002242 005737 000042 TST @#42 ;IS IT CHAINED?
540 002246 001033 BNE REST1
541 .SBTTL DETERMINE IF VT55 TYPE TERMINAL IS PRESENT
542 002250 042777 000100 176666 BIC #100,@STKS
543 002256 104401 013671 TYPE CO ;TYPE ASCIZ STRING
544 002262 004737 002656 JSR PC,VTFLG ;GET A CHARACTER
545 002266 020027 000033 CMP RO,#33
546 002272 001017 BNE NOVT55 ;NO VT55 PRESENT
547 002274 004737 002656 JSR PC,VTFLG ;GET A CHARACTER
548 002300 020027 000057 CMP RO,#57
549 002304 001012 BNE NOVT55 ;NO VT55 PRESENT
550 002306 004737 002656 JSR PC,VTFLG ;GET A CHARACTER
551 002312 020027 000103 CMP RO,#103
552 002316 001403 BEQ VT55 ;VT55 IS PRESENT
553 002320 020027 000105 CMP RO,#105
554 002324 001002 BNE NOVT55
555 002326 005237 001400 VT55: INC FLAG

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556 .SBTTL DIALOGUE TO DETERMINE WHICH TEST TO RUN
557 002332 104401 014034 NOVTS5: TYPE ,HEAD1
558 002336 REST1: ;RESET
559 002336 004737 005376 JSR PC, FIXONE ; INITIALIZE ADDRESSES
560 002342 013700 001334 MOV KAVECT, RO
561 002346 012720 001550 MOV #ISERV, (RO)+
562 002352 012710 000340 MOV #340, (RO)
563 002356 012737 062341 001366 MOV #62341, RNA ; RANDOM NO, VARIABLES
564 002364 012737 142315 001370 MOV #142315, RNB
565 002372 012737 127623 001372 MOV #127623, RNC
566 002400 004737 011650 JSR PC, WFADJ ; STANDARD OR OPTION TEST TOLERANCES?
567 002404 BEG2: ; RESET ; RESTART ADDRESS
568 002404 012706 001100 MOV #STACK, SP ; RESET STACK IN CASE RESTARTED
569 002410 005737 000042 TST @#42 ; IS IT CHAINED?
570 002414 001402 BEQ 1$
571 002416 000137 005114 JMP BEGL ; GO TO LOGIC TESTS
572 002422 104401 013477 1$: TYPE ,MSG71
573 002426 104406 TRYAG: RDLIN
574 002430 052777 000100 176506 BIS #100, @STKS
575 002436 005037 177776 CLR PSW
576 002442 012600 MOV (SP)+, RO ; READ ANSWER
577 002444 142710 000040 BICB #40, (RO)
578 002450 121027 000101 CMPB (RO), #'A ; IS IT A?
579 002454 001002 BNE 1$ ;; NO, TRY C
580 002456 000137 005156 JMP BEGINA ; GO TO AUTO TEST
581 002462 121027 000103 1$: CMPB (RO), #'C ; IS IT C?
582 002466 001002 BNE 2$ ;; NO, TRY L
583 002470 000137 004656 JMP BEGINC ; GO TO CALIBRATION TEST
584 002474 121027 000114 2$: CMPB (RO), #'L ; IS IT L?
585 002500 001002 BNE 3$ ;; NO, TRY N
586 002502 000137 005114 JMP BEGL ; GO TO LOGIC TESTS
587 002506 121027 000116 3$: CMPB (RO), #'N ; IS IT N?
588 002512 001002 BNE 4$ ;; NO, TRY S
589 002514 000137 005540 JMP BEGINN ; GO TO NOISE TEST
590 002520 121027 000123 4$: CMPB (RO), #'S ; IS IT S?
591 002524 001002 BNE 5$ ;; NO, TRY W
592 002526 000137 005610 JMP BEGINS ; GO TO SETTLE TEST
593 002532 121027 000127 5$: CMPB (RO), #'W ; IS IT W?
594 002536 001002 BNE 6$ ;; NO, TRY AGAIN
595 002540 000137 005250 JMP BEGINW ; GO TO WRAPAROUND TEST
596 002544 104401 012235 6$: TYPE
597 002550 000726 BR ; WAIT FOR CHARACTER
598 002552 013737 001250 001126 TESTAD: MOV $BASE, $BDDAT ; SETUP TO TEST FOR AD11K'S
599 002560 005037 001464 CLR .DVLS
600 002564 005037 001466 CLR .DVLS+2
601 002570 005037 001354 CLR NBEXT ; CLEAR AD11K COUNTER
602 002574 1$: ; ADDRESS AD11K
603
604 ; * MOV $GDDAT, @SBDDAT ; / PUT DATA FROM $GDDAT TO DEVICE REG $BDDAT
605 002604 005737 017450 TST $AERR ; DEVICE EXSIST? =0, YES
606 002610 001006 BNE 2$ ; =1, NO.
607
608 002612 005237 001354 . INC NBEXT ; INCREMENT AD11K COUNTER
609 002616 063737 001326 001126 ADD VADR, $BDDAT ; GET NEXT AD11K

```


D03

MAINDEC-11-DRLPKA
DRLPK.P11

MACY11 27(654) 15-DEC-77 08:40 PAGE 16
DIALOGUE TO DETERMINE WHICH TEST TO RUN

SEQ 0030

610 002624 000763

BR 1\$

::TRY NEXT AD11K

```

611 002626          2$:
612 002626 013746 001354      MOV      NBEXT,-(SP)      ;;SAVE NBEXT FOR TYPEOUT
613                                     ;;TYPE NUMBER OF AD11K'S
614 002632 104403          TYPOS      ;;GO TYPE--OCTAL ASCII
615 002634          .BYTE      2      ;;TYPE 2 DIGIT(S)
616 002635          .BYTE      0      ;;SUPPRESS LEADING ZEROS
617 002636 104401 013037      TYPE      ,MSG50
618 002642 005337 001354      DEC      NBEXT      ;ADJUST AD11K COUNT
619 002646 013737 001354 001356  MOV     NBEXT,NMBEXT ;KEEP COUNT OF NUMBER
620 002654 000207          RTS      PC
621
622 002656 005000          VTFLG: CLR      RO      ;TEST FOR PRESENCE
623 002660 105777 176260      1$: TSTB     @TKS      ;OF VT55
624 002664 100404          BMI      2$      ;;VT55 RESPONDS WITH <33><57>[<103> OR <105>]
625 002666 005300          DEC      RO
626 002670 001373          BNE      1$
627 002672 005726          TST     (SP)+      ;;POP A WORD OFF STACK
628 002674 000616          BR      NOVT55      ;;NO VT55 PRESENT
629 002676 017700 176244      2$: MOV     @TKB,R()
630 002702 042700 177600      BIC     #177600,RO      ;TEST VT55 CODE
631 002706 000207          RTS      PC

```

```

632 002710 BEGINL:
633 ;*****
634 ;*TEST 1 FLOAT A ONE THRU MULTIPLEXER BITS
635 ;*****
636 002710 012737 002710 001106 †ST1: MOV #TST1,$LPADR
637 002716 012737 002710 001110 MOV #TST1,$LPERR
638 002724 012737 000400 001124 MOV #BIT8,$GDDAT ;LOAD FIRST BIT
639 002732 004737 003400 2$: JSR PC,TESTIT
640 002736 104001 ERROR 1 ;FAILED TO LOAD + READ BIT
641 002740 006137 001124 1$: ROL $GDDAT ;GET NEXT BIT
642 002744 023727 001124 040000 CMP $GDDAT,#BIT14 ;FINISHED?
643 002752 001367 BNE 2$ ;;NO,GO TO NEXT TEST
644
645 ;*****
646 ;*TEST 2 LOAD AND READ BACK INTERRUPT ENABLE BIT6
647 ;*****
648 002754 000004 †ST2: SCOPE
649 002756 012777 001526 176472 MOV #UNEXP,$VECTOR ;SETUP FOR UNEXPECTED INTERRUPT
650 002764 012737 000100 001124 MOV #BIT6,$GDDAT ;LOAD EXPECTED DATA
651 002772 004737 003400 JSR PC,TESTIT
652 002776 104001 ERROR 1 ;FAILED TO LOAD + READ INTERRUPT ENABLE
653
654 ;*****
655 ;*TEST 3 LOAD AND READ BACK CLOCK OVERFLOW START ENABLE BITS
656 ;*****
657 003000 000004 †ST3: SCOPE
658 003002 012737 000040 001124 MOV #BIT5,$GDDAT ;LOAD EXPECTED DATA
659 003010 004737 003400 JSR PC,TESTIT
660 003014 104001 ERROR 1 ;FAILED TO LOAD + READ CLOCK OVERFLOW START ENAB
661
662 ;*****
663 ;*TEST 4 LOAD AND READ BACK EXTERNAL START ENABLE BIT4
664 ;*****
665 003016 000004 †ST4: SCOPE
666 003020 012737 000020 001124 MOV #BIT4,$GDDAT ;LOAD EXPECTED DATA
667 003026 004737 003400 JSR PC,TESTIT
668 003032 104001 ERROR 1 ;FAILED TO LOAD + READ EXT. START ENABLE
669
670 ;*****
671 ;*TEST 5 LOAD AND READ BACK ERROR FLAG BIT15
672 ;*****
673 003034 000004 †ST5: SCOPE
674 003036 012737 100000 001124 MOV #BIT15,$GDDAT ;LOAD EXPECTED DATA
675 003044 004737 003400 JSR PC,TESTIT
676 003050 104001 ERROR 1 ;FAILED TO LOAD + READ ERROR FLAG

```


G03

MAINDEC-11-DRLPKA
DRLPK.P11 T6

MACY11 27(654) 15-DEC-77 08:40 PAGE 19
TEST DONE FLAG SETS AND BIT0 CLEARS ON END OF CONV.

SEQ 0033

```

676                                     ;*****
677                                     ;*TEST 6      TEST DONE FLAG SETS AND BIT0 CLEARS ON END OF CONV.
678                                     ;*****
679 003052 000004                       †ST6: SCOPE
680 003054 012700 001000                 MOV      #BIT9,RO      ;STALL TIME COUNTER
681
682
683                                     ;*      MOV      @STREG,MYTEMP    ;/READ DEVICE REG STREG,PUT DATA IN MYTEMP.
684 003070 005237 001426                 INC      MYTEMP
685
686                                     ;*      MOV      MYTEMP,@STREG    ;/ PUT DATA FROM MYTEMP TO DEVICE REG STREG
687 003104 012737 000200 001124         MOV      #BIT7,$GDDAT  ;LOAD EXPECTED
688 003112 005300                       1$:      DEC      RO      ;STALL
689 003114 001376                       BNE     1$             ;TIME
690
691
692                                     ;*      MOV      @STREG,MYTEMP    ;/READ DEVICE REG STREG,PUT DATA IN MYTEMP.
693 003126 042737 100000 001426         BIC     #BIT15,MYTEMP
694
695                                     ;*      MOV      MYTEMP,@STREG    ;/ PUT DATA FROM MYTEMP TO DEVICE REG STREG
696 003144 004737 003410                 JSR     PC,TEST
697 003150 104001                       ERROR    1             ;A/D DONE FLAG FAILED TO SET;BIT0 FAILED TO CLEAR
698
699                                     ;*      MOV      @ADDBUFF,MYTEMP  ;/READ DEVICE REG ADDBUFF,PUT DATA IN MYTEMP.
700 003162 013700 001426                 MOV     MYTEMP,RO     ;/PUT CONVERTED VALUE IN RO.
701
702                                     ;*****
703                                     ;*TEST 7      TEST A/D DONE FLAG CLEARS WHEN READ CONVERTED VALUE
704                                     ;*****
705 003166 000004                       †ST7: SCOPE
706 003170 012737 000001 001426         MOV     #BIT0,MYTEMP
707
708                                     ;*      MOV      MYTEMP,@STREG    ;/ PUT DATA FROM MYTEMP TO DEVICE REG STREG
709 003206 005037 001124                 CLR     $GDDAT
710 003212
711
712                                     ;*      MOV      @STREG,MYTEMP    ;/READ DEVICE REG STREG,PUT DATA IN MYTEMP.
713 003222 105737 001426                 TSTB   MYTEMP
714 003226 100371                       BPL     1$
715
716                                     ;*      MOV      @ADDBUFF,MYTEMP  ;/READ DEVICE REG ADDBUFF,PUT DATA IN MYTEMP.
717 003240 013700 001426                 MOV     MYTEMP,RO     ;/PUT CONVERTED VALUE IN RO.
718 003244 004737 003410                 JSR     PC,TEST
719 003250 104001                       ERROR    1             ;DONE FLAG FAILED TO CLEAR

```

H03

MAINDEC-11-DRLPKA
DRLPK.P11 T10

MACY11 27(654) 15-DEC-77 08:40 PAGE 20
TEST ERROR FLAG SETS IF 2ND CONVERSION ENDS BEFORE READING BUFFER

SEQ 0034

```
720 ;:*****
721 ;*TEST 10 TEST ERROR FLAG SETS IF 2ND CONVERSION ENDS BEFORE READING BUFFER
722 ;:*****
723 003252 000004          †ST10: SCOPE
724 003254 012737 000010 001160      MOV #10,$TIMES ;;DO 10 ITERATIONS
725 003262 012737 000001 001426      MOV #BIT0,MYTEMP
726
727 ;* MOV MYTEMP,@STREG ;/ PUT DATA FROM MYTEMP TO DEVICE REG STREG
728 003300 1$:
729
730 ;* MOV @STREG,MYTEMP ;/READ DEVICE REG STREG,PUT DATA IN MYTEMP.
731 003310 105737 001426      TSTB MYTEMP
732 003314 100371          BPL 1$
733 003316 012737 100200 001124 2$: MOV #BIT15:BIT7,$GDDAT ;LOAD EXPECTED VALUE
734 003324 012737 000001 001426      MOV #BIT0,MYTEMP
735
736 ;* MOV MYTEMP,@STREG ;/ PUT DATA FROM MYTEMP TO DEVICE REG STREG
737 003342 012700 001000      MOV #BIT9,RO ;WAIT FOR 2ND
738 003346 005300          DEC RO ;CONVERSION TO END
739 003350 001376          BNE 3$
740 003352 004737 003410 4$: JSR PC,TEST
741 003356 104001          ERROR 1 ;ERROR FLAG NOT SET WHEN 2ND
742 ; CONVERT ENDS BEFORE READ BUFFER FROM FIRST
743
744 ;* MOV @ADBUFF,MYTEMP ;/READ DEVICE REG ADBUFF,PUT DATA IN MYTEMP.
745 003370 013700 001426      MOV MYTEMP,RO ;/PUT CONVERTED VALUE IN RO.
```

MAINDEC-11-DRLPKA
DRLPK.P11 T10

MACY11 27(654) 15-DEC-77 08:40 PAGE 21
TEST ERROR FLAG SETS IF 2ND CONVERSION ENDS BEFORE READING BUFFER

SEQ 0035

```

746 003374 000004          SCOPE
747 003376 000207          RTS      PC          ;RETURN TO TEST SECTION
748
749
750          ;;SUBROUTINE FOR LOGIC TESTS;;
751 003400          TESTIT:
752
753          ;*      MOV      $GDDAT,@STREG      ;/ PUT DATA FROM $GDDAT TO DEVICE REG STREG
754 003410          TEST:
755
756          ;*      MOV      @STREG,$BDDAT      ;/READ DEVICE REG STREG,PUT DATA IN $BDDAT.
757 003420 023737 001124 001126      CMP      $GDDAT,$BDDAT      ;COMPARE RESULTS
758 003426 001002          BNE      RETERR      ;:ERROR RETURN
759 003430 062716 000002          ADD      #2,(SP)          ;BUMP RETURN ADDRESS TO GET AROUND ERROR
760 003434 000207          RETERR: RTS      PC
    
```



```

761 .SBTTL WRAPAROUND TEST SECTION
762 003436 WRAP:
763 ;*****
764 ;*TEST 11 TEST CH14 GROUND
765 ;*****
766 003436 000240 †ST11: NOP
767 003440 012737 000010 001160 MOV #10,$TIMES ;;DO 10 ITERATIONS
768 003446 012737 000011 001102 MOV #$TN-1,$TSTNM
769 003454 012737 003776 001110 MOV #TST17,$LPERR
770 003462 012737 003776 001106 MOV #TST17,$LPADR
771 003470 004537 011072 JSR RS,CONVRT ;DO 8 CONVERSIONS
772 003474 000014 14 JSR RS,COMPAR ;COMPARE RESULTS
773 003476 004537 011314 4000 ;NOMINAL
774 003502 004000 V50 ;TOLERANCE
775 003504 011726 ERROR 4 ;ERROR-CH14 NOT GROUND-AD11K MUST BE IN
776 003506 104004 ;SINGLE-ENDED CONFIGURATION,G5036 WRAPAROUND
777 ;MODULE MUST BE PRESENT,CHECK CONNECTION A-VV,VV-A
778
779
780 ;*****
781 ;*TEST 12 TEST CONVERSION FROM EXT. START
782 ;*****
783 †ST12: SCOPE
784 003510 000004 MOV #10,$TIMES ;;DO 10 ITERATIONS
785 003512 012737 000010 001160 TST BASECH ;TESTING AN AM?
786 003520 005737 001332 BNE TST13 ;;YES, GOTO NEXT TEST
787 003524 001044 MOV #BIT4,MYTEMP
788 003526 012737 000020 001426
789
790 ;* MOV MYTEMP,$STREG ;/ PUT DATA FROM MYTEMP TO DEVICE REG STREG
791 003544 012700 001000 MOV #BIT9,RO ;TIME DELAY COUNTER
792 003550 012737 000220 001124 MOV #BIT7:BIT4,$GDDAT ;LOAD EXPECTED
793 003556 012737 000200 001426 MOV #200,MYTEMP
794
795 ;* MOV MYTEMP,$ADBUFF ;/ PUT DATA FROM MYTEMP TO DEVICE REG ADBUFF
796 ;WRAPAROUND MODULE PRESENT
797 003574 005300 1$: DEC RO
798 003576 001376 BNE 1$
799 003600 004737 003410 JSR PC,TEST
800 003604 104001 ERROR 1 ;FAILED TO DO CONVERSION FROM EXT. START
801
802 ;* MOV $ADBUFF,MYTEMP ;/READ DEVICE REG ADBUFF,PUT DATA IN MYTEMP.
803 003616 013700 001426 MOV MYTEMP,RO ;/PUT CONVERTED VALUE IN RO.
804 003622 005037 001426 CLR MYTEMP
805
806 ;* MOV MYTEMP,$STREG ;/ PUT DATA FROM MYTEMP TO DEVICE REG STREG
807
808
809 ;*****
810 ;*TEST 13 TEST CHO GROUND
811 ;*****
812 003636 000004 †ST13: SCOPE
813 003640 012737 000010 001160 MOV #10,$TIMES ;;DO 10 ITERATIONS
814 003646 004537 011072 JSR RS,CONVRT ;CONVERT 8 TIMES

```

K03

MAINDEC-11-DRLPKA
DRLPK.P11 T13

MACY11 27(654) 15-DEC-77 08:40 PAGE 23
TEST CHO GROUND

SEQ 0037

815	003652	000000	
816	003654	004537	011314
817	003660	004000	
818	003662	011720	
819	003664	104004	

0
JSR RS, COMPAR
4000
V1
ERROR 4

: COMPARE RESULTS
: NOMINAL
: TOLERANCE
: ERROR ON A/D CHANNEL

```

820
821
822
823 003666 000004
824 003670 012737 000010 001160
825 003676 004537 011072
826 003702 000001
827 003704 004537 011314
828 003710 004000
829 003712 011724
830 003714 104004
831
832
833
834
835 003716 000004
836 003720 012737 000010 001160
837 003726 004537 011072
838 003732 000002
839 003734 004537 011314
840 003740 004632
841 003742 011726
842 003744 104004
843
844
845
846
847
848 003746 000004
849 003750 012737 000010 001160
850 003756 004537 011072
851 003762 000003
852 003764 004537 011314
853 003770 006000
854 003772 011734
855 003774 104004
856
857
858
859
860 003776 000004
861 004000 012737 000010 001160
862 004006 004537 011072
863 004012 000004
864 004014 004537 011314
865 004020 002000
866 004022 011734
867 004024 104004

*****
*TEST 14 TEST CH1 GROUND
*****
†ST14: SCOPE
MOV #10,$TIMES ;;DO 10 ITERATIONS
JSR R5,CONVRT ;:CONVERT 8 TIMES
1 ;:CHANNEL 1
JSR R5,COMPAR ;:COMPARE RESULTS
4000 ;:NOMINAL
V10 ;:TOLERANCE
ERROR 4 ;:ERROR ON A/D CHANNEL

*****
*TEST 15 TEST CH2 +1 VOLT
*****
†ST15: SCOPE
MOV #10,$TIMES ;;DO 10 ITERATIONS
JSR R5,CONVRT ;:CONVERT 8 TIMES
2 ;:CHANNEL 2
JSR R5,COMPAR ;:COMPARE RESULTS
4632 ;:NOMINAL
V50 ;:TOLERANCE
ERROR 4 ;:ERROR ON A/D CHANNEL
;AD11K MUST BE SET UP FOR +OR- 5V OR +OR- 5.12V

*****
*TEST 16 TEST CH3 +2.5 VOLTS
*****
†ST16: SCOPE
MOV #10,$TIMES ;;DO 10 ITERATIONS
JSR R5,CONVRT ;:CONVERT 8 TIMES
3 ;:CHANNEL 3
JSR R5,COMPAR ;:COMPARE RESULTS
6000 ;:NOMINAL
V240 ;:TOLERANCE
ERROR 4 ;:ERROR ON A/D CHANNEL

*****
*TEST 17 TEST CH4 -2.5 VOLTS
*****
†ST17: SCOPE
MOV #10,$TIMES ;;DO 10 ITERATIONS
JSR R5,CONVRT ;:CONVERT 8 TIMES
4 ;:CHANNEL 4
JSR R5,COMPAR ;:COMPARE RESULTS
2000 ;:NOMINAL
V240 ;:TOLERANCE
ERROR 4

```



```

868 ;*****
869 ;*TEST 20 TEST VERNIER OFFSET DAC ON CH12
870 ;*****
871 004026 000004 000001 001160 †ST20: SCOPE
872 004030 012737 000001 001160 MOV #1,$TIMES ;;DO 1 ITERATION
873 004036 005037 001426 CLR MYTEMP
874
875 ;* MOV MYTEMP,$ADBUFF ;/ PUT DATA FROM MYTEMP TO DEVICE REG ADBUFF
876 004052 004737 004646 JSR PC,$AWAIT ;DELAY FOR DAC SETTLING
877 004056 004537 011072 JSR R5,$CONVRT ;CONV. CH12, DIRECT VERNIER DAC
878 004062 000012 12
879 004064 013704 001346 MOV TEMP,R4 ;SAVE VALUE IN R4
880 004070 004537 011314 JSR R5,$COMPAR ;COMPARE RESULTS
881 004074 002376 2376 ;WITH -1.875 VOLTS
882 004076 011732 V115 ;TOLERANCE OF 10%
883 004100 104004 ERROR 4
884 004102 005037 001420 CLR MAX
885 004106 012702 000001 MOV #1,R2
886 004112 010237 001426 1$: MOV R2,MYTEMP ;SET UP NEXT VERNIER DAC VALUE
887
888 ;* MOV MYTEMP,$ADBUFF ;/ PUT DATA FROM MYTEMP TO DEVICE REG ADBUFF
889 004126 004737 004646 JSR PC,$AWAIT ;DELAY FOR DAC SETTLING
890 004132 004537 011072 JSR R5,$CONVRT ;CONVERT IT
891 004136 000012 12
892 004140 005737 001420 TST MAX
893 004144 001010 BNE 2$
894 004146 023727 001346 004000 CMP TEMP,#4000
895 004154 002404 BLT 2$
896 004156 005237 001420 INC MAX
897 004162 010237 001414 MOV R2,$MIN
898 004166 020227 000200 2$: CMP R2,$200
899 004172 001003 BNE 3$
900 004174 013737 001346 004266 MOV TEMP,$4$
901 004202 013703 001346 3$: MOV TEMP,R3 ;SAVE VALUE
902 004206 160437 001346 SUB R4,TEMP ;TEMP=DIFF. BETWEEN VALUE&PREVIOUS
903 004212 010304 MOV R3,R4 ;SET UP PREVIOUS VALUE FOR NEXT TIME THRU
904 004214 004537 011314 JSR R5,$COMPAR ;COMPARE RESULTS
905 004220 000006 6 ;WITH 15 MILLIVOLTS(1 DAC LSB)
906 004222 011736 V5
907 004224 104004 ERROR 4
908 004226 005202 INC R2
909 004230 020227 000400 CMP R2,$400 ;DONE?
910 004234 001326 BNE 1$ ;NO-DO NEXT VERNIER DAC VALUE
911 004236 004737 020422 JSR PC,$RESET
912 004242 052777 000100 174674 BIS #100,$STKS
913 004250 004737 004646 JSR PC,$AWAIT ;LET DAC SETTLE
914 004254 004537 011072 JSR R5,$CONVRT ;CONVERT IT
915 004260 000012 12
916 004262 004537 011314 JSR R5,$COMPAR ;COMPARE RESULTS
917 004266 000000 0
918 004270 011722 V2
919 004272 104004 ERROR 4

```

```

920      ;*****
921      ;*TEST 21      TEST CH13 +2.5 VOLTS
922      ;*****
923      004274 000004      †ST21: SCOPE
924      004276 012737      MOV      #10,STIMES      ;;DO 10 ITERATIONS
925      004304 004537      000010 001160      JSR      R5,CONVRT      ;CONVERT 8 TIMES
926      004310 000013      011072      13
927      004312 004537      011314      JSR      R5,COMPAR      ;COMPARE RESULTS
928      004316 006000      6000      ;NOMINAL
929      004320 011730      V144      ;TOLERANCE
930      004322 104004      ERROR      4
931      ;*****
932      ;*TEST 22      TEST CH17 +4V
933      ;*****
934      004324 000004      †ST22: SCOPE
935      004326 012737      MOV      #10,STIMES      ;;DO 10 ITERATIONS
936      004334 004537      000010 001160      JSR      R5,CONVRT      ;CONVERT 8 TIMES
937      004340 000017      011072      17      ;CHANNEL 17
938      004342 004537      011314      JSR      R5,COMPAR      ;COMPARE RESULTS
939      004346 007146      7146      ;NOMINAL
940      004350 011734      V240      ;TOLERANCE
941      004352 104004      ERROR      4      ;ERROR ON A/D CHANNEL

```

```

942
943
944
945 004354 000004
946 004356 012737 000001 001160
947 004364 013737 001332 001362
948 004372 013737 001332 001360
949 004400 012737 004001 001410
950 004406 004537 006452
951 004412 000062
952 004414 013737 001404 001346
953 004422 004537 006452
954 004426 000062
955 004430 063737 001404 001346
956 004436 162737 000062 001346
957 004444 013700 001414
958 004450 006300
959 004452 160037 001346
960 004456 104401 013703
961 004462 013702 001346
962 004466 004737 011504
963 004472 104401 013716
964 004476 004537 011314
965 004502 000000
966 004504 011740
967 004506 000401
968 004510 000403
969 004512 104401 012505
970 004516 000402
971 004520 104401 012474

```

```

*****
*TEST 23      OFFSET ON CHO
*****
†ST23:  SCOPE
        MOV      #1,STIMES      ;;DO 1 ITERATION
        MOV      BASECH,CHANL   ;LOAD CHANNEL
        MOV      BASECH,DUMMY   ;LOAD DUMmY
        MOV      #4001,EDGE
        JSR      RS,SARSUB
        SO.
        MOV      DAC,TEMP
        JSR      RS,SARSUB
        SO.
        ADD      DAC,TEMP
        SUB      #62,TEMP
        MOV      MIN,RO
        ASL      RO
        SUB      RO,TEMP
        TYPE     MOFSET          ;TYPE ASCIZ STRING
        MOV      †TEMP,R2
        JSR      PC,DECTYP
        TYPE     MLSB           ;TYPE ASCIZ STRING
        JSR      RS,COMPAR      ;IS RESULT WITHIN LIMITS?
        O
        V500
        BR       OFFERR         ;NO-ERROR
        BR       OFFOK         ;YES-OK
        TYPE     ERMSG
        BR       †ST24
        TYPE     ,OKMSG
        ;;GO TO NEXT TEST

```

```

OFFERR: TYPE
OFFOK:  TYPE

```



```

972      ;:*****
973      ;:TEST 24      NOISE TEST ON 8 EDGES
974      ;:*****
975      004524 000004      †ST24: SCOPE
976      004526 012737 000001 001160      MOV      #1,$TIMES      ;;DO 1 ITERATION
977      004534 012737 000116 001346      MOV      #116,TEMP      ;DAC VALUE
978      004542 004537 010664      JSR      R5,NOI8      ;NOISE AT -FULL SCALE
979      004546 000015      15
980      004550 004537 010664      JSR      R5,NOI8      ;NOISE AT MID-RANGE
981      004554 000007      7
982      004556 004537 010664      JSR      R5,NOI8      ;NOISE AT +FULL SCALE
983      004562 000016      16
984
985      ;:*****
986      ;:TEST 25      SETTLE TEST ON 8 EDGES
987      ;:*****
988      004564 000004      †ST25: SCOPE
989      004566 012737 000001 001160      MOV      #1,$TIMES      ;;DO 1 ITERATION
990      004574 004537 006122      JSR      R5,SET8      ;SETTLE-POSITIVE DIRECTION
991      004600 000015      15
992      004602 000016      16
993      004604 012737 000116 001346      MOV      #116,TEMP
994      004612 004537 006122      JSR      R5,SET8      ;SETTLE-NEGATIVE DIRECTION
995      004616 000016      16
996      004620 000015      15
997
998      ;:*****
999      ;:TEST 26      DIFFERENTIAL LINEARITY AND RELATIVE ACCURACY TEST
1000     ;:*****
1001     004622 000004      †ST26: SCOPE
1002     004624 012737 000001 001160      MOV      #1,$TIMES      ;;DO 1 ITERATION
1003     004632 005737 001202      TST      $PASS      ;FIRST TIME-SKIP DIFLIN
1004     004636 001402      BEQ      LEND
1005     004640 004737 006750      JSR      PC,DIFLIN
1006     004644 000207      LEND:   RTS      PC      ;RETURN TO TEST SECTION
1007     004646 005000      DAWAIT: CLR      RO
1008     004650 105300      1$:    DECB     RO
1009     004652 001376      BNE     1$
1010     004654 000207      RTS     PC

```

```

1011          .SBTTL      CALIBRATION TEST
1012 004656 012737 004656 001364 BEGINC: MOV      #BEGINC,TADDR      ;TEST ADDRESS IN TADDR
1013 004664 005037 001426          CLR      MYTEMP
1014
1015          ;*      MOV      MYTEMP,@STREG  ;/ PUT DATA FROM MYTEMP TO DEVICE REG STREG
1016 004700 104401 013613          TYPE    HEADS      ;TYPE OUT HEADING
1017 004704 005037 177776          CLR      PSW
1018 004710 017700 174224          1$:     MOV      @SWR,R0      ;READ CHANNEL FROM SWITCH REG.
1019 004714 042700 177700          BIC      #177700,R0      ;ISOLATE MUX BITS
1020 004720 032777 020000 174212 BIT      #BIT13,@SWR      ;IS BIT 13 SET?
1021 004726 001005          BNE      2$              ;;YES,SKIP TYPEOUT
1022 004730 104401 012317          TYPE    CH
1023 004734 010046          MOV      R0,-(SP)        ;;SAVE R0 FOR TYPEOUT
1024          ;;TYPE CHANNEL
1025 004736 104403          TYPOS   2              ;;GO TYPE--OCTAL ASCII
1026 004740          .BYTE  0              ;;TYPE 2 DIGIT(S)
1027 004741          .BYTE  0              ;;SUPPRESS LEADING ZEROS
1028 004742
1029 004742 000300          2$:     SWAB    R0              ;SWITCH BYTES
1030 004744 010037 001426          MOV      R0,MYTEMP
1031
1032          ;*      MOV      MYTEMP,@STREG  ;/ PUT DATA FROM MYTEMP TO DEVICE REG STREG
1033 004760 012702 000010          MOV      #10,R2        ;TYPEOUT COUNTER
1034 004764
1035
1036          ;*
1037          MOV      @STREG,MYTEMP ;/READ DEVICE REG STREG,PUT DATA IN MYTEMP.
1038 004774 005237 001426          INC     MYTEMP
1039
1040          ;*      MOV      MYTEMP,@STREG  ;/ PUT DATA FROM MYTEMP TO DEVICE REG STREG
1041 005010          30$:   MOV
1042
1043          ;*      MOV      @STREG,MYTEMP ;/READ DEVICE REG STREG,PUT DATA IN MYTEMP.
1044 005020 105737 001426          TSTB   MYTEMP
1045 005024 100371          BPL    30$
1046
1047          ;*      MOV      @ADBUFF,MYTEMP ;/READ DEVICE REG ADBUFF,PUT DATA IN MYTEMP.
1048 005036 013700 001426          MOV     MYTEMP,R0      ;/PUT CONVERTED VALUE IN R0.
1049 005042 032777 020000 174070 BIT     #BIT13,@SWR      ;IS BIT 13 SET?
1050 005050 001403          BEQ    4$              ;NOT SET, TYPE OUT LIST
1051 005052 010077 174064          MOV     R0,@DISPLAY    ;PUT VALUE IN DISPLAY FOR DISPLAY CONTRO
1052 005056 000714          BR     1$              ;REPEAT CONVERSION
1053 005060 104401 012322          4$:     TYPE    SPACE
1054 005064 010046          MOV     R0,-(SP)        ;;SAVE R0 FOR TYPEOUT
1055          ;;PRINT OCTAL CONVERTED VALUE
1056 005066 104403          TYPOS   4              ;;GO TYPE--OCTAL ASCII
1057 005070          .BYTE  1              ;;TYPE 4 DIGIT(S)
1058 005071          .BYTE  1              ;;TYPE LEADING ZEROS
1059 005072 012701 010000          5$:     MOV     #10000,R1
1060 005076 005301          DEC    R1
1061 005100 001376          BNE    5$
1062 005102 005302          DEC    R2              ;DECREMENT THE COUNTER
1063 005104 001327          BNE    3$              ;NO CARRIAGE RETURN
1064 005106 104401 001171          TYPE    , $CRLF        ;CARRIAGE RETURN

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E04

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CALIBRATION TEST

SEQ 0044

1065 005112 000676

BR 1\$

;REPEAT CONVERSION

1066					.SBTTL		LOGIC TEST SECTION	
1067	005114	012737	005114	001364	BEGL:	MOV	#BEGL,TADDR	;TEST ADDRESS
1068	005122	005037	001430			CLR	EDINT	
1069	005126	004737	002552			JSR	PC,TESTAD	;NO OF ADDITIONAL AD'S
1070	005132	004737	002710		1\$:	JSR	PC,BEGINL	;LOGIC TESTS
1071	005136	004737	005322			JSR	PC,BUMPAD	;MORE TO TEST?
1072	005142	000773				BR	1\$;TEST NEXT A/D
1073	005144	012737	005132	012016		MOV	#1\$,AGTST	;ADDRESS FOR EOP
1074	005152	000137	012020			JMP	\$EOP	;TYPE END OF PASS
1075								
1076					.SBTTL		AUTO TEST	
1077	005156	012737	005156	001364	BEGINA:	MOV	#BEGINA,TADDR	;TEST ADDRESS
1078	005164	005037	001430			CLR	EDINT	
1079	005170	005037	001202			CLR	\$PASS	;CLEAR PASS COUNTER
1080	005174	004737	002552			JSR	PC,TESTAD	;NO. OF AD'S TO BE TESTED
1081	005200	004737	002710		1\$:	JSR	PC,BEGINL	;LOGIC TESTS
1082	005204	104401	012775			TYPE	MEND	;TYPE END OF LOGIC TEST
1083	005210	013746	001316			MOV	\$TREG,-(SP)	;SAVE STREG FOR TYPEOUT
1084	005214	104403				TYPOS		;TYPE OCTAL NUMBER
1085	005216	006				.BYTE	6	;TYPE 6 DIGITS
1086	005217	001				.BYTE	1	;TYPE LEADING ZEROS
1087	005220	104401	001171			TYPE	\$CRLF	;TYPE A CR,LF
1088	005224	004737	003436			JSR	PC,WRAP	
1089	005230	004737	005322			JSR	PC,BUMPAD	;TEST NEXT A/D
1090	005234	000761				BR	1\$;TEST NEXT AD
1091	005236	012737	005200	012016		MOV	#1\$,AGTST	;ADDRESS FOR EOP
1092	005244	000137	012020			JMP	\$EOP	;TYPE END OF PASS
1093								
1094					.SBTTL		WRAPAROUND TEST	
1095	005250	012737	005250	001364	BEGINW:	MOV	#BEGINW,TADDR	;TEST ADDRESS
1096	005256	005037	001430			CLR	EDINT	
1097	005262	005037	001202			CLR	\$PASS	;CLEAR PASS COUNT
1098	005266	004737	002552			JSR	PC,TESTAD	;NO. OF AD'S TO BE TESTED
1099	005272	004737	003436		1\$:	JSR	PC,WRAP	;WRAPAROUND TESTS
1100	005276	005037	001430			CLR	EDINT	
1101	005302	004737	005322			JSR	PC,BUMPAD	;MORE A/D'S TO BE TESTED?
1102	005306	000771				BR	1\$;YES-GO TEST NEXT ADIJK
1103	005310	012737	005272	012016		MOV	#1\$,AGTST	
1104	005316	000137	012020			JMP	\$EOP	;INCREMENTS \$PASS

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1105          SBTTL      DETERMINE IF MORE AD11K'S TO BE TESTED
1106 005322 005737 001354      BUMPAD: TST      NBEXT      ;ADDITIONAL AD'S?
1107 005326 001421          BEQ      FIXADR      ;NO-INITIALIZE ADDRESSES
1108 005330 063737 001326 001316  ADD      VADR,STREG  ;SET UP NEW ST. REG.
1109 005336 063737 001326 001320  ADD      VADR,ADBUFF ;SET UP NEW BUFFER ADDRESS
1110 005344 063737 001330 001456  ADD      VVCT,VECTOR ;SET UP NEW VECTOR
1111 005352 063737 001330 001324  ADD      VVCT,VECTR1
1112 005360 005077 173740      CLR      VVECTR1
1113 005364 005337 001354      DEC      NBEXT      ;ONE LESS AD11K
1114 005370 000441          BR       BYPASS
1115 005372 062716 000002      FIXADR: ADD      #2,(SP)
1116 005376 013737 001250 001316  FIXONE: MOV      $BASE,STREG ;RELOAD INITIAL ADDRESSES
1117 005404 013737 001250 001320  MOV      $BASE,ADBUFF
1118 005412 062737 000002 001320  ADD      #2,ADBUFF
1119 005420 013737 001244 001456  MOV      $VECT1,VECTOR
1120 005426 042737 170000 001456  BIC      #170000,VECTOR
1121 005434 113737 001245 001322  MOVB     $VECT1+1,BASEBR
1122 005442 105037 001323          CLRB     BASEBR+1 ;CLEAR HIGH BYTE
1123 005446 013737 001456 001324  MOV      VECTOR,VECTR1
1124 005454 062737 000002 001324  ADD      #2,VECTR1
1125 005462 005077 173636      CLR      VVECTR1
1126 005466 013737 001356 001354  MOV      NMBEXT,NBEXT ;RESET COUNTER
1127          ;:LOAD .+2 AND HALT TRAP CATCH;;
1128 005474 012700 000216  BYPASS: MOV      #216,R0 ;FILL .+2
1129 005500 012701 000214      MOV      #214,R1 ;LOAD HALT
1130 005504 020137 001334      1$:      CMP      R1,K$VECT
1131 005510 001410          BEQ      2$
1132 005512 010021          MOV      R0,(R1)+
1133 005514 005021          CLR      (R1)+
1134 005516 010100          MOV      R1,R0
1135 005520 005720          TST      (R0)+
1136 005522 020027 001002      CMP      R0,#1002
1137 005526 001366          BNE     1$
1138 005530 000207          RTS     PC ;TEST NEXT A/D
1139 005532 022021      2$:      CMP      (R0)+,(R1)+
1140 005534 022021      CMP      (R0)+,(R1)+
1141 005536 000762          BR       1$
1142
1143
1144          SBTTL      NOISE TEST, 1 EDGE
1145 005540 012737 005540 001364  BEGINN: MOV      #BEGINN,TADDR ;TEST ADDRESS IN TADDR
1146 005546 104401 012126          TYPE     ,NOIMSG ;ASK FOR CHANNEL
1147 005552 104401 013632          TYPE     ,ASKCH
1148 005556 017737 173356 001350  1$:      MOV      $SWR,CH1 ;LOAD CHANNEL
1149 005564 042737 177700 001350  BIC      #177700,CH1
1150 005572 012737 000200 001346  MOV      #200,TEMP ;LOAD DAC VALUE
1151 005600 004537 010400      JSR     R5,NOITST ;GO TO NOISE SUBROUTINE
1152 005604 001350      CH1
1153 005606 000763      BR       1$

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1154          SBTTL      INTERCHANNEL SETTLING TEST, 1 EDGE
1155 005610 012737 005610 001364 BEGINS: MOV #BEGINS,TADDR ;TEST ADDRESS IN TADDR
1156 005616 104401 012146          TYPE ,SETMSG ;ASK FOR CHANNELS
1157 005622 104407          RDOCT
1158 005624 012637 001350 MOV (SP)+,CH1
1159 005630 104401 012433          TYPE ,TOMSG
1160 005634 104407          RDOCT
1161 005636 012637 001352 MOV (SP)+,CH2
1162 005642 012737 000200 001346 BK3: MOV #200,TEMP ;LOAD DAC
1163 005650 013737 001352 001362 MOV CH2,CHANL
1164 005656 004737 006226 JSR PC,GETEDG ;GET EDGE VALUES
1165 005662 005002          CLR R2
1166 005664 004737 006060 JSR PC,SET1A ;SCALING = .02 LSB
1167 005670 004737 006060 JSR PC,SET1A ;MAKE IT .01 LSB
1168 005674 100001          BPL POSR2
1169 005676 005402          NEG R2
1170 005700 010204          MOV R2,R4
1171 005702 012737 000001 006450 MOV #1,EDGFLG
1172 005710 004737 005716 JSR PC,TYPSET
1173 005714 000752          BR BK3
1174 005716 004737 011504 TYPSET: JSR PC,DECLYP
1175 005722 104401 012327          TYPE LSB
1176 005726 013746 001352 MOV CH2,-(SP) ;:SAVE CH2 FOR TYPEOUT
1177          ;:TYPE CH
1178          ;:GO TYPE--OCTAL ASCII
1179          ;:TYPE 2 DIGIT(S)
1180          ;:SUPPRESS LEADING ZEROS
1181          ;:TYPE ASCIIZ STRING
1182          ;:SAVE CH1 FOR TYPEOUT
1183          ;:TYPE CH
1184          ;:GO TYPE--OCTAL ASCII
1185          ;:TYPE 2 DIGIT(S)
1186          ;:SUPPRESS LEADING ZEROS
1187          ;:TYPE ASCIIZ STRING
1188          ;:SAVE CH1 FOR TYPEOUT
1189          ;:TYPE CH
1190          ;:GO TYPE--OCTAL ASCII
1191          ;:TYPE 2 DIGIT(S)
1192          ;:SUPPRESS LEADING ZEROS
1193          ;:TYPE ASCIIZ STRING
1194          ;* MOV MYTEMP,ADDBUFF ;/ PUT DATA FROM MYTEMP TO DEVICE REG ADBUFF
1195          JSR RS,CONVRT
1196          1$: 0
1197          MOV TEMP,-(SP) ;:SAVE TEMP FOR TYPEOUT
1198          ;:TYPE VALUE
1199          ;:GO TYPE--OCTAL ASCII
1200          ;:TYPE 4 DIGIT(S)
1201          ;:TYPE LEADING ZEROS
1202 006036 020437 011746 CMP R4,VSET
1203 006042 003003          BGT ERR
1204 006044 104401 012474          TYPE OKMSG
1205 006050 000207          RTS PC

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1206	006052	104401	012505	ERR:	TYPE	ERMSG
1207	006056	000207			RTS	PC
1208						
1209						
1210						
1211						
1212	006060	013737	001352	001360	;; SUBROUTINE FOR SETTling TESTS;;	
1213	006066	004537	006452		SET1A: MOV CH2, DUMMY ; LOAD DUMMY	
1214	006072	000062			JSR R5, SAR SUB ; DO SAR ROUTINE AT 50%	
1215	006074	063702	001404		50.	
1216	006100	013737	001350	001360	ADD DAC, R2 ; ADD RESULT TO R2	
1217	006106	004537	006452		MOV CH1, DUMMY ; CHANGE DUMMY VALUE	
1218	006112	000062			JSR R5, SAR SUB ; DO SAR ROUTINE AT 50%	
1219	006114	163702	001404		50.	
1220	006120	000207			SUB DAC, R2 ; SUBTRACT RESULT FROM R2	
1221					RTS ; RETURN	
1222	006122	012537	001350		SETB: MOV (R5)+, CH1 ; GET FIRST CHANNEL	
1223	006126	012537	001352		MOV (R5)+, CH2 ; GET SECOND CHANNEL	
1224	006132	063737	001332	001350	ADD BASECH, CH1	
1225	006140	063737	001332	001352	ADD BASECH, CH2	
1226	006146	004737	006226		JSR PC, GET EDG ; GET EDGE VALUES	
1227	006152	005002			CLR R2	
1228	006154	012703	000010		MOV #10, R3 ; SET UP COUNTER	
1229	006160	004737	006060		JSR PC, SET1A ; GET SETTLE VALUES	
1230	006164	005237	001410		INC EDGE	
1231	006170	005303			DEC R3	
1232	006172	001372			BNE SETAA ; REPEAT 8 TIMES	
1233	006174	162737	000010	001410	SUB #10, EDGE	
1234	006202	005702			TST R2	
1235	006204	100001			BPL R2POS	
1236	006206	005402			NEG R2	
1237	006210	010204			MOV R2, R4	
1238	006212	012737	000010	006450	MOV #8, EDGFLG	
1239	006220	004737	005716		JSR PC, TYPSET ; TYPE OUT RESULTS	
1240	006224	000205			RTS ; RETURN	

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1241 ;SUBROUTINE TO GET EDGE VALUE
1242 ;CALL=JSR PC,GETEDG
1243 ;CONVERSIONS ON A/D CHANNEL 'CHANL'
1244 ;RESULT IN EDGE, USES R0
1245 GETEDG:
1246
1247 ;*      MOV      TEMP,@ADBUFF      ;/ PUT DATA FROM TEMP TO DEVICE REG ADBUFF
1248 006236 113700 001362      MOV      CHANL,R0      ;GET CHANNEL
1249 006242 000300      SWAB     RO           ;SET UP A.D STATUS REG.
1250 006244 010037 001426      MOV      RO,MYTEMP
1251
1252 ;*      MOV      MYTEMP,@STREG      ;/ PUT DATA FROM MYTEMP TO DEVICE REG STREG
1253 006260 012700 000100      MOV      #100,R0     ;DAC SETTling DELAY
1254 006264 005300      1$:      DEC      RO
1255 006266 001376      BNE     1$
1256 006270 005037 001410      CLR     EDGE
1257 006274 012700 000010      MOV      #10,R0
1258 006300      CONV:
1259
1260 ;*      MOV      @STREG,MYTEMP      ;/READ DEVICE REG STREG,PUT DATA IN MYTEMP.
1261 006310 005237 001426      INC     MYTEMP
1262
1263 ;*      MOV      MYTEMP,@STREG      ;/ PUT DATA FROM MYTEMP TO DEVICE REG STREG
1264 006324      30$:
1265 ;*      MOV      @STREG,MYTEMP      ;/READ DEVICE REG STREG,PUT DATA IN MYTEMP.
1266 006334 105737 001426      TSTB   MYTEMP
1267 006340 100371      BPL     30$
1268
1269 ;*      MOV      @ADBUFF,MYTEMP      ;/READ DEVICE REG ADBUFF,PUT DATA IN MYTEMP.
1270 006352 063737 001426 001410  ADD     MYTEMP,EDGE
1271 006360 005300      DEC     RO
1272 006362 001346      BNE     CONV
1273 006364 006237 001410      ASR    EDGE
1274 006370 006237 001410      ASR    EDGE
1275 006374 006237 001410      ASR    EDGE
1276 006400 005537 001410      ADC    EDGE
1277 006404 000207      RTS    PC
1278
1279 ;:SUBROUTINE TO TYPE EDGE VALUES;;
1280 †TYPE: MOV      EDGE,R3
1281      MOV      R3,-(SP)      ;;SAVE R3 FOR TYPEOUT
1282      TYPOS      ;;TYPE OCTAL VALUE OF EDGE
1283      .BYTE     4      ;;GO TYPE--OCTAL ASCII
1284      .BYTE     1      ;;TYPE 4 DIGIT(S)
1285      .BYTE     1      ;;TYPE LEADING ZEROS
1286 006414 104403      CMP    EDGFLG,#1
1287 006416 004      BEQ    RET
1288 006417 001      ADD    #7,R3
1289 006420 023727 006450 000001  TYPE   C1      ;TYPE ASCII STRING
1290 006426 001407      MOV    R3,-(SP)      ;;SAVE R3 FOR TYPEOUT
1291 006430 062703 000007      ;;TYPE EDGE VALUE
1292 006434 104401 013674
1293 006440 010346
1294

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INTERCHANNEL SETTling TEST, 1 EDGE

SEQ 0050

1295	006442	104403
1296	006444	004
1297	006445	001
1298	006446	000207
1299	006450	000000

RET:	TYPOS	
EDGFLG: 0	.BYTE	4
	.BYTE	1
	RTS	PC

::GO TYPE--OCTAL ASCII
::TYPE 4 DIGIT(S)
::TYPE LEADING ZEROS


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1300 ;SUBROUTINE TO DO SUCCESSIVE APPROXIMATION ROUTINE
1301 ;CALL=JSR R5,SARSUB
1302 ; XXX:XXX=PERCENT
1303 ;RESULT RETURNED IN 'DAC' USES R0,R1,R4
1304 006452 012537 001422 SARSUB: MOV (R5)+,PERCNT ;GET PERCENT
1305 006456 006337 001422 ASL PERCNT
1306 006462 006337 001422 ASL PERCNT
1307 006466 012737 000620 006746 MOV #400,CNNO ;NO OF SAMPLES FOR SHORT PASS.
1308 006474 032777 004000 172436 BIT #BIT11,ASWR ;USER WANT SHORT PASS?
1309 006502 001010 BNE SAR1
1310 006504 000407 BR SAR1 ;ALWAYS USE SHORT SAMPLE COUNT.
1311 006506 012737 003100 006746 MOV #1600.,CNNO
1312 006514 006337 001422 ASL PERCNT ;RESCALE PERCENT FOR 1600.
1313 006520 006337 001422 ASL PERCNT ;POINTS PER BURST
1314 006524 012737 000200 001412 SAR1: MOV #200,BITPNT ;INITIALIZE BIT POINTER AT MSB
1315 006532 005037 001404 CLR DAC ;INITIALIZE DAC VALUE
1316 006536 004537 020740 JSR R5,$PUTS
1317 006542 001316 .WORD STREG
1318 006544 005000 TRY: CLR R0
1319 006546 063737 001412 001404 ADD BITPNT,DAC ;TRY BIT
1320
1321 ;* MOV DAC,ADDBUFF ;/ PUT DATA FROM DAC TO DEVICE REG ADBUFF
1322 006564 012737 000100 001406 MOV #100,DELAY
1323 006572 005337 001406 1$: DEC DELAY ;STALL TIME
1324 006576 001375 BNE 1$
1325 006600 013701 006746 MOV CNNO,R1 ;SET UP FOR 1600. OR 400. CONVERSIONS
1326 006604 113737 001362 001435 MOVB CHANL,$TEMP2+1
1327 006612 052737 000001 001434 BIS #1,$TEMP2
1328 006620 113737 001360 001433 MOVB DUMMY,$TEMP1+1
1329 006626 052737 000001 001432 BIS #1,$TEMP1
1330 006634
1331 006634 013777 001432 172604 NXTCVT: MOV $TEMP1,$KMAD4
1332 006642 112777 000006 172572 $T6Mp: MOVB #6,$KMAD2
1333 006650 122777 000377 172564 10$: CMPB #377,$KMAD2
1334 006656 001374 BNE 10$
1335 006660 013777 001434 172560 MOV $TEMP2,$KMAD4
1336 006666 112777 000006 172546 MOVB #6,$KMAD2
1337 006674 122777 000377 172540 20$: CMPB #377,$KMAD2
1338 006702 001374 BNE 20$
1339 006704 027737 172536 001410 CMP $KMAD4,EDGE
1340 006712 002001 BGE 2$
1341 006714 005200 INC R0 ;COUNT RESULTS .LT. EDGE
1342 006716 005301 2$: DEC R1
1343 006720 001345 BNE NXTCVT
1344 006722 020037 001422 CMP R0,PERCNT
1345 006726 003003 BGT SHIFT
1346 006730 163737 001412 001404 SUB BITPNT,DAC ;TAKE THE BIT OUT
1347 006736 006237 001412 SHIFT: ASR BITPNT
1348 006742 001300 BNE TRY
1349 006744 000205 RTS
1350
1351 006746 000000 CNNO: .WORD 0

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1352      ;: DIFFERENTIAL LINEARITY SUBROUTINE;;
1353 006750 104401 013120      OIFLIN: TYPE      ,MSG20
1354 006754 005037 001424      CLR      OUT
1355 006760 012700 022354      MOV      #BUFFER,RO
1356 006764 012701 010000      MOV      #4096.,R1      ;4096 WORDS FOR HISTOGRAM
1357 006770 005020      CLEAR1: CLR      (RO)+      ;CLEAR BUFFER AREA
1358 006772 005301      DEC      R1
1359 006774 001375      BNE      CLEAR1
1360 006776 012700 021534      MOV      #DIST,RO      ;DISTRIBUTION BUFFER POINTER
1361 007002 012701 000310      MOV      #200.,R1      ;200. WORDS FOR DISTRIBUTION
1362 007006 005003      CLR      R3
1363 007010 005037 001424      CLR      OUT
1364 007014 005037 001336      CLR      WIDE
1365 007020 005037 001340      CLR      NARROW
1366 007024 005037 001342      CLR      FIRST
1367 007030 005037 001344      CLR      SKIPST
1368 007034 005020      CLEAR2: CLR      (RO)+      ;CLEAR DISTRIBUTION BUFFER AREA
1369 007036 005301      DEC      R1
1370 007040 001375      BNE      CLEAR2
1371 007042 012700 000011      CHANNL: MOV      #11,RO      ;CHANNEL 11
1372 007046 063700 001332      ADD      BASECH,RO
1373 007052 000300      SWAB      RO      ;LOAD MUX BITS
1374 007054 004537 020740      JSR      R5,$SPUTS
1375 007060 001316      .WORD    STREG
1376 007062 010037 001426      MOV      RO,MYTEMP
1377
1378      ;*      MOV      MYTEMP,STREG      ;/ PUT DATA FROM MYTEMP TO DEVICE REG STREG
1379 007076 010037 001432      MOV      RO,$STEMP1
1380 007102 052737 000001 001432      BIS      #1,$STEMP1
1381 007110 012700 001440      MOV      #800.,RO      ;NOMINAL STATE WIDTH - 1 LSB
1382 007114 012777 001704 172334      MOV      #RETURN,$VECTOR
1383 007122 012701 007776      AGAIN: MOV      #4094.,R1
1384 007126 004737 011010      NEXT: JSR      PC,RANDY      ;GET RANDOM NUMBER
1385 007132 013702 001366      MOV      R1A,R2
1386 007136 042702 177760      BIC      #177760,R2      ;MASK IT TO 4 BITS ONLY
1387 007142 001402      BEQ      CONVR
1388 007144 005302      DELAY3: DEC      R2      ;STALL
1389 007146 001376      BNE      DELAY3      ;TIME
1390 007150
1391 007150 013777 001432 172270      CONVR: MOV      $STEMP1,$KMAD4
1392 007156 112777 000006 172256      STBF4: MOV      #6,$KMAD2
1393 007164 122777 000377 172250      31$:  CMP      #377,$KMAD2
1394 007172 001374      BNE      31$
1395 007174 017702 172246      MOV      $KMAD4,R2
1396 007200 001413      BEQ      DELAY1      ;IGNORE IF =0
1397 007202 020227 007777      CMP      R2,#7777      ;IGNORE IF =7777
1398 007206 001413      BEQ      DELAY2
1399 007210 006302      ASL      R2
1400 007212 005262 022354      INC      BUFFER(R2)      ;MAKE HISTOGRAM
1401 007216 100013      BPL      OKAY
1402 007220 012762 077777 022354      MOV      #077777,BUFFER(R2)      ;PREVENT OVERFLOW
1403 007226 000407      BR      OKAY
1404 007230 020227 007777      DELAY1: CMP      R2,#7777      ;EQUALIZE LOOP TIME
1405 007234 001400      BEQ      DELAY2      ;WITH DUMMY INSTR.

```

1406	007236	005201		DELAY2:	INC	R1	
1407	007240	005263	001346		INC	TEMP(R3)	
1408	007244	100403			BMI	NOTOK	
1409	007246	005301		OKAY:	DEC	R1	
1410	007250	001326			BNE	NEXT	
1411	007252	000403			BR	AROUND	
1412	007254	005037	001346	NOTOK:	CLR	TEMP	
1413	007260	000772			BR	OKAY	
1414	007262	005300		AROUND:	JEC	R0	
1415	007264	001316			BNE	AGAIN	
1416	007266	012700	007776		MOV	#4094, R0	
1417	007272	012701	022356		MOV	#BUFFER+2, R1	
1418	007276	012102		READ:	MOV	(R1)+, R2	;GET STATE WIDTH
1419	007300	006202			ASR	R2	;1 LSB = 800.
1420	007302	006202			ASR	R2	
1421	007304	006202			ASR	R2	
1422	007306	005502			ADC	R2	;1 LSB = 100.
1423	007310	020227	000310		CMP	R2, #200.	;OUT OF RANGE?
1424	007314	002403			BLT	INRNGE	
1425	007316	005237	001424		INC	OUT	;YES - INCREMENT COUNTER
1426	007322	000423			BR	TYPBAD	
1427	007324	006302		INRNGE:	ASL	R2	
1428	007326	005262	021534		INC	DIST(R2)	;MAKE STATE WIDTH DISTRIBUTION
1429	007332	006202			ASR	R2	
1430	007334	020227	000062		CMP	R2, #50.	;IS IT 1/2 LSB?
1431	007340	002007			BGE	NOTNAR	
1432	007342	005237	001340		INC	NARROW	
1433	007346	005702			TST	R2	;IS IT A SKIPPED STATE?
1434	007350	001002			BNE	31\$	
1435	007352	005237	001344		INC	SKIPST	
1436	007356	000405		31\$:	BR	TYPBAD	
1437	007360	020227	000226	NOTNAR:	CMP	R2, #150.	;IS IT 1.5 LSB?
1438	007364	003426			BLE	LAST	
1439	007366	005237	001336		INC	WIDE	
1440	007372	005737	001342	TYPBAD:	TST	FIRST	
1441	007376	001004			BNE	60\$	
1442	007400	005237	001342		INC	FIRST	
1443	007404	104401	012277		TYPE	STATE	
1444	007410	010103		60\$:	MOV	R1, R3	
1445	007412	162703	022356		SUB	#BUFFER+2, R3	
1446	007416	006203			ASR	R3	
1447	007420	010346			MOV	R3, -(SP)	::SAVE R3 FOR TYPEOUT
1448							::TYPE STATE
1449	007422	104403			TYPOS		::GO TYPE--OCTAL ASCII
1450	007424	004			.BYTE	4	::TYPE 4 DIGIT(S)
1451	007425	001			.BYTE	1	::TYPE LEADING ZEROS
1452	007426	104401	012273		TYPE	DASH	
1453	007432	004737	011504		JSR	PC, DECTYP	
1454	007436	104401	012264		TYPE	LSBMSG	
1455	007442	005300		LAST:	DEC	R0	
1456	007444	001314			BNE	READ	
1457	007446	112737	000177 014572		MOVB	#177, DECPNT	
1458	007454	013702	001344		MOV	SKIPST, R2	;GET NO. OF SKIPPED STATES
1459	007460	004737	011504		JSR	PC, DECTYP	;TYPE IT

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SEQ 0054

1460	007464	104401	012522		TYPE	SKPMSG		; TYPE MESSAGE
1461	007470	005737	001344		TST	SKIPST		
1462	007474	001403			BEQ	1\$		
1463	007476	104401	012505		TYPE	ERMSG		; TYPE "ERROR"
1464	007502	000402			BR	NAR		
1465	007504	104401	012474	1\$:	TYPE	,OKMSG		; TYPE #OK#


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1509          ;CHANGE HISTOGRAM ERROR TO RELATIVE ACCURACY ERROR
1510
1511 007736 005001          RELACC: CLR      R1          ;RUNNING ERROR = 0
1512 007740 005003          CLR      R3          ;MAXIMUM ERROR = 0
1513 007742 104401 013545  TYPE      MSG21
1514 007746 012700 022356  MOV      #BUFFER+2,R0
1515 007752 011002          NXTSTA: MOV      (R0),R2      ; STATE WIDTH = R2
1516 007754 162702 001440  SUB      #800.,R2      ; STATE WIDTH ERROR IN R2
1517 007760 060201          ADD      R2,R1          ; UPDATE RUNNING ERROR
1518 007762 010120          MOV      R1,(R0)+      ; SAVE IN BUFFER
1519 007764 010104          MOV      R1,R4          ; SAVE IN R4 ALSO
1520 007766 100001          BPL      PLUS          ; IS IT POSITIVE?
1521 007770 005404          NEG      R4          ; NO - MAKE IT POSITIVE
1522 007772 020403          PLUS:  CMP      R4,R3      ; CHECK AGAINST PREVIOUS MAX. ERROR
1523 007774 003405          BLE      NOTNEW       ; NOT A NEW MAXIMUM
1524 007776 010403          MOV      R4,R3          ; UPDATE MAXIMUM IN R3
1525 010000 010005          MOV      R0,R5
1526 010002 162705 022356  SUB      #BUFFER+2,R5
1527 010006 006205          ASR      R5          ; R5=EDGE VALUE AT MAX. RELACC
1528 010010 020027 042352  NOTNEW: CMP      R0,#BUFFER+8190. ; DONE?
1529 010014 001356          BNE      NXTSTA        ; NO - REPEAT
1530 010016 006203          ASR      R3          ; RESCALE FROM 1 LSB = 800. SCALING
1531 010020 006203          ASR      R3          ; TO 1 LSB = 100. SCALING
1532 010022 006203          ASR      R3
1533 010024 005503          ADC      R3
1534 010026 010302          MOV      R3,R2
1535 010030 004737 011504  JSR      PC,DECTYP
1536 010034 104401 013572  TYPE      LINEA
1537 010040 010546          MOV      R5,-(SP)      ;; SAVE R5 FOR TYPEOUT
1538          ;; TYPE VALUE
1539 010042 104403          TYPOS   4          ;; GO TYPE--OCTAL ASCII
1540 010044 004          .BYTE  4          ;; TYPE 4 DIGIT(S)
1541 010045 001          .BYTE  1          ;; TYPE LEADING ZEROS
1542 010046 104401 012431  TYPE      SLASH      ;PRINT '/'
1543 010052 005205          INC      R5
1544 010054 010546          MOV      R5,-(SP)      ;; SAVE R5 FOR TYPEOUT
1545          ;; TYPE VALUE
1546 010056 104403          TYPOS   4          ;; GO TYPE--OCTAL ASCII
1547 010060 004          .BYTE  4          ;; TYPE 4 DIGIT(S)
1548 010061 001          .BYTE  1          ;; TYPE LEADING ZEROS
1549 010062 020337 011750  CMP      R3,VLIN
1550 010066 003403          BLE      41$
1551 010070 104401 012505  TYPE      ,ERMSG
1552 010074 000402          BR      42$
1553 010076 104401 012474  41$:  TYPE      ,OKMSG
1554 010102 005737 001400  42$:  TST      FLAG          ;VT55?
1555 010106 001503          BEQ      L02
1556 010110 012700 022354  MOV      #BUFFER,R0
1557 010114 012701 010000  MOV      #4096.,R1

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1558 010120 011002          GETDAT: MOV      (R0),R2          ;GET RELATIVE ACCURACY ERROR SCALED 1LSB = 800.
1559 010122 006202          ASR      R2              ;RESCALE IT TO 1 LSB = 100.
1560 010124 006202          ASR      R2
1561 010126 006202          ASR      R2
1562 010130 005502          ADC      R2
1563 010132 062702 000166  ADD      #118.,R2          ;AND MOVE IT TO MID-SCREEN
1564 010136 010220          MOV      R2,(R0)+        ;PUT IT BACK INTO BUFFER
1565 010140 005301          DEC      R1
1566 010142 001366          BNE     GETDAT
1567 010144 012700 022354  MOV      #BUFFER,R0
1568 010150 012704 022354  MOV      #BUFFER,R4
1569 010154 012705 022356  MOV      #BUFFER+2,R5
1570 010160 012701 001000  MOV      #512.,R1
1571 010164 012702 000007  NXTB:   MOV      #7.,R2
1572 010170 012003          MOV      (R0)+,R3
1573 010172 010337 001414  MOV      R3,MIN          ;MINIMUM
1574 010176 010337 001420  MOV      R3,MAX          ;MAXIMUM
1575 010202 012003          NXTCMP: MOV      (R0)+,R3
1576 010204 020337 001414  CMP      R3,MIN
1577 010210 002002          BGE     MAXTST
1578 010212 010337 001414  MOV      R3,MIN          ;NEW MINIMUM
1579 010216 020337 001420  MAXTST: CMP      R3,MAX
1580 010222 003402          BLE     TSTB
1581 010224 010337 001420  TSTB:   MOV      R3,MAX          ;NEW MAXIMUM
1582 010230 005302          DEC      R2
1583 010232 001363          BNE     NXTCMP
1584 010234 013724 001414  MOV      MIN,(R4)+
1585 010240 013725 001420  MOV      MAX,(R5)+
1586 010244 022425          CMP      (R4)+,(R5)+    ;BUMP EACH ONCE MORE
1587 010246 005301          DEC      R1
1588 010250 001345          BNE     NXTB
1589 010252 104401 013060  TYPE    ,MSG18
1590 010256 104401 014001  TYPE    ,BUFF2          ;TYPE BUFF2
1591 010262 012700 022354  MOV      #BUFFER,R0
1592 010266 004737 010320  JSR     PC,LOAD
1593 010272 104401 013701  TYPE    ,C3            ;TYPE ASCIZ STRING
1594 010276 012700 022356  MOV      #BUFFER+2,R0
1595 010302 004737 010320  JSR     PC,LOAD
1596 010306 104401 013676  TYPE    ,C2            ;TYPE ASCIZ STRING
1597 010312 004737 010342  JSR     PC,DELCLR
1598 010316 000207          RTS     PC
1599 010320 012701 001000  LOAD:   MOV      #512.,R1
1600 010324 012002          LOAD0: MOV      (R0)+,R2
1601 010326 005720          TST     (R0)+
1602 010330 004737 011402  JSR     PC,LOADY
1603 010334 005301          DEC      R1
1604 010336 001372          BNE     LOAD0
1605 010340 000207          RTS     PC

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1606 010342 005000          DELCLR: CLR      RO
1607 010344 012701 000020    MOV      #20,R1      ;DELAY BEFORE CLEANING SCREEN
1608 010350 005300          1$:      DEC      RO
1609 010352 001376          BNE      1$
1610 010354 005301          DEC      R1
1611 010356 001374          BNE      1$
1612 010360 032777 010000 170552 BIT      #BIT12,DSWR ;TEST FOR HALT FOR DISPLAY
1613 010366 001401          BEQ      2$          ;;DON'T HALT FOR DISPLAY
1614 010370 000000          HALT
1615 010372 104401 014021    2$:      TYPE     VTINIT
1616 010376 000207          RTS      PC
1617
1618 010400 013537 001362    NOITST: SUBROUTINE:
1619 010404 013737 001362 001360 MOV      @R5+,CHANL ;LOAD CHANNEL
1620 010412 004737 006226          MOV      CHANL,DUMMY ;LOAD DUMMy CHANNEL
1621 010416 004737 010572          JSR      PC,GETEDG ;GET EDGE VALUE
1622 010422 012737 000001 006450 JSR      PC,NOIA ;GET RMS AND PEAK VALUES
1623 010430 004737 010436          MOV      #1,EDGFLG
1624 010434 000205          JSR      PC,TYPRP ;TYPE RMS AND PEAK VALUES
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1631 010436 104401 012371    ;;TYPE RMS AND PEAK VALUES;;
1632 010442 005737 001374    TYPRP:  TYPE     NOI
1633 010446 100002          TST      RMS
1634 010450 005037 001374          BPL      POSRMS
1635 010454 005737 001376          CLR      RMS ;RMS<0,SET RMS=0
1636 010460 100002          POSRMS: TST      PEAK
1637 010462 005037 001376          BPL      POSPEA
1638 010466 013702 001374          CLR      PEAK ;PEAK<0,SET PEAK=0
1639 010472 004737 011504          POSPEA: MOV      RMS,R2
1640 010476 104401 012744          JSR      PC,DECTYP
1641 010502 013702 001376          TYPE     MESR
1642 010506 004737 011504          MOV      PEAK,R2
1643 010512 104401 012757          JSR      PC,DECTYP
1644 010516 004737 006406          TYPE     MESP
1645 010522 104401 012401          JSR      PC,TYPEDG
1646 010526 013746 001362          TYPE     CHAN
1647
1648 010532 104403          MOV      CHANL,-(SP) ;;SAVE CHANL FOR TYPEOUT
1649 010534 002          ;;TYPE CHANL
1650 010535 000          ;;GO TYPE--OCTAL ASCII
1651 010536 023737 001374 011742 .BYTE   2
1652 010544 003007          .BYTE   0
1653 010546 023737 001376 011744 .BYTE   0
1654 010554 003003          CMP      RMS,VNR
1655 010556 104401 012474          BGT      ER
1656 010562 000207          CMP      PEAK,VNP ;WITHIN LIMITS?
1657 010564 104401 012505          BGT      ER
1658 010570 000207          TYPE     OKMSG
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1659      ;:SUBROUTINES FOR NOISE TEST;;
1660 010572 005037 001374      NOIA: CLR RMS ;CLEAR RMS VLAUE
1661 010576 005037 001376      CLR PEAK ;CLEAR PEAK VALUE
1662 010602 004537 006452      NOI1: JSR RS,SAR SUB ;DO SAR ROUTINE AT 16%
1663 010606 000020      16.
1664 010610 063737 001404 001374      ADD DAC,RMS ;ADD RESULT TO RMS
1665 010616 004537 006452      JSR RS,SAR SUB ;DO SAR ROUTINE AT 84%
1666 010622 000124      84.
1667 010624 163737 001404 001374      SUB DAC,RMS ;SUBTRACT RESULT FROM RMS
1668 010632 004537 006452      JSR RS,SAR SUB ;DO SAR ROUTINE AT 1%
1669 010636 000001      1
1670 010640 063737 001404 001376      ADD DAC,PEAK ;ADD RESULT TO PEAK
1671 010646 004537 006452      JSR RS,SAR SUB ;DO SAR ROUTINE AT 99%
1672 010652 000143      99.
1673 010654 163737 001404 001376      SUB DAC,PEAK ;SUBTRACT RESULT FROM PEAK
1674 010662 000207      RTS PC ;RETURN
1675
1676 010664 012537 001362      NOI8: MOV (RS)+,CHANL ;GET CHANNEL VALUE
1677 010670 063737 001332 001362      ADD BASECH,CHANL
1678 010676 013737 001362 001360      MOV CHANL,DUMMY ;LOAD DUMMY CHANNEL
1679 010704 004737 006226      JSR PC,GETEDG ;GET EDGE VALUES
1680 010710 005037 001374      CLR RMS ;CLEAR RMS VALUE
1681 010714 005037 001376      CLR PEAK ;CLEAR PEAK VALUE
1682 010720 012737 000010 011006      MOV #10,10$ ;SET UP COUNTER
1683 010726 004737 010602      1$: JSR PC,NOI1 ;GET NOISE VALUES
1684 010732 005237 001410      INC EDGE
1685 010736 005337 011006      DEC 10$
1686 010742 001371      BNE 1$ ;REPEAT 8 TIMES
1687 010744 162737 000010 001410      SUB #10,EDGE
1688 010752 006237 001374      ASR RMS ;SCALE IT TO 1 LSB=100.
1689 010756 005537 001374      ADC RMS
1690 010762 006237 001376      ASR PEAK
1691 010766 005537 001376      ADC PEAK
1692 010772 012737 000010 006450      MOV #8,EDGFLG
1693 011000 004737 010436      JSR PC,↑YPRP ;TYPE RESULTS
1694 011004 000205      RTS RS
1695 011006 000000      10$: 0 ;RETURN
1696 ;COUNTER
1697
1698      ;:RANDOM NUMBER GENERATOR;;
1699 011010 063737 001370 001366      RANDY: ADD RNB,RNA
1700 011016 063737 001372 001366      ADD RNC,RNA
1701 011024 005537 001366      ADC RNA
1702 011030 063737 001366 001370      ADD RNA,RNB
1703 011036 063737 001372 001370      ADD RNC,RNB
1704 011044 005537 001370      ADC RNB
1705 011050 063737 001366 001372      ADD RNA,RNC
1706 011056 063737 001370 001372      ADD RNB,RNC
1707 011064 005537 001372      ADC RNC
1708 011070 000207      RTS PC

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1709          ;:ROUTINE TO AVERAGE 8 CONVERSIONS;;
1710 011072 012500          CONVRT: MOV      (R5)+,RO          ;GET CHANNEL VALUE
1711 011074 063700 001332  ADD      BASECH,PO
1712 011100 010037 001362  MOV      RO,CHANL
1713 011104 000300          SWAB     RO
1714 011106 005037 001346  CLR      TEMP
1715
1716          ;*      MOV      @ADBUFF,MYTEMP ;/READ DEVICE REG ADBUFF,PUT DATA IN MYTEMP.
1717 011122 010037 001426  MOV      RO,MYTEMP
1718
1719          ;*      MOV      MYTEMP,@STREG ;/ PUT DATA FROM MYTEMP TO DEVICE REG STREG
1720 011136 012700 010000  MOV      #10000,RO
1721 011142 005300          2$:      DEC      RO
1722 011144 001376          BNE     2$
1723 011146 012777 001704 170302  MOV      #RETURN,@VECTOR ;LOAD VECTOR
1724 011154 012700 000010  MOV      #10,RO ;SET UP COUNTER
1725 011160
1726
1727          ;*      MOV      @STREG,MYTEMP ;/READ DEVICE REG STREG,PUT DATA IN MYTEMP.
1728 011170 052737 000001 001426  BIS      #1,MYTEMP
1729
1730          ;*      MOV      MYTEMP,@STREG ;/ PUT DATA FROM MYTEMP TO DEVICE REG STREG
1731 011206 005001          CLR     R1
1732 011210 105201          10$:     INCB   R1
1733 011212 001007          BNE     11$
1734 011214 012737 000200 001124  MOV      #BIT7,$GDDAT ;EXPECT DONE TO sET BY NOW
1735 011222 013737 001426 001126  MOV      MYTEMP,$BDDAT
1736
1737 011230 104001          ERROR  1 ;DONE FAILED TO sET ON A/D
1738
1739 011232          11$:
1740
1741          ;*      MOV      @STREG,MYTEMP ;/READ DEVICE REG STREG,PUT DATA IN MYTEMP.
1742 011242 105737 001426  TSTB   MYTEMP
1743 011246 100360          BPL     10$
1744
1745          ;*      MOV      @ADBUFF,MYTEMP ;/READ DEVICE REG ADBUFF,PUT DATA IN MYTEMP.
1746 011260 063737 001426 001346  ADD     MYTEMP,TEMP
1747          ;WAIT FOR CONVERSION
1748          ;READ BUFFER
1749
1750          DEC      RO
1751 011270 001333          BNE     1$ ;DO 8 TIMES
1752 011272 006237 001346  ASR     TEMP ;AVERAGE VALUE
1753 011276 006237 001346  ASR     TEMP
1754 011302 006237 001346  ASR     TEMP
1755 011306 005537 001346  ASR     TEMP
1756 011312 000205          ADC     TEMP
1757          RTS      RS ;RETURN
1758
1758 011314 012537 001124          ;COMPARE $GDDAT AND $BDDAT;;
1759 011320 013537 001402 001126  COMPAR: MOV      (R5)+,$GDDAT ;GET GOOD DATA
1760 011324 013737 001346          MOV      @R5,$SPREAD ;GET SPREAD
1761 011332 013701 001126          MOV      TEMP,$BDDAT ;GET BAD(ACTUAL) DATA
1762 011336 013700 001124          MOV      $BDDAT,R1
1762          MOV      $GDDAT,RO

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SEQ 0061

1763	011342	160100			SUB	R1,RO		;GET DIFFERENCE
1764	011344	100001			BPL	7\$		
1765	011346	005400			NEG	RO		
1766	011350	020037	001402	7\$:	CMP	RO,SPREAD		;COMPARE IT TO SPREAD
1767	011354	003001			BGT	10\$;GO TO ERROR PRINTOUT
1768	011356	005725			TST	(P5)+		;BUMP RETURN POINTER AROUND ERROR CALL
1769	011360	000205		10\$:	RTS	RS		

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1770
1771 011362 004737 020422
1772 011366 052777 000100 167550
1773 011374 005037 177776
1774 011400 000207
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1779 011402 005702
1780 011404 100001
1781 011406 005002
1782 011410 020227 000353
1783 011414 002402
1784 011416 012702 000353
1785 011422 010203
1786 011424 042702 177740
1787 011430 052702 000040
1788 011434 105777 167510
1789 011440 100375
1790 011442 110277 167504
1791 011446 006203
1792 011450 006203
1793 011452 006203
1794 011454 006203
1795 011456 006203
1796 011460 042703 177770
1797 011464 052703 000040
1798 011470 105777 167454
1799 011474 100375
1800 011476 110377 167450
1801 011502 000207
1802
1803

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;SUBROUTINE TO RESET & SET INTRPT. EN.;
RST: JSR PC,$RESET
      BIS #100,$STKS
      CLR PSW
      RTS PC

;SUBROUTINE LOADY:
LOADY: TST R2
      BPL PLUSR2
      CLR R2
      PLUSR2: CMP R2,#235.
      BLT LESS
      MOV #235.,R2
      LESS: MOV R2,R3
      BIC #177740,R2
      BIS #40,R2
      B10: TSTB $STPS
      BPL B10
      MOVB R2,$STPB
      ASR R3
      ASR R3
      ASR R3
      ASR R3
      BIC #177770,R3
      BIS #40,R3
      B11: TSTB $STPS
      BPL B11
      MOVB R3,$STPB
      RTS PC

;ROUTINE TO LOAD VLAUE INTO R2
;AS A VT55 Y-VALUE

;PRINT CHARACTER

;PRINT CHARACTER

```



```

1804      ;:SUBROUTINE TO TYPE DECIMAL VALUE;;
1805      ;:IN R2 AS X.XX;;
1806      DECTYP: TST      R2      ;TEST VALUE TO BE TYPED
1807      BPL      POS      ;TYPE MINUS SIGN
1808      TYPE     MINUS
1809      NEG      R2      ;>999. REPLACE IT WITH 999.
1810      POS:    CMP      R2,#999.
1811      BLE      OKAYD
1812      MOV      #999.,R2
1813      OKAYD:  CLRB     ONES     ;CLEAR ONES
1814      CLRB     TENS      ;CLEAR TENS
1815      CLRB     HUNS      ;CLEAR HUNS
1816      TESTR2: TST      R2      ;CONVERT VALUE TO A DECIMAL VALUE
1817      BEQ      TYP0UT
1818      DEC      R2
1819      INCB     ONES
1820      CMPB     ONES,#10.
1821      BNE      TESTR2
1822      CLRB     ONES
1823      INCB     TENS
1824      CMPB     TENS,#10.
1825      BNE      TESTR2
1826      CLRB     TENS
1827      INCB     HUNS
1828      BR       TESTR2
1829      TYP0UT: BISB     #60,HUNS ;PREPARE FOR TYP0UT
1830      BISB     #60,TENS
1831      BISB     #60,ONES
1832      TYPE     HUNS      ;TYPE VALUE
1833      RTS      PC
1834
1835      WFADJ: MOV      #VNR,R1 ;SUBROUTINE TO SET UP LIMITS
1836      TST      BASECH ;TESTING AN AM11K?
1837      BEQ      1$      ;;
1838      MOV      #VARLT3,R2 ;BASECH NOT ZERO, USE AM11K LIMITS
1839      BR       3$      ;;
1840      1$:    TST      WFTEST
1841      BNE      2$
1842      MOV      #VARLT1,R2 ;WFTEST=0,USE NORMAL LIMITS
1843      BR       3$
1844      2$:    MOV      #VARLT2,R2 ;WFTEST=1,USE OPTION AREA LIMITS
1845      3$:    MOV      (R2)+,(R1)+
1846      TST      (R1)
1847      BPL      3$
1848      RTS      PC

```

1849 011720 000001
 1850 011722 000002
 1851 011724 000010
 1852 011726 000050
 1853 011730 000144
 1854 011732 000115
 1855 011734 000240
 1856 011736 000005
 1857 011740 000062
 1858
 1859 011742 000000
 1860 011744 000000
 1861 011746 000000
 1862 011750 000000
 1863 011752 100000
 1864
 1865 011754 000031
 1866 011756 000310
 1867 011760 000144
 1868 011762 000144
 1869
 1870 011764 000027
 1871 011766 000226
 1872 011770 000132
 1873 011772 000132
 1874
 1875 011774 000062
 1876 011776 000310
 1877 012000 000226
 1878 012002 000226
 1879
 1880 012004 052777 000100 167132
 1881 012012 000177 000000
 1882 012016 001714

V1: 1
 V2: 2
 V10: 10
 V50: 50
 V144: 144
 V115: 115
 V240: 240
 V5: 5
 V500: 50.

 VNR: 0
 VNP: 0
 VSET: 0
 VLIN: 0
 BIT15

 VARLT1: 25.
 200.
 100.
 100.

 VARLT2: 23.
 150.
 90.
 90.

 VARLT3: 50.
 200.
 150.
 150.

 AGATST: BIS
 JMP
 AGTST: BEGIN

; TOLERANCE VALUES FOR FUNCTIONAL TESTS

 ; RMS NOISE LIMIT
 ; PEAK NOISE LIMIT
 ; INTER-CHANNEL SETTling LIMIT
 ; RELATIVE ACCURACY ERROR LIMIT

 ; .25 LSB, NORMAL LIMITS FOR SYSTEM
 ; 2. LSB, INTEGRATION AND FIELD USE ON SPEC TESTS
 ; 1 LSB
 ; 1 LSB

 ; .23 LSB, TIGHTER LIMITS FOR OPTION
 ; 1.5 LSB, AREA USE ON SPEC TESTS
 ; .9 LSB
 ; .9 LSB

 ; .5 LSB, LIMITS FOR AM11K TESTING
 ; 2. LSB
 ; 1.5 LSB
 ; 1.5 LSB

#100, 2STKS
 2AGTST

```

1883
1884
1885
1886
1887
1888
1889
1890
1891
1892
1893 012020
1894 012020 000240
1895 012022 005037 001102
1896 012026 005037 001160
1897 012032 005237 001202
1898 012036 042737 100000 001202
1899 012044 005327
1900 012046 000001
1901 012050 003015
1902 012052 012737
1903 012054 000001
1904 012056 012046
1905 012060 104401 012113
1906 012064 013700 000042
1907 012070 001405
1908 012072 000005
1909 012074 004710
1910 012076 000240
1911 012100 000240
1912 012102 000240
1913 012104
1914 012104 000137
1915 012106 012004
1916 012110 377 377 000
1917 012113 015 042412 042116
1918 012120 050040 051501 000123
1919

```

```

.SBTTL END OF PASS ROUTINE
;*****
;*INCREMENT THE PASS NUMBER ($PASS)
;*TYPE "END PASS"
;*IF THERES A MONITOR GO TO IT
;*IF THERE ISN'T JUMP TO AGATST
;*IF IT IS DESIRED TO HAVE A BELL INDICATE THE "END OF PASS" LOCATION
;*SENDMG CAN BE CHANGED TO 7.

$EOP:
NOP
CLR $STNM ;; ZERO THE TEST NUMBER
CLR $TIMES ;; ZERO THE NUMBER OF ITERATIONS
INC $PASS ;; INCREMENT THE PASS NUMBER
BIC #100000,$PASS ;; DON'T ALLOW A NEG. NUMBER
DEC (PC)+ ;; LOOP?
$EOPCT: .WORD 1
BGT $DOAGN ;; YES
MOV (PC)+,2(PC)+ ;; RESTORE COUNTER
$ENDCT: .WORD 1
$EOPCT
TYPE $SENDMG ;; TYPE "END PASS"
$GET42: MOV #42,R0 ;; GET MONITOR ADDRESS
BEQ $DOAGN ;; BRANCH IF NO MONITOR
RESET ;; CLEAR THE WORLD
$SENDAD: JSR PC,(R0) ;; GO TO MONITOR
NOP ;; SAVE ROOM
NOP ;; FOR
NOP ;; ACT11
$DOAGN: JMP 2(PC)+ ;; RETURN
$RTNAD: .WORD AGATST
$ENULL: .BYTE -1,-1,0 ;; NULL CHARACTER STRING
$SENDMG: .ASCIZ <15><12>/END PASS/

```


1920
1921 012126 005015 047516 051511
1922 012134 020105 042524 052123
1923 012142 026455 000040
1924 012146 005015 042523 052124
1925 012154 044514 043516 052040
1926 012162 051505 026524 020055
1927 012170 054524 042520 042040
1928 012176 051505 051111 042105
1929 012204 023440 051106 046517
1930 012212 020047 044103 047101
1931 012220 042516 020114 020046
1932 012226 051103 020072 000
1933 012233 055 000
1934 012235 077 000
1935 012237 136 101 040
1936 012242 040 000
1937 012244 136 103 040
1938 012247 040 000
1939 012251 136 107 015
1940 012254 012 123 127
1941 012257 122 105 107
1942 012262 072 000
1943 012264 046040 041123 005015
1944 012272 000
1945 012273 055 020055 000
1946 012277 123 040524 042524
1947 012304 026455 053440 042111
1948 012312 044124 005015 000
1949 012317 103 000110
1950 012322 020040 020040 000
1951 012327 040 051514 020102
1952 012334 047117 041440 000110
1953 012342 051440 052105 046124
1954 012350 047111 020107 051106
1955 012356 046517 041440 000110
1956 012364 040440 020124 000
1957 012371 116 044517 042523
1958 012376 020072 000
1959 012401 040 047117 041440
1960 012406 040510 047116 046105
1961 012414 000040
1962 012416 020040 020040 047504
1963 012424 042516 005015 000
1964 012431 057 000
1965 012433 124 050131 020105
1966 012440 042504 044523 042522
1967 012446 020104 052047 023517
1968 012454 041440 040510 047116
1969 012462 046105 023040 041440
1970 012470 035122 000040
1971 012474 020040 020040 045517
1972 012502 005015 000

.SBTTL ASCII MESSAGES
NOIMSG: .ASCIZ <15><12>/NOISE TEST-- /
SETMSG: .ASCIZ <15><12>/SETTLING TEST-- TYPE DESIRED 'FROM' CHANNEL & CR: /
MINUS: .BYTE 55,0
QUEST: .BYTE 77,0
AMSG: .BYTE 136,101,40,40,0
CMMSG: .BYTE 136,103,40,40,0
GMSG: .BYTE 136,107,15,12,123,127,122,105,107,72,0
LSBMSG: .ASCIZ / LSB/<15><12>
DASH: .ASCIZ /-- /
STATE: .ASCIZ /STATE-- WIDTH/<15><12>
CH: .ASCIZ /CH/
SPACE: .ASCIZ / /
LSB: .ASCIZ / LSB ON CH/
SETCH: .ASCIZ / SETTling FROM CH/
ATMSG: .ASCIZ / AT /
NOI: .ASCIZ /NOISE: /
CHAN: .ASCIZ / ON CHANNEL /
DONE: .ASCIZ / DONE/<15><12>
SLASH: .ASCIZ #/#
TOMSG: .ASCIZ /TYPE DESIRED 'TO' CHANNEL & CR: /
CKMSG: .ASCIZ / OK/<15><12>

1973	012505	040	025052	051105	ERMSG: .ASCIZ / **ERROR**/(15)<12>
1974	012512	047522	025122	006452	
1975	012520	000012			
1976	012522	051440	044513	050120	SKPMSG: .ASCIZ / SKIPPED STATE(S)/
1977	012530	042105	051440	040524	
1978	012536	042524	051450	000051	
1979	012544	047040	051101	047522	NARMSG: .ASCIZ # NARROW (< 1/2 LSB) STATE(S)#<15><12>
1980	012552	020127	036050	030440	
1981	012560	031057	046040	041123	
1982	012566	020051	052123	052101	
1983	012574	024105	024523	005015	
1984	012602	000			
1985	012603	040	044527	042504	WIDMSG: .ASCIZ # WIDE (> 1 1/2 LSB) STATE(S)#<15><12>
1986	012610	024040	020076	020061	
1987	012616	027461	020062	051514	
1988	012624	024502	051440	040524	
1989	012632	042524	051450	006451	
1990	012640	000012			
1991	012642	051440	040524	042524	OUTMSG: .ASCIZ / STATE(S) WIDER THAN 2 LSB/
1992	012650	051450	020051	044527	
1993	012656	042504	020122	044124	
1994	012664	047101	031040	046040	
1995	012672	041123	000		
1996	012675	040	052123	052101	HAFMSG: .ASCIZ # STATE-WIDTH(S) OUTSIDE + OR - 1/2 LSB#
1997	012702	026505	044527	052104	
1998	012710	024110	024523	047440	
1999	012716	052125	044523	042504	
2000	012724	025440	047440	020122	
2001	012732	020055	027461	020062	
2002	012740	051514	000102		
2003	012744	046040	041123	051040	MESR: .ASCIZ / LSB RMS, /
2004	012752	051515	020054	000	
2005	012757	040	051514	020102	MESP: .ASCIZ / LSB PEAK AT /
2006	012764	042520	045501	040440	
2007	012772	020124	000		
2008	012775	015	042412	042116	MEND: .ASCII <15><12>/END OF LOGIC TESTS/
2009	013002	047440	020106	047514	
2010	013010	044507	020103	042524	
2011	013016	052123	123		
2012	013021	040	047117	040440	ONAD: .ASCIZ / ON AD11K AT /
2013	013026	030504	045461	040440	
2014	013034	020124	000		
2015	013037	040	042101	030461	MSG50: .ASCIZ / AD11K'S FOUND/(15)<12>
2016	013044	023513	020123	047506	
2017	013052	047125	006504	000012	
2018	013060	005012	025412	027461	MSG18: .ASCII <12><12><12>#+1/2 LSB#<15><12><12><12><12><12><12><12><12><12><12><1
2019	013066	020062	051514	006502	
2020	013074	005012	005012	005012	
2021	013102	005012	005012	005012	
2022	013110	030455	031057	051514	.ASCIZ \-1/2LSB\
2023	013116	000102			
2024					

2025
 2026 013120 044504 043106 051105
 2027 013126 047105 044524 046101
 2028 013134 046040 047111 040505
 2029 013142 044522 054524 006472
 2030 013150 000012
 2031 013152 020040 020040 020040
 2032 013160 020040 020040 020040
 2033 013166 020040 020040 020040
 2034 013174 020040 052123 052101
 2035 013202 026505 044527 052104
 2036 013210 020110 044504 052123
 2037 013216 044522 052502 044524
 2038 013224 047117 005015 005012
 2039 013232 020040 020043 043117
 2040 013240 051440 040524 042524
 2041 013246 005123 005012 005012
 2042 013254 005012 005012 005012
 2043 013262 005012 005012 005012
 2044 013270 005012
 2045 013272 020040 020040 020040
 2046 013300 020040 020040 020040
 2047 013306 020040 020040 020040
 2048 013314 020040 020040 020040
 2049 013322 020040 020040 020040
 2050 013330 020040 020040 020040
 2051 013336 020040 020040 020040
 2052 013344 020040 020040 020040
 2053 013352 051440 040524 042524
 2054 013360 053440 042111 044124
 2055 013366 024040 051514 024502
 2056 013374 005015
 2057 013376 030040 020040 020040
 2058 013404 020040 020040 020040
 2059 013412 020040 020040 027461
 2060 013420 020062 020040 020040
 2061 013426 020040 020040 020040
 2062 013434 020040 020061 020040
 2063 013442 020040 020040 020040
 2064 013450 020040 030440 030440
 2065 013456 031057 020040 020040
 2066 013464 020040 020040 020040
 2067 013472 020040 031040 000
 2068 013477 015 052012 050131
 2069 013504 020105 042514 052124
 2070 013512 051105 023040 041440
 2071 013520 020122 047506 020122
 2072 013526 042504 044523 042522
 2073 013534 020104 042524 052123
 2074 013542 020072 000
 2075 013545 122 046105 052101
 2076 013552 053111 020105 041501
 2077 013560 052503 040522 054503
 2078 013566 006472 000012

MSG20: .EVEN
.ASCIZ /DIFFERENTIAL LINEARITY: /<15><12>

MSG16: .ASCII / STATE-WIDTH DISTRIBUTION/<15><12><12><12>

.ASCII / # OF STATES/<12><12><12><12><12><12><12><12><12><12><12><12><12><12><12><

.ASCII / STATE WIDTH (LSB)/<15>

.ASCIZ # 0 1/2 1 1 1/2 2#

MSG71: .ASCIZ <15><12>/TYPE LETTER & CR FOR DESIRED TEST: /

MSG21: .ASCIZ /RELATIVE ACCURACY: /<15><12>

2079	013572	046040	041123	046440
2080	013600	054101	046511	046525
2081	013606	040440	020124	000
2082	013613	015	041412	046101
2083	013620	041111	040522	044524
2084	013626	047117	026455	
2085	013632	051440	052105	041440
2086	013640	040510	047116	046105
2087	013646	044440	020116	053523
2088	013654	020122	047514	020127
2089	013662	054502	042524	005015
2090	013670	000		
2091	013671	033	000132	
2092	013674	000055		
2093	013676	031033	000	
2094	013701	112	000	
2095	013703	015	047412	043106
2096	013710	042523	020124	000075
2097	013716	046040	041123	000040
2098	013724	040440	020124	000
2099	013731	015	020012	047105
2100	013736	042524	044522	043516
2101	013744	052040	051505	020124
2102	013752	000		
2103	013753	033	061	101
2104	013756	061	111	062
2105	013761	114	041	060
2106	013764	045	063	051
2107	013767	066	055	071
2108	013772	061	074	110
2109	013775	041	040	112
2110	014000	000		
2111	014001	033	061	101
2112	014004	047	111	061
2113	014007	104	050	065
2114	014012	044	062	110
2115	014015	040	040	102
2116	014020	000		
2117	014021	033	110	033
2118	014024	112	033	061
2119	014027	101	040	033
2120	014032	062	000	
2121	014034	005015	046412	026504
2122	014042	030461	042055	046122
2123	014050	045520	040455	020040
2124	014056	020040	042101	030461
2125	014064	027513	050114	026501
2126	014072	030461	042040	040511
2127	014100	047107	051517	044524
2128	014106	006503	012	
2129	014111	012	035101	040440
2130	014116	052125	020117	042524
2131	014124	052123		
2132	014126	005015	035103	041440

LINEA: .ASCIZ / LSB MAXIMUM AT /

HEADS: .ASCII <15><12>/CALIBRATION--/

ASKCH: .ASCIZ / SET CHANNEL IN SWR LOW BYTE/<15><12>

CO: .ASCIZ <33><132>

C1: .ASCIZ <55>

C2: .ASCIZ <33><62>

C3: .ASCIZ <112>

MOFSET: .ASCIZ <15><12>/OFFSET =/

MLSB: .ASCIZ / LSB /

MAT: .ASCIZ / AT /

METST: .ASCIZ <15><12>/ ENTERING TEST /

BUFF1: .BYTE 33,61,101,61,111,62,114,41,60,45,63,51,66,55,71,61,74,110,41,40,112,0

BUFF2: .BYTE 33,61,101,47,111,61,104,50,65,44,62,110,40,40,102,0

VTINIT: .BYTE 33,110,33,112,33,61,101,40,33,62,0

HEAD1: .ASCII <15><12><12>#MD-11-DRLPK-A AD11K/LPA-11 DIAGNOSTIC#<15><12>

.ASCII <12>/A: AUTO TEST/

.ASCII <15><12>/C: CALIBRATION/

2133	014134	046101	041111	040522
2134	014142	044524	047117	
2135	014146	005015	035114	046040
2136	014154	043517	041511	052040
2137	014162	051505	124	
2138	014165	015	047012	020072
2139	014172	047516	051511	020105
2140	014200	042524	052123	

.ASCII <15><12>/L: LOGIC TEST/

.ASCII <15><12>/N: NOISE TEST/

2141	014204	005015	035123	051440		.ASCII	<15><12>/S: SETTLE TEST/
2142	014212	052105	046124	020105			
2143	014220	042524	052123				
2144	014224	005015	035127	053440		.ASCIZ	<15><12>/W: WRAPAROUND TEST/<15><12>
2145	014232	040522	040520	047522			
2146	014240	047125	020104	042524			
2147	014246	052123	005015	000			
2148	014253	015	051412	040524	EM1:	.ASCIZ	<15><12>/STATUS REG. ERROR/<15><12>
2149	014260	052524	020123	042522			
2150	014266	027107	042440	051122			
2151	014274	051117	005015	000			
2152	014301	015	043012	044501	EM2:	.ASCIZ	<15><12>/FAILED TO INTERRUPT/<15><12>
2153	014306	042514	020104	047524			
2154	014314	044440	052116	051105			
2155	014322	052522	052120	005015			
2156	014330	000					
2157	014331	015	052412	042516	EM3:	.ASCIZ	<15><12>/UNEXPECTED INTERRUPT/<15><12>
2158	014336	050130	041505	042524			
2159	014344	020104	047111	042524			
2160	014352	051122	050125	006524			
2161	014360	000012					
2162	014362	005015	051105	047522	EM4:	.ASCIZ	<15><12>#ERROR ON A/D CHANNEL#<15><12>
2163	014370	020122	047117	040440			
2164	014376	042057	041440	040510			
2165	014404	047116	046105	005015			
2166	014412	000					
2167	014413	105	051122	041520	DH1:	.ASCIZ	/ERRPC STREG EXPECTED ACTUAL/<15><12>
2168	014420	051440	051124	043505			
2169	014426	042440	050130	041505			
2170	014434	042524	020104	041501			
2171	014442	052524	046101	005015			
2172	014450	000					
2173	014451	105	051122	041520	DH2:	.ASCIZ	/ERRPC STREG CHANNEL NOMINAL TOL#RANCE ACTUAL/
2174	014456	020040	052123	042522			
2175	014464	020107	020040	044103			
2176	014472	047101	042516	020114			
2177	014500	047040	046517	047111			
2178	014506	046101	020040	047524			
2179	014514	042514	040522	041516			
2180	014522	020105	040440	052103			
2181	014530	040525	000114				
2182	014534	051105	050122	020103	DH3:	.ASCIZ	/ERRPC STREG ACTUAL/<15><12>
2183	014542	020040	020040	051440			
2184	014550	051124	043505	020040			
2185	014556	020040	041501	052524			
2186	014564	046101	005015	000			

2187	014571	000			HUNS:	.BYTE	0
2188	014572	056			DECPNT:	.BYTE	56
2189	014573	000			TENS:	.BYTE	0
2190	014574	000	000		ONES:	.BYTE	0,0
2191					.EVEN		
2192							
2193	014576	001116	001316	001124	DT1:	\$ERRPC, STREG, \$GDDAT, \$BDDAT,0	
2194	014604	001126	000000				
2195	014610	001116	001316	001362	DT2:	\$ERRPC, STREG, CHANL, \$GDDAT, SPREAD, \$BDDAT,0	
2196	014616	001124	001402	001126			
2197	014624	000000					
2198	014626	001116	001316	001126	DT3:	\$ERRPC, STREG, \$BDDAT,0	
2199	014634	000000					
2200							
2201	014636	000000			DF1:	0	
2202							
2203							
2204							
2205							

```

2206 .SBTTL TTY INPUT ROUTINE
2207
2208 ;*****
2209 .ENABL LSB
2210
2211 .DSABL LSB
2212
2213
2214 ;*****
2215 ;THIS ROUTINE WILL INPUT A SINGLE CHARACTER FROM THE TTY
2216 ;CALL:
2217 ;* RDCHR ;: INPUT A SINGLE CHARACTER FROM THE TTY
2218 ;* RETURN HERE ;: CHARACTER IS ON THE STACK
2219 ;* ;: WITH PARITY BIT STRIPPED OFF
2220 ;
2221
2222 $RDCHR: MOV (SP), -(SP) ;: PUSH DOWN THE PC
2223 MOV 4(SP), 2(SP) ;: SAVE THE PS
2224 1$: TSTB @STKS ;: WAIT FOR
2225 BPL 1$ ;: A CHARACTER
2226 MOV 2STKB, 4(SP) ;: READ THE TTY
2227 BIC #177, 4(SP) ;: GET RID OF JUNK IF ANY
2228 CMP 4(SP), #23 ;: IS IT A CONTROL-S?
2229 BNE 3$ ;: BRANCH IF NO
2230 2$: TSTB @STKS ;: WAIT FOR A CHARACTER
2231 BPL 2$ ;: LOOP UNTIL ITS THERE
2232 MOV 2STKB, -(SP) ;: GET CHARACTER
2233 BIC #177, (SP) ;: MAKE IT 7-BIT ASCII
2234 CMP (SP)+, #21 ;: IS IT A CONTROL-Q?
2235 BNE 2$ ;: IF NOT DISCARD IT
2236 BR 1$ ;: YES, RESUME
2237 3$: CMP 4(SP), #140 ;: IS IT UPPER CASE?
2238 BLT 4$ ;: BRANCH IF YES
2239 CMP 4(SP), #175 ;: IS IT A SPECIAL CHAR?
2240 BGT 4$ ;: BRANCH IF YES
2241 BIC #40, 4(SP) ;: MAKE IT UPPER CASE
2242 4$: RTI ;: GO BACK TO USER
2243 ;*****
2244 ;THIS ROUTINE WILL INPUT A STRING FROM THE TTY
2245 ;CALL:
2246 ;* RDLIN ;: INPUT A STRING FROM THE TTY
2247 ;* RETURN HERE ;: ADDRESS OF FIRST CHARACTER WILL BE ON THE STACK
2248 ;* ;: TERMINATOR WILL BE A BYTE OF ALL 0'S
2249 ;
2250 $RDLIN: MOV R3, -(SP) ;: SAVE R3
2251 1$: MOV #TTYIN, R3 ;: GET ADDRESS
2252 2$: CMP #TTYIN+8., R3 ;: BUFFER FULL?
2253 BLOS 4$ ;: BR IF YES
2254 RDCHR ;: GO READ ONE CHARACTER FROM THE TTY
2255 MOV (SP)+, (R3) ;: GET CHARACTER
2256 10$: CMPB #177, (R3) ;: IS IT A RUBOUT
2257 BNE 3$ ;: SKIP IF NOT
2258 4$: TYPE $QUES ;: TYPE A '?'
2259 BR 1$ ;: CLEAR THE BUFFER AND LOOP

```

```

2260 015014 111337 015064      3$:   MOVB   (R3),9$      ;;ECHO THE CHARACTER
2261 015020 104401 015064      TYPE   9$
2262 015024 122723 000015      CMPB   #15,(R3)+    ;;CHECK FOR RETURN
2263 015030 001356      BNE    2$           ;;LOOP IF NOT RETURN
2264 015032 105063 177777      CLRB   -1(R3)       ;;CLEAR RETURN (THE 15)
2265 015036 104401 001172      TYPE   $LF          ;;TYPE A LINE FEED
2266 015042 012603      MOV    (SP)+,R3     ;;RESTORE R3
2267 015044 011646      MOV    (SP)-,(SP)   ;;ADJUST THE STACK AND PUT ADDRESS OF THE
2268 015046 016666 000004 000002      MOV    4(SP),2(SP) ;;FIRST ASCII CHARACTER ON IT
2269 015054 012766 015066 000004      MOV    #STTYIN,4(SP)
2270 015062 000002      RTI
2271 015064      000      9$:   .BYTE   0           ;;RETURN
2272 015065      000      .BYTE   0           ;;STORAGE FOR ASCII CHAR. TO TYPE
2273 015066 000010      $TTYIN: .BLKB  8.    ;;TERMINATOR
2274 015076 052536 005015 000      $CNTLU: .ASCIZ  /↑U/<15><12> ;;RESERVE 8 BYTES FOR TTY INPUT
2275 015103      136 006507 000012      $CNTLG: .ASCIZ  /↑G/<15><12> ;;CONTROL "U"
2276 015110 005015 053523 020122      $MSWR:  .ASCIZ  <15><12>/SWR = / ;;CONTROL "G"
2277 015116 020075      000
2278 015121      040 047040 053505      $MNEW:  .ASCIZ  / NEW = /
2279 015126 036440 000040
  
```



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2280 .SBTTL READ AN OCTAL NUMBER FROM THE TTY
2281
2282 ;*****
2283 ;*THIS ROUTINE WILL READ AN OCTAL (ASCII) NUMBER FROM THE TTY AND
2284 ;*CHANGE IT TO BINARY.
2285 ;*CALL:
2286 ;* RDOCT ;: READ AN OCTAL NUMBER
2287 ;* RETURN HERE ;: LOW ORDER BITS ARE ON TOP OF THE STACK
2288 ;* ;: HIGH ORDER BITS ARE IN $HIOCT
2289
2290 015132 011646 $RDOCT: MOV (SP),-(SP) ;: PROVIDE SPACE FOR THE
2291 015134 016666 000004 000002 MOV 4(SP),2(SP) ;: INPUT NUMBER
2292 015142 010046 MOV RO,-(SP) ;: PUSH RO ON STACK
2293 015144 010146 MOV R1,-(SP) ;: PUSH R1 ON STACK
2294 015146 010246 MOV R2,-(SP) ;: PUSH R2 ON STACK
2295 015150 104406 1$: RDLIN ;: READ AN ASCII LINE
2296 015152 012600 MOV (SP)+,RO ;: GET ADDRESS OF 1ST CHARACTER
2297 015154 005001 R1 ;: CLEAR DATA WORD
2298 015156 005002 CLR R2
2299 015160 112046 2$: MOV B (RO)+,-(SP) ;: PICKUP THIS CHARACTER
2300 015162 001412 BEQ 3$ ;: IF ZERO GET OUT
2301 015164 006301 ASL R1 ;: *2
2302 015166 006102 ROL R2 ;: *4
2303 015170 006301 ASL R1 ;: *8
2304 015172 006102 ROL R2
2305 015174 006301 ASL R1
2306 015176 006102 ROL R2
2307 015200 042716 177770 BIC #C7,(SP) ;: STRIP THE ASCII JUNK
2308 015204 062601 ADD (SP)+,R1 ;: ADD IN THIS DIGIT
2309 015206 000764 BR 2$ ;: LOOP
2310 015210 005726 3$: TST (SP)+ ;: CLEAN TERMINATOR FROM STACK
2311 015212 010166 000012 MOV R1,12(SP) ;: SAVE THE RESULT
2312 015216 010237 015232 MOV R2,$HIOCT
2313 015222 012602 MOV (SP)+,R2 ;: POP STACK INTO R2
2314 015224 012601 MOV (SP)+,R1 ;: POP STACK INTO R1
2315 015226 012600 MOV (SP)+,RO ;: POP STACK INTO RO
2316 015230 000002 RTI ;: RETURN
2317 015232 000000 $HIOCT: .WORD 0 ;: HIGH ORDER BITS GO HERE

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2333 015234 032777 040000 163676
2334 015242 001114
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2336 015244 000416
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2338 015246 013746 000004
2339 015252 012737 015272 000004
2340 015260 005737 177060
2341 015264 012637 000004
2342 015270 000463
2343 015272 022626
2344 015274 012637 000004
2345 015300 000423
2346 015302
2347 015302 032777 000400 163630
2348 015310 001404
2349 015312 127737 163622 001102
2350 015320 001465
2351 015322 105737 001103
2352 015326 001421
2353 015330 123737 001115 001103
2354 015336 101015
2355 015340 032777 001000 163572
2356 015346 001404
2357 015350 013737 001110 001106
2358 015356 000446
2359 015360 105037 001103
2360 015364 005037 001160
2361 015370 000415
2362 015372 032777 004000 163540
2363 015400 001011
2364 015402 005737 001202
2365 015406 001406
2366 015410 005237 001104
2367 015414 023737 001160 001104
2368 015422 002024
2369 015424 012737 000001 001104
2370 015432 013737 015510 001160
2371 015440 105237 001102

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.SBTTL SCOPE HANDLER ROUTINE

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*****
; THIS ROUTINE CONTROLS THE LOOPING OF SUBTESTS. IT WILL INCREMENT
; AND LOAD THE TEST NUMBER($STNM) INTO THE DISPLAY REG.(DISPLAY<7:0>)
; AND LOAD THE ERROR FLAG ($ERFLG) INTO DISPLAY<15:08>
; THE SWITCH OPTIONS PROVIDED BY THIS ROUTINE ARE:
; SW14=1 LOOP ON TEST
; SW11=1 INHIBIT ITERATIONS
; SW09=1 LOOP ON ERROR
; SW08=1 LOOP ON TEST IN SWR<7:0>
; CALL
; * SCOPE ; ;SCOPE=IOT

$SCOPE:
1$: BIT #BIT14,$SWR ; ; LOOP ON PRESENT TEST?
   BNE $OVER ; ; YES IF SW14=1
; *****START OF CODE FOR THE XOR TESTER*****
$XTSTR: BR 6$ ; ; IF RUNNING ON THE "XOR" TESTER CHANGE
; ; THIS INSTRUCTION TO A "NOP" (NOP=240)
   MOV 2$,$ERRVEC,-(SP) ; ; SAVE THE CONTENTS OF THE ERROR VECTOR
   MOV 5$,$ERRVEC ; ; SET FOR TIMEOUT
   TST 2$177060 ; ; TIME OUT ON XOR?
   MOV (SP)+,2$,$ERRVEC ; ; RESTORE THE ERROR VECTOR
   BR $SVLAD ; ; GO TO THE NEXT TEST
5$: CMP (SP)+,(SP)+ ; ; CLEAR THE STACK AFTER A TIME OUT
   MOV (SP)+,2$,$ERRVEC ; ; RESTORE THE ERROR VECTOR
   BR 7$ ; ; LOOP ON THE PRESENT TEST
6$; ; *****END OF CODE FOR THE XOR TESTER*****
   BIT #BIT08,$SWR ; ; LOOP ON SPEC. TEST?
   BEQ 2$ ; ; BR IF NO
   CMPB 2$,$SWR,$STNM ; ; ON THE RIGHT TEST? SWR<7:0>
   BEQ $OVER ; ; BR IF YES
2$: TSTB $ERFLG ; ; HAS AN ERROR OCCURRED?
   BEQ 3$ ; ; BR IF NO
   CMPB $ERMAX,$ERFLG ; ; MAX. ERRORS FOR THIS TEST OCCURRED?
   BHI 3$ ; ; BR IF NO
   BIT #BIT09,$SWR ; ; LOOP ON ERROR?
   BEQ 4$ ; ; BR IF NO
7$: MOV $LPERR,$LPADR ; ; SET LOOP ADDRESS TO LAST SCOPE
   BR $OVER
4$: CLRB $ERFLG ; ; ZERO THE ERROR FLAG
   CLR $TIMES ; ; CLEAR THE NUMBER OF ITERATIONS TO MAKE
   BR 1$ ; ; ESCAPE TO THE NEXT TEST
3$: BIT #BIT11,$SWR ; ; INHIBIT ITERATIONS?
   BNE 1$ ; ; BR IF YES
   TST $PASS ; ; IF FIRST PASS OF PROGRAM
   BEQ 1$ ; ; INHIBIT ITERATIONS
   INC $ICNT ; ; INCREMENT ITERATION COUNT
   CMP $TIMES,$ICNT ; ; CHECK THE NUMBER OF ITERATIONS MADE
   BGE $OVER ; ; BR IF MORE ITERATION REQUIRED
1$: MOV #1,$ICNT ; ; REINITIALIZE THE ITERATION COUNTER
   MOV $MXCNT,$TIMES ; ; SET NUMBER OF ITERATIONS TO DO
$SVLAD: INCB $STNM ; ; COUNT TEST NUMBERS

```

```

2372 015444 113737 001102 001200      MOVB  $STNM,$STIN  ;; SET TEST NUMBER IN APT MAILBOX
2373 015452 011637 001106      MOV   (SP), $LPADR ;; SAVE SCOPE LOOP ADDRESS
2374 015456 011637 001110      MOV   (SP), $LPERR ;; SAVE ERROR LOOP ADDRESS
2375 015462 005037 001162      CLR   $ESCAPE     ;; CLEAR THE ESCAPE FROM ERROR ADDRESS
2376 015466 112737 000001 001115      MOVB  #1,$ERMAX   ;; ONLY ALLOW ONE(1) ERROR ON NEXT TEST
2377 015474 013777 001102 163440 $OVER: MOV  $STNM,$DISPLAY ;; DISPLAY TEST NUMBER
2378 015502 013716 001106      MOV   $LPADR,(SP) ;; FUDGE RETURN ADDRESS
2379 015506 000002      RTI                ;; FIXES PS
2380 015510 003720      $MXCNT: 2000.     ;; MAX. NUMBER OF ITERATIONS
2381      .SBTTL  ERROR HANDLER ROUTINE
2382
2383      ;; *****
2384      ;; *THIS ROUTINE WILL INCREMENT THE ERROR FLAG AND THE ERROR COUNT,
2385      ;; *SAVE THE ERROR ITEM NUMBER AND THE ADDRESS OF THE ERROR CALL
2386      ;; *AND GO TO $ERRTYP ON ERROR
2387      ;; *THE SWITCH OPTIONS PROVIDED BY THIS ROUTINE ARE:
2388      ;; *SW15=1      HALT ON ERROR
2389      ;; *SW13=1      INHIBIT ERROR TYPEOUTS
2390      ;; *SW10=1     BELL ON ERROR
2391      ;; *SW09=1     LOOP ON ERROR
2392      ;; *CALL
2393      ;; *      ERROR  N      ;; ERROR=EMT AND N=ERROR ITEM NUMBER
2394
2395      $ERROR:
2396 015512 105237 001103      7$:  INCB  $ERFLG   ;; SET THE ERROR FLAG
2397 015516 001775      BEQ   7$         ;; DON'T LET THE FLAG GO TO ZERO
2398 015520 013777 001102 163414      MOV  $STNM,$DISPLAY ;; DISPLAY TEST NUMBER AND ERROR FLAG
2399 015526 032777 002000 163404      BIT  #BIT10,$SWR   ;; BELL ON ERROR?
2400 015534 001402      BEQ   1$         ;; NO - SKIP
2401 015536 104401 001164      TYPE  $BELL       ;; RING BELL
2402 015542 005237 001112      1$:  INC  $ERTTL   ;; COUNT THE NUMBER OF ERRORS
2403 015546 011637 001116      MOV  (SP), $ERRPC ;; GET ADDRESS OF ERROR INSTRUCTION
2404 015552 162737 000002 001116      SUB  #2,$ERRPC
2405 015560 117737 163332 001114      MOVB $ERRPC,$ITEMB ;; STRIP AND SAVE THE ERROR ITEM CODE
2406 015566 032777 020000 163344      BIT  #BIT13,$SWR   ;; SKIP TYPEOUT IF SET
2407 015574 001004      BNE  20$         ;; SKIP TYPEOUTS
2408 015576 004737 015706      JSR  PC,$ERRTYP  ;; GO TO USER ERROR ROUTINE
2409 015602 104401 001171      TYPE  $CRLF
2410 015606
2411 015606 122737 000001 001214      20$: CMPB  #APTENV,$ENV ;; RUNNING IN APT MODE
2412 015614 001007      BNE  2$         ;; NO SKIP APT ERROR REPORT
2413 015616 113737 001114 015630      MOVB $ITEMB,21$  ;; SET ITEM NUMBER AS ERROR NUMBER
2414 015624 004737 016342      JSR  PC,$ATY4   ;; REPORT FATAL ERROR TO APT
2415 015630 000      21$: .BYTE 0
2416 015631 000      .BYTE 0
2417 015632 000777      22$: BR   22$       ;; APT ERROR LOOP
2418 015634 005777 163300      2$:  TST  $SWR     ;; HALT ON ERROR
2419 015640 100001      BPL  3$         ;; SKIP IF CONTINUE
2420 015642 000000      HALT          ;; HALT ON ERROR!
2421 015644 032777 001000 163266      3$:  BIT  #BIT09,$SWR ;; LOOP ON ERROR SWITCH SET?
2422 015652 001402      BEQ  4$         ;; BR IF NO
2423 015654 013716 001110      MOV  $LPERR,(SP) ;; FUDGE RETURN FOR LOOPING
2424 015660 005737 001162      TST  $ESCAPE    ;; CHECK FOR AN ESCAPE ADDRESS
2425 015664 001402      BEQ  5$         ;; BR IF NONE

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2426 015666 013716 001162          MOV      $ESCAPE,(SP)      ;;FUDGE RETURN ADDRESS FOR ESCAPE
2427 015672 022737 012074 000042 5$:      CMP      #SENDAD,2#42     ;;ACT-11 AUTO-ACCEPT?
2428 015672 001001 000000          BNE      6$              ;;BRANCH IF NO
2429 015700 000000 000000          HALT                    ;;YES
2430 015702 000000 000000
2431 015704 000002 000002 6$:      RTI                      ;;RETURN
2432 015704 000002 000002 .SBTTL  ERROR MESSAGE TYPEOUT ROUTINE
2433
2434
2435 ;;*****
2436 ;;*THIS ROUTINE USES THE "ITEM CONTROL BYTE" ($ITEMB) TO DETERMINE WHICH
2437 ;;*ERROR IS TO BE REPORTED. IT THEN OBTAINS, FROM THE "ERROR TABLE" ($ERRTB),
2438 ;;*AND REPORTS THE APPROPRIATE INFORMATION CONCERNING THE ERROR.
2439
2440 $ERRTYP:
2441 015706 104401 001171          TYPE    $CRLF            ;; "CARRIAGE RETURN" & "LINE FEED"
2442 015712 010046 000000          MOV     RO,-(SP)         ;; SAVE RO
2443 015714 005000 000000          CLR     RO               ;; PICKUP THE ITEM INDEX
2444 015716 153700 001114          BISB   2#$ITEMB,RO
2445 015722 001004 000000          BNE     1$              ;; IF ITEM NUMBER IS ZERO, JUST
2446                                     ;; TYPE THE PC OF THE ERROR
2447 015724 013746 001116          MOV     $ERRPC,-(SP)    ;; SAVE $ERRPC FOR TYPEOUT
2448                                     ;; ERROR ADDRESS
2449 015730 104402 000000          TYPOC  6$              ;; GO TYPE--OCTAL ASCII(ALL DIGITS)
2450 015732 000426 000000          BR      6$              ;; GET OUT
2451 015734 005300 000000 1$:      DEC     RO               ;; ADJUST THE INDEX SO THAT IT WILL
2452 015736 006300 000000          ASL     RO               ;; WORK FOR THE ERROR TABLE
2453 015740 006300 000000          ASL     RO
2454 015742 006300 000000          ASL     RO
2455 015744 062700 001256          ADD     #ERRTB,RO       ;; FORM TABLE POINTER
2456 015750 012037 015760          MOV     (RO)+,2$
2457 015754 001404 000000          BEQ     3$              ;; PICKUP "ERROR MESSAGE" POINTER
2458 015756 104401 000000          TYPE   0               ;; SKIP TYPEOUT IF NO POINTER
2459 015760 000000 000000 2$:      .WORD  0               ;; "ERROR MESSAGE" POINTER GOES HERE
2460 015762 104401 001171          TYPE   $CRLF            ;; "CARRIAGE RETURN" & "LINE FEED"
2461 015766 012037 015776 3$:      MOV     (RO)+,4$
2462 015772 001404 000000          BEQ     5$              ;; PICKUP "DATA HEADER" POINTER
2463 015774 104401 000000          TYPE   0               ;; SKIP TYPEOUT IF 0
2464 015776 000000 000000 4$:      .WORD  0               ;; TYPE THE "DATA HEADER"
2465 016000 104401 001171          TYPE   $CRLF            ;; "DATA HEADER" POINTER GOES HERE
2466 016004 011000 000000 5$:      MOV     (RO),RO         ;; "CARRIAGE RETURN" & "LINE FEED"
2467 016006 001004 000000          BNE     7$              ;; PICKUP "DATA TABLE" POINTER
2468 016010 012600 000000 6$:      MOV     (SP)+,RO        ;; GO TYPE THE DATA
2469 016012 104401 001171          TYPE   $CRLF            ;; RESTORE RO
2470 016016 000207 000000          RTS     PC              ;; "CARRIAGE RETURN" & "LINE FEED"
2471 016020 013046 000000 7$:      MOV     2(RO)+,-(SP)    ;; RETURN
2472 016022 104402 000000          TYPOC  6$              ;; SAVE 2(RO)+ FOR TYPEOUT
2473 016024 005710 000000          TST    (RO)             ;; GO TYPE--OCTAL ASCII(ALL DIGITS)
2474 016026 001770 000000          BEQ     8$              ;; IS THERE ANOTHER NUMBER?
2475 016030 104401 016036          TYPE   8$              ;; BR IF NO
2476 016034 000771 000000          BR      7$              ;; TYPE TWO(2) SPACES
2477 016036 020040 000000 8$:      .ASCIZ / /            ;; LOOP
2478 016042 000000 000000          .EVEN                  ;; TWO(2) SPACES
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.SBTTL TYPE ROUTINE

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*****
*ROUTINE TO TYPE ASCIZ MESSAGE. MESSAGE MUST TERMINATE WITH A 0 BYTE.
*THE ROUTINE WILL INSERT A NUMBER OF NULL CHARACTERS AFTER A LINE FEED.
*NOTE1: $NULL CONTAINS THE CHARACTER TO BE USED AS THE FILLER CHARACTER.
*NOTE2: $FILLS CONTAINS THE NUMBER OF FILLER CHARACTERS REQUIRED.
*NOTE3: $FILLC CONTAINS THE CHARACTER TO FILL AFTER.
*
*CALL:
*1) USING A TRAP INSTRUCTION
* TYPE ,MESADR ;;MESADR IS FIRST ADDRESS OF AN ASCIZ STRING
*OR
* TYPE
* MESADR
*
```

```
016042 105737 001157 $TYPE: TSTB $TFPLG ;; IS THERE A TERMINAL?
016046 100002 BPL 1$ ;; BR IF YES
016050 000000 HALT ;; HALT HERE IF NO TERMINAL
016052 000430 BR 3$ ;; LEAVE
016054 010046 1$: MOV RO, -(SP) ;; SAVE RO
016056 017600 000002 MOV @2(SP), RO ;; GET ADDRESS OF ASCIZ STRING
016062 122737 000001 001214 CMPB #APTENV, $ENV ;; RUNNING IN APT MODE
016070 001011 BNE 62$ ;; NO GO CHECK FOR APT CONSOLE
016072 132737 000100 001215 BITB #APTSPool, $ENVm ;; SPOOL MESSAGE TO APT
016100 001405 BEQ 62$ ;; NO GO CHECK FOR CONSOLE
016102 010037 016112 MOV RO, 61$ ;; SETUP MESSAGE ADDRESS FOR APT
016106 004737 016332 JSR PC, $ATY3 ;; SPOOL MESSAGE TO APT
016112 000000 61$: .WORD 0 ;; MESSAGE ADDRESS
016114 132737 000040 001215 62$: BITB #APTCSUP, $ENVm ;; APT CONSOLE SUPPRESSED
016122 001003 BNE 60$ ;; YES, SKIP TYPE OUT
016124 112046 2$: MOVB (RO)+, -(SP) ;; PUSH CHARACTER TO BE TYPED ONTO STACK
016126 001005 BNE 4$ ;; BR IF IT ISN'T THE TERMINATOR
016130 005726 TST (SP)+ ;; IF TERMINATOR POP IT OFF THE STACK
016132 012600 60$: MOV (SP)+, RO ;; RESTORE RO
016134 062716 000002 3$: ADD #2, (SP) ;; ADJUST RETURN PC
016140 000002 RTI ;; RETURN
016142 122716 000011 4$: CMPB #HT, (SP) ;; BRANCH IF <HT>
016146 001430 BEQ 8$ ;;
016150 122716 000200 CMPB #CRLF, (SP) ;; BRANCH IF NOT <CRLF>
016154 001006 BNE 5$ ;;
016156 005726 TST (SP)+ ;; POP <CR><LF> EQUIV
016160 104401 TYPE ;; TYPE A CR AND LF
016162 001171 $CRLF ;;
016164 105037 016320 CLRB $CHARCNT ;; CLEAR CHARACTER COUNT
016170 000755 BR 2$ ;; GET NEXT CHARACTER
016172 004737 016254 5$: JSR PC, $TYPEC ;; GO TYPE THIS CHARACTER
016176 123726 001156 6$: CMPB $FILLC, (SP)+ ;; IS IT TIME FOR FILLER CHARS.?
016202 001350 BNE 2$ ;; IF NO GO GET NEXT CHAR.
016204 013746 001154 MOV $NULL, -(SP) ;; GET # OF FILLER CHARS. NEEDED
; AND THE NULL CHAR.
016210 105366 000001 7$: DECB 1(SP) ;; DOES A NULL NEED TO BE TYPED?
016214 002770 BLT 6$ ;; BR IF NO--GO POP THE NULL OFF OF STACK
```



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2534 016216 004737 016254      JSR    PC,$TYPEC      ;;GO TYPE A NULL
2535 016222 105337 016320      DECB   $CHARCNT      ;;DO NOT COUNT AS A COUNT
2536 016226 000770                BR     7$            ;;LOOP
2537
2538                ;HORIZONTAL TAB PROCESSOR
2539
2540 016230 112716 000040      8$:    MOVB   #' (SP)      ;;REPLACE TAB WITH SPACE
2541 016234 004737 016254      9$:    JSR    PC,$TYPEC      ;;TYPE A SPACE
2542 016240 132737 000007 016320      BITB   #7,$CHARCNT     ;;BRANCH IF NOT AT
2543 016246 001372                BNE    9$            ;;TAB STOP
2544 016250 005726                TST   (SP)+          ;;POP SPACE OFF STACK
2545 016252 000724                BR     2$            ;;GET NEXT CHARACTER
2546 016254 105777 162670      $TYPEC: TSTB   $STPS        ;;WAIT UNTIL PRINTER IS READY
2547 016260 100375                BPL   $TYPEC
2548 016262 116677 000002 162662      MOVB   2(SP),$STPB     ;;LOAD CHAR TO BE TYPED INTO DATA REG.
2549 016270 122766 000015 000002      CMPB   #CR,2(SP)      ;;IS CHARACTER A CARRIAGE RETURN?
2550 016276 001003                BNE    1$            ;;BRANCH IF NO
2551 016300 105037 016320      CLRB   $CHARCNT       ;;YES--CLEAR CHARACTER COUNT
2552 016304 000406                BR     $TYPEX
2553 016306 122766 000012 000002 1$:    CMPB   #LF,2(SP)      ;;IS CHARACTER A LINE FEED?
2554 016314 001402                BEQ   $TYPEX          ;;BRANCH IF YES
2555 016316 105227                INCB  (PC)+           ;;COUNT THE CHARACTER
2556 016320 000000      $CHARCNT: .WORD 0    ;;CHARACTER COUNT STORAGE
2557 016322 000207      $TYPEX: RTS    PC
2558
2559                .SBTTL  APT COMMUNICATIONS ROUTINE
2560
2561                ;*****
2562 016324 112737 000001 016570  $ATY1: MOVB   #1,$FFLG     ;;TO REPORT FATAL ERROR
2563 016332 112737 000001 016566  $ATY3: MOVB   #1,$MFLG     ;;TO TYPE A MESSAGE
2564 016340 000403                BR     $ATYC
2565 016342 112737 000001 016570  $ATY4: MOVB   #1,$FFLG     ;;TO ONLY REPORT FATAL ERROR
2566 016350  $ATYC:
2567 016350 010046                MOV   R0,-(SP)        ;;PUSH R0 ON STACK
2568 016352 010146                MOV   R1,-(SP)        ;;PUSH R1 ON STACK
2569 016354 105737 016566      TSTB   $MFLG          ;;SHOULD TYPE A MESSAGE?
2570 016360 001450                BEQ   5$            ;;IF NOT: BR
2571 016362 122737 000001 001214      CMPB   #APTENV,$ENV    ;;OPERATING UNDER APT?
2572 016370 001031                BNE   3$            ;;IF NOT: BR
2573 016372 132737 000100 001215      BITB   #APTPOOL,$ENVM  ;;SHOULD SPOOL MESSAGES?
2574 016400 001425                BEQ   3$            ;;IF NOT: BR
2575 016402 017600 000004                MOV   @4(SP),R0        ;;GET MESSAGE ADDR.
2576 016406 062766 000002 000004      ADD   #2,4(SP)         ;;BUMP RETURN ADDR.
2577 016414 005737 001174      1$:    TST   $MSGTYPE      ;;SEE IF DONE W/ LAST XMISSION?
2578 016420 001375                BNE   1$            ;;IF NOT: WAIT
2579 016422 010037 001210      MOV   R0,$MSGAD        ;;PUT ADDR IN MAILBOX
2580 016426 105720      2$:    TSTB   (R0)+          ;;FIND END OF MESSAGE
2581 016430 001376                BNE   2$
2582 016432 163700 001210      SUB   $MSGAD,R0        ;;SUB START OF MESSAGE
2583 016436 006200                ASR   R0              ;;GET MESSAGE LGTH IN WORDS
2584 016440 010037 001212                MOV   R0,$MSGGLT      ;;PUT LENGTH IN MAILBOX
2585 016444 012737 000004 001174      MOV   #4,$MSGTYPE     ;;TELL APT TO TAKE MSG.
2586 016452 000413                BR     5$
2587 016454 017637 000004 016500  3$:    MOV   @4(SP),4$      ;;PUT MSG ADDR IN JSR LINKAGE

```



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2588 016462 062766 000002 000004 ADD #2,4(SP) ;:BUMP RETURN ADDRESS
2589 016470 013746 177776 MOV 177776,-(SP) ;:PUSH 177776 ON STACK
2590 016474 004737 016042 JSR PC,$TYPE ;:CALL TYPE MACRO
2591 016500 000000 4$: .WORD 0
2592 016502 5$:
2593 016502 105737 016570 10$: TST $FFLG ;:SHOULD REPORT FATAL ERROR?
2594 016506 001416 BEQ 12$ ;:IF NOT: BR
2595 016510 005737 001214 TST $ENV ;:RUNNING UNDER APT?
2596 016514 001413 BEQ 12$ ;:IF NOT: BR
2597 016516 005737 001174 11$: TST $MSGTYPE ;:FINISHED LAST MESSAGE?
2598 016522 001375 BNE 11$ ;:IF NOT: WAIT
2599 016524 017637 000004 001176 MOV #4(SP),$FATAL ;:GET ERROR #
2600 016532 062766 000002 000004 ADD #2,4(SP) ;:BUMP RETURN ADDR.
2601 016540 005237 001174 INC $MSGTYPE ;:TELL APT TO TAKE ERROR
2602 016544 105037 016570 12$: CLRB $FFLG ;:CLEAR FATAL FLAG
2603 016550 105037 016567 CLRB $LFLG ;:CLEAR LOG FLAG
2604 016554 105037 016566 CLRB $MFLG ;:CLEAR MESSAGE FLAG
2605 016560 012601 MOV (SP)+,R1 ;:POP STACK INTO R1
2606 016562 012600 MOV (SP)+,R0 ;:POP STACK INTO R0
2607 016564 000207 RTS PC ;:RETURN
2608 016566 000 SMFLG: .BYTE 0 ;:MESSG. FLAG
2609 016567 000 SLFLG: .BYTE 0 ;:LOG FLAG
2610 016570 000 SFFLG: .BYTE 0 ;:FATAL FLAG
2611 016572 .EVEN
2612 000200 APTSIZE=200
2613 000001 APTENV=001
2614 000100 APTSPool=100
2615 000040 APTCSUP=040
2617 ;:
2618 ;:*THIS SUB CODE IS USED TO INITIALIZE THE LPA-11
2619 ;:*FIRST WE WILL LOAD MICROCODE INTO KMC-11
2620 ;:*NEXT WE WILL INIT BOTH UPROCESSORS
2621 ;:*THEN WE WILL LOAD DEVICE TABLE IN SLAVE UP.
2622 ;:*THE ORDER OF LOAD IS DETERMINED BY THE USER.
2623 ;:*
2624 ;:* CALL= JSR R5,$LPAI
2625 ;:* .WORD 0 ;:ADDR. OF DEVICE ADDRESS.
2626 ;:* ROUTINES REQUIRED: .LOADLP
2627 ;:* PROGRAMS REQUIRED: DRLPX2
2628 ;:*
2629 ;:*
2630 ;:* ;:RETURNS WITH $AERR=1 IF SLAVE
2631 ;:* ;:MICRO SAYS AN ADDR. DOES NOT EXSIST. IN THE LIST.
2632 ;:*
2633 016572 $LPAI:
2634 016572 013746 000004 MOV 4,-(SP)
2635
2636 016576 000413 BR 31$ ;:FIELD DOES NOT HAVE A BUS SWITCH TO
2637 ;:WORRY ABOUT,SO WE WILL UNCONDITIONALLY
2638 ;:BRANCH ARROUD THE NEXT CODE THAT
2639 ;:WORKS BASED ON A BUS SWITCH.
2640 ;:CODE LEFT IN HERE FOR IN HOUSE
2641 ;:PERSONAL WHO MAY PATCH THIS BRANCH

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2643 ; INSTRUCTION TO A <NOP> OCTAL <240>
2644 ; IN ORDER TO RUN PROGRAM WITH A SWITCH.
2645 ; NOTE THIS "SWITCH" IS A PIECE OF INHOUSE
2646 ; TEST EQUIPMENT ONLY IT CONNECTS
2647 ; THE UNIBUS TO THE I/O BUS FOR
2648 ; CERTAIN TESTING.
2649 016600 012737 016624 000004      MOV      #30$ , 4
2650 016606 005237 170000              INC      170000
2651 016612 104401 016620              TYPE    65$
2652 016616 000401                      BR       64$
2653 ;:65$: .ASCIZ <7>##
2654 016622                                BR       31$
2655 016622 000401                      BR       31$
2656 016624 022626              30$:    CMP      (SP)+ , (SP)+
2657 016626 012637 000004              31$:    MOV      (SP)+ , 4
2658 016632 005037 017450              CLR      $AERR
2659 016636 004537 017452              JSR     R5 , $LOAD
2660 016642 000000G              .WORD   DRLPX2
2661 016644 052777 040000 162564      BIS     #BIT14 , @KMADD ; ISSUE KMC+DMC INIT.
2662
2663 016652                                1$:
2664 ; "HANGS" HERE THEN KMC-11 ERROR.
2665
2666 016652 010146              MOV     R1 , -(SP)
2667 016654 005001              CLR     R1
2668 016656 005201              2$:    INC     R1
2669 016660 001376              BNE
2670 016662 012777 104000 162546      MOV     #BIT15!BIT11 , @KMADD ; SET RUN, AND ENABLE ARBITRATION.
2671 016670 105201              25$:   INCB   R1
2672 016672 001376              BNE     25$
2673
2674 016674 032777 000040 162534      BIT     #BITS , @KMADD ; SLAVE READY? (READING IPBM SR)
2675 016702 001401              BEQ     3$
2676 ; FATAL LPA-11 ERROR SLAVE NOT READY.
2677 016704 104000              ERROR
2678
2679 016706 012777 000004 162526      3$:    MOV     #4 , @KMAD2 ; READ FAST PATH
2680 016714                                4$:
2681 016714 004537 020362              JSR     R5 , $TOUT ; -TOUT-CHECK FOR TIMEOUT
2682
2683 016720 104000              ERROR
2684 ; /TIME-OUT ERROR
2685 ; /WE FAILED TO COMPLETE
2686 ; /CURRENT OPERATION.
2687 ; /CONTINUES IN THIS LOOP
2688 ; /WOULD MAKE US "HANG" HERE
2689 016722 000774              BR      4$
2690
2691 ; /RETURNS HERE-FROM-TIMED OUT.
2692 016724 122777 000377 162510      CMPB   #377 , @KMAD2 ; WAIT TILL KMC DONE COMMAND.
2693 016732 001370              BNE     4$
2694 016734 122777 000377 162504      CMPB   #377 , @KMAD4 ; IF FAST PATH=377 THEN ERROR.
2695 016742 001001              BNE     35$

```



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2750                                     ;SLAVE WILL RETURN CODE 0 IF
2751 017404 005237 017450             INC     $AERR    ;DEV PRESENT.  ELSE
2752                                     ;EXIT $AERR=1 IF SLAVE GIVES ERROR.
2753 017410 005041                     CLR     -(1)    ;GET RID OF REFERENCE TO BAD ADDR.
2754 017412 012601             10$:    MOV     (SP)+,R1
2755 017414 000205             RTS     R5      ;RETURN ALL ADDR. CHECKED.
2756
2757 017416 000000             11$:    .WORD  0      ;HOLDS DAC CODE PLUS OFFSET
2758                                     ;TO SLAVES ADDR. TABLE.
2759
2760 017420 112777 000003 162014    20$:    MOVB   #3, @KMAD2 ;ISSUE FIFO WRITE
2761 017426 000000             21$:
2762 017426 004537 020362             JSR    R5, $TOUT ;-TOUT-CHECK FOR TIMEOUT
2763
2764 017432 104000             ERROR
2765                                     ;/TIME-OUT ERROR
2766                                     ;/WE FAILED TO COMPLETE
2767                                     ;/CURRENT OPERATION.
2768                                     ;/CONTINUES IN THIS LOOP
2769                                     ;/WOULD MAKE US "HANG" HERE
2770
2771 017434 000774             BR          21$
2772
2773 017436 122777 000377 161776    CMPB   #377, @KMAD2 ;/RETURNS HERE-FROM-TIMED OUT.
2774 017444 001370             BNE   21$      ;KMC CODE WILL RETURN A "377"
2775 017446 000207             RTS    PC     ;WHEN DONE COMMAND.
2776
2777 017450 000000             $AERR: .WORD  0      ;=0 IF ADDR. LIST OK,=1 IF BAD.
2778
2779                                     ;*
2780                                     ;*THIS SUB CODE USED TO LOAD MICRO-CODE INTO LPA-11.
2781                                     ;* CALL = JSR R5,$LOAD
2782                                     ;* .WORD XX ;ADDR. OF MICRO CODE.
2783                                     ;* ;RETURNS HERE
2784                                     ;* NOTE: MICRO CODE FILE MUST END IN -1 DATA.
2785                                     ;*
2786
2787 017452 010446             $LOAD: MOV    R4, -(SP) ;SAVE R4.
2788 017454 010046             MOV    R0, -(SP) ;SAVE R0.
2789 017456 012500             1$:    MOV    (5)+, R0 ;GET PROG. ADDR.
2790 017460 005077 161752             CLR    @KMAD0    ;CLEAR CSR
2791 017464 005077 161756             CLR    @KMAD4    ;CLEAR CRAM ADDR.
2792 017470 052777 002000 161740    2$:    BIS    #2000, @KMAD0 ;SELECT CRAM.
2793 017476 012077 161750             MOV    (0)+, @KMAD6 ;WRITE DATA.
2794 017502 052777 020000 161726    BIS    #20000, @KMAD0 ;SET CRAM WRITE
2795 017510 005077 161722             CLR    @KMAD0    ;DISABLE CRAM.
2796 017514 005277 161726             INC    @KMAD4    ;UPDATE CRAM ADDR.
2797 017520 021027 177777             CMP    (0), #-1  ;ALL DONE?
2798 017524 001361             BNE   2$        ;NO LOOP.
2799 017526 005077 161714             CLR    @KMAD4    ;CLEAR CRAM ADDR.
2800 017532 016500 177776             MOV    -2(5), R0 ;GET MICRO CODE ADDR.
2801
2802 017536 052777 002000 161672    3$:    BIS    #2000, @KMAD0 ;SELECT CRAM
2803 017544 022077 161702             CMP    (R0)+, @KMAD6 ;DATA OK?
    
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2804 017550 001013          BNE      5$          ;NO - REPORT AN ERROR.
2805 017552 021027 177777  CMP      (0),#-1    ;ALL DONE?
2806 017556 001405          BEQ      4$          ;YES - EXIT
2807 017560 005077 161652  CLR      @KMADO     ;NO - DESELECT CRAM.
2808 017564 005277 161656  INC      @KMAD4     ;UPDATE CRAM ADDR.
2809 017570 000762          BR       3$
2810
2811 017572 012600          4$:      MOV      (SP)+,R0    ;RESTORE R0
2812 017574 012604          MOV      (SP)+,R4    ;RESTORE R4
2813 017576 000205          RTS      R5          ;EXIT
2814
2815 017600          5$:      ;COME HERE ON LOAD ERROR
2816 017600 005745          TST      -(5)
2817 017602 105204          INCB    R4          ;UPDATE ERROR COUNTER.
2818 017604 100324          BPL     1$          ;IF NOT TOO MANY, TRY AGAIN.
2819 017606 000000          HALT
2820
2821 017610 000722          BR       1$          ;MICRO CODE LOAD ERROR.
2822
2823
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2843
2844
2845
2846
2847
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2854
2855
2856
2857
017612 010046          $TLKW:  MOV      R0,-(SP)    ;SAVE R0
017614 012500          MOV      (5)+,R0     ;GET DEVICE OFFSET
017616 052700 000340  BIS      #340,R0     ;ADD WRITE CODE.
017622 004737 020074  JSR      PC,$LPW     ;WAIT FOR FAST PATH READY
017626 010037 017720  MOV      R0,W1
017632 010077 161610  MOV      R0,@KMAD4
017636 112777 000005 161576  MOVVB   #5,@KMAD2    ;ISSUE FAST PATH WRITE
017644 004737 020074  JSR      PC,$LPW     ;WAIT FOR RDY
017650 011537 017722  MOV      (5),W2
017654 112577 161566  MOVVB   (5)+,@KMAD4 ;WRITE LOW BYTE DATA.
017660 112777 000005 161554  MOVVB   #5,@KMAD2    ;FP WRITE
017666 004737 020074  JSR      PC,$LPW
017672 111537 017724  MOVVB   (5),W3
017676 112577 161544  MOVVB   (5)+,@KMAD4 ;WRITE HIGH BYTE
017702 112777 000005 161532  MOVVB   #5,@KMAD2
017710 004737 020074  JSR      PC,$LPW
017714 012600          MOV      (SP)+,R0
017716 000205          RTS      R5          ;EXIT DONE.
017720 000000          W1:      0
017722 000000          W2:      0
017724 000000          W3:      0

```

```

; *THIS ROUTINE ISSUES A WRITE COMMAND TO THE LPA-11
; *
; *      CALL = JSR      R5,$TLKW
; *              .WORD  0          ;OFFSET OF DEVICE ADDR.
; *              .WORD  0          ;DATA TO BE WRITTEN
; *

```

```

2858
2859
2860
2861
2862
2863
2864
2865
2866
2867 017726 010046          $TLKr: MOV      R0, -(SP)          ;SAVE R0
2868 017730 012500          MOV      (5)+, R0        ;GET OFFSET
2869 017732 052700 000300  BIS      #300, R0        ;ADD READ CODE
2870 017736 004737 020074  JSR      PC, $SLPw       ;WAIT TILL READY
2871 017742 110077 161500  MOVb     R0, @KMAD4
2872 017746 112777 000005 161466  MOVb     #5, @KMAD2      ;ISSUE WRITE FP
2873 017754 004737 020074  JSR      PC, $SLPw
2874 017760 010037 020070  MOV      R0, RD1
2875 017764
2876 017764 004537 020362  1$:     JSR      R5, $TOUT      ; -TOUT-CHECK FOR TIMEOUT
2877
2878 017770 104000          ERROR          ; /TIME-OUT ERROR
2879
2880
2881
2882
2883
2884 017772 000774          BR              1$
2885
2886
2887 017774 032777 000040 161434  BIT      #BITS, @KMAD0   ; /RETURNS HERE-FROM-TIMED OUT.
2888 020002 001370          BNE       1$           ; FAST PATH GOT DATA?
2889 020004 112777 000004 161430  MOVb     #4, @KMAD2      ;ISSUE FAST PATH READ
2890 020012 004737 020074  JSR      PC, $SLPw
2891 020016 117737 161424 020072  MOVb     @KMAD4, $DATR   ;GET LOW BYTE
2892 020024
2893 020024 004537 020362  2$:     JSR      R5, $TOUT      ; -TOUT-CHECK FOR TIMEOUT
2894
2895 020030 104000          ERROR          ; /TIME-OUT ERROR
2896
2897
2898
2899
2900
2901 020032 000774          BR              2$
2902
2903
2904 020034 032777 000040 161374  BIT      #BITS, @KMAD0   ; /RETURNS HERE-FROM-TIMED OUT.
2905 020042 001370          BNE       2$           ; FAST PATH READY?
2906 020044 112777 000004 161370  MOVb     #4, @KMAD2      ;ISSUE FAST PATH READ
2907 020052 004737 020074  JSR      PC, $SLPw
2908 020056 117737 161364 020073  MOVb     @KMAD4, $DATR+1 ;SAVE HIGH BYTE
2909 020064 012600          MOV      (SP)+, R0
2910 020066 000205          RTS      R5
2911 020070 000000          RD1: 0

```



```

2912 020072 000000          $DATR: .WORD 0
2913
2914          ; THIS ROUTINE WAITS FOR KMC-CODE TO BECOME READY AS WELL
2915          ; AS FAST PATH TO BE READ.
2916
2917          ; CALL = JSR PC,$LPW
2918
2919          ; IT WILL TIME OUT IF TOO MUCH TIME IS TAKEN BY
2920          ; THE MICRO-PROCESSORS AND REPORT AN ERROR, THEN HALT.
2921
2922
2923 020074 010146          $LPW: MOV R1,-(SP)          ;SAVE R1
2924 020076 005001          CLR R1
2925 020100 122777 000377 161334 1$: CMPB #377,$KMA22          ;FINISHED INSTRUCTION?
2926 020106 001403          BEQ 2$
2927 020110 005201          INC R1          ;TIME OUT?
2928 020112 001372          BNE 1$
2929 020114 000411          BR 10$
2930
2931 020116 032777 000020 161312 2$: BIT #BIT4,$KMA20          ;FAST PATH READ?
2932 020124 001403          BEQ 3$
2933 020126 005201          INC R1          ;NO - TIME OUT?
2934 020130 001372          BNE 2$
2935 020132 000402          BR 10$          ;YES - REPORT AN ERROR
2936
2937 020134 012601          3$: MOV (SP)+,R1          ;RESTORE R1
2938 020136 000207          RTS PC          ;EXIT
2939
2940 020140
2941 020140 104401 020146          10$: TYPE 65$          ;;TYPE ASCIZ STRING
2942 020144 000407          BR 64$          ;;GET OVER THE ASCIZ
2943
2944 020164          ;:65$: .ASCIZ <200>#LPA-11 FAULT#
2945          64$:
2946 020164 000000          11$: HALT          ;LPA-11 FAULT RUN LPA-11
2947 020166 000776          BR 11$          ;DIAGNOSTICS.
2948
2949
2950
2951
2952          ;*
2953          ;*THIS ROUTINE PROVIDES THE LINKAGE FROM USER CODE TO
2954          ;*A DEVICE ADDRESS ON THE I/O BUSS FOR WRITE ONLY.
2955          ;*
2956          ;* FIRST WE WILL DETERMINE IF THE ADDRESS HAS BEEN USED
2957          ;* BEFORE. IF NOT WE HAVE TO INITIALIZE THE LPA WITH
2958          ;* THAT ADDRESS.
2959          ;* WHEN THE ADDR. IS KNOWN BY THE LPA, DO THE OUTPUT BY
2960          ;* $TLKW
2961          ;*
2962 020170 010046          $OUTLP: MOV R0,-(SP)          ;SAVE R0
2963 020172 010146          MOV R1,-(SP)          ;SAVE R1
2964
2965 020174 012700 001464          MOV #.DVLS,R0          ;PROGRAM DEFINED LIST.

```

```

2966 020200 005001          CLR      R1
2967 020202 005710          1$:     TST      (0)          ; TERMINATOR REACHED?
2968 020204 001421          BEQ      10$          ; YES NEXT STEP.
2969 020206 027520 000000   CMP      2(5), (0)+   ; MATCH WITH ADDR IN LIST?
2970 020212 001402          BEQ      2$
2971 020214 005201          INC      R1
2972 020216 000771          BR       1$
2973
2974 020220 010137 020236   2$:     MOV      R1, 3$          ; SAVE OFFSET, DEVICE KNOWN.
2975 020224 005725          TST      (5)+
2976 020226 013537 020240   MOV      2(5)+, 4$          ; GET DATA TO BE WRITTEN
2977 020232 004537 017612   JSR      R5, $TLKW          ; DO WRITE
2978 020236 000000          3$:     .WORD    0          ; DEVICE OFFSET
2979 020240 000000          4$:     .WORD    0          ; DATA TO BE WRITTEN.
2980 020242 012601          MOV      (SP)+, R1
2981 020244 012600          MOV      (SP)+, R0
2982 020246 000205          RTS      R5
2983 020250 017520 000000   10$:    MOV      2(5), (0)+   ; SAVE ADDR.
2984 020254 005010          CLR      (0)
2985 020256 004537 016572   JSR      R5, $LPAI
2986 020262 001464          .WORD    .DVLS
2987 020264 000755          BR       2$
2988
2989
2990
2991
2992
2993
2994
2995
2996
2997
2998
2999
3000
3001
3002
3003
3004 020266 010046          $INLP:  MOV      R0, -(SP)          ; SAVE R0
3005 020270 010146          MOV      R1, -(SP)          ; SAVE R1
3006
3007 020272 012700 001464   MOV      #.DVLS, R0          ; PROG DEFINED ADDR. LIST.
3008 020276 005001          CLR      R1
3009 020300 005710          1$:     TST      (0)          ; EOL REACHED?
3010 020302 001420          BEQ      10$          ; YES - DEFINE NEW ADDR.
3011
3012 020304 027520 000000   CMP      2(5), (0)+   ; ADDR. MATCH?
3013 020310 001402          BEQ      2$
3014 020312 005201          INC      R1
3015 020314 000771          BR       1$
3016
3017 020316 010137 020330   2$:     MOV      R1, 3$          ; SAVE LIST OFFSET
3018 020322 005725          TST      (5)+
3019 020324 004537 017726   JSR      R5, $TLKR          ; GO READ DEVICE
    
```

```

; *
; * THIS ROUTINE PROVIDES THE LINKAGE FROM USER CODE
; * TO A DEVICE ADDR. ON THE I/O BUSS FOR READ ONLY.
; *
; * FIRST WE WILL DETERMINE IF THE ADDRESS HAS BEEN
; * USED BEFORE. IF NOT, WE HAVE TO INITIALIZE THE LPA
; * WITH THE NEW ADDR.
; * WHEN THE ADDR IS KNOWN WE CAN DO OUTPUT THROUGH
; * $TLKR
; *
; * CALL THROUGH MOVEI DATA, ADDR.
; * WHICH EQUALS:
; * JSR R5, $INLP
; * .WORD XX ADDR OF DEVICE
; * .WORD YY ADDR TO STORE READ DATA.
    
```

```

3020          020330 020330          $OFS=.
3021 020330 000000          3$: .WORD 0 ;OFFSET OF DEVICE
3022
3023 020332 013735 020072          MOV $DATR,@(5)+ ;STORE DATA.
3024 020336 012601          MOV (SP)+,R1 ;RESTORE R1
3025 020340 012600          MOV (SP)+,R0 ;RESTORE R2
3026 020342 000205          RTS R5 ;EXIT
3027
3028 020344 017520 000000          10$: MOV @(5),(0)+
3029 020350 005010          CLR (0)
3030 020352 004537 016572          JSR R5,$LPAI
3031 020356 001464          .WORD DVL5
3032 020360 000756          BR 2$
3033
3034          ;*$STOUT ROUTINE USED TO WATCH IF
3035          ;* WE'RE IN A LOOP TOO-LONG
3036          ;* CALL= JSR R5,$STOUT
3037          ;* ERROR X ;RETURNS HERE ON TIMEOUT
3038          ;* BR
3039          ;* ;RETURNS HERE NO ERROR
3040          ;*
3041
3042 020362 020537 020416          $STOUT: CMP R5,$SAD ;SAME ADDR?
3043 020366 001405          BEQ 1$
3044 020370 010537 020416          MOV R5,$SAD ;NO-SAVE THIS ADDR.
3045 020374 005037 020420          CLR $CNT ;CLR CNT AT ADDR.
3046 020400 000403          BR 2$
3047 020402 005237 020420          1$: INC $CNT ;OVERFLOW?
3048 020406 100402          BMI 3$ ;YES-ERROR RETURN
3049 020410 062705 000004          2$: ADD #4,R5 ;NO-NON ERROR RETURN
3050 020414 000205          3$: RTS R5 ;RETURN.
3051
3052 020416 000000          $SAD: .WORD 0 ;CONTAINS LOOP ADDR.
3053 020420 000000          $CNT: .WORD 0 ;# OF TIMES AT ADDR.
3054
3055          ;*
3056          ;* THIS ROUTINE REPLACES WHAT THE USER WOULD ORDINARILY
3057          ;* USE FOR A RESET. FIRST WE DO A RESET INSTRUCTION.
3058          ;* THEN WE CLR ".DVL5" WHICH FORCES US TO RESET BOTH THE
3059          ;* KMC AND DMC AS SOON AS A DEVICE IS REFERENCED.
3060          ;*
3061          ;* CALL=JSR PC,$RESET ;REPLACES "RESET INSTRUCTION
3062          ;* ;RETURNS HERE.
3063          ;*
3064 020422 000005          $RESET: RESET ;RESET THE WORLD.
3065
3066          ;*
3067 020434 005737 017450          ;* MOV @2$,1$ ;/READ DEVICE REG 2$,PUT DATA IN 1$.
3068 020440 001004          TST $AERR ;IF NO ERROR,LOOP
3069 020442 062737 000002 020456          BNE 10$ ;THERE WAS AN ERROR.
3070          ADD #2,2$ ;UPDATE DEVICE ADDR.
3071          ;YOU SEE, WE HAVE TO PROTECT OUR SELF!
3072          ;IF 2$ CONTAINED A VALID ADDR,WE
3073          ;MUST KEEP TRYING UNTIL WE GENERATE
          ;AN INVALID ADDR.

```



```

3074 020450 000764          BR      $RESET
3075 020452          10$:      RTS      PC
3076 020452 000207          1$:      .WORD  0          ;JUNK LOC.
3077 020454 000000          2$:      .WORD  160000       ;DUMB ADDR. FORCES INIT OF DMC/KMC.
3078 020456 160000
3079
3080
3081
3082          ;SDELAY- ROUTINE TO GIVE A MINOR DELAY.
3083          ;IS NOT TIME DEPENDENT CODE SENCE
3084          ;NOT USED TO GET SPECIFIC TIME BUT
3085          ;JUST A LITTLE DELAY.
3086
3087          ;
3088          ;THAT IS UNLESS A REAL TIME CLOCK IS PRESENT!
3089          ;THEN WE'LL GENERATE A TIME BETWEEN 16MS TO 32 MS
3090
3091          ;
3092          ;CALL= JSR PC, SDELAY
3093
3093 020460          SDELAY:    TST      RTCCSR       ;CLOCK PRESENT?
3094 020460 005737 020542    BPL      10$
3095 020464 100016          MOV      #2, TIME
3096 020466 012737 000002 020532  BIS      #15, @RTCCSR   ;START CLOCK
3097 020474 052777 000115 000040    CLR      PS
3098 020502 005037 177776          TST      TIME
3099 020506 005737 020532  1$:      BNE      1$
3100 020512 001375          CLR      @RTCCSR       ;STOP CLOCK
3101 020514 005077 000022
3102
3103 020520 000207          10$:      RTS      PC
3104 020522 105237 020532    INCB     TIME
3105 020526 001375          BNE      10$
3106 020530 000207          RTS      PC
3107
3108 020532 000000          TIME:    .WORD  0
3109
3110 020534 005337 020532    CLKINT: DEC      TIME
3111 020540 000002          RTCCSR: .WORD  0          ;CLOCK CSR IF USED.
3112 020542 000000
3113
3114          ;
3115          ;*THIS MACRO ALLOWS THE OPERATOR TO TALK TO
3116          ;*ANY DEVICE ON THE I/O BUS
3117          ;*USER MUST START AT THIS ADDR.
3118          ;*HE MUST SAY EITHER "E" FOR EXAMINE, OR "D" FOR DEPOSIT.
3119          ;*"E" IS DEFAULT.
3120          ;*NEXT, HE MUST SUPPLY AN ADDR.
3121          ;*NOTE IF ADDR. IS NOT FOUND ON I/O BUS, A HALT
3122          ;*WILL OCCUR.
3123
3124 020544          $UTK:    CLR      .DVLS
3125 020544 005037 001464    21$:
3126 020550          TYPE     ,65$
3127 020550 104401 020556          ;;TYPE ASCIZ STRING

```

```

3128 020554 000405          BR      64$          ;;GET OVER THE ASCIZ
3129          ;;65$: .ASCIZ <200>#E OR D?#
3130 020570          64$:
3131 020570 105777 160350    1$:      TSTB      2$TKS
3132 020574 100375          BPL      1$
3133 020576 117737 160344 020720    MOVB     2$TKB,20$      ;GET INPUT
3134 020604 104401 020720    TYPE,    20$          ;ECHO NEXT MESSAGE.
3135 020610 142737 000240 020720    BICB     #240,20$     ;STRIP PARITY, LC
3136 020616 104407          RDOCT
3137 020620 012637 020716    MOV      (SP)+,14$    ;GET ADDR.
3138 020624 123727 020720 000104    CMPB     20$,#1'D    ;DEPOSIT?
3139 020632 001411          BEQ      10$
3140
3141 020634 004537 020266          JSR      R5,$INLP    ;GET DATA
3142 020640 020716          2$:      .WORD     14$
3143 020642 020654          .WORD     5$
3144
3145 020644 013746 020654          MOV      5$,-(SP)    ;;SAVE 5$ FOR TYPEOUT
3146 020650 104402          TYPOC
3147 020652 000736          BR       21$        ;;GO TYPE--OCTAL ASCII(ALL DIGITS)
3148 020654 000000          5$:      .WORD     0          ;LOOP.
3149
3150 020656          10$:
3151 020656 104401 020664          TYPE     67$
3152 020662 000404          BR       66$        ;;TYPE ASCIZ STRING
3153          ;;67$: .ASCIZ <200>#DATA= #
3154 020674          66$:
3155 020674 104407          RDOCT
3156 020676 012637 020714          MOV      (SP)+,13$
3157
3158 020702 004537 020170          11$:     JSR      R5,$OUTLP   ;OUTPUT ROUTINE.
3159 020706 020716          12$:     .WORD     14$   ;DEVICE ADDR.
3160 020710 020714          .WORD     13$
3161 020712 000716          BR       21$        ;DATA
3162
3163 020714 000000          13$:     .WORD     0
3164 020716 000000          14$:     .WORD     0
3165 020720 100001 042504 044526    20$:     .ASCIZ   <1><200>#DEVICE ADDR= #
3166 020726 042503 040440 042104
3167 020734 036522 000040
3168          .EVEN
3169
3170
3171
3172
3173          ; THIS ROUTINE LOOKS THROUGH CURENT .DVLS FOR A/D ADDR.
3174          ; IF UNFOUND, GENERATES IT. THIS ROUTINE'S WHOLE PURPOSE IS
3175          ; TO SET UP THE USER PROGRAM TO LINK TO FILE "DRLPX2" FOR
3176          ; SAMPLE TAKEING PURPOSES.
3177          ; TO TAKE SAMPLES, THE USER PROGRAM MUST SET UP
3178          ; A/D CSR IN BSEL 4 AND 5.
3179          ; (2) HE MUST CALL THIS ROUTINE:
3180          ;          JSR      R5,$PUTS      ;CALL SET UP ROUTINE.
3181          ;          .WORD   ADCSR        ;ADDR. OF A/D CSR.

```

```

3182
3183
3184
3185
3186
3187
3188
3189
3190
3191
3192 020740 012537 020750
3193 020744 004537 020266
3194 020750 000000
3195 020752 021046
3196 020754 113777 020330 160470
3197 020762 113777 020330 160464
3198 020770 013737 020750 021010
3199 020776 062737 000002 021010
3200 021004 004537 020266
3201 021010 000000
3202 021012 021046
3203 021014 113777 020330 160422
3204 021022 152777 000340 160422
3205 021030 152777 000300 160416
3206 021036 152777 000300 160400
3207 021044 000205
3208 021046 000000
3209

```

```

$PUTS:
1$:
2$:
10$:

```

```

MOV (5)+,1$
JSR R5,$INLP
.WORD 0
.WORD 10$
MOVB $OFS,$KMA06
MOVB $OFS,$KMA07
MOV 1$,$2$
ADD #2,$2$
JSR R5,$INLP
.WORD 0
.WORD 10$
MOVB $OFS,$KMA03
BISB #340,$KMA06
BISB #300,$KMA07
BISB #300,$KMA03
RTS R5
.WORD 0

```

```

;RETURNS HERE ;KMC BSEL 3,6,7 PERMINENTLY SET UP
;(UNTILL ONE DOES A RESET)

```

```

(3)THE USER MUST PUT CODE 006 INTO KMC REG 2 TO
START CONVERSION CAUTION*DO WITH MOV B INSTR.!
(4)MONITOR KMC REG 2 FOR CODE 377 (DRLPX2 IS DONE)
(5)READ KMC REG 4,5 FOR A/D RESULT.
(6) TO TAKE MORE SAMPLES, SIMPLY PUT A/D CSR INTO
BSEL 4,5 AND CODE 6 INTO BSEL 2.

```

```

;GET ADDR OF ADDR. OF A/D

```



```

3210 .SBTTL BINARY TO OCTAL (ASCII) AND TYPE
3211
3212 ;*****
3213 ;THIS ROUTINE IS USED TO CHANGE A 16-BIT BINARY NUMBER TO A 6-DIGIT
3214 ;OCTAL (ASCII) NUMBER AND TYPE IT.
3215 ;$TYPOS---ENTER HERE TO SETUP SUPPRESS ZEROS AND NUMBER OF DIGITS TO TYPE
3216 ;CALL:
3217 ; MOV NUM,-(SP) ;:NUMBER TO BE TYPED
3218 ; TYPOS ;:CALL FOR TYPEOUT
3219 ; .BYTE N ;:N=1 TO 6 FOR NUMBER OF DIGITS TO TYPE
3220 ; .BYTE M ;:M=1 OR 0
3221 ; ;:1=TYPE LEADING ZEROS
3222 ; ;:0=SUPPRESS LEADING ZEROS
3223 ;
3224 ;$TYPON---ENTER HERE TO TYPE OUT WITH THE SAME PARAMETERS AS THE LAST
3225 ;$TYPOS OR $TYPOC
3226 ;CALL:
3227 ; MOV NUM,-(SP) ;:NUMBER TO BE TYPED
3228 ; TYPON ;:CALL FOR TYPEOUT
3229 ;
3230 ;$TYPOC---ENTER HERE FOR TYPEOUT OF A 16 BIT NUMBER
3231 ;CALL:
3232 ; MOV NUM,-(SP) ;:NUMBER TO BE TYPED
3233 ; TYPOC ;:CALL FOR TYPEOUT
3234 ;
3235 021050 017646 000000 021273 $TYPOS: MOV 2(SP),-(SP) ;:PICKUP THE MODE
3236 021054 116637 000001 MOV 1(SP),SOFILL ;:LOAD ZERO FILL SWITCH
3237 021062 112537 021275 MOV (SP)+,SOMODE+1 ;:NUMBER OF DIGITS TO TYPE
3238 021066 062716 000002 ADD #2,(SP) ;:ADJUST RETURN ADDRESS
3239 021072 000406 BR $TYPON
3240 021074 112737 000001 021273 $TYPOC: MOV #1,SOFILL ;:SET THE ZERO FILL SWITCH
3241 021102 112737 000006 021275 MOV #6,SOMODE+1 ;:SET FOR SIX(6) DIGITS
3242 021110 112737 000005 021272 $TYPON: MOV #5,SOCNT ;:SET THE ITERATION COUNT
3243 021116 010346 MOV R3,-(SP) ;:SAVE R3
3244 021120 010446 MOV R4,-(SP) ;:SAVE R4
3245 021122 010546 MOV R5,-(SP) ;:SAVE R5
3246 021124 113704 021275 MOV #SOMODE+1,R4 ;:GET THE NUMBER OF DIGITS TO TYPE
3247 021130 005404 NEG R4
3248 021132 062704 000006 ADD #6,R4 ;:SUBTRACT IT FOR MAX. ALLOWED
3249 021136 110437 021274 MOV R4,SOMODE ;:SAVE IT FOR USE
3250 021142 113704 021273 MOV #SOFILL,R4 ;:GET THE ZERO FILL SWITCH
3251 021146 016605 000012 MOV 12(SP),R5 ;:PICKUP THE INPUT NUMBER
3252 021152 005003 CLR R3 ;:CLEAR THE OUTPUT WORD
3253 021154 006105 1$: ROL R5 ;:ROTATE MSB INTO "C"
3254 021156 000404 BR 3$ ;:GO DO MSB
3255 021160 006105 2$: ROL R5 ;:FORM THIS DIGIT
3256 021162 006105 ROL R5
3257 021164 006105 ROL R5
3258 021166 010503 MOV R5,R3
3259 021170 006103 3$: ROL R3 ;:GET LSB OF THIS DIGIT
3260 021172 105337 021274 DECB #SOMODE ;:TYPE THIS DIGIT?
3261 021176 100016 BPL 7$ ;:BR IF NO
3262 021200 042703 177770 BIC #177770,R3 ;:GET RID OF JUNK
3263 021204 001002 BNE 4$ ;:TEST FOR 0

```

3264	021206	005704		TST	R4	:: SUPPRESS THIS 0?
3265	021210	001403		BEQ	5\$:: BR IF YES
3266	021212	005204		4\$: INC	R4	:: DON'T SUPPRESS ANYMORE 0'S
3267	021214	052703	000060	BIS	#'0,R3	:: MAKE THIS DIGIT ASCII
3268	021220	052703	000040	5\$: BIS	#',R3	:: MAKE ASCII IF NOT ALREADY
3269	021224	110337	021270	MOVB	R3,8\$:: SAVE FOR TYPING
3270	021230	104401	021270	TYPE	8\$:: GO TYPE THIS DIGIT
3271	021234	105337	021272	7\$: DECB	\$OCNT	:: COUNT BY 1
3272	021240	003347		BGT	2\$:: BR IF MORE TO DO
3273	021242	002402		BLT	6\$:: BR IF DONE
3274	021244	005204		INC	R4	:: INSURE LAST DIGIT ISN'T A BLANK
3275	021246	000744		BR	2\$:: GO DO THE LAST DIGIT
3276	021250	012605		6\$: MOV	(SP)+,R5	:: RESTORE R5
3277	021252	012604		MOV	(SP)+,R4	:: RESTORE R4
3278	021254	012603		MOV	(SP)+,R3	:: RESTORE R3
3279	021256	016666	000002 000004	MOV	2(SP),4(SP)	:: SET THE STACK FOR RETURNING
3280	021264	012616		MOV	(SP)+,(SP)	
3281	021266	000002		RTI		:: RETURN
3282	021270	000		8\$: .BYTE	0	:: STORAGE FOR ASCII DIGIT
3283	021271	000		.BYTE	00	:: TERMINATOR FOR TYPE ROUTINE
3284	021272	000		\$OCNT: .BYTE	00	:: OCTAL DIGIT COUNTER
3285	021273	000		\$OFILL: .BYTE	00	:: ZERO FILL SWITCH
3286	021274	000000		\$OMODE: .WORD	0	:: NUMBER OF DIGITS TO TYPE

```

3287
3288
3289
3290
3291
3292
3293
3294
3295 021276 010046
3296 021300 016600 000002
3297 021304 005740
3298 021306 111000
3299 021310 006300
3300 021312 016000 021332
3301 021316 000200

```

.SBTTL TRAP DECODER

```

;*****
;THIS ROUTINE WILL PICKUP THE LOWER BYTE OF THE "TRAP" INSTRUCTION
;AND USE IT TO INDEX THROUGH THE TRAP TABLE FOR THE STARTING ADDRESS
;OF THE DESIRED ROUTINE. THEN USING THE ADDRESS OBTAINED IT WILL
;GO TO THAT ROUTINE.

```

```

$TRAP: MOV    RO,-(SP)      ;;SAVE RO
        MOV    2(SP),RO    ;;GET TRAP ADDRESS
        TST   -(RO)       ;;BACKUP BY 2
        MOVB  (RO),RO     ;;GET RIGHT BYTE OF TRAP
        ASL   RO          ;;POSITION FOR INDEXING
        MOV   $TRPAD(RO),RO ;;INDEX TO TABLE
        RTS   RO          ;;GO TO ROUTINE

```

;;THIS IS USE TO HANDLE THE "GETPRI" MACRO

```

3305 021320 011646
3307 021322 016666 000004 000002
3308 021330 000002

```

```

$TRAP2: MOV   (SP),-(SP)  ;;MOVE THE PC DOWN
        MOV   4(SP),2(SP) ;;MOVE THE PSW DOWN
        RTI                          ;;RESTORE THE PSW

```

.SBTTL TRAP TABLE

```

;THIS TABLE CONTAINS THE STARTING ADDRESSES OF THE ROUTINES CALLED
;BY THE "TRAP" INSTRUCTION.

```

```

3315
3316
3317 021332 021320
3318 021334 016042
3319 021336 021074
3320 021340 021050
3321 021342 021110
3322
3323
3324 021344 014640
3325 021346 014760
3326 021350 015132

```

```

;
; ROUTINE
;-----
$TRPAD: .WORD  $TRAP2
        $TYPE  ;;CALL=TYPE      TRAP+1(104401)  TTY TYPEOUT ROUTINE
        $TYPOC ;;CALL=TYPOC    TRAP+2(104402)  TYPE OCTAL NUMBER (WITH LEADING ZEROS)
        $TYPOS  ;;CALL=TYPOS   TRAP+3(104403)  TYPE OCTAL NUMBER (NO LEADING ZEROS)
        $TYPON  ;;CALL=TYPON   TRAP+4(104404)  TYPE OCTAL NUMBER (AS PER LAST CALL)

        $RDCHR ;;CALL=RDCHR    TRAP+5(104405)  TTY TYPEIN CHARACTER ROUTINE
        $RDLIN ;;CALL=RDLIN    TRAP+6(104406)  TTY TYPEIN STRING ROUTINE
        $RDOCT ;;CALL=RDOCT    TRAP+7(104407)  READ AN OCTAL NUMBER FROM TTY

```



```

3327          .SBTTL  POWER DOWN AND UP ROUTINES
3328
3329          ;:*****
3330          :POWER DOWN ROUTINE
3331 021352 012737 021516 000024 $PWRDN: MOV    $SILLUP, @PWRVEC ;:SET FOR FAST UP
3332 021360 012737 000340 000026      MOV    #340, @PWRVEC+2 ;:PRIO:7
3333 021366 010046      MOV    R0, -(SP) ;:PUSH R0 ON STACK
3334 021370 010146      MOV    R1, -(SP) ;:PUSH R1 ON STACK
3335 021372 010246      MOV    R2, -(SP) ;:PUSH R2 ON STACK
3336 021374 010346      MOV    R3, -(SP) ;:PUSH R3 ON STACK
3337 021376 010446      MOV    R4, -(SP) ;:PUSH R4 ON STACK
3338 021400 010546      MOV    R5, -(SP) ;:PUSH R5 ON STACK
3339 021402 017746 157532      MOV    @SWR, -(SP) ;:PUSH @SWR ON STACK
3340 021406 010637 021522      MOV    SP, $SAVR6 ;:SAVE SP
3341 021412 012737 021424 000024      MOV    $PWRUP, @PWRVEC ;:SET UP VECTOR
3342 021420 000000      HALT
3343 021422 000776      BR      -2 ;:HANG UP
3344
3345          ;:*****
3346          :POWER UP ROUTINE
3347 021424 012737 021516 000024 $PWRUP: MOV    $SILLUP, @PWRVEC ;:SET FOR FAST DOWN
3348 021432 013706 021522      MOV    $SAVR6, SP ;:GET SP
3349 021436 005037 021522      CLR    $SAVR6 ;:WAIT LOOP FOR THE TTY
3350 021442 005237 021522      1$: INC    $SAVR6 ;:WAIT FOR THE INC
3351 021446 001375      BNE    1$ ;:OF WORD
3352 021450 012677 157464      MOV    (SP)+, @SWR ;:POP STACK INTO @SWR
3353 021454 012605      MOV    (SP)+, R5 ;:POP STACK INTO R5
3354 021456 012604      MOV    (SP)+, R4 ;:POP STACK INTO R4
3355 021460 012603      MOV    (SP)+, R3 ;:POP STACK INTO R3
3356 021462 012602      MOV    (SP)+, R2 ;:POP STACK INTO R2
3357 021464 012601      MOV    (SP)+, R1 ;:POP STACK INTO R1
3358 021466 012600      MOV    (SP)+, R0 ;:POP STACK INTO R0
3359 021470 012737 021352 000024      MOV    $PWRDN, @PWRVEC ;:SET UP THE POWER DOWN VECTOR
3360 021476 012737 000340 000026      MOV    #340, @PWRVEC+2 ;:PRIO:7
3361 021504 104401      TYPE ;:REPORT THE POWER FAILURE
3362 021506 021524      $PWRMG: .WORD $POWER ;:POWER FAIL MESSAGE POINTER
3363 021510 012716      MOV    (PC)+, (SP) ;:RESTART AT BEG2
3364 021512 002404      $PWRAD: .WORD BEG2 ;:RESTART ADDRESS
3365 021514 000002      RTI
3366 021516 000000      $SILLUP: HALT ;:THE POWER UP SEQUENCE WAS STARTED
3367 021520 000776      BR      -2 ;:BEFORE THE POWER DOWN WAS COMPLETE
3368 021522 000000      $SAVR6: 0 ;:PUT THE SP HERE
3369 021524 005015 047520 042527 $POWER: .ASCIZ <15><12>"POWER"
3370 021532 000122      .EVEN
3371
3372      .EVEN
3373 021534 000310      DIST: .BLKW 200 ;:STATE-WIDTH DISTRIBUTION
3374 022354 010000      BUFFER: .BLKW 4096 ;:BUFFER AREA
3375
3376      .END

```


B10	011434	1788#	1789											
B11	011470	1798#	1799											
CH	012317	1022	1949#											
CHAN	012401	1645	1959#											
CHANL	001362	376#	947*	1163*	1248	1326	1618*	1619	1646	1676*	1677*	1678	1712*	2195
CHANNL	007042	1371#												
CH1	001350	371#	1148*	1149*	1152	1158*	1184	1190	1216	1222*	1224*			
CH2	001352	372#	1161*	1163	1176	1212	1223*	1225*						
CLEAR1	006770	1357#	1359											
CLEAR2	007034	1368#	1370											
CLKINT	020534	3110#												
CMSG	012244	445	1937#											
CNNO	006746	1307*	1311*	1325	1351#									
COMPAR	011314	773	816	827	839	852	864	880	904	916	927	938	964	1758#
CONV	006300	1258#	1275											
CONVR	007150	1387	1390#											
CONVRT	011072	771	814	825	837	850	862	877	890	914	925	936	1195	1710#
CR =	000015	50#	2549	2559										
CRLF =	000200	51#	2520	2559										
CD	013671	543	2091#											
C1	013674	1292	2092#											
C2	013676	1506	1596	2093#										
C3	013701	1593	2094#											
DAC	001404	385#	952	955	1215	1219	1315*	1319*	1322	1346*	1664	1667	1670	1673
DASH	012273	1452	1945#											
DAWAIT	004646	876	889	913	1007#									
DDISP =	177570	57#	248	504										
DECNT	014572	1457*	1486*	2188#										
DECTYP	011504	962	1174	1453	1459	1467	1471	1474	1485	1535	1639	1642	1806#	
DELAY	001406	386#	1322*	1323*										
DELAY1	007230	1396	1404#											
DELAY2	007236	1398	1405	1406#										
DELAY3	007144	1388#	1389											
DELCLR	010342	1495	1507	1597	1606#									
DF1	014636	335	342	348	354	2201#								
DH1	014413	333	2167#											
DH2	014451	352	2173#											
DH3	014534	340	346	2182#										
DIFLIN	006750	1004	1353#											
DISPLA	001142	248#	504*	512*	1051*	2377*	2398*							
DISPRE	000174	181#	512											
DIST	021534	1360	1428*	1498	3373#									
DON#	012416	1962#												
DRLPX2=	*****	14#	2660											
DSWR =	177570	56#	247	503										
DT1	014576	334	2193#											
DT2	014610	353	2195#											
DT3	014626	341	347	2198#										
DUMMY	001360	375#	948*	1212*	1216*	1328	1619*	1678*						
EDGE	001410	387#	949*	1230*	1233*	1256*	1273*	1276*	1277*	1278*	1279*	1283	1339	1684*
		1687*												
EDGFLG	006450	1171*	1238*	1289	1299#	1622*	1692*							
EDINT	001430	395#	1068*	1078*	1096*	1100*								
EMTVEC=	000030	145#	487*	488*										

G

OUTMSG 012642
PC =%000007

PEAK 001376
PERCNT 001422
PIRQ = 177772
PIRQVE= 000240
PLUS 007772
PLUSR2 011410
POPRO 001702
POS 011516
POSPEA 010466
POS RMS 010454
POSR2 005700
PRO = 000000
PR1 = 000040
PR2 = 000100
PR3 = 000140
PR4 = 000200
PR5 = 000240
PR6 = 000300
PR7 = 000340
PS = 177776
PSW = 177776
PWAVEC= 000024
QUEST 012235
RANDY 011010
RBEG 001730
RDCHR = 104405
RDLIN = 104406
RDOCT = 104407
RD1 020070
READ 007276
RELACC 007736
REST1 002336
RESVEC= 000010
RET 006446
RETErr 003434
RETURN 001704
RMS 001374
RNA 001366
RNB 001370
RNC 001372
RST 011362
RTCCSR 020542
RO =%000000

1475	1991#												
69#	447*	453*	544*	547*	550*	559*	566*	620*	631*	639*	651*	659*	
667*	674*	696*	718*	740*	747*	760*	799*	876*	889*	911*	913*	962*	
1004*	1005*	1010*	1069*	1070*	1071*	1080*	1081*	1088*	1089*	1098*	1099*	1101*	
1138*	1164*	1166*	1167*	1172*	1174*	1182*	1205*	1207*	1220*	1226*	1229*	1239*	
1280*	1298*	1384*	1453*	1459*	1467*	1471*	1474*	1485*	1495*	1501*	1503*	1507*	
1535*	1592*	1595*	1597*	1598*	1602*	1605*	1616*	1620*	1621*	1623*	1639*	1642*	
1644*	1656*	1658*	1674*	1679*	1683*	1693*	1708*	1771*	1774*	1801*	1833*	1848*	
1899*	1902*	1909*	1914	2408*	2414*	2470*	2508*	2527*	2534*	2541*	2555*	2557*	
2590*	2607*	2724*	2726*	2728*	2775*	2838*	2842*	2847*	2851*	2870*	2873*	2890*	
2907*	2938*	3076*	3103*	3106*	3363								
382#	1635	1637*	1641	1653	1661*	1670*	1673*	1681*	1690*	1691*			
392#	1304*	1305*	1306*	1312*	1313*	1344							
55#													
149#													
1520	1522#												
1780	1782#												
468#	471												
1807	1810#												
1636	1638#												
1633	1635#												
1168	1170#												
72#													
73#													
74#													
75#													
76#													
77#													
78#													
79#													
52#		53	3098*										
53#		575*	1017*	1773*									
53#		491*	492*	3331*	3332*	3341*	3347*	3359*	3360*				
144#		596	1934#										
470		1699#											
1384		476#											
474		2254											
2254		3324#											
573		2295	3325#										
466		1157	1160	3136	3155	3326#							
2874*		2911#											
1418#		1456											
1494		1511#											
540		558#											
139#													
1290		1298#											
758		760#											
469#		1382	1723										
381#		1632	1634*	1638	1651	1660*	1664*	1667*	1680*	1688*	1689*		
378#		563*	1385	1699*	1700*	1701*	1702	1705					
379#		564*	1699	1702*	1703*	1704*	1706						
380#		565*	1700	1703	1705*	1706*	1707*						
447		453	1771#										
3094		3097*	3101*	3112#									
60#		440	441*	442*	443	449	455	468*	526	528*	531*	532	537*

SW4 =	000020	103#																
SW5 =	000040	102#																
SW6 =	000100	101#																
SW7 =	000200	100#																
SW8 =	000400	99#																
SW9 =	001000	98#																
TADDR	001364	377#	454	1012*	1067*	1077*	1095*	1145*	1155*									
TBITVE=	000014	140#																
TEMP	001346	370#	879	894	900	901	902*	952*	955*	956*	959*	961	977*	993*				
		1150*	1162*	1197	1248	1407*	1412*	1714*	1746*	1751*	1752*	1753*	1754*	1760				
TENS	014573	1814*	1823*	1824	1826*	1830*	2189#											
TEST	003410	696	718	740	754#	799												
TESTAD	002552	598#	1069	1080	1098													
TESTIT	003400	639	651	659	667	674	751#											
TESTR2	011544	1816#	1821	1825	1828													
TIME	020532	3096*	3099	3104*	3108#	3110*												
TKVEC =	000060	147#																
TOMSG	012433	1159	1965#															
TPVEC =	000064	148#																
TRAPVE=	000034	146#	489*	490*														
TRTVEC=	000014	141#																
TRY	006544	1318#	1348															
TRYAG	002426	573#	597															
TST1	002710	636#	637															
TST10	003252	723#																
TST11	003436	766#																
TST12	003510	784#																
TST13	003636	787	812#															
TST14	003666	823#																
TST15	003716	835#																
TST16	003746	848#																
TST17	003776	769	770	860#														
TST2	002754	648#																
TST20	004026	871#																
TST21	004274	923#																
TST22	004324	934#																
TST23	004354	945#																
TST24	004524	970	975#															
TST25	004564	988#																
TST26	004622	1000#																
TST3	003000	657#																
TST4	003016	665#																
TST5	003034	672#																
TST6	003052	679#																
TST7	003166	705#																
TST8	010230	1580	1582#															
TYPBAD	007372	1426	1436	1440#														
TYPE =	104401	445	451	459	465	470	543	557	572	596	617	960	963	969				
		971	1016	1022	1053	1064	1082	1087	1146	1147	1156	1159	1175	1181				
		1183	1189	1204	1206	1292	1353	1443	1452	1454	1460	1463	1465	1468				
		1472	1475	1478	1480	1487	1490	1492	1496	1497	1506	1513	1536	1542				
		1551	1553	1589	1590	1593	1596	1615	1631	1640	1643	1645	1655	1657				
		1808	1832	1905	2258	2261	2265	2401	2409	2441	2458	2460	2463	2465				
		2469	2476	2523	2651	2705	2709	2713	2941	3127	3134	3151	3270	3318#				

TYPEDG	006406	3361																			
TYPC =	104402	1182	1283#	1644																	
TYPON =	104404	2449	2473	3146	3319#																
TYPOS =	104403	3321#																			
		462	614	1025	1056	1084	1178	1186	1199	1286	1295	1449	1539	1546							
		1648	3320#																		
TYPOUT	011620	1817	1829#																		
TYPRP	010436	1623	1631#	1693																	
TYPSET	005716	1172	1174#	1239																	
UNEXP	001526	433#	649																		
VADR	001326	362#	609	1108	1109																
VARL T1	011754	1842	1865#																		
VARL T2	011764	1844	1870#																		
VARL T3	011774	1838	1875#																		
VECTOR	001456	424#	649*	1110*	1119*	1120*	1123	1382*	1723*												
VECTPS	001460	425#																			
VECTR1	001324	361#	1111*	1112*	1123*	1124*	1125*														
VERSN	001462	427#																			
VLIN	011750	1549	1862#																		
VNP	011744	1653	1860#																		
VNR	011742	1651	1835	1859#																	
VSET	011746	1202	1861#																		
VTFLG	002656	544	547	550	622#																
VTINIT	014021	1615	2117#																		
VT55	002326	552	555#																		
VVCT	001330	363#	1110	1111																	
V1	011720	818	1849#																		
V10	011724	829	1851#																		
V115	011732	882	1854#																		
V144	011730	929	1853#																		
V2	011722	918	1850#																		
V240	011734	854	866	940	1855#																
V5	011736	906	1856#																		
V50	011726	775	841	1852#																	
V500	011740	966	1857#																		
WFADJ	011650	566	1835#																		
WFTST	001416	390#	473*	475*	1840																
WIDE	001336	366#	1364*	1439*	1469	1482															
WIDMSG	012603	1472	1985#																		
WRAP	003436	762#	1088	1099																	
W1	017720	2839#	2854#																		
W2	017722	2843#	2855#																		
W3	017724	2848#	2856#																		
WAERR	017450	605	2658*	2751*	2777*	3067															
WAPTHD	001000	207	213#																		
WASTAT=	***** U	2593	2608																		
WATYC	016350	2564	2566#																		
WATY1	016324	2562#																			
WATY3	016332	2508	2563#																		
WATY4	016342	2414	2565#																		
WAUTOB	001134	244#																			
WBASE	001250	308#	598	1116	1117																
WBDADR	001122	239#																			
WBDDAT	001126	241#	598*	605	609*	757	1735*	1760*	1761	2193	2195	2198									

\$SCOPE 015234	485	2332#																			
\$SETUP= 000037	328#	484	485	487	489	491	493	494	495	497	1895	2211	2280								
\$STUP = 177777	2333	2396	2421	2428																	
\$SVLAD 015440	328#																				
\$SVPC = 000214	2342	2371#																			
\$SWR = 167400	191#	196	154	155	156	157	158	159	160	161	257	258	259								
	30#	40	497	498	637	649	658	666	673	680	706	724	767								
	494	495	824	836	849	861	872	924	935	946	976	989	1001								
	785	813	1908	1914	1916	2324	2325	2326	2327	2328	2333	2345	2347								
	1888	1896	2352	2353	2360	2361	2362	2374	2377	2380	2387	2388	2389								
	2348	2351	2399	2406	2418	2421	2433	3365													
	2390	2391																			
\$SWREG 001216	280#	518																			
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\$TBF4 007150	1391#																				
\$TEMP1 001432	396#	1328*	1329*	1331	1379*	1380*	1391														
\$TEMP2 001434	397#	1326*	1327*	1335																	
\$TESTN 001200	271#	2372*																			
\$TIMES 001160	257#	494*	724*	767*	785*	813*	824*	836*	849*	861*	872*	924*	935*								
	946*	976*	989*	1001*	1896*	2360*	2367	2370*	2380												
\$TKB 001146	250#	441	629	2209	2226	2232	3133														
\$TKs 001144	249#	542*	574*	623	912*	1772*	1880*	2209	2224	2230	3131										
\$TLKR 017726	2867#	3019																			
\$TLKW 017612	2835#	2977																			
\$TN = 000027	30#	40	633	637#	645	649#	654	658#	662	666#	669	673#	676								
	680#	702	706#	720	724#	763	767#	768	781	785#	787	809#	813#								
	820	824#	832	836#	845	849#	857	861#	868	872#	920	924#	931								
	935#	942	946#	970	972	976#	985	989#	997	1001#											
\$TOUT 020362	2681	2735	2762	2876	2893	3042#															
\$TPB 001152	252#	1790*	1800*	2548*	2559																
\$TPFLG 001157	256#	2497	2559																		
\$TPS 001150	251#	1788	1798	2546	2559																
\$TRAP 021276	489	3295#																			
\$TRAP2 021320	3306#	3317																			
\$TRP = 000010	3310#	3319#	3320#	3321#	3322#	3324	3325#	3326#	3327#												
\$TRPAD 021332	3300	3317#																			
\$STSM 001004	216#																				
\$STSTM 001102	229#	768*	1895*	2323	2349	2371*	2372	2377	2381	2398	2433										
\$TTYIN 015066	2251	2252	2269	2273#																	
\$TYPBN= *****	3322																				
\$TYPDS= *****	3322																				
\$TYPE 016042	2497#	2590	3310	3318																	
\$TYPEC 016254	2527	2534	2541	2546#	2547																
\$TYPEX 016322	2552	2554	2557#																		
\$TYPOC 021074	3240#	3319																			
\$TYPON 021110	3239	3242#	3321																		
\$TYPOS 021050	3235#	3320																			
\$T6MP 006634	1331#																				
\$UNIT 001206	274#																				
\$UNITM 001010	218#																				
\$USWR 001220	281#																				
\$UTK 020544	3124#																				
\$VECT1 001244	306#	1119	1121																		
\$VECT2 001246	307#																				

U

ADC	1279	1422	1533	1562	1689	1691	1701	1704	1707	1754					
ADD	609	759	955	1108	1109	1110	1111	1115	1118	1124	1215	1224	1225	1273	1291
	1319	1372	1470	1482	1483	1517	1563	1664	1670	1677	1699	1700	1702	1703	1705
ASL	1706	1711	1746	2308	2455	2516	2576	2588	2600	3049	3069	3199	3238	3248	
ASR	958	1305	1306	1312	1313	1399	1427	2301	2303	2305	2452	2453	2454	3299	
	1276	1277	1278	1347	1419	1420	1421	1429	1446	1527	1530	1531	1532	1559	1560
BEG	1561	1688	1690	1751	1752	1753	1751	1792	1793	1794	1795	2583			
	458	517	552	570	1003	1050	1107	1131	1290	1387	1396	1398	1405	1462	1477
	1494	1555	1613	1817	1837	1907	2300	2348	2350	2352	2356	2365	2397	2400	2422
	2425	2457	2462	2475	2506	2519	2554	2570	2574	2594	2596	2675	2700	2721	2749
	2806	2926	2932	2968	2970	3010	3013	3043	3139	3265					
BGE	1340	1431	1577	2368											
BGT	1203	1345	1652	1654	1767	1901	2240	3272							
BHI	2354														
BIC	442	542	630	693	1019	1120	1149	1386	1786	1796	1898	2227	2233	2241	2307
	3262														
BICB	577	3135													
BIS	574	912	1327	1329	1380	1728	1772	1787	1797	1880	2662	2792	2794	2802	2837
	2869	3097	3267	3268											
BISB	1829	1830	1831	2444	3204	3205	3206								
BIT	1020	1049	1308	1612	2333	2347	2355	2362	2399	2406	2421	2674	2730	2887	2904
	2931														
BITB	516	2505	2510	2542	2573										
BLE	1438	1489	1523	1550	1580	1811									
BLOS	2253														
BLT	895	1424	1783	2238	2533	3273									
BMI	624	1408	3048												
BNE	444	450	456	482	506	534	540	546	549	554	579	582	585	588	591
	594	606	626	643	689	739	758	787	798	893	899	910	1009	1021	1061
	1063	1137	1232	1255	1275	1309	1324	1334	1338	1343	1348	1359	1370	1389	1394
	1410	1415	1434	1441	1456	1505	1529	1566	1583	1588	1604	1609	1611	1686	1722
	1733	1750	1821	1825	1841	2229	2235	2257	2263	2334	2363	2407	2412	2429	2445
	2467	2504	2511	2513	2521	2529	2543	2550	2572	2578	2581	2598	2669	2672	2693
	2695	2702	2704	2731	2747	2774	2798	2804	2888	2905	2928	2934	3068	3100	3105
	3263	3351													
BPL	714	732	1045	1168	1235	1269	1401	1520	1633	1636	1743	1764	1780	1789	1799
	1807	1847	2225	2231	2419	2498	2547	2818	3095	3132	3261				
BR	471	474	508	597	610	628	967	968	970	1052	1065	1072	1090	1102	1114
	1141	1153	1173	1310	1403	1411	1413	1426	1436	1464	1479	1491	1552	1828	1839
	1843	2236	2259	2309	2336	2342	2345	2358	2361	2417	2450	2477	2500	2526	2536
	2545	2552	2564	2586	2636	2652	2655	2689	2706	2710	2714	2743	2770	2809	2821
	2884	2901	2929	2935	2942	2947	2972	2987	3015	3032	3046	3074	3128	3147	3152
	3161	3239	3254	3275	3343	3367									
CLR	437	473	480	494	495	515	535	538	575	599	600	601	622	709	804
	873	884	1007	1013	1017	1068	1078	1079	1096	1097	1100	1112	1125	1133	1165
	1227	1256	1315	1318	1354	1357	1362	1363	1364	1365	1366	1367	1368	1412	1502
	1511	1512	1606	1634	1637	1660	1661	1680	1681	1714	1731	1773	1781	1895	1896
	2297	2298	2360	2375	2443	2658	2667	2753	2790	2791	2795	2799	2807	2924	2966
	2984	3008	3029	3045	3098	3101	3125	3252	3349						
CLRB	1122	1813	1814	1815	1822	1826	2264	2359	2525	2551	2602	2603	2604		
CMP	457	481	505	533	545	548	551	553	642	757	894	898	909	1130	1136
	1139	1140	1202	1289	1339	1344	1397	1404	1423	1430	1437	1488	1522	1528	1549
	1576	1579	1586	1651	1653	1766	1782	1810	2228	2234	2237	2239	2252	2343	2367
	2428	2656	2720	2797	2803	2805	2969	3012	3042						

CMPB	443	449	455	578	581	584	587	590	593	1333	1337	1393	1820	1824	2256
	2262	2349	2353	2411	2503	2518	2520	2528	2549	2553	2571	2692	2694	2699	2746
DEC	618	625	688	738	797	1060	1062	1113	1231	1254	1274	1323	1342	1358	1369
	1388	1409	1414	1455	1504	1565	1582	1587	1603	1608	1610	1685	1721	1749	1818
DECb	1899	2451	3110												
EMT	1008	2532	2535	3260	3271										
HALT	44														
INC	180	1614	2420	2430	2499	2819	2946	3342	3366						
	435	531	555	608	684	896	908	1038	1230	1262	1341	1400	1406	1407	1425
	1428	1432	1435	1439	1442	1543	1684	1897	2366	2402	2601	2650	2668	2701	2703
	2751	2796	2808	2927	2933	2971	3014	3047	3266	3274	3350				
INCB	1732	1819	1823	1827	2371	2396	2555	2671	2722	2817	3104				
IOT	45														
JMP	184	185	186	448	454	571	580	583	586	589	592	595	1074	1092	1104
	1881	1914													
JSR	447	453	544	547	550	559	566	605	639	651	659	667	674	684	687
	693	696	700	709	713	717	718	728	731	737	740	745	754	757	771
	773	791	796	799	803	807	814	816	825	827	837	839	850	852	862
	864	876	877	880	889	890	904	911	913	914	916	925	927	936	938
	950	953	962	964	978	980	982	990	994	1004	1016	1033	1038	1041	1044
	1048	1069	1070	1071	1080	1081	1088	1089	1098	1099	1101	1151	1164	1166	1167
	1172	1174	1182	1195	1213	1217	1226	1229	1239	1248	1253	1262	1265	1268	1273
	1316	1322	1374	1379	1384	1453	1459	1467	1471	1474	1485	1495	1501	1503	1507
	1535	1592	1595	1597	1602	1620	1621	1623	1639	1642	1644	1662	1665	1668	1671
	1679	1683	1693	1717	1720	1728	1731	1742	1746	1771	1909	2408	2414	2508	2527
	2534	2541	2590	2659	2681	2724	2726	2728	2735	2762	2838	2842	2847	2851	2870
	2873	2876	2890	2893	2907	2977	2985	3019	3030	3067	3141	3158	3193	3200	
MOV	434	440	441	446	452	460	467	468	475	479	483	485	486	487	488
	489	490	491	492	493	497	498	501	502	503	504	509	511	512	513
	518	526	527	528	529	532	536	537	560	561	562	563	564	565	568
	576	598	612	619	629	636	637	638	649	650	658	666	673	680	687
	700	706	717	724	725	733	734	737	745	767	768	769	770	785	788
	791	792	793	803	813	824	836	849	861	872	879	885	886	897	900
	901	903	924	935	946	947	948	949	952	957	961	976	977	989	993
	1001	1012	1018	1023	1030	1033	1048	1051	1054	1059	1067	1073	1077	1083	1091
	1095	1103	1116	1117	1119	1123	1126	1128	1129	1132	1134	1145	1148	1150	1155
	1158	1161	1162	1163	1170	1171	1176	1184	1190	1192	1197	1212	1216	1222	1223
	1228	1237	1238	1250	1253	1257	1283	1284	1293	1304	1307	1311	1314	1322	1325
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DRLPK.P11

CROSS REFERENCE TABLE

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ERRORS DETECTED: 0

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N09

SEQ 0118

*DRLPK,DRLPK/SOL/CRF=DRLPA.MAC,DRLPK
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