

LSI

MACRO INSTRUCTION EXERCISER
MD-11-DVKAA-A

EP-DVKAA-A-DL-A

OCT 1976

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FICHE 1 OF 1

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761 CHECK MODE 6 USING THE MOVB AND MOV INSTRUCTIONS
762 CHECK MODE 7 USING THE MOVB AND MOV INSTRUCTIONS
CHECK BYTE INSTRUCTIONS, NOT DESTINATION MODE 0
763 NEW INSTRUCTIONS USED IN THIS SECTION ARE TSTB, CLRB, MOVB
764 NEW INSTRUCTIONS USED IN THIS SECTION ARE CMPB, ORB, ANDB
765 NEW INSTRUCTIONS USED IN THIS SECTION ARE BICB, EORB, XORB
766 NEW INSTRUCTIONS USED IN THIS SECTION ARE INCB, DECB
767 NEW INSTRUCTION IN THIS SECTION IS COMB
768 NEW INSTRUCTION IN THIS SECTION IS NEG
769 NEW INSTRUCTION IN THIS SECTION IS ROL
770 NEW INSTRUCTION IN THIS SECTION IS ROR
771 NEW INSTRUCTION IN THIS SECTION IS ASL
772 NEW INSTRUCTION IN THIS SECTION IS ASR
773 NEW INSTRUCTION IN THIS SECTION IS RCL
774 NEW INSTRUCTION IN THIS SECTION IS RCR
775 NEW INSTRUCTION IN THIS SECTION IS SBCB
776 NEW INSTRUCTION IN THIS SECTION IS SCB
CHECK WORD INSTRUCTIONS, NOT DESTINATION MODE 0
777 NEW INSTRUCTIONS USED IN THIS SECTION ARE TST, CLRW, MOV
778 NEW INSTRUCTIONS USED IN THIS SECTION ARE CMP, ORW, ANDW
779 NEW INSTRUCTIONS USED IN THIS SECTION ARE BIC, EORW, XORW
780 NEW INSTRUCTIONS USED IN THIS SECTION ARE INC, DEC
781 NEW INSTRUCTION IN THIS SECTION IS COM
782 NEW INSTRUCTION IN THIS SECTION IS NEG
783 NEW INSTRUCTION IN THIS SECTION IS ROL
784 NEW INSTRUCTION IN THIS SECTION IS ROR
785 NEW INSTRUCTION IN THIS SECTION IS ASL
786 NEW INSTRUCTION IN THIS SECTION IS ASR
787 NEW INSTRUCTION IN THIS SECTION IS RCL
788 NEW INSTRUCTION IN THIS SECTION IS RCR
789 NEW INSTRUCTION IN THIS SECTION IS SBC
790 NEW INSTRUCTION IN THIS SECTION IS SCW
791 NEW INSTRUCTION IN THIS SECTION IS XOR
792 NEW INSTRUCTION IN THIS SECTION IS ROL
793 NEW INSTRUCTION IN THIS SECTION IS ROR
794 NEW INSTRUCTION IN THIS SECTION IS SUB
795 NEW INSTRUCTION IN THIS SECTION IS SOB
796 NEW INSTRUCTIONS IN THIS SECTION ARE MTPS & MFPD
797 BYTE INSTRUCTIONS REQUIRING WORD INST. TO CHECK
798
799 END OF PASS ROUTINE
800 POWER FAIL ROUTINE
801 TYPE ROUTINE
802 ROUTINES TO CHECK CONDITION CODES

EO1

DVKAAA MACY11 27(732) 25-AUG-76 13:25 PAGE 54-1
DVKAAA.P11

*** SEQ 0004

4890


```

5034      000430      .=$APTHD
5035      000430      ADR:
5036      000432      .=ADR+2
5037      000432      ADR1:
5038      000434      .=ADR1+2
5039      000434      ADR2:
5040      000436      .=ADR2+2
5041      000436      DUMMY:
5042      000440      .=DUMMY+2
5043      000440      TEMP:
5044      000442      .=TEMP+2
5045      000442      TEMP1:
5046      000444      .=TEMP1+2
5047      000444      TEMP2:
5048      000446      .=TEMP2+2
5049      000446      TPS: 177564      ; OUTPUT TTY STATUS REGISTER
5050      000450      177566      TPB: 177566      ; OUTPUT BUFFER
5051      000452      006402      MARK2: MARK 2
5052      000454      005015      042440 042116 ENOPAS: .ASCIZ <15><12>" END PASS"
5053      000462      051501 000123
5054      000470      005015      047520 042527 POWER: .ASCIZ <15><12> /POWER/
5055      000476      000122
5056      000500      000023      .EVEN
5057      .BLKW 19.
5058      000020      .=20
5059      000020      017020      TYPE
5060      000022      000000      0
5061
5062
5063

```

```

5068
5069                                     :STARTING OF THE PROGRAM
5070                                     :-----
5071
5072
5073                                     .=200
5074 000200 012737 016770 000024      MOV    #PWRDN,2#24      ; SERVICE POWER DOWN ROUTINE ON ANY FUTURE POWER DOWN
5075 000206 012700 000420              MOV    #SETABLE,R0
5076 000212 005040                    2$: CLR    -(R0)           ; START CLEANING THE STACK
5077 000214 020027 000400              CMP    R0,#SMAIL      ; FOR INITIALIZATION
5078 000220 101374                    BHI   2$
5079 000222 000167 000302              JMP    START
5080                                     ;
5081
5082                                     .=530
5083                                     .
5084                                     .
5085 000530 012706 000530      START: MOV    #START,SP      ; SET THE STACK POINTER
5086 000534 012705 000404      MOV    #STESTN,R5     ; PLACE THE ADDRESS OF LOCATION STESTN IN R5
5087 000540 005715              TST    (R5)           ; CHECK THE SEQUENCE COUNTER
5088 000542 001401              BEQ    NOBIT          ; IF THIS IS THE STARTING OF THE TEST THEN
5089                                     ; GO TO NOBIT TEST
5090 000544 000000              HALT                 ; OTHERWISE HALT AND WAIT FOR THE OPERATOR
5091                                     ; TO START AT THE PROPER TEST NUMBER
5092

```

K01

DVKAAA MACY11 27(732)
 DVKAAA.P11 TO

25-AUG-76 13:25 PAGE 54-7
 CHECK BRANCH INSTRUCTIONS WITH ZERO CONDITION CODES

*** SEQ 0010

```

5093
(2)
(3)
5094
5095 000546 021527 000000
(2) 000546 001017
5096 000552 005215
5097 000554 000257
5098 000556 103414
5099 000560 102413
5100 000562 001412
5101 000564 100411
5102 000566 000260
5103 000570 103407
5104 000572 102406
5105 000574 001405
5106 000576 100404
5107 000600 002403
5108 000602 003402
5109 000604 101401
5110 000606 101004
5111 000610 012745 000001
(2) 000612 005245
(2) 000616 000000
(2) 000620 102000
5113 000622
5114
5115
5116
(2)
(3)
5117
5118 000624 021527 000001
(2) 000624 001012
5119 000630 005215
5120 000632 000270
5121 000634 100007
5122 000636 001406
5123 000640 002005
5124 000642 003004
5125 000644 103403
5126 000646 101402
5127 000650 103401
5128 000652 003404
5129 000654
5130 000656 012745 000002
(2) 000656 005245
(2) 000662 000000
(2) 000664 001000
5131 000666
  
```

```

*****
*TEST: 0 CHECK BRANCH INSTRUCTIONS WITH ZERO CONDITION CODES
*****
  
```

```

NOBIT:
      CMP      (RS),#0
      BNE     CC0      ; IF IN WRONG SEQUENCE GO TO HALT AT END OF THE TEST
IS:   INC     (RS)
      CCC
      BCS     CC0      ; ZERO CONDITION CODES, NZVC=0000
      BVS     CC0
      BEQ     CC0
      BMI     CC0
      NOP1
      BCS     CC0      ; CHECK NOP1 INSTRUCTION I.E. OP-CODE 260
      BVS     CC0
      BEQ     CC0
      BMI     CC0
      BLS     CC0
      BLOS    CC0
      BHI     ENDCC0
CC0:  MOV     #1, -(RS)
      INC     -(RS)
      HALT
ENDCC0: BVC     NBIT      ; ONE OF THE ABOVE BRANCHES FAILED OR WRONG SEQUENCE
  
```

```

*****
*TEST: 1 CHECK BRANCH INSTRUCTIONS WITH N BIT SET
*****
  
```

```

NBIT:
      CMP      (RS),#1
      BNE     CC1      ; IF IN WRONG SEQUENCE GO TO HALT AT THE END OF THE TEST
IS:   INC     (RS)
      SEN
      BPL     CC1      ; NBIT IS SET, NZVC=1000
      BEQ     CC1
      BGE     CC1
      BGT     CC1
      BCS     CC1
      BLOS    CC1
      BLO     CC1
      BLE     ENDCC1
CC1:  MOV     #2, -(RS)
      INC     -(RS)
      HALT
ENDCC1: BNE     VBIT      ; ONE OF THE ABOVE BRANCHES FAILED OR WRONG SEQUENCE
  
```

L01

DVKAAA MACY11 27(732)
DVKAAA.P11 T2

25-AUG-76 13:25 PAGE 54-8
CHECK BRANCH INSTRUCTIONS WITH N&V BITS SET

*** SEQ 0011

```

S132
(2)
(3)
S133
S134 000670
(2) 000670 021527 000002
S135 000674 001014
S136 000676 005215
S137 000700 000270
S138 000702 000262
S139 000704 102010
S140 000706 001407
S141 000710 100006
S142 000712 103405
S143 000714 002404
S144 000716 003403
S145 000720 101402
S146 000722 103401
S147 000724 003004
S148 000726
(2) 000726 012745 000003
(2) 000732 005245
(2) 000734 000000
S149 000736 002000
S150
S151
S152
(2)
(3)
S153
S154 000740
(2) 000740 021527 000003
S155 000744 001013
S156 000746 005215
S157 000750 000270
S158 000752 000262
S159 000754 000261
S160 000756 001406
S161 000760 100005
S162 000762 102004
S163 000764 002403
S164 000766 003402
S165 000770 101001
S166 000772 002004
S167 000774
(2) 000774 012745 000004
(2) 001000 005245
(2) 001002 000000
S168

```

```

:*****
;*TEST: 2 CHECK BRANCH INSTRUCTIONS WITH N&V BITS SET
:*****

VBIT:
      CMP      (R5),#2
      BNE     CC2      ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
IS:   INC     (R5)
      SEN
      SEV
      BVC     CC2      ; V AND N BIT SET, NZVC = 1010
      BEQ     CC2
      BPL     CC2
      BCS     CC2
      BLT     CC2
      BLE     CC2
      BLOS    CC2
      BLO     CC2
      BGT     ENDCC2
CC2:  MOV     #3, -(R5)
      INC     -(R5)
      HALT
ENDCC2: BGE     CBIT
      ; ONE OF THE ABOVE BRANCHES FAILED OR WRONG SEQUENCE

:*****
;*TEST: 3 CHECK BRANCH INSTRUCTIONS WITH N,V&C BITS SET
:*****

CBIT:
      CMP      (R5),#3
      BNE     CC3      ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
IS:   INC     (R5)
      SEN
      SEV
      SEC
      BEQ     CC3      ; C, V, AND N BITS ARE SET, NZVC=1011
      BPL     CC3
      BVC     CC3
      BLT     CC3
      BLE     CC3
      BHI     CC3
      BGE     ZBIT
CC3:  MOV     #4, -(R5)
      INC     -(R5)
      HALT
      ; ONE OF THE ABOVE BRANCHES FAILED
      ; OR WRONG SEQUENCE

```

MO1

DVKAAA MACY11 27(732)
DVKAAA.P11 T4

25-AUG-76 13:25 PAGE 54-9
CHECK BRANCH INSTRUCTIONS WITH N,Z,V&C BITS SET

*** SEQ 0012

```

S169
(2)
(3)
S170
S171 001004
(2) 001004 021527 000004
S172 001010 001015
S173 001012 005215
S174 001014 000270
S175 001016 000262
S176 001020 000261
S177 001022 000264
S178 001024 001007
S179 001026 100006
S180 001030 102005
S181 001032 103004
S182 001034 002403
S183 001036 003002
S184 001040 101001
S185 001042 001404
S186 001044
(2) CC1044 012745 000005
(2) 001050 005245
(2) 001052 000000

```

```

;*****
;TEST: 4 CHECK BRANCH INSTRUCTIONS WITH N,Z,V&C BITS SET
;*****

```

```

ZBIT:
CMP (R5),#4
BNE CC4 ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
INC (R5)
SEN
SEV
SEC
SEZ ; ALL BITS SET, NZVC=1111
BNE CC4
BPL CC4
BVC CC4
BCC CC4
BLT CC4
BGT CC4
BHI CC4
BEQ YESCC

CC4:
MOV #5, -(R5)
INC -(R5)
HALT ; ONE OF THE ABOVE BRANCHES FAILED
; OR WRONG SEQUENCE

```

```

S187
S188
S189
S190
(2)
(3)
S191
S192 001054
(2) 001054 021527 000005
S193 001060 001014
S194 001062 005215
S195 001064 000277
S196 001066 100011
S197 001070 001010
S198 001072 102007
S199 001074 103006
S200 CC1076 000240
S201 001100 100004
S202 001102 001003
S203 001104 102002
S204 001106 103001
S205 CC1110 101404
S206 001112
(2) 001112 012745 000006
(2) 001116 005245
(2) 001120 000000

```

```

;*****
;TEST: 5 CHECK BRANCH INSTRUCTIONS WITH ALL THE CONDITION CODES SET
;*****

```

```

YESCC:
CMP (R5),#5
BNE CC6 ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
INC (R5) ; NZVC=1111
SCC ; NZVC=1111
BPL CC6
BNE CC6
BVC CC6
BCC CC6
NOP ; CHECK NOP INSTRUCTION
BPL CC6
BNE CC6
BVC CC6
BCC CC6
BLOS NOTCC

CC6:
MOV #6, -(R5)
INC -(R5)
HALT ; SCC OR A BRANCH FAILED, OR WRONG SEQUENCE

```

5207
(2)
(3)
5208
5209
(2)
5210
5211
5212
5213
5214
5215
5216
5217
5218
5219
5220
5221
5222
(2)
(2)
(2)
5223
5224
5225
5226
(2)
(3)
5228
5229
(2)
5230
5231
(2)
(2)
5232
5233
5234
(2)
(2)
5235
5236
(2)
(2)
5237
5238
(2)
(2)
5239
5240
(2)
(2)
5241

001122 021527 000006
001122 001013
001126 005215
001130 000277
001132 000241
001134 103407
001136 000242
001140 102405
001142 000244
001144 001403
001146 000250
001150 100401
001152 101004
001156
001155 012745 000007
001162 005245
001164 000000
001166 100000

001170 021527 000007
001170 001404
001174 012745 000010
001176 005245
001202 000000
001204 005215
001206 000416
001210 012745 000011
001212 005245
001216 000000
001220 000404
001222 012745 000012
001224 005245
001230 000000
001232 000411
001234 012745 000013
001236 005245
001242 000000
001244 000765
001246 012745 000014
001250 005245
001254 000000
001256 000400

*TEST: 6 CLEAR THE CONDITION CODES

NOTCC:
CMP (R5),#6
BNE CC5 ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
INC (R5)
SCC ; NZVC=1111
CLC ; NZVC=1110
BCS CC5
CLV ; NZVC=1100
BVS CC5
CLZ ; NZVC=1000
BEQ CC5
CLN ; NZVC=0000
BMI CC5
BHI ENDCCS
CC5:
MOV #7, -(R5)
INC -(R5)
HALT ; ONE OF THE ABOVE CLEARS FAILED OR WRONG SEQUENCE
ENDCC5: BPL BRANCH

*TEST: 7 CHECK FORWARD AND BACKWARD BRANCHES.

BRANCH:
CMP (R5),#7
BEQ 1\$; IF IN WRONG SEQUENCE GO TO HLT
MOV #10, -(R5)
INC -(R5)
HALT
1\$: INC (R5)
BR 4\$; CHECK BRANCH FORWARD AND BACKWARD
MOV #11, -(R5)
INC -(R5)
HALT ; FORWARD BRANCH FAILED
2\$: BR 3\$
MOV #12, -(R5)
INC -(R5)
HALT ; FORWARD BRANCH FAILED
3\$: BR 5\$
MOV #13, -(R5)
INC -(R5)
HALT ; FORWARD BRANCH FAILED
4\$: BR 2\$
MOV #14, -(R5)
INC -(R5)
HALT ; BACKWARD BRANCH FAILED
5\$: BR JMP1

```

5260 (2) 001262 021527 000010
5261 (2) 001263 001033
5262 (2) 001264 005215
5263 (2) 001270 012700 001312
5264 (2) 001272 000277
5265 (2) 001300 000110
5266 (2) 001302 012745 000015
5267 (2) 001306 005245
5268 (2) 001310 000000
5269 (2) 001312 100003
5270 (1) 001314 001002
5271 (1) 001316 102001
5272 (1) 001320 103404
5273 (2) 001322 012745 000016
5274 (2) 001326 005245
5275 (2) 001330 000000
5276 (2) 001332 020027 001312
5277 (2) 001336 001404
5278 (2) 001340 012745 000017
5279 (2) 001344 005245
5280 (2) 001346 000000
5281 (2) 001350 012700 001366
5282 (2) 001354 000110
5283 (2) 001356 012745 000020
5284 (2) 001362 005245
5285 (2) 001364 000000
5286 (2) 001366 021527 000011
5287 (2) 001372 001073
5288 (2) 001374 005215 001416
5289 (2) 001376 012700
5290 (2) 001402 000277
5291 (2) 001404 000120
5292 (2) 001406 012745 000021
5293 (2) 001412 005245
5294 (2) 001414 000000
5295 (2) 001416 100003
5296 (1) 001416 001002
5297 (1) 001420 102001
5298 (1) 001422 103404
5299 (2) 001424 103404
5300 (2) 001426

```

```

*****
:TEST: 10 CHECK JMP INSTRUCTIONS FOR MODE 1
*****

```

```

JMP1:
      CMP      (RS),#10
      BNE     ENOJPI ; IF IN WRONG SEQUENCE GO TO HALT AT THE END OF THE TEST
1$:   INC      (RS)
      MOV     #25,RO ; TEST JUMP INSTRUCTION MODE 1
      SCC
      JMP     (RO)
      MOV     #15,-(RS)
      INC    -(RS)
      HALT    ; JUMP INSTRUCTION FAILED
2$:   BPL     3$
      BNE     3$
      BVC     3$
      BCS     4$
3$:   MOV     #16,-(RS)
      INC    -(RS)
      HALT    ; WRONG CC
4$:   CMP     RO,#25
      BEQ     5$ ; CONTINUE IF RO IS OK
      MOV     #17,-(RS)
      INC    -(RS)
      HALT
5$:   MOV     #JMP2,RO ; TEST JUMP INSTRUCTION MODE 1
      JMP     (RO)
ENOJPI:
      MOV     #20,-(RS)
      INC    -(RS)
      HALT    ; JUMP INSTRUCTION FAILED OR WRONG SEQUENCE

```

```

*****
:TEST: 11 CHECK JMP INSTRUCTIONS FOR MODES 2 AND 3
*****

```

```

JMP2:
      CMP     (RS),#11
      BNE     ENOJPI3 ; IF IN WRONG SEQUENCE GO TO HALT AT THE END OF TEST
      INC     (RS)
      MOV     #35,RO ; TEST JUMP INSTRUCTION MODE 2
      SCC
      JMP     (RO)+
      MOV     #21,-(RS)
      INC    -(RS)
      HALT    ; JUMP INSTRUCTION FAILED
3$:   BPL     4$
      BNE     4$
      BVC     4$
      BCS     5$
4$:

```

| | | | | | | | | | |
|-----|--------|--------|--------|--------|----------|------|-------------|---|-----------------------------------|
| (3) | 001426 | 012745 | 000022 | | | MOV | #22, -(R5) | | |
| (3) | 001432 | 005245 | | | | INC | -(R5) | | |
| (3) | 001434 | 000000 | | | | HALT | | : | WRONG CC |
| (3) | 001436 | 020027 | 001420 | 55: | | CMP | R0, #35+2 | : | IS THERE AUTO INC.? |
| (3) | 001440 | 001404 | | | | BEG | 65 | | |
| (3) | 001444 | 012745 | 000023 | | | MOV | #23, -(R5) | | |
| (3) | 001450 | 005245 | | | | INC | -(R5) | | |
| (3) | 001452 | 000000 | | | | HALT | | : | MODE 2 FAILED FOR JMP INSTRUCTION |
| (3) | 001454 | 012700 | 001472 | 55: | | MOV | #JMP3, R0 | : | TEST JUMP INSTRUCTION MODE 2 |
| (3) | 001456 | 000120 | | | | JMP | (R0)+ | | |
| (3) | 001462 | 012745 | 000024 | | | MOV | #24, -(R5) | | |
| (3) | 001466 | 005245 | | | | INC | -(R5) | | |
| (3) | 001470 | 000000 | | | | HALT | | : | JUMP INSTRUCTION FAILED |
| (3) | 001472 | 012767 | 001526 | 176740 | JMP3: | MOV | #35, TEMP | : | TEST JUMP INSTRUCTION MODE 3 |
| (3) | 001500 | 012767 | 001546 | 176734 | | MOV | #45, TEMP+2 | : | |
| (3) | 001506 | 012700 | 000440 | | | MOV | #TEMP, R0 | : | |
| (3) | 001512 | 000277 | | | | SCC | | : | |
| (3) | 001514 | 000130 | | | | JMP | @(R0)+ | : | |
| (3) | 001516 | 012745 | 000025 | | | MOV | #25, -(R5) | | |
| (3) | 001522 | 005245 | | | | INC | -(R5) | | |
| (3) | 001524 | 000000 | | | | HALT | | : | JUMP INSTRUCTION FAILED |
| (3) | 001526 | 027067 | 000000 | 000012 | 35: | CMP | @(R0), 45 | : | IS THERE AUTO INC.? |
| (3) | 001534 | 001404 | | | | BEG | 45 | | |
| (3) | 001536 | 012745 | 000026 | | | MOV | #26, -(R5) | | |
| (3) | 001542 | 005245 | | | | INC | -(R5) | | |
| (3) | 001544 | 000000 | | | | HALT | | : | JMP INSTRUCTION FAILED IN MODE 2 |
| (3) | 001546 | 012767 | 001572 | 176664 | 45: | MOV | #JMP4, TEMP | : | TEST JUMP INSTRUCTION MODE 3 |
| (3) | 001554 | 012700 | 000440 | | | MOV | #TEMP, R0 | | |
| (3) | 001560 | 000130 | | | | JMP | @(R0)+ | | |
| (3) | 001562 | | | | ENDJMP3: | | | | |
| (3) | 001564 | 012745 | 000027 | | | MOV | #27, -(R5) | | |
| (3) | 001566 | 005245 | | | | INC | -(R5) | | |
| (3) | 001570 | 000000 | | | | HALT | | : | JUMP ERROR OR WRONG SEQUENCE |

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: TEST: 12 TEST JUMP INSTRUCTION FOR MODE 4, 5
: *****

001572 021527 000012
001572 001075
001576 005215
001600 012700 001624
001602 000277
001606 000140
001610 012745 000030
001612 005245
001616 000000
001620 000404
001622 012745 000031
001624 005245
001630 000000
001632 022700 001622
001634 001404
001640 012745 000032
001642 005245
001646 000000
001650 012700 001672
001652 000140
001656 012745 000033
001660 005245
001664 000000
001666 012767 001722 176544
001670 012700 000442
001676 012767 001732 176530
001702 000150
001710 012745 000034
001712 005245
001716 000000
001720 012745 000035
001722 005245
001726 000000
001730 022700 000440
001732 001404
001736 012745 000036
001740 005245
001744 000000
001746 012767 001722 176464
001750 012700 000442
001756 012767 002002 176450
001762 000150
001770 012745 000037
001772 005245
001776 000000
002000 000000

JMP4:
CMP (R5), #12
BNE ENOJPS ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
INC (R5)
MOV #35, R0 ; TEST JUMP INSTRUCTION MODE 4
SCC
JMP -(R0)
MOV #30, -(R5)
INC -(R5)
HALT ; JUMP INSTRUCTION FAILED
BR 45 ; JUMP SHOULD LAND HERE
3\$:
MOV #31, -(R5)
INC -(R5)
HALT ; NO AUTO DECREMENT FROM JMP4
4\$:
CMP #35-2, R0 ; CHECK R0
BEQ 55
MOV #32, -(R5)
INC -(R5)
HALT
5\$:
MOV #JMP5+2, R0 ; TEST JUMP INSTRUCTION MODE 4
JMP -(R0)
MOV #33, -(R5)
INC -(R5)
HALT ; JUMP INSTRUCTION FAILED
JMP5:
MOV #35, TEMP1 ; TEST JUMP INSTRUCTION MODE 5
MOV #TEMP1, R0
MOV #45, TEMP1-2 ;
JMP 2-(R0)
MOV #34, -(R5)
INC -(R5)
HALT ; JUMP INSTRUCTION FAILED
3\$:
MOV #35, -(R5)
INC -(R5)
HALT ; ERROR, NO AUTO DECREMENT
4\$:
CMP #TEMP1-2, R0 ; CHECK R0
BEQ 55
MOV #36, -(R5)
INC -(R5)
HALT ; JUMP ONSTRUCTION FAILED IN MODE 5
5\$:
MOV #35, TEMP1 ; TEST JUMP INSTRUCTION MODE 5
MOV #TEMP1, R0
MOV #JMP6, TEMP1-2 ;
JMP 2-(R0)
ENOJPS:
MOV #37, -(R5)
INC -(R5)
HALT ; JUMP ERROR OR WRONG SEQUENCE

E02

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DVKAAA.P11 T13 TEST JMP INSTRUCTION FOR MODE 6 AND 7

*** SEQ 0017

;*TEST: 13 TEST JMP INSTRUCTION FOR MODE 6 AND 7
;*****

| | | | | | | | |
|------|--------|--------|--------|---------|------|---------------|---|
| 5332 | 002002 | | | JMP6: | CMP | (R5), #13 | |
| 5333 | 002002 | 021527 | 000013 | | BNE | ENDJP7 | ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST |
| 5334 | 002006 | 001071 | | | INC | (R5) | |
| 5335 | 002010 | 005215 | | | MOV | #15+6, R3 | |
| 5336 | 002012 | 012703 | 002040 | | JMP | -6(R3) | |
| 5337 | 002016 | 000163 | 177772 | | MOV | #40, -(R5) | |
| 5338 | 002022 | 012745 | 000040 | | INC | -(R5) | |
| 5339 | 002026 | 005245 | | | HALT | | ; JUMP INSTRUCTION FAILED |
| 5340 | 002030 | 000000 | | 1S: | CMP | R3, #15+6 | ; CHECK R3 |
| 5341 | 002032 | 020327 | 002040 | | BEQ | 2S | |
| 5342 | 002036 | 001404 | | | MOV | #41, -(R5) | |
| 5343 | 002040 | 012745 | 000041 | | INC | -(R5) | |
| 5344 | 002044 | 005245 | | | HALT | | ; WRONG VALUE IN REGISTER AFTER JUMP MODE 6 |
| 5345 | 002046 | 000000 | | | | | ; OR JUMP INSTRUCTION FAILED IN MODE 6 |
| 5346 | 002050 | 000167 | 000010 | 2S: | JMP | 3S, -4(PC) | ; TEST JUMP INSTRUCTION MODE 6 |
| 5347 | 002054 | 012745 | 000042 | | MOV | #42, -(R5) | |
| 5348 | 002060 | 005245 | | | INC | -(R5) | |
| 5349 | 002062 | 000000 | | | HALT | | ; JUMP INSTRUCTION FAILED |
| 5350 | 002064 | 012703 | 002104 | 3S: | MOV | #JMP7, R3 | ; JUMP SHOULD LAND HERE |
| 5351 | 002070 | 000163 | 000030 | | JMP | 0(R3) | |
| 5352 | 002074 | 012745 | 000043 | | MOV | #43, -(R5) | |
| 5353 | 002100 | 005245 | | | INC | -(R5) | |
| 5354 | 002102 | 000000 | | | HALT | | ; JUMP INSTRUCTION FAILED |
| 5355 | 002104 | 012703 | 000440 | JMP7: | MOV | #TEMP, R3 | |
| 5356 | 002110 | 012713 | 002130 | | MOV | #15, (R3) | |
| 5357 | 002114 | 000173 | 000030 | | JMP | 2(R3) | |
| 5358 | 002120 | 012745 | 000044 | | MOV | #44, -(R5) | |
| 5359 | 002124 | 005245 | | | INC | -(R5) | |
| 5360 | 002126 | 000000 | | | HALT | | ; JUMP INSTRUCTION FAILED |
| 5361 | 002130 | 012713 | 002154 | 1S: | MOV | #3S, (R3) | ; TEST JUMP INSTRUCTION MODE 7 |
| 5362 | 002134 | 012700 | 000434 | | MOV | #TEMP-4, R0 | |
| 5363 | 002140 | 000170 | 000034 | | JMP | 24(R0) | |
| 5364 | 002144 | 012745 | 000045 | | MOV | #45, -(R5) | |
| 5365 | 002150 | 005245 | | | INC | -(R5) | |
| 5366 | 002152 | 000000 | | | HALT | | ; JUMP INSTRUCTION FAILED |
| 5367 | 002154 | 012767 | 002202 | 3S: | MOV | #JSRTST, TEMP | ; CONTINUE |
| 5368 | 002162 | 012700 | 000440 | | MOV | #TEMP, R0 | |
| 5369 | 002166 | 000170 | 000000 | | JMP | 20(R0) | |
| 5370 | 002172 | | | ENDJP7: | | | |
| 5371 | 002172 | 012745 | 000046 | | MOV | #46, -(R5) | |
| 5372 | 002176 | 005245 | | | INC | -(R5) | |
| 5373 | 002200 | 000000 | | | HALT | | ; JUMP ERROR OR SEQUENCE ERROR |

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5353          ;*****
(2)          ;*TEST: 14    CHECK JSR AND MARK INSTRUCTIONS
(3)          ;*****
5354
5355 002202    JSRTST:
(2) 002202    021527 000014    CMP      (R5), #14
5356 002206    001177    BNE     ENDJSR      ; IF IN WRONG SEQUENCE GO TO HALT AT THE END OF THE TEST
5357 002210    005215    INC     (R5)
5358 002212    012706 000530    MOV     #START, SP  ; SET UP STACK POINTER.
5359 002216    000277    SCC
5360 002220    004767 000026    JSR     PC, 3$
5361 002224
(2) 002224    012745 000047    1$:    MOV     #47, -(R5)
(2) 002230    005245    INC     -(R5)
(2) 002232    000000    HALT
5362 002234    022706 000530    2$:    CMP     #START, SP  ; JSR INSTRUCTION FAILED
5363 002240    001441    BEQ     JSRM        ; HAS SP BEEN RESTORED?
5364 002242    012745 000050    MOV     #50, -(R5)
(2) 002246    005245    INC     -(R5)
(2) 002250    000000    HALT        ; SP WAS NOT RESTORED BY RTS INSTRUCTION
5365 002252
(1) 002252    100003    BPL     4$
(1) 002254    001002    BNE     4$
(1) 002256    102001    BVC     4$
(1) 002260    103404    BCS     5$
(2) 002262
(3) 002262    012745 000051    4$:    MOV     #51, -(R5)
(3) 002266    005245    INC     -(R5)
(3) 002270    000000    HALT        ; WRONG CC
5366 002272    022706 000526    5$:    CMP     #START-2, SP ; WAS THE SP EFFECTED?
5367 002276    001404    BEQ     6$
5368 002300    012745 000052    MOV     #52, -(R5)
(2) 002304    005245    INC     -(R5)
(2) 002306    000000    HALT        ; WRONG SP AFTER EXECUTION OF JSR INSTRUCTION
5369 002310    022716 002224    6$:    CMP     #1$, (SP)   ; IS THE RETURN ADDRESS =1$
5370 002314    001404    BEQ     7$
5371 002316    012745 000053    MOV     #53, -(R5)
(2) 002322    005245    INC     -(R5)
(2) 002324    000000    HALT        ; SP DID NOT HAVE CORRECT RETURN ADDRESS
5372          ; AFTER EXECUTION OF JSR INSTRUCTION
5373 002326    012716 002234    7$:    MOV     #2$, (SP)   ; SET 2$ AS THE RETURN ADDRESS
5374 002332    000207    RTS     PC
5375 002334    012745 000054    MOV     #54, -(R5)
(2) 002340    005245    INC     -(R5)
(2) 002342    000000    HALT        ; RTS INSTRUCTION FAILED
5376 002344    010546 000054    JSRM:  MOV     R5, -(SP) ; MOV R5 TO STACK
5377 002346    016746 176064    MOV     DUMMY, -(SP)
5378 002352    016746 176060    MOV     DUMMY, -(SP)
5379 002356    016746 176070    MOV     MARK2, -(SP) ; STORE MARK 2 ON THE STACK.
5380 002362    010503 000130    MOV     R5, R3      ; SAVE R5 IN R3
5381 002364    004467    JSR     R4, 10$
5382 002370
(2) 002370    012745 000055    1$:    MOV     #55, -(R5)
(2) 002374    005245    INC     -(R5)
(2) 002376    000000    HALT        ; JSR INSTRUCTION FAILED
5383 002400    2$:
    
```

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 DVKAAA.P11 T14 CHECK JSR AND MARK INSTRUCTIONS

*** SEQ 0019

| | | | | | | |
|------|--------|--------|--------|-------|------------|--|
| (1) | 002400 | 100003 | | BP | 3\$ | |
| (1) | 002402 | 001002 | | BNE | 3\$ | |
| (1) | 002404 | 102001 | | BVC | 3\$ | |
| (1) | 002406 | 103404 | | BCS | 4\$ | |
| (2) | 002410 | | 3\$: | | | |
| (2) | 002410 | 012743 | 000056 | MOV | #56, -(R3) | |
| (2) | 002414 | 005243 | | INC | -(R3) | |
| (2) | 002416 | 000000 | | HALT | | ; WRONG CC |
| 5384 | 002420 | 022705 | 000404 | 4\$: | CMP | #START, R5 |
| 5385 | 002424 | 001404 | | BEQ | 5\$ | |
| 5386 | 002426 | 012743 | 000057 | MOV | #57, -(R3) | |
| (2) | 002432 | 005243 | | INC | -(R3) | |
| (2) | 002434 | 000000 | | HALT | | ; MARK INSTRUCTION FAILED |
| 5387 | 002436 | 022706 | 000530 | 5\$: | CMP | #START, SP |
| 5388 | 002442 | 001404 | | BEQ | 6\$ | |
| 5389 | 002444 | 012745 | 000060 | MOV | #60, -(R5) | |
| (2) | 002450 | 005245 | | INC | -(R5) | |
| (2) | 002452 | 000000 | | HALT | | ; MARK INSTRUCTION FAILED |
| 5390 | 002454 | 012701 | 002562 | 6\$: | MOV | #12\$, R1 |
| 5391 | 002460 | 004011 | | JSR | R0, (R1) | ; PLACE THE ADDRESS OF 12\$ IN R1 |
| 5392 | 002462 | | | 7\$: | | ; GO TO TAG 12\$ |
| (2) | 002462 | 012745 | 000061 | MOV | #61, -(R5) | |
| (2) | 002466 | 005245 | | INC | -(R5) | |
| (2) | 002470 | 000000 | | HALT | | ; JSR INSTRUCTION FAILED |
| 5393 | 002472 | 012745 | 000062 | MOV | #62, -(R5) | |
| (2) | 002476 | 005245 | | INC | -(R5) | |
| (2) | 002500 | 000000 | | HALT | | ; RTS BROUGHT THE PROGRAM BACK IN WRONG |
| 5394 | | | | | | ; PLACE |
| 5395 | 002502 | 022706 | 000530 | 8\$: | CMP | #START, SP |
| 5396 | 002506 | 001443 | | BEQ | REGS | |
| 5397 | 002510 | 012745 | 000063 | MOV | #63, -(R5) | |
| (2) | 002514 | 005245 | | INC | -(R5) | |
| (2) | 002516 | 000000 | | HALT | | ; STACK POINTER WAS NOT RESET |
| 5398 | | | | | | |
| 5399 | 002520 | 020427 | 002370 | 10\$: | CMP | R4, #1\$ |
| 5400 | 002524 | 001404 | | BEQ | 11\$ | ; IS THE RETURN ADDRESS =1\$? |
| 5401 | 002526 | 012745 | 000064 | MOV | #64, -(R5) | |
| (2) | 002532 | 005245 | | INC | -(R5) | |
| (2) | 002534 | 000000 | | HALT | | ; WRONG RETURN ADDRESS IN UNKAGE REGISTER R4 |
| 5402 | 002536 | 010605 | | 11\$: | MOV | SP, R5 |
| 5403 | 002540 | 005725 | | TST | (R5)+ | ; SET UP ADDRESS IN R5 AT MARK 2 INSTRUCTION |
| 5404 | 002542 | 012716 | 002400 | MOV | #2\$, (SP) | ; SET RETURN ADDRESS =2\$ |
| 5405 | 002546 | 000277 | | SCC | | |
| 5406 | 002550 | 000205 | | RTS | R5 | ; RETURN USING R5 AND IN-TURN USING MARK INSTRUCTION |
| 5407 | 002552 | 012745 | 000065 | MOV | #65, -(R5) | |
| (2) | 002556 | 005245 | | INC | -(R5) | |
| (2) | 002560 | 000000 | | HALT | | ; RTS INSTRUCTION FAILED |
| 5408 | | | | | | |
| 5409 | 002562 | 020027 | 002462 | 12\$: | CMP | R0, #7\$ |
| 5410 | | | | | | ; DOES R0 CONTAIN THE RETURN ADDRESS? |
| 5411 | 002566 | 001404 | | BEQ | 13\$ | |
| 5412 | 002570 | 012745 | 000066 | MOV | #66, -(R5) | |
| (2) | 002574 | 005245 | | INC | -(R5) | |
| (2) | 002576 | 000000 | | HALT | | ; WRONG RETURN ADDRESS IN LINKAGE REGISTER R0 |
| 5413 | 002600 | 012700 | 002502 | 13\$: | MOV | #8\$, R0 |
| 5414 | 002604 | 000200 | | RTS | R0 | ; SET RETURN ADDRESS AT 8\$ |

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5415 002606
      002606 012745 000067
      002612 005245
      002614 000000
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      002616
      002616 021527 000015
      002622 001034
      002624 005215
      002626 010667 175606
      002632 012700 000001
      002636 012701 000004
      002642 012702 000020
      002646 012703 000100
      002652 012704 000400
      002656 005006
      002660 060006
      002662 060106
      002664 060206
      002666 060306
      002670 060406
      002672 060506
      002674 022706 001131
      002700 001003
      002702 016706 175532
      002706 000406
      002710 016706 175524
      002714
      002714 012745 00007C
      002720 005245
      002722 000000

```

```

ENDJSR:
MOV      #67, -(R5)
INC      -(R5)
HALT
; RTS INSTRUCTION FAILED OR SEQUENCE ERROR

```

```

*****
*TEST: 15 CHECK REGISTER SELECTION
*****

```

```

REGS:
CMP      (R5), #15
BNE      EREGS ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
INC      (R5)
MOV      R6, TEMP ; SAVE THE STACK POINTER
MOV      #1, R0 ; LOAD THE REGISTERS
MOV      #4, R1
MOV      #20, R2
MOV      #100, R3
MOV      #400, R4
CLR      R6
ADD      R0, R6 ; ADD UP THE REGISTERS
ADD      R1, R6
ADD      R2, R6
ADD      R3, R6
ADD      R4, R6
ADD      R5, R6
CMP      #TESTN+525, R6 ; CHECK IT
BNE      IS ; FAILED
MOV      TEMP, R6 ; RESTORE STACK POINTER
BR       ; CONTINUE
MOV      TEMP, R6 ; RESTORE STACK POINTER

IS:
REGS:
MOV      #70, -(R5)
INC      -(R5)
HALT
; REGISTER SELECTION FAILURE OR SEQUENCE ERROR

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: CHECK BYTE INSTRUCTIONS, DESTINATION MODE 0 ONLY
:-----

: *TEST: 16 NEW INSTRUCTIONS USED IN THIS SECTION ARE TSTB, CLRB, MOVB
: *****

002724 021527 000016
002724 001404
002730 012745 000071
002732 005245
002736 000000
002740 005215
002742 000277
002744 105000
002746 004737 017164
002750 105700
002754 004737 017164
002756 112701 000377
002762 004737 017252
002766 105701
002772 004737 017252
002774 021527 000017
003000 001027
003004 005215
003010 000277
003012 152702 000377
003016 004737 017272
003022 122702 000377
003026 001404
003030 012745 000072
003034 005245
003036 000000
003040 112700 000077
003044 120002
003046 100004
003050 012745 000073
003054 005245
003056 000000
003060 120200
003062 100404
003064 012745 000074
003070 005245
003072 000000

TSTB0:
CMP (R5), #16
BEQ 25 ; IF IN WRONG SEQUENCE GO TO HLT BELOW
MOV #71, -(R5)
INC -(R5)
HALT ; PROGRAM IS IN WRONG SEQUENCE
25: INC (R5)
SCC
CLRB R0 ; CLEAR THE REGISTER
JSR PC, #SCC4 ; CHECK FOR CC = 4
TSTB R0 ; CHECK IT
JSR PC, #SCC4 ; CHECK FOR CC = 4
MOVB #377, R1 ; LOAD THE REGISTER
JSR PC, #SCC10 ; CHECK FOR CC = 10
TSTB R1 ; CHECK IT
JSR PC, #SCC10 ; CHECK FOR CC = 10

: *TEST: 17 NEW INSTRUCTIONS USED IN THIS SECTION ARE CMPB, BISB
: *****

CMPB0:
15: CMP (R5), #17
BNE ECMPB0 ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
INC (R5)
SCC
BISB #377, R2 ; LOAD REGISTER
JSR PC, #SCC11 ; CHECK FOR CC = 11
CMPB #377, R2 ; CHECK COMPARE
BEQ 25 ; CONTINUE IF OK
MOV #72, -(R5)
INC -(R5)
HALT ; BISB OR CMPB INSTRUCTION FAILED
25: MOVB #77, R0
CMPB R0, R2 ; CHECK IT AGAIN
BPL 35 ; CONTINUE IF OK
MOV #73, -(R5)
INC -(R5)
HALT ; CMPB INSTRUCTION FAILED [WRONG CC]
35: CMPB R2, R0
BMI ECMPB0 ; ONCE MORE
; CONTINUE IF OK
ECMPB0: MOV #74, -(R5)
INC -(R5)
HALT ; WRONG CC OR WRONG SEQUENCE

```

5485
(2)
(3)
5486
5487 003074
(2) 003074 021527 000020
5488 003100 001404
5489 003102 012745 000075
(2) 003106 005245
(2) 003110 000000
5490 003112 005215
5491 003114 112703 000377
5492 003120 112700 000252
5493 003124 000277
5494 003126 140003
5495 003130 004737 017102
5496 003134 130003
5497 003136 001404
5498 003140 012745 000076
(2) 003144 005245
(2) 003146 000000
5499 003150 132703 000125
5500 003154 004737 017102
5501 003160 150003
5502 003162 100404
5503 003164 012745 000077
(2) 003170 005245
(2) 003172 000000
5504 003174 142703 000177
5505 003200 004737 017272
5506 003204 132703 000377
5507 003210 004737 017272
5508
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*****
; *TEST: 20 NEW INSTRUCTIONS USED IN THIS SECTION ARE BICB, BITB
*****

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BICB0:
      CMP      (R5), #20
      BEQ      2$      ; IF IN WRONG SEQUENCE GO TO HLT BELOW
      MOV      #75, -(R5)
      INC      -(R5)
      HALT     ; PROGRAM IS IN WRONG SEQUENCE
2$:   INC      (R5)
      MOVB     #377, R3      ; LOAD REGISTER
      MOVB     #252, R0     ; PLACE #252 IN R0
      SCC
      BICB     R0, R3      ; CLEAR EVERY OTHER BIT
      JSR      PC, @#5CC1  ; CHECK FOR CC = 1
      BITB     R0, R3      ; CHECK IT
      BEQ      4$      ; CONTINUE IF OK
      MOV      #76, -(R5)
      INC      -(R5)
      HALT     ; BICB OR BITB INSTRUCTION FAILED
4$:   BITB     #125, R3     ; CHECK IT
      JSR      PC, @#5CC1  ; CHECK FOR CC = 1
      BISB     R0, R3      ; SET THE BITS THAT WERE CLEARED
      BMI      6$
      MOV      #77, -(R5)
      INC      -(R5)
      HALT     ; BISB INSTRUCTION FAILED
6$:   BICB     #177, R3     ; CLEAR ALL THE BITS EXCEPT FOR SIGN
      JSR      PC, @#5CC1  ; CHECK FOR CC = 11
      BITB     #377, R3     ; CHECK IT
      JSR      PC, @#5CC1  ; CHECK FOR CC = 11

```

```

5512
(2)
(3)
5513 003214
(2) 003214 021527 000021
5514 003220 001404
5515 003222 012745 000100
(2) 003226 005245
(2) 003230 000000
5516 003232 005215
5517 003234 112704 000177
5518 003240 000261
5519 003242 105204
5520 003244 004737 017334
5521 003250 112704 000376
5522 003254 105204
5523 003256 004737 017272
5524 003262 105204
5525 003264 004737 017206
5526 003270 105204

```

```

*****
; *TEST: 21 NEW INSTRUCTIONS USED IN THIS SECTION ARE INCB, DECB
*****

```

```

INCB0:
      CMP      (R5), #21
      BEQ      1$      ; IF IN WRONG SEQUENCE GO TO HLT
      MOV      #100, -(R5)
      INC      -(R5)
      HALT     ; PROGRAM IS IN WRONG SEQUENCE
1$:   INC      (R5)
      MOVB     #177, R4     ; R4 = 177
      SEC
      INCB     R4      ; ADD ONES INTO REG. 4
      JSR      PC, @#5CC13 ; CHECK FOR CC = 13
      MOVB     #376, R4
      INCB     R4
      JSR      PC, @#5CC11 ; CHECK FOR CC = 11
      INCB     R4
      JSR      PC, @#5CC5  ; CHECK FOR CC = 5
      INCB     R4

```

K02

DVKAAA MACY11 27(732)
DVKAAA.P11 T21

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NEW INSTRUCTIONS USED IN THIS SECTION ARE INCB, DECB

*** SEQ 0023

| | | | | | | |
|------|--------|--------|--------|------|------------|-----------------------------|
| 5527 | 003272 | 004737 | 017102 | JSR | PC,2#SCC1 | : CHECK FOR CC = 1 |
| 5528 | 003276 | 122704 | 000001 | CMPB | #1,R4 | : CHECK IT |
| 5529 | 003302 | 001404 | | BEQ | 2\$ | : CONTINUE IF OK |
| 5530 | 003304 | 012745 | 000101 | MOV | #101,-(R5, | |
| (2) | 003310 | 005245 | | INC | -(R5) | |
| (2) | 003312 | 000000 | | HALT | | ; INCB INSTRUCTION FAILED |
| 5531 | 003314 | 000261 | | SEC | | |
| 5532 | 003316 | 105304 | | DECB | R4 | : SUBTRACT ONES FROM REG. 4 |
| 5533 | 003320 | 004737 | 017206 | JSR | PC,2#SCC5 | : CHECK FOR CC = 5 |
| 5534 | 003324 | 105304 | | DECB | R4 | |
| 5535 | 003326 | 004737 | 017272 | JSR | PC,2#SCC11 | : CHECK FOR CC = 11 |
| 5536 | 003332 | 012704 | 000200 | MOV | #200,R4 | |
| 5537 | 003336 | 105304 | | DECB | R4 | |
| 5538 | 003340 | 004737 | 017142 | JSR | PC,2#SCC3 | : CHECK FOR CC = 3 |
| 5539 | 003344 | 105304 | | DECB | R4 | |
| 5540 | 003346 | 004737 | 017102 | JSR | PC,2#SCC1 | : CHECK FOR CC = 1 |

2\$:

5541 (2) (3) *****
: *TEST: 22 NEW INSTRUCTION IN THIS SECTION IS COMB
: *****

5542
5543 003352 COMBO: CMP (R5), #22
(2) 003352 021527 000022 BEQ 1\$; IF IN WRONG SEQUENCE GO TO HLT
5544 003356 001404 MOV #102, -(R5)
5545 003360 012745 000102 INC -(R5)
(2) 003364 005245
(2) 003366 000000 ; PROGRAM IS IN WRONG SEQUENCE
5546 003370 005215 1\$: INC (R5)
5547 003372 112703 000252 MOVB #252, R3 ; LOAD EVERY OTHER BIT
5548 003376 000277 SCC
5549 003400 105103 COMB R3 ; 1'S COMPLEMENT
5550 003402 004737 017102 JSR PC, #2#SCC1 ; CHECK FOR CC = 1
5551 003406 122703 000125 CMPB #125, R3 ; CHECK IT
5552 003412 001404 BEQ 2\$; CONTINUE IF OK
5553 003414 012745 000103 MOV #103, -(R5)
(2) 003420 005245 INC -(R5)
(2) 003422 000000 ; COMB INSTRUCTION FAILED
5554 003424 000277 2\$: SCC
5555 003426 105103 COMB R3 ; COMPLEMENT BACK
5556 003430 004737 017272 JSR PC, #2#SCC11 ; CHECK FOR CC = 11
5557 003434 122703 000252 CMPB #252, R3 ; CHECK IT
5558 003440 001404 BEQ 3\$; CONTINUE IF OK
5559 003442 012745 000104 MOV #104, -(R5)
(2) 003446 005245 INC -(R5)
(2) 003450 000000 ; COMB INSTRUCTION FAILED
5560 003452 012703 000377 3\$: MOV #377, R3
5561 003456 000277 SCC
5562 003460 105103 COMB R3
5563 003462 004737 017206 JSR PC, #2#SCC5 ; CHECK FOR CC = 5

5564
5565
5566
5567 (2) (3) *****
: *TEST: 23 NEW INSTRUCTION IN THIS SECTION IS NEGB
: *****

5568
5569 003466 NEGBO: CMP (R5), #23
(2) 003466 021527 000023 BNE ENEGBO ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
5570 003472 001025 1\$: INC (R5)
5571 003474 005215 MOVB #1, R0 ; LOAD THE REGISTER
5572 003476 112700 000001 NEGB R0 ; 2'S COMPLEMENT
5573 003502 105400 JSR PC, #2#SCC11 ; CHECK FOR CC = 11
5574 003504 004737 017272 CMPB #377, R0 ; CHECK IT
5575 003510 122700 000377 BEQ 2\$; CONTINUE IF OK
5576 003514 001404 MOV #105, -(R5)
5577 003516 012745 000105 INC -(R5)
(2) 003522 005245
(2) 003524 000000 ; NEGB INSTRUCTION FAILED
5578 003526 012700 000200 2\$: MOV #200, R0
5579 003532 105400 NEGB R0 ; 2'S COMPLEMENT
5580 003534 004737 017334 JSR PC, #2#SCC13 ; CHECK FOR CC = 13
5581 003540 122700 000200 CMPB #200, R0 ; CHECK IT
5582 003544 001404 BEQ ROLBO ; CONTINUE IF OK

M02

DVKAAA MACY11 27(732) 25-AUG-76 13:25 PAGE 54-22
DVKAAA.P11 T23 NEW INSTRUCTION IN THIS SECTION IS NEGB

*** SEQ 0025

| | | | | | |
|------|--------|--------|---------|------|-------------|
| 5583 | 003546 | | ENEGB0: | | |
| .2) | 003546 | 012745 | 000106 | MOV | #106, -(R5) |
| (2) | 003552 | 005245 | | INC | -(R5) |
| .2) | 003554 | 000000 | | HALT | |

; WRONG RESULT IN R0 OR WRONG SEQUENCE

N02

DVKAAA MACY11 27(732) 25-AUG-76 13:25 PAGE 54-23
DVKAAA.P11 T24 NEW INSTRUCTION IN THIS SECTION IS ROLB

*** SEQ 0026

```
5584          ;*****
(2)          ;*TEST: 24      NEW INSTRUCTION IN THIS SECTION IS ROLB
(3)          ;*****
5585
5586 003556    ROLBO:  CMP      (R5), #24
(2) 003556    021527 000024  BNE     EROLBO      ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
5587 003562    001026    INC     (R5)
5588 003564    005215    MOVB   #40, R1      ; LOAD REGISTER
5589 003566    1:2701 000040  CCC     ; CLEAR FLAGS
5590 003572    000257    ROLB   R1           ; SHIFT
5591 003574    106101    ROLB   R1           ;
5592 003576    106101    JSR    PC, @#SCC12  ; CHECK FOR CC = 12
5593 003600    004737 017314  CMPB   #200, R1     ; CHECK IT
5594 003604    122701 000200  BEQ    IS           ; CONTINUE IF OK
5595 003610    001404
5596 003612    012745 000107  MOV    #107, -(R5)
(2) 003616    005245    INC    -(R5)
(2) 003620    000000    HALT   ; ROLB INSTRUCTION FAILED
5597 003622    106101    IS:    ROLB   R1       ; SHIFT
5598 003624    004737 017230  JSR    PC, @#SCC7  ; CHECK FOR CC = 7
5599 003630    106101    ROLB   R1       ; SHIFT
5600 003632    122701 000001  CMPB   #1, R1      ; CHECK IT
5601 003636    001404    BEQ    RORBO      ; CONTINUE IF OK
5602 003640
(2) 003640    012745 000110  MOV    #110, -(R5)
(2) 003644    005245    INC    -(R5)
(2) 003646    000000    HALT   ; WRONG RESULT IN R1 OR WRONG SEQUENCE
5603
5604
5605          ;*****
(2)          ;*TEST: 25      NEW INSTRUCTION IN THIS SECTION IS RORB
(3)          ;*****
5606
5607 003650    RORBO:  CMP      (R5), #25
(2) 003650    021527 000025  BNE     ERORBO      ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
5608 003654    001026    INC     (R5)
5609 003656    005215    MOVB   #4, R2      ; LOAD REGISTER
5610 003660    112702 000004  CCC     ; CLEAR FLAGS
5611 003664    000257    RORB   R2           ; SHIFT
5612 003666    106002    RORB   R2           ;
5613 003670    106002    CMPB   #1, R2     ; CHECK IT
5614 003672    122702 000001  BEQ    IS           ; CONTINUE IF OK
5615 003676    001404
5616 003700    012745 000111  MOV    #111, -(R5)
(2) 003704    005245    INC    -(R5)
(2) 003706    000000    HALT   ; RORB INSTRUCTION FAILED
5617 003710    106002    IS:    RORB   R2       ; SHIFT
5618 003712    004737 017230  JSR    PC, @#SCC7  ; CHECK FOR CC = 7
5619 003716    106002    RORB   R2       ; SHIFT
5620 003720    004737 017314  JSR    PC, @#SCC12 ; CHECK FOR CC = 12
5621 003724    122702 000200  CMPB   #200, R2    ; CHECK IT
5622 003730    001404    BEQ    ASLBO      ; CONTINUE IF OK
5623 003732
(2) 003732    012745 000112  MOV    #112, -(R5)
(2) 003736    005245    INC    -(R5)
(2) 003740    000000    HALT
```

```

5624 (2)
5625 (3)
5626 003742
5627 003742 021527 000026
5628 003746 001404
5629 003750 012745 000113
5630 003754 005245
5631 003756 000000
5632 003758 005215
5633 003760 112703 000040
5634 003766 000257
5635 003770 106303
5636 003772 106303
5637 003774 004737 017314
5638 004000 122703 000200
5639 004004 001404
5640 004006 012745 000114
5641 (2) 004012 005245
5642 (2) 004014 000000
5643 004016 106303
5644 004020 004737 017230
5645 004024 106303
5646 004026 004737 017164
5647 (2)
5648 (3)
5649 004032
5650 004032 021527 000027
5651 004036 001034
5652 004040 005215
5653 004042 112704 000004
5654 004046 000257
5655 004050 106204
5656 004052 106204
5657 004054 122704 000001
5658 004060 001404
5659 004062 012745 000115
5660 (2) 004066 005245
5661 (2) 004070 000000
5662 004072 106204
5663 004074 004737 017230
5664 004100 106204
5665 004102 004737 017164
5666 004106 112703 000202
5667 004112 106203
5668 004114 106203
5669 004116 004737 017272
5670 004122 122703 000340
5671 004126 001404
5672 (2) 004130 012745 000116
5673 (2) 004134 005245

```

```

*****
: TEST: 26 NEW INSTRUCTION IN THIS SECTION IS ASLB
*****

```

```

ASLBO:
CMP (R5),#26
BEQ 25 ; IF IN WRONG SEQUENCE GO TO HLT BELOW
MOV #113, -(R5)
INC -(R5)
HALT ; PROGRAM IS IN WRONG SEQUENCE
25:
INC (R5)
MOVB #40, R3 ; LOAD REGISTER
CCC ; CLEAR FLAGS
ASLB R3 ; SHIFT
ASLB R3
JSR PC, #50012 ; CHECK FOR CC = 12
CMPB #200, R3 ; CHECK IT
BEQ 45 ; CONTINUE IF OK
MOV #114, -(R5)
INC -(R5)
HALT ; ASLB INSTRUCTION FAILED
45:
ASLB R3 ; SHIFT
JSR PC, #50007 ; CHECK FOR CC = 7
ASLB R3 ; SHIFT
JSR PC, #50004 ; CHECK FOR CC = 4

```

```

*****
: TEST: 27 NEW INSTRUCTION IN THIS SECTION IS ASRB
*****

```

```

ASRBO:
CMP (R5),#27
BNE EASRBO ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
15:
INC (R5)
MOVB #4, R4 ; LOAD REGISTER
CCC ; CLEAR FLAGS
ASRB R4 ; SHIFT
ASRB R4
CMPB #1, R4 ; CHECK IT
BEQ 25 ; CONTINUE IF OK
MOV #115, -(R5)
INC -(R5)
HALT ; ASRB INSTRUCTION FAILED
25:
ASRB R4 ; SHIFT
JSR PC, #50007 ; CHECK FOR CC = 7
ASRB R4 ; SHIFT
JSR PC, #50004 ; CHECK FOR CC = 4
MOVB #202, R3 ; LOAD REGISTER
ASRB R3 ; SHIFT
ASRB R3
JSR PC, #50011 ; CHECK FOR CC = 11
CMPB #340, R3 ; CHECK IT
BEQ ADCBC ; CONTINUE IF OK
EASRBO:
MOV #116, -(R5)
INC -(R5)

```

C03

DVKAAA MACY11 270732 25-AUG-76 13:25 PAGE 54-25
DVKAAA.P11 127 NEW INSTRUCTION IN THIS SECTION IS ASRB
(2) 004136 000000 HALT

*** SEQ 0028

```

5666
(2)
(3)
5667
5668 004140 021527 000030
(2) 004140 001404
5669 004144 012745 000117
5670 004146 005245
(2) 004152 000000
(2) 004154 005215
5671 004156 105000
5672 004160 000257
5673 004162 105500
5674 004164 004737 017164
5675 004166 000261
5676 004172 105500
5677 004174 000261
5678 004176 105500
5679 004200 004737 017062
5680 004202 122700 000002
5681 004206 001404
5682 004212 012745 000120
(2) 004220 005245
(2) 004222 000000
5684 004224 112700 000177
5685 004230 000261
5686 004232 105500
5687 004234 004737 017314
5688 004240 122700 000200
5689 004244 001404
5690 004246 012745 000121
(2) 004252 005245
(2) 004254 000000
5691 004256 112700 000377
5692 004262 000261
5693 004264 105500
5694 004266 004737 017206
5695
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```

```

*****
:TEST: 30 NEW INSTRUCTION IN THIS SECTION IS ADCB
*****

```

```

ADCB0:
      CMP      (RS),#30
      BEQ      2S ; IF IN WRONG SEQUENCE GO TO HLT BELOW
      MOV      #117,-(RS)
      INC      -(RS)
      HALT
2S:   INC      (RS) ; PROGRAM IS IN WRONG SEQUENCE
      CLR      RO ; CLEAR THE REGISTER
      CCC      ; CLEAR FLAGS
      ADCB     RO ; ADD C BIT = 0
      JSR      PC,#$CC4 ; CHECK FOR CC = 4
      SEC      ; C=1
      ADCB     RO ; ADD C BIT=1
      SEC      ; C=1
      ADCB     RO ; AGAIN
      JSR      PC,#$CC0 ; CHECK FOR CC = 0
      CMPB     #2,RO ; CHECK IT
      BEQ      4S ; CONTINUE IF OK
      MOV      #120,-(RS)
      INC      -(RS)
      HALT
4S:   MOV      #177,RO ; ADCB INSTRUCTION FAILED
      SEC      ; LOAD LARGEST POSITIVE NUMBER
      ADCB     RO ; C=1
      JSR      PC,#$CC12 ; ADD C BIT=1
      CMPB     #200,RO ; CHECK FOR CC = 12
      BEQ      6S ; CHECK IT
      MOV      #121,-(RS) ; CONTINUE IF OK
      INC      -(RS)
      HALT
6S:   MOV      #377,RO ; ADCB INSTRUCTION FAILED
      SEC      ; LOAD -1
      ADCB     RO ; C=1
      JSR      PC,#$CC5 ; ADD C BIT=1
      ; CHECK FOR CC = 5

```

```

*****
:TEST: 31 NEW INSTRUCTION IN THIS SECTION IS SBCB
*****

```

```

5700 004272 021527 000031
(2) 004272 001404
5701 004276 012745 000122
(2) 004300 005245
(2) 004304 000000
(2) 004306 005215
5703 004310 112701 000003
5704 004312 000257
5705 004316 105601
5706 004320 004737 017062
5707

```

```

SBCB0:
      CMP      (RS),#31
      BEQ      1S ; IF IN WRONG SEQUENCE GO TO HLT BELOW
      MOV      #122,-(RS)
      INC      -(RS)
      HALT
1S:   INC      (RS) ; PROGRAM IS IN WRONG SEQUENCE
      MOV      #3,R1 ; TEST IS IN WRONG SEQUENCE
      CCC      ; LOAD REGISTER
      SBCB     R1 ; CLEAR FLAGS
      JSR      PC,#$CC0 ; SUBTRACT C BIT=0
      ; CHECK FOR CC = 0

```

E03

DVKAAA MACY11 27(732) 25-AUG-76 13:25 PAGE 54-27
 DVKAAA.P11 T31 NEW INSTRUCTION IN THIS SECTION IS SBCB

*** SEQ 0030

| | | | | | | | | |
|------|--------|--------|--------|------|------|-------------|---|-------------------------|
| 5708 | 004326 | 122701 | 000003 | | CMPB | #3,R1 | : | CHECK IT |
| 5709 | 004332 | 001404 | | | BEQ | 2\$ | : | CONTINUE IF OK |
| 5710 | 004337 | 012745 | 000123 | | MOV | #123,-(R5) | | |
| (2) | 004340 | 005245 | | | INC | -(R5) | | |
| (2) | 004342 | 000000 | | | HALT | | : | SBCB INSTRUCTION FAILED |
| 5711 | 004344 | 000261 | | 2\$: | SEC | | : | C=1 |
| 5712 | 004346 | 105601 | | | SBCB | R1 | : | SUBTRACT C BIT=1 |
| 5713 | 004350 | 000261 | | | SEC | | : | C=1 |
| 5714 | 004352 | 105601 | | | SBCB | R1 | : | |
| 5715 | 004354 | 004737 | 017062 | | JSR | PC,2\$S000 | : | CHECK FOR CC = 0 |
| 5716 | 004360 | 122701 | 000001 | | CMPB | #1,R1 | : | CHECK IT |
| 5717 | 004364 | 001404 | | | BEQ | 3\$ | : | CONTINUE IF OK |
| 5718 | 004366 | 012745 | 000124 | | MOV | #124,-(R5) | | |
| (2) | 004372 | 005245 | | | INC | -(R5) | | |
| (2) | 004374 | 000000 | | | HALT | | : | SBCB INSTRUCTION FAILED |
| 5719 | 004376 | 000261 | | 3\$: | SEC | | : | C=1 |
| 5720 | 004400 | 105601 | | | SBCB | R1 | : | SUBTRACT C BIT=1 |
| 5721 | 004402 | 004737 | 017164 | | JSR | PC,2\$S004 | : | CHECK FOR CC = 4 |
| 5722 | 004406 | 000261 | | | SEC | | : | C=1 |
| 5723 | 004410 | 105601 | | | SBCB | R1 | : | SUBTRACT C BIT = 1 |
| 5724 | 004412 | 004737 | 017272 | | JSR | PC,2\$S0011 | : | CHECK FOR CC = 11 |
| 5725 | 004416 | 122701 | 000377 | | CMPB | #377,R1 | : | CHECK IT |
| 5726 | 004422 | 001404 | | | BEQ | 4\$ | : | CONTINUE IF OK |
| 5727 | 004424 | 012745 | 000125 | | MOV | #125,-(R5) | | |
| (2) | 004430 | 005245 | | | INC | -(R5) | | |
| (2) | 004432 | 000000 | | | HALT | | : | SBCB INSTRUCTION FAILED |
| 5728 | 004434 | 112701 | 000200 | 4\$: | MOVB | #200,R1 | : | LOAD R1 |
| 5729 | 004440 | 000261 | | | SEC | | : | C=1 |
| 5730 | 004442 | 105601 | | | SBCB | R1 | : | SUBTRACT C BIT = 1 |
| 5731 | 004444 | 004737 | 017122 | | JSR | PC,2\$S002 | : | CHECK FOR CC = 2 |

F03

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004450 021527 000032
 004451 001404
 004452 012745 000126
 004453 005245
 004454 000000
 004455 005215
 004456 000277
 004457 005000
 004458 004737 017164
 004459 005700
 004460 004737 017164
 004461 012704 177777
 004462 010401
 004463 004737 017252
 004464 005701
 004465 004737 017252
 004466 020401
 004467 001404
 004468 012745 000127
 004469 005245
 004470 000000
 004471 000263
 004472 010000
 004473 004767 012434

.....
 CHECK WORD INSTRUCTIONS, DESTINATION MODE 0 ONLY

 *TEST: 32 NEW INSTRUCTIONS USED IN THIS SECTION ARE TST, CLR, MOV

*ST0:
 CMP (R5), #32
 BEQ 15 ; IF IN WRONG SEQUENCE GO TO HLT
 MOV #126, -(R5)
 INC -(R5)
 HALT ; TEST IS IN WRONG SEQUENCE
 15:
 INC (R5)
 SCC
 CLR R0 ; CLEAR THE REGISTER
 JSR PC, @#SCC4 ; CHECK FOR CC = 4
 TST R0 ; CHECK IT
 JSR PC, @#SCC4 ; CHECK FOR CC = 4
 MOV #177777, R4 ; LOAD THE REGISTER
 MOV R4, R1
 JSR PC, @#SCC10 ; CHECK FOR CC = 10
 TST R1 ; CHECK IT
 JSR PC, @#SCC10 ; CHECK FOR CC = 10
 CMP R4, R1 ; CHECK R1 TO CONTAIN PROPER DATA
 BEQ 25
 MOV #127, -(R5)
 INC -(R5)
 HALT
 25:
 SEVC ; SET V & C BITS
 MOV R0, R0
 JSR PC, SCC5

 *TEST: 33 NEW INSTRUCTIONS USED IN THIS SECTION ARE CMP, BIS

CMPO:
 CMP (R5), #33
 BNE ECMP0 ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
 15:
 INC (R5)
 MOV #177777, R0 ; LOAD REGISTER
 BIS R0, R2 ; CHECK THE BIS INSTRUCTION
 JSR PC, @#SCC10 ; CHECK FOR CC = 10
 CMP R0, R2 ; CHECK COMPARE
 BEQ 25 ; CONTINUE IF OK
 MOV #130, -(R5)
 INC -(R5)
 HALT ; BIS OR CMP INSTRUCTION FAILED

G03

DVKAAA MACY11 27(732) 25-AUG-76 13:25 PAGE 54-29
DVKAAA.P11 T33 NEW INSTRUCTIONS USED IN THIS SECTION ARE CMP, BIS

*** SEQ 0032

| | | | | | | | |
|------|--------|--------|--------|--------|------|-------------|-------------------------------------|
| 5780 | 004610 | 022702 | 000077 | 2S: | CMP | #77,R2 | : CHECK IT AGAIN |
| 5781 | 004614 | 100004 | | | BPL | 3S | : CONTINUE IF OK |
| 5782 | 004616 | 012745 | 000131 | | MOV | #131, -(R5) | |
| (2) | 004622 | 005245 | | | INC | -(R5) | |
| (2) | 004624 | 000000 | | | HALT | | : CMP INSTRUCTION FAILED [WRONG CC] |
| 5783 | 004626 | 020227 | 000077 | 3S: | CMP | R2, #77 | : ONCE MORE |
| 5784 | 004632 | 100404 | | | BMI | BIC0 | : CONTINUE IF OK |
| 5785 | 004634 | | | ECMPD: | | | |
| (2) | 004634 | 012745 | 000132 | | MOV | #132, -(R5) | |
| (2) | 004640 | 005245 | | | INC | -(R5) | |
| (2) | 004642 | 000000 | | | HALT | | : WRONG CC OR WRONG SEQUENCE |

H03

DVKAAA MACY11 27(732)
DVKAAA.P11 T34

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NEW INSTRUCTIONS USED IN THIS SECTION ARE BIC, BIT

*** SEQ 0033

```

5786
(2)
5787
5788 004644 021527 000034
(2) 004644
5789 004650 001053
5790 004652 005215
5791 004654 012703 177777
5792 004660 012700 000440
5793 004664 012710 125252
5794 004670 000277
5795 004672 041003
5796 004674 004737 017102
5797 004700 031003
5798 004702 001404
5799 004704 012745 000133
(2) 004710 005245
(2) 004712 000000
5800 004714 032703 052525
5801 004720 004737 017102
5802 004724 052703 125252
5803 004730 100404
5804 004732 012745 000134
(2) 004736 005245
(2) 004740 000000
5805 004742 042703 077777
5806 004746 004737 017272
5807 004752 012700 177777
5808 004756 030003
5809 004760 004737 017272
5810 004764 000263
5811 004766 040000
5812 004770 004737 017206
5813 004774 005700
5814 004776 001404
5815 005000
(2) 005000 012745 000135
(2) 005004 005245
(2) 005006 000000

```

```

*****
; *TEST: 34 NEW INSTRUCTIONS USED IN THIS SECTION ARE BIC, BIT
*****

```

```

BICO:
CMP (R5), #34
BNE EBICO ; IF IN WRONG SEQUENCE GO TO HLT ABOVE
INC (R5)
MOV #177777, R3 ; LOAD REGISTER
MOV #TEMP, R0 ; PLACE THE ADDRESS OF LOCATION TEMP IN R0
MOV #125252, (R0) ; SET (R0)
SCC
BIC (R0), R3 ; CLEAR EVERY OTHER BIT
JSR PC, #SCC1 ; CHECK FOR CC = 1
BIT (R0), R3 ; CHECK IT
BEQ 1$ ; CONTINUE IF OK
MOV #133, -(R5)
INC -(R5)
HALT ; BIC OR BIT INSTRUCTION FAILED
1$: BIT #52525, R3 ; CHECK IT
JSR PC, #SCC1 ; CHECK FOR CC = 1
BIS #125252, R3 ; SET THE BITS THAT WERE CLEARED
BMI 2$ ; CONTINUE IF OK
MOV #134, -(R5)
INC -(R5)
HALT ; BIT OR BIS INSTRUCTION FAILED
2$: BIC #77777, R3 ; CLEAR ALL THE BITS EXCEPT FOR SIGN
JSR PC, #SCC1 ; CHECK FOR CC = 11
MOV #177777, R0
BIT R0, R3 ; CHECK IT
JSR PC, #SCC1 ; CHECK FOR CC = 11
SEVC ; SET V & C BITS
BIC R0, R0
JSR PC, #SCC5 ; CHECK CC = 5
TST R0 ; CHECK R0 TO CONTAIN 0
BEQ INCO
EBICO:
MOV #135, -(R5)
INC -(R5)
HALT ; BIC FAILED OR SEQUENCE ERROR

```

```

5816
5817
5818
5819
(2)
(3)
5820
5821 005010 021527 000035
(2) 005010 001404
5822 005014 012745 000136
(2) 005022 005245
(2) 005024 000000
5824 005026 005215
5825 005030 012704 077777
5826 005034 000261

```

```

*****
; *TEST: 35 NEW INSTRUCTIONS USED IN THIS SECTION ARE INC, DEC
*****

```

```

INCO:
CMP (R5), #35
BEQ 2$ ; IF IN WRONG SEQUENCE GO TO HLT BELOW
MOV #136, -(R5)
INC -(R5)
HALT ; PROGRAM IS IN WRONG SEQUENCE
2$: INC (R5)
MOV #77777, R4 ; R4=77777
SEC

```

| | | | | | | | |
|------|--------|--------|--------|------|------------|---|---------------------------|
| 5827 | 005036 | 005204 | | INC | R4 | : | ADD ONES INTO REG. 4 |
| 5828 | 005040 | 004737 | 017334 | JSR | PC,2#5CC13 | : | CHECK FOR CC = 13 |
| 5829 | 005044 | 012704 | 177776 | MOV | #177776,R4 | | |
| 5830 | 005050 | 005204 | | INC | R4 | | |
| 5831 | 005052 | 004737 | 017272 | JSR | PC,2#5CC11 | : | CHECK FOR CC = 11 |
| 5832 | 005056 | 005204 | | INC | R4 | | |
| 5833 | 005060 | 004737 | 017206 | JSR | PC,2#5CC5 | : | CHECK FOR CC = 5 |
| 5834 | 005064 | 005204 | | INC | R4 | | |
| 5835 | 005066 | 004737 | 017102 | JSR | PC,2#5CC1 | : | CHECK FOR CC = 1 |
| 5836 | 005072 | 022704 | 000001 | CMP | #1,R4 | : | CHECK IT |
| 5837 | 005076 | 001404 | | BEQ | 4\$ | : | FAILED |
| 5838 | 005100 | 012745 | 000137 | MOV | #137,-(R5) | | |
| (2) | 005104 | 005245 | | INC | -(R5) | | |
| (2) | 005106 | 000000 | | HALT | | : | INC INSTRUCTION FAILED |
| 5839 | 005110 | 000261 | | SEC | | | |
| 5840 | 005112 | 005304 | | DEC | R4 | : | SUBTRACT ONES FROM REG. 4 |
| 5841 | 005114 | 004737 | 017206 | JSR | PC,2#5CC5 | : | CHECK FOR CC = 5 |
| 5842 | 005120 | 005304 | | DEC | R4 | | |
| 5843 | 005122 | 004737 | 017272 | JSR | PC,2#5CC11 | : | CHECK FOR CC = 11 |
| 5844 | 005126 | 012704 | 100000 | MOV | #100000,R4 | | |
| 5845 | 005132 | 005304 | | DEC | R4 | | |
| 5846 | 005134 | 004737 | 017142 | JSR | PC,2#5CC3 | : | CHECK FOR CC = 3 |
| 5847 | 005140 | 005304 | | DEC | R4 | : | |
| 5848 | 005142 | 004737 | 017102 | JSR | PC,2#5CC1 | : | CHECK FOR CC = 1 |

4\$:

5849
 (2)
 (3)
 5850
 5851
 (2)
 5852
 5853
 (2)
 (2)
 5854
 5855
 5856
 5857
 5858
 5859
 5860
 5861
 (2)
 (2)
 5862
 5863
 5864
 5865
 5866
 5867
 (2)
 (2)
 5868
 5869
 5870
 5871
 5872
 5873
 5874
 5875
 (2)
 (3)

```

*****
; *TEST: 36 NEW INSTRUCTION IN THIS SECTION IS COM
*****
    
```

005146 021527 000036
 005146 001404
 005154 012745 000140
 005160 005245
 005162 000000
 005164 005215
 005166 012703 125252
 005172 000277
 005174 005103
 005176 004737 017102
 005202 022703 052525
 005206 001404
 005210 012745 000141
 005214 005245
 005216 000000
 005220 000277
 005222 005103
 005224 004737 017272
 005230 022703 125252
 005234 001404
 005236 012745 000142
 005242 005245
 005244 000000
 005246 012703 177777
 005252 000277
 005254 005103
 005256 004737 017206

```

COMO:
      CMP      (R5), #36
      BEQ      1$ ; IF IN WRONG SEQUENCE GO TO HLT BELOW
      MOV      #140, -(R5)
      INC      -(R5)
      HALT     ; TEST IS IN WRONG SEQUENCE
1$:   INC      (R5)
      MOV      #125252, R3 ; LOAD EVERY OTHER BIT
      SCC
      COM      R3 ; 1'S COMPLEMENT
      JSR      PC, @#SCC1 ; CHECK FOR CC = 1
      CMP      #52525, R3 ; CHECK IT
      BEQ      2$ ; CONTINUE IF OK
      MOV      #141, -(R5)
      INC      -(R5)
      HALT     ; COM INSTRUCTION FAILED
2$:   SCC
      COM      R3 ; COMPLEMENT BACK
      JSR      PC, @#SCC11 ; CHECK FOR CC = 11
      CMP      #125252, R3 ; CHECK IT
      BEQ      3$ ; CONTINUE IF OK
      MOV      #142, -(R5)
      INC      -(R5)
      HALT     ; COM INSTRUCTION FAILED
3$:   MOV      #177777, R3
      SCC
      COM      R3
      JSR      PC, @#SCC5 ; CHECK FOR CC = 5
    
```

5876
 (2)
 (3)
 5877
 (2)
 5878
 5879
 5880
 5881
 5882
 5883
 5884
 5885
 (2)
 (2)
 5886
 5887
 5888
 5889
 5890

```

*****
; *TEST: 37 NEW INSTRUCTION IN THIS SECTION IS NEG
*****
    
```

```

NEGO:
      CMP      (R5), #37
      BNE      ENEG0 ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
1$:   INC      (R5)
      MOV      #1, RO ; LOAD THE REGISTER
      NEG      RO ; 2'S COMPLEMENT
      JSR      PC, @#SCC11 ; CHECK FOR CC = 11
      CMP      #177777, RO ; CHECK IT
      BEQ      2$ ; CONTINUE IF OK
      MOV      #143, -(R5)
      INC      -(R5)
      HALT     ; NEG INSTRUCTION FAILED
2$:   MOV      #100000, RO
      NEG      RO ; 2'S COMPLEMENT
      JSR      PC, @#SCC13 ; CHECK FOR CC = 13
      CMP      #100000, RO ; CHECK IT
      BEQ      ROLO ; CONTINUE IF OK
    
```

K03

DVAAA MACY11 27(732) 25-AUG-76 13:25 PAGE 54-33
DVAAA.P11 T37 NEW INSTRUCTION IN THIS SECTION IS NEG

*** SEQ 0036

| | | | | | |
|------|--------|--------|--------|--------|-------------|
| 5891 | 005342 | | | ENEGO: | |
| (2) | 005342 | 012745 | 000144 | MOV | #144, -(R5) |
| (2) | 005346 | 005245 | | INC | -(R5) |
| (2) | 005350 | 000000 | | HALT | |

; WRONG RESULT IN R0 OR WRONG SEQUENCE

```

5892
(2)
(3)
5893
5894 005352
(2) 005352 021527 000040
5895 005356 001026
5896 005360 005215
5897 005362 012701 020000
5898 005366 000257
5899 005370 006101
5900 005372 006101
5901 005374 004737 017314
5902 005400 022701 100000
5903 005404 001404
5904 005406 012745 000145
(2) 005412 005245
(2) 005414 000000
5905 005416 006101
5906 005420 004737 017230
5907 005424 006101
5908 005426 022701 000001
5909 005432 001404
5910 005434
(2) 005434 012745 000146
(2) 005440 005245
(2) 005442 000000
5911
5912
5913
5914
(2)
(3)
5915
5916 005444
(2) 005444 021527 000041
5917 005450 001026
5918 005452 005215
5919 005454 012702 000004
5920 005460 000257
5921 005462 006002
5922 005464 006002
5923 005466 022702 000001
5924 005472 001404
5925 005474 012745 000147
(2) 005500 005245
(2) 005502 000000
5926 005504 006002
5927 005506 004737 017230
5928 005512 006002
5929 005514 004737 017314
5930 005520 022702 100000
5931 005524 001404
5932 005526
(2) 005526 012745 000150
(2) 005532 005245

```

```

*****
*TEST: 40 NEW INSTRUCTION IN THIS SECTION IS ROL
*****

```

```

ROLO:
CMP (R5),#40
BNE EROLO ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
INC (R5)
MOV #20000,R1 ; LOAD REGISTER
CCC ; CLEAR FLAGS
ROL R1 ; SHIFT
ROL R1
JSR PC,#$CC12 ; CHECK FOR CC = 12
CMP #100000,R1 ; CHECK IT
BEQ 1$ ; CONTINUE IF OK
MOV #145,-(R5)
INC -(R5)
HALT ; ROL INSTRUCTION FAILED
1$: ROL R1 ; SHIFT
JSR PC,#$CC7 ; CHECK FOR CC = 7
ROL R1 ; SHIFT
CMP #1,R1 ; CHECK IT
BEQ RORO ; CONTINUE IF OK
EROLO:
MOV #146,-(R5)
INC -(R5)
HALT ; WRONG RESULT IN R1 OR WRONG SEQUENCE

```

```

*****
*TEST: 41 NEW INSTRUCTION IN THIS SECTION IS ROR
*****

```

```

RORO:
CMP (R5),#41
BNE ERORO ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
INC (R5)
MOV #4,R2 ; LOAD REGISTER
CCC ; CLEAR FLAGS
ROR R2 ; SHIFT
ROR R2
CMP #1,R2 ; CHECK IT
BEQ 1$ ; CONTINUE IF OK
MOV #147,-(R5)
INC -(R5)
HALT ; ROR INSTRUCTION FAILED
1$: ROR R2 ; SHIFT
JSR PC,#$CC7 ; CHECK FOR CC = 7
ROR R2 ; SHIFT
JSR PC,#$CC12 ; CHECK FOR CC = 12
CMP #100000,R2 ; CHECK IT
BEQ ASLO ; CONTINUE IF OK
ERORO:
MOV #150,-(R5)
INC -(R5)

```

M03

DVKAAA MACY11 27(732) 25-AUG-76 13:25 PAGE 54-35
DVKAAA.P11 T41 NEW INSTRUCTION IN THIS SECTION IS ROR

*** SEQ 0038

(2) 005534 000000

HALT

; WRONG RESULT IN R2 OR WRONG SEQUENCE

NO3

DVKAAA MACY11 27(732) 25-AUG-76 13:25 PAGE 54-36
 DVKAAA.P11 T42 NEW INSTRUCTION IN THIS SECTION IS ASL

*** SEQ 0039

```

5933          ;*****
(2)          ;*TEST: 42      NEW INSTRUCTION IN THIS SECTION IS ASL
(3)          ;*****
5934
5935          ASLO:
  
```

```

(2) 005536 021527 000042      CMP      (R5), #42
5936 005542 001404      BEQ      2$      ; IF IN WRONG SEQUENCE GO TO HLT BELOW
5937 005544 012745 000151      MOV      #151, -(R5)
(2) 005550 005245      INC      -(R5)
(2) 005552 000000      HALT     ; PROGRAM IS IN WRONG SEQUENCE
5938 005554 005215      2$: INC      (R5)
5939 005556 012703 020000      MOV      #20000, R3      ; LOAD REGISTER
5940 005562 000257      CCC     ; CLEAR FLAGS
5941 005564 006303      ASL     R3      ; SHIFT
5942 005566 006303      ASL     R3
5943 005570 004737 017314      JSR     PC, @#5CC12      ; CHECK FOR CC = 12
5944 005574 022703 100000      CMP      #100000, R3      ; CHECK IT
5945 005600 001404      BEQ     4$      ; CONTINUE IF OK
5946 005602 012745 000152      MOV      #152, -(R5)
(2) 005606 005245      INC      -(R5)
(2) 005610 000000      HALT     ; ASL INSTRUCTION FAILED
5947 005612 006303      4$: ASL     R3      ; SHIFT
5948 005614 004737 017230      JSR     PC, @#5CC7      ; CHECK FOR CC = 7
5949 005620 006303      ASL     R3      ; SHIFT
5950 005622 004737 017164      JSR     PC, @#5CC4      ; CHECK FOR CC = 4
5951
5952          ;*****
(2)          ;*TEST: 43      NEW INSTRUCTION IN THIS SECTION IS ASR
(3)          ;*****
  
```

```

5953
5954          ASRO:
  
```

```

(2) 005626 021527 000043      CMP      (R5), #43
5955 005632 001034      BNE     EASRO      ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
5956 005634 005215      1$: INC      (R5)
5957 005636 012704 000004      MOV      #4, R4      ; LOAD REGISTER
5958 005642 000257      CCC     ; CLEAR FLAGS
5959 005644 006204      ASR     R4      ; SHIFT
5960 005646 006204      ASR     R4
5961 005650 022704 000001      CMP      #1, R4      ; CHECK IT
5962 005654 001404      BEQ     2$      ; CONTINUE IF OK
5963 005656 012745 000153      MOV      #153, -(R5)
(2) 005662 005245      INC      -(R5)
(2) 005664 000000      HALT     ; ASR INSTRUCTION FAILED
5964 005666 006204      2$: ASR     R4      ; SHIFT
5965 005670 004737 017230      JSR     PC, @#5CC7      ; CHECK FOR CC = 7
5966 005674 006204      ASR     R4      ; SHIFT
5967 005676 004737 017164      JSR     PC, @#5CC4      ; CHECK FOR CC = 4
5968 005702 012703 100002      MOV      #100002, R3      ; LOAD REGISTER
5969 005706 006203      ASR     R3      ; SHIFT
5970 005710 006203      ASR     R3
5971 005712 004737 017272      JSR     PC, @#5CC11      ; CHECK FOR CC = 11
5972 005716 022703 160000      CMP      #160000, R3      ; CHECK IT
5973 005722 001404      BEQ     ADCO      ; CONTINUE IF OK
5974          EASRO:
(2) 005724 012745 000154      MOV      #154, -(R5)
(2) 005730 005245      INC      -(R5)
  
```


B04

DVKAAA MACY11 27.732' 25-AUG-76 13:25 PAGE 54-37
DVKAAA.P11 143 NEW INSTRUCTION IN THIS SECTION IS ASP

*** SEQ 0040

(2) 005732 000000

HALT

: WRONG RESULT IN R3 OR WRONG SEQUENCE

```

5975
(2)
5976
(3)
5977
(2)
5978
(2)
5979
(2)
5980
(2)
5981
(2)
5982
(2)
5983
(2)
5984
(2)
5985
(2)
5986
(2)
5987
(2)
5988
(2)
5989
(2)
5990
(2)
5991
(2)
5992
(2)
5993
(2)
5994
(2)
5995
(2)
5996
(2)
5997
(2)
5998
(2)
5999
(2)
6000
(2)
6001
(2)
6002
(2)
6003
(2)
6004
(2)
6005
(2)
6006
(2)
6007
(2)
6008
(2)
6009
(2)
6010
(2)
6011
(2)
6012
(2)
6013
(2)
6014
(2)
6015
(2)
6016
(2)

```

```

*****
:TEST: 44      NEW INSTRUCTION IN THIS SECTION IS ADC
*****

```

```

ADCO:
      CMP      (R5),#44
      BEQ      2$      ; IF IN WRONG SEQUENCE GO TO HLT BELOW
      MOV      #155,-(R5)
      INC      -(R5)
      HALT     ; PROGRAM IS IN WRONG SEQUENCE
2$:   INC      (R5)
      CLR      R0      ; CLEAR THE REGISTER
      CCC     ; CLEAR FLAGS
      ADC      R0      ; ADD C BIT = 0
      JSR      PC,#$C04 ; CHECK FOR CC = 4
      SEC     ; C=1
      ADC      R0      ; ADD C BIT=1
      SEC     ; C=1
      ADC      R0      ; AGAIN
      JSR      PC,#$C00 ; CHECK FOR CC = 0
      CMP      #2,R0   ; CHECK IT
      BEQ      4$      ; CONTINUE IF OK
      MOV      #77777,R0 ; ADC INSTRUCTION FAILED
      SEC     ; LOAD LARGEST POSITIVE NUMBER
      ADC      R0      ; C=1
      JSR      PC,#$C12 ; ADD C BIT=1
      CMP      #100000,R0 ; CHECK FOR CC = 12
      BEQ      6$      ; CHECK IT
      MOV      #157,-(R5) ; FAILED
      INC      -(R5)
      HALT     ; ADC INSTRUCTION FAILED
6$:   MOV      #-1,R0  ; LOAD -1
      SEC     ; C=1
      ADC      R0      ; ADD C BIT=1
      JSR      PC,#$C05 ; CHECK FOR CC = 5

```

```

*****
:TEST: 45      NEW INSTRUCTION IN THIS SECTION IS SBC
*****

```

```

SBCO:
      CMP      (R5),#45
      BEQ      1$      ; IF IN WRONG SEQUENCE GO TO HLT
      MOV      #160,-(R5)
      INC      -(R5)
      HALT     ; TEST IS IN WRONG SEQUENCE
1$:   INC      (R5)
      MOV      #3,R1   ; LOAD REGISTER
      CCC     ; CLEAR FLAGS
      SBC      R1      ; SUBTRACT C BIT=0
      JSR      PC,#$C00 ; CHECK FOR CC = 0

```

| | | | | | | | | |
|------|--------|--------|--------|-----|------|------------|---|------------------------|
| 6017 | 006122 | 022701 | 000003 | | CMP | #3,R1 | : | CHECK IT |
| 6018 | 006126 | 001404 | | | BEO | 25 | : | CONTINUE IF OK |
| 6019 | 006130 | 012745 | 000161 | | MOV | #161,-R5 | | |
| (2) | 006134 | 005245 | | | INC | -(R5) | | |
| (2) | 006136 | 000000 | | | HALT | | : | SBC INSTRUCTION FAILED |
| 6020 | 006140 | 000261 | | 25: | SEC | | : | C=1 |
| 6021 | 006142 | 005601 | | | SBC | R1 | : | SUBTRACT C BIT=1 |
| 6022 | 006144 | 000261 | | | SEC | | : | C=1 |
| 6023 | 006146 | 005601 | | | SBC | R1 | : | |
| 6024 | 006150 | 004737 | 017062 | | JSR | PC,#5000 | : | CHECK FOR CC = 0 |
| 6025 | 006154 | 022701 | 000001 | | CMP | #1,R1 | : | CHECK IT |
| 6026 | 006160 | 001404 | | | BEO | 35 | : | CONTINUE IF OK |
| 6027 | 006162 | 012745 | 000162 | | MOV | #162,-(R5) | | |
| (2) | 006166 | 005245 | | | INC | -(R5) | | |
| (2) | 006170 | 000000 | | | HALT | | : | SBC INSTRUCTION FAILED |
| 6028 | 006172 | 000261 | | 35: | SEC | | : | C=1 |
| 6029 | 006174 | 005601 | | | SBC | R1 | : | SUBTRACT C BIT=1 |
| 6030 | 006176 | 004737 | 017164 | | JSR | PC,#5004 | : | CHECK FOR CC = 4 |
| 6031 | 006202 | 000261 | | | SEC | | : | C=1 |
| 6032 | 006204 | 005601 | | | SBC | R1 | : | SUBTRACT C BIT = 1 |
| 6033 | 006206 | 004737 | 017272 | | JSR | PC,#50011 | : | CHECK FOR CC = 11 |
| 6034 | 006212 | 022701 | 177777 | | CMP | #-1,R1 | : | CHECK IT |
| 6035 | 006216 | 001404 | | | BEO | 45 | : | CONTINUE IF F OK |
| 6036 | 006220 | 012745 | 000163 | | MOV | #163,-(R5) | | |
| (2) | 006224 | 005245 | | | INC | -(R5) | | |
| (2) | 006226 | 000000 | | | HALT | | : | SBC INSTRUCTION FAILED |
| 6037 | 006230 | 012701 | 100000 | 45: | MOV | #100000,R1 | : | LOAD R1 |
| 6038 | 006234 | 000261 | | | SEC | | : | C=1 |
| 6039 | 006236 | 005601 | | | SBC | R1 | : | SUBTRACT C BIT = 1 |
| 6040 | 006240 | 004737 | 017122 | | JSR | PC,#5002 | : | CHECK FOR CC = 2 |

E04

DVKAAA MACY11 27(732) 25-AUG-76 13:25 PAGE 54-40
DVKAAA.P11 T46 NEW INSTRUCTION IN THIS SECTION IS SXT

*** SEQ 0043

6041
(2)
(3)
6042
6043 006244
(2) 006244 021527 000046
6044 006250 001024
6045 006252 005215
6046 006254 005002
6047 006256 000277
6048 006260 000254
6049 006262 006702
6050 006264 004737 017206
6051 006270 005702
6052 006272 001404
6053 006274 012745 000164
(2) 006300 005245
(2) 006302 000000
6054 006304 000273
6055 006306 006702
6056 006310 004737 017272
6057 006314 022702 177777
6058 006320 001404
6059 006322
(2) 006322 012745 000165
(2) 006326 005245
(2) 006330 000000
6060
6061
6062
6063
(2)
(3)
6064
6065 006332
(2) 006332 021527 000047
6066 006336 001031
6067 006340 005215
6068 006342 012703 125125
6069 006346 000277
6070 006350 000250
6071 006352 000303
6072 006354 004737 017252
6073 006360 022703 052652
6074 006364 001404
6075 006366 012745 000166
(2) 006372 005245
(2) 006374 000000
6076 006376 012703 000377
6077 006402 000277
6078 006404 000244
6079 006406 000303
6080 006410 004737 017164
6081 006414 022703 177400
6082 006420 001404
6083 006422

: *TEST: 46 NEW INSTRUCTION IN THIS SECTION IS SXT
: *****

SXT0:
CMP (R5), #46
BNE ESXT0 ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
1S: INC (R5)
CLR R2 ; CLEAR REGISTER
SCC
CLNZ
SXT R2 ; SIGN EXTEND
JSR PC, @#SCC5 ; CHECK FOR CC = 5
JST R2 ; REG. 2 SHOULD STILL BE 0
BEQ 2S ; CONTINUE IF OK
MOV #164, -(R5)
INC -(R5)
2S: HALT ; SXT INSTRUCTION FAILED
SENV ; SET N, V & C BITS
SXT R2 ; SIGN EXTEND
JSR PC, @#SCC11 ; CHECK FOR CC = 11
CMP #-1, R2 ; REG. 2 SHOULD NOW HAVE -1
BEQ SWAB0 ; CONTINUE IF OK
ESXT0: MOV #165, -(R5)
INC -(R5)
HALT ; WRONG RESULT IN R2 OR WRONG SEQUENCE

: *TEST: 47 NEW INSTRUCTION IN THIS SECTION IS SWAB
: *****

SWAB0:
CMP (R5), #47
BNE ESWAB0 ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
INC (R5)
MOV #125125, R3 ; LOAD BIT PATTERN INTO REGISTER
SCC
CLN
SWAB R3 ; SWAP BYTES OF REGISTER
JSR PC, @#SCC10 ; CHECK FOR CC = 10
CMP #52652, R3 ; CHECK IT
BEQ 1S ; CONTINUE IF OK
MOV #166, -(R5)
INC -(R5)
1S: HALT ; SWAB INSTRUCTION FAILED
MOV #377, R3
SCC
CLZ
SWAB R3 ; CHECK FOR CC = 4
JSR PC, @#SCC4
CMP #177400, R3
BEQ XORD
ESWAB0:

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DVKAAA.P11 T47 NEW INSTRUCTION IN THIS SECTION IS SWAB

*** SEQ 0044

| | | | | | |
|-----|--------|--------|--------|------|-------------|
| (2) | 006422 | 012745 | 000167 | MOV | #167, -(R5) |
| (2) | 006426 | 005245 | | INC | -(R5) |
| (2) | 006430 | 000000 | | HALT | |

; WRONG RESULT IN R3 OR WRONG SEQUENCE

```

6084
(2)
(3)
6085
6086 006432
(2) 006432 021527 000050
6087 006436 001034
6088 006440 005215
6089 006442 012704 177777
6090 006446 012703 177777
6091 006452 000277
6092 006454 074403
6093 006456 004737 017206
6094 006462 012703 077777
6095 006466 010400
6096 006470 000263
6097 006472 000244
6098 006474 074003
6099 006476 004737 017272
6100 006502 012702 125252
6101 006506 012704 052525
6102 006512 000277
6103 006514 074204
6104 006516 004737 017272
6105 006522 022704 177777
6106 006526 001404
6107 006530
(2) 006530 012745 000170
(2) 006534 005245
(2) 006536 000000
6108
6109
6110
6111
(2)
(3)
6112
6113 006540
(2) 006540 021527 000051
6114 006544 001055
6115 006546 005215
6116 006550 012701 021421
6117 006554 060101
6118 006556 004737 017062
6119 006562 022701 043042
6120 006566 001404
6121 006570 012745 000171
(2) 006574 005245
(2) 006576 000000
6122 006600 012700 156357
6123 006604 060000
6124 006606 004737 017272
6125 006612 022700 134736
6126 006616 001404
6127 006620 012745 000172
(2) 006624 005245

```

```

*****
*TEST: 50 NEW INSTRUCTION IN THIS SECTION IS XOR
*****

```

```

XORO:
      CMP      (R5),#50
      BNE     EXOR0      ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
      INC     (R5)
      MOV     #-1,R4      ; LOAD REGISTERS
      MOV     #-1,R3
      SCC
      XOR     R4,R3      ; SHOULD PRODUCE 0'S IN REG. 3
      JSR    PC,2#SCCS   ; CHECK FOR CC = 5
      MOV     #77777,R3
      MOV     R4,RC      ; PLACE A -1 IN R0
      SEVC
      CLZ
      XOR     R0,R3
      JSR    PC,2#SCC11  ; CHECK FOR CC = 11
      MOV     #12525,R2  ; LOAD REGISTERS
      MOV     #52525,R4
      SCC
      XOR     R2,R4      ; SHOULD PRODUCE ALL 1'S IN REG. 4
      JSR    PC,2#SCC11  ; CHECK FOR CC = 11
      CMP     #-1,R4     ; CHECK IT
      BEQ
      ADD0
      EXORO:
      MOV     #170,-(R5)
      INC     -(R5)
      HALT
      ; WRONG RESULT IN R4 OR WRONG SEQUENCE

```

```

*****
*TEST: 51 NEW INSTRUCTION IN THIS SECTION IS ADD
*****

```

```

ADD0:
      CMP     (R5),#51
      BNE     EADD0     ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
      INC     (R5)
      MOV     #21421,R1  ; LOAD REGISTERS
      ADD     R1,R1
      JSR    PC,2#SCC0   ; CHECK FOR CC = 0
      CMP     #43042,R1 ; CHECK IT
      BEQ     IS        ; CONTINUE IF OK
      MOV     #171,-(R5)
      INC     -(R5)
      HALT
      ; ADD INSTRUCTION FAILED
      IS:
      MOV     #-21421,R0 ; LOAD REGISTERS
      ADD     R0,R0
      JSR    PC,2#SCC11  ; CHECK FOR CC = 11
      CMP     #-43042,R0 ; CHECK IT
      BEQ     2$        ; CONTINUE IF OK
      MOV     #172,-(R5)
      INC     -(R5)

```

H04

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DVKAAA.P11 T51 NEW INSTRUCTION IN THIS SECTION IS ADD

*** SEQ 0046

| | | | | | | |
|------|--------|--------|--------|------------|------------|--|
| (2) | 006626 | 000000 | | HALT | | : ADD INSTRUCTION FAILED |
| 6128 | 006630 | 012702 | 100000 | 2\$: MOV | #100000,R2 | : LOAD REGISTERS |
| 6129 | 006634 | 060202 | | ADD | R2,R2 | : ADD SHOULD RESULT AS 0'S |
| 6130 | 006636 | 004737 | 017230 | JSR | PC,2#5CC7 | : CHECK FOR CC = 7 |
| 6131 | 006642 | 012704 | 021421 | MOV | #21421,R4 | : LOAD REGISTERS |
| 6132 | 006646 | 012701 | 156357 | MOV | #-21421,R1 | : ADD SHOULD RESULT AS 0'S |
| 6133 | 006652 | 060401 | | ADD | R4,R1 | : CONTINUE IF OK |
| 6134 | 006654 | 001404 | | BEQ | 3\$ | |
| 6135 | 006656 | 012745 | 000173 | MOV | #173,-(R5) | |
| (2) | 006662 | 005245 | | INC | -(R5) | |
| (2) | 006664 | 000000 | | HALT | | : ADD INSTRUCTION FAILED |
| 6136 | 006666 | 005404 | | 3\$: NEG | R4 | : SWITCH SOURCE AND DESTINATION |
| 6137 | 006670 | 012701 | 021421 | MOV | #21421,R1 | |
| 6138 | 006674 | 060104 | | ADD | R1,R4 | : SHOULD RESULT AS 0'S |
| 6139 | 006676 | 001404 | | BEQ | SUB0 | : CONTINUE IF OK |
| 6140 | 006700 | | | | | |
| (2) | 006700 | 012745 | 000174 | EADD0: MOV | #174,-R5) | |
| (2) | 006704 | 005245 | | INC | -(R5) | |
| (2) | 006706 | 000000 | | HALT | | : WRONG RESULT IN R1 OR WRONG SEQUENCE |

```

6141          ;*****
        (2)    ;*TEST: 52      NEW INSTRUCTION IN THIS SECTION IS SUB
        (3)    ;*****
    
```

```

6142
6143 006710      SUBO:
        (2) 006710 021527 000052      CMP      (R5),#52
6144 006714 001404      BEQ      2$      ; IF IN WRONG SEQUENCE GO TO HLT BELOW
6145 006716 012745 000175      MOV      #175,-(R5)
        (2) 006722 005245      INC      -(R5)
        (2) 006724 000000      HALT     ; PROGRAM IS IN WRONG SEQUENCE
6146 006726 005215      2$:      INC      (R5)
6147 006730 012702 021421      MOV      #21421,R2      ; LOAD REGISTERS
6148 006734 012703 156357      MOV      #-21421,R3
6149 006740 160203      SUB     R2,R3      ; RESULT SHOULD=-43042
6150 006742 004737 017252      JSR     PC,2$S0C10  ; CHECK FOR CC = 10
6151 006746 022703 134736      CMP     #-43042,R3  ; CHECK IT
6152 006752 001404      BEQ     4$      ; CONTINUE IF OK
6153 006754 012745 000176      MOV     #176,-(R5)
        (2) 006760 005245      INC     -(R5)
        (2) 006762 000000      HALT     ; SUB INSTRUCTION FAILED
6154 006764 012703 021421      4$:      MOV     #21421,R3      ; LOAD REGISTER
6155 006770 010204      MOV     R2,R4      ; NOW R4 = #21421
6156 006772 160403      SUB     R4,R3      ; RESULT SHOULD=0
6157 006774 001404      BEQ     6$
6158 006776 012745 000177      MOV     #177,-(R5)
        (2) 007002 005245      INC     -(R5)
        (2) 007004 000000      HALT     ; SUB INSTRUCTION FAILED
6159 007006 012703 177777      6$:      MOV     #-1,R3      ; LOAD REGISTERS
6160 007012 012702 077777      MOV     #77777,R2  ; LOAD REGISTERS
6161 007016 160302      SUB     R3,R2      ; RESULT SHOULD BE 100000 AND OVERFLOW
6162 007020 004737 017334      JSR     PC,2$S0C13  ; CHECK FOR CC = 13
6163 007024 022702 100000      CMP     #100000,R2 ; CHECK IT
6164 007030 001404      BEQ     8$      ; CONTINUE IF OK
6165 007032 012745 000200      MOV     #200,-(R5)
        (2) 007036 005245      INC     -(R5)
        (2) 007040 000000      HALT     ; SUB INSTRUCTION FAILED
6166 007042 012704 177777      8$:      MOV     #-1,R4
6167 007046 160304      SUB     R3,R4
6168 007050 004737 017164      JSR     PC,2$S0C4  ; CHECK FOR CC = 4
6169
6170
    
```

```

6171          ;*****
        (2)    ;*TEST: 53      NEW INSTRUCTIONS IN THARE SECTION IS MTPS & MFPS
        (3)    ;*****
    
```

```

6172
6173 007054      PSW:
        (2) 007054 021527 000053      CMP     (R5),#53
6174 007060 001032      BNE     EPSW      ; IF IN WRONG SEQUENCE THEN GO TO HLT AT THE END OF THE
6175 007062 005215      1$:      INC     (R5)
6176 007064 012701 177777      MOV     #177777,R1
6177 007070 005000      CLR     R0
6181 007072      MTPS  R0      ; SET PSW TO 0
        (1) 007072 106400      .WORD  106400!..C
6182 007074 004737 017062      JSR     PC,2$S0C0  ; CHECK FOR CC = 0
6183 007100      MFPS  R1      ; MOVE PSW TO R1
        (1) 007100 106701      .WORD  106700!..C
    
```


| | | | | | | |
|------|--------|--------|--------|-------|-------------|--|
| 6187 | 007102 | 001404 | | BEQ | 25 | ; CONTINUE IF BIT 8 OF PSW WAS EXTENDED IN R1 |
| 6188 | 007104 | 012745 | 000201 | MOV | #201, -(R5) | |
| (2) | 007110 | 005245 | | INC | -(R5) | |
| (2) | 007112 | 000000 | | HALT | | ; MTPS OR MFPS INSTRUCTION FAILED |
| 6189 | 007114 | 004737 | 017164 | JSR | PC, 2#5CC4 | ; CHECK FOR CC = 4 |
| 6190 | 007116 | 012700 | 000377 | MOV | #377, R0 | |
| 6194 | 007124 | | | MTPS | R0 | ; SET PSW TO 357 SINCE MTPS DOES NOT SET T BIT |
| (1) | 007124 | 106400 | | .WORD | 106400!..C | |
| 6195 | 007126 | 004737 | 017354 | JSR | PC, 2#5CC17 | ; CHECK FOR CC = 17 |
| 6196 | 007132 | | | MFPS | R1 | ; MOVE PSW TO R1 |
| (1) | 007132 | 106701 | | .WORD | 106700!..C | |
| 6200 | 007134 | 004737 | 017272 | JSR | PC, 2#5CC11 | ; CHECK FOR CC = 11 IC BIT SHOULD NOT BE EFFECTED BY MFP |
| 6201 | 007140 | 022701 | 177757 | CMP | #177757, R1 | ; CHECK TO SEE IF BIT 8 OF PSW WAS EXTENDED *HRJ R1 |
| 6202 | 007144 | 001404 | | BEQ | MODE0 | |
| 6203 | 007146 | | | | | |
| (2) | 007146 | 012745 | 000202 | MOV | #202, -(R5) | |
| (2) | 007152 | 005245 | | INC | -(R5) | |
| (2) | 007154 | 000000 | | HALT | | ; MTPS OR MFPS INSTRUCTION FAILED OR WRONG SEQUENCE |

EPSW:

6208
6209
6210
6211
6212
6213
(2)
(3)
6214
6215 007156
(2) 007156 021527 000054
6216 007162 001063
6217 007164 005215
6218 007166 112700 000252
6219 007172 110001
6220 007174 110102
6221 007176 122702 000252
6222 007202 001404
6223 007204 012745 000203
(2) 007210 005245
(2) 007212 000000
6224 007214 012700 125252
6225 007220 010001
6226 007222 010102
6227 007224 022702 125252
6228 007230 001404
6229 007232 012745 000204
(2) 007236 005245
(2) 007240 000000
6230
6231 007242 012700 000440
6232 007246 012701 000442
6233 007252 012702 000444
6234 007256 005067 171162
6235 007262 112710 000125
6236 007266 111011
6237 007270 111112
6238 007272 122767 000125 171144
6239 007300 001404
6240 007302 012745 000205
(2) 007306 005245
(2) 007310 000000
6241 007312 012710 052525
6242 007316 011011
6243 007320 011112
6244 007322 022767 052525 171114
6245 007330 001404
6246 007332
(2) 007332 012745 000206
(2) 007336 005245
(2) 007340 000000
6247
6248
6249
6250
6251

LSI-11 INSTRUCTIONS NOT MODE 0

*TEST: 54 CHECK MODES 0 & 1 USING THE MOVB AND MOV INSTRUCTIONS

```
MODE0:  CMP      (R5), #54
        BNE     EMODE0      ; IF IN WRONG SEQUENCE GO TO HL* AT THE END OF THE TEST
        INC     (R5)
        MOVB   #252, R0     ; LOAD REGISTERS
        MOVB   R0, R1
        MOVB   R1, R2
        CMPB   #252, R2     ; CHECK IT
        BEQ    1$          ; OK, CONTINUE
        MOV    #203, -(R5)
        INC    -(R5)
        HALT
1$:     MOV    #125252, R0   ; MOV INSTRUCTION FAILED IN MODE 0
        MOV    R0, R1
        MOV    R1, R2
        CMPB   #125252, R2  ; CHECK IT
        BEQ    MODE1       ; OK, CONTINUE
        MOV    #204, -(R5)
        INC    -(R5)
        HALT
        ; MOV INSTRUCTION FAILED IN MODE 0

MODE1:  MOV    #TEMP, R0    ; LOAD ADDRESSES INTO REGS.
        MOV    #TEMP1, R1
        MOV    #TEMP2, R2
        CLR    TEMP2
        MOVB   #125, (R0)   ; START CLEAN
        MOVB   (R0), (R1)  ; LOAD THE LOCATIONS
        MOVB   (R1), (R2)  ; TEMP ----> TEMP1
        CMPB   #125, TEMP2 ; TEMP1 ----> TEMP2
        BEQ    1$          ; CHECK IT
        MOV    #205, -(R5) ; OK, CONTINUE
        INC    -(R5)
        HALT
1$:     MOV    #52525, (R0) ; MOV INSTRUCTION FAILED IN MODE 1
        MOV    (R0), (R1)  ; LOAD THE LOCATIONS
        MOV    (R1), (R2)  ; TEMP ----> TEMP1
        CMPB   #52525, TEMP2 ; TEMP1 ----> TEMP2
        BEQ    MODE2       ; CHECK IT
        MOV    #206, -(R5) ; OK, CONTINUE
        INC    -(R5)
        HALT
        ; MOV INSTRUCTION FAILED IN MODE 1
        ; OR WRONG SEQUENCE
```

L04

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DVKAAA.P11 T55 CHECK MODE 2 USING THE MOVB AND MOV INSTRUCTIONS

*** SEQ 0050

(2)
(3)
6252
6253 007342
(2) 007342 021527 000055
6254 007346 001050
6255 007350 005215
6256 007352 012700 000440
6257 007356 012701 000442
6258 007362 012702 000444
6259 007366 105022
6260 007370 112710 000252
6261 007374 112021
6262 007376 105201
6263 007400 111167 171034
6264 007404 105200
6265 007406 112021
6266 007410 124227 000252
6267 007414 001003
6268 007416 105767 171016
6269 007422 001404
6270 007424
(2) 007424 012745 000207
(2) 007430 005245
(2) 007432 000000
6271
6272 007434 005741
6273 007436 005022
6274 007440 012740 125252
6275 007444 012020
6276 007446 111067 170766
6277 007452 012121
6278 007454 024227 125252
6279 007460 001003
6280 007462 005767 170752
6281 007466 001404
6282 007470
(2) 007470 012745 000210
(2) 007474 005245
(2) 007476 000000
6283

:*TEST: 55 CHECK MODE 2 USING THE MOVB AND MOV INSTRUCTIONS
:*****

MODE2:
CMP (R5),#55
BNE EMODE2 ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
INC (R5)
MOV #TEMP,RO ; LOAD ADDRESSES
MOV #TEMP1,R1
MOV #TEMP2,R2
CLRB (R2)+ ; START CLEAN
MOVB #252,(RO) ; LOAD THE LOCATIONS
MOVB (RO)+,(R1)+ ; TEMP ---> TEMP1
INCB R1 ; MAKE IT EVEN
MOVB (R1),TEMP ; MORE 0'S INTO TEMP
INCB RO ; MAKE IT EVEN
MOVB (RO)+,(R1)+ ; TEMP1 ---> TEMP2
CMPB -(R2),#252 ; CHECK IT
BNE 1\$; FAILED
TSTB TEMP ; CHECK IT
BEQ 2\$; OK, CONTINUE

1\$:
MOV #207,-(R5)
INC -(R5)
HALT ; INSTRUCTIONS FAILED IN MODE 2

2\$:
TST -(R1)
CLR (R2)+ ; START CLEAN
MOV #125252,-(RO) ; LOAD LOCATIONS
MOV (RO)+,(RO)+ ; TEMP ---> TEMP1
MOV (RO),TEMP ; 0 ---> TEMP
MOV (R1)+,(R1)+ ; 125252 ---> TEMP2
CMPB -(R2),#125252 ; CHECK IT
BNE EMODE2 ; FAILED
TST TEMP ; CHECK IT
BEQ MODE3 ; OK, CONTINUE

EMODE2:
MOV #210,-(R5)
INC -(R5)
HALT ; INSTRUCTIONS FAILED IN MODE 2
; OR WRONG SEQUENCE

M04

DVKAAA MACY11 27(732) 25-AUG-76 13:25 PAGE 54-48
DVKAAA.P11 T56 CHECK MODE 3 USING THE MOVB AND MOV INSTRUCTIONS

*** SEQ 0051

6284
(2)
(3)

```
*****  
*TEST: 56 CHECK MODE 3 USING THE MOVB AND MOV INSTRUCTIONS  
*****
```

6285
6286 007500
(2) 007500 021527 000056
6287 007504 001066
6288 007506 005215
6289 007510 012767 000440 170712
6290 007516 012767 000442 170706
6291 007524 012767 000444 170702
6292 007532 012700 000430
6293 007536 012701 000432
6294 007542 105067 170676
6295 007546 112767 000125 170664
6296 007554 113031
6297 007556 113167 170656
6298 007562 113030
6299 007564 122767 000125 170652
6300 007572 001003
6301 007574 105767 170640
6302 007600 001404
6303 007602
(2) 007602 012745 000211
(2) 007606 005245
(2) 007610 000000
6304 007612 005067 170626
6305 007616 012767 052525 170614
6306 007624 012700 000430
6307 007630 012701 000432
6308 007634 013030
6309 007636 013067 170576
6310 007642 013131
6311 007644 022767 052525 170572
6312 007652 001003
6313 007654 005767 170560
6314 007660 001404
6315 007662
(2) 007662 012745 000212
(2) 007666 005245
(2) 007670 000000

MODE3:

```
CMP (R5),#56  
BNE EMODE3 ; IF IN WRONG SEQUENCE GO TO HLT ABOVE  
INC (R5)  
MOV #TEMP,ADR ; LOAD ADDRESSES  
MOV #TEMP1,ADR1  
MOV #TEMP2,ADR2  
MOV #ADR,R0 ; LOAD ADDRESSES OF ADDRESSES  
MOV #ADR1,R1  
CLRB TEMP2 ; START CLEAN  
MOVB #125,TEMP  
MOVB @ (R0)+,@ (R1)+ ; TEMP ---> TEMP1  
MOVB @ (R1)+,TEMP ; TEMP2 ---> TEMP  
MOVB @ (R0)+,@ (R0)+ ; TEMP1 ---> TEMP2  
CMPB #125,TEMP2 ; CHECK IT  
BNE IS ; FAILED  
TSTB TEMP ; CHECK IT  
BEQ 2$ ; OK, CONTINUE
```

1\$:

```
MOV #211,-(R5)  
INC -(R5)  
HALT
```

2\$:

```
CLR TEMP2 ; INSTRUCTIONS FAILED IN MODE 3  
MOV #52525,TEMP ; START CLEAN  
MOV #ADR,R0 ; LOAD LOCATIONS  
MOV #ADR1,R1 ; LOAD ADDRESSES OF ADDRESSES  
MOV @ (R0)+,@ (R0)+ ; TEMP ---> TEMP1  
MOV @ (R0)+,TEMP ; TEMP2 ---> TEMP  
MOV @ (R1)+,@ (R1)+ ; TEMP1 ---> TEMP2  
CMPB #52525,TEMP2 ; CHECK IT  
BNE EMODE3 ; FAILED  
TSTB TEMP ; CHECK IT  
BEQ MODE4 ; OK, CONTINUE
```

EMODE3:

```
MOV #212,-(R5)  
INC -(R5)  
HALT
```

; INSTRUCTIONS FAILED IN MODE 3

6316
6317
6318
6319
(2)
(3)

```
*****  
*TEST: 57 CHECK MODE 4 USING THE MOVB AND MOV INSTRUCTIONS  
*****
```

6320
6321 007672
(2) 007672 021527 000057
6322 007676 001120
6323 007700 005215
6324 007702 105067 170532
6325 007706 012700 000440
6326 007712 012701 000442
6327 007716 012702 000444

MODE4:

```
CMP (R5),#57  
BNE EMODE4 ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST  
INC (R5)  
CLRB TEMP ; START CLEAN  
MOV #TEMP,R0 ; LOAD ADDRESSES  
MOV #TEMP1,R1  
MOV #TEMP2,R2
```

| | | | | | | | | |
|------|--------|--------|--------|---------|-------|----------------|--|---------------------------------|
| 6328 | 007722 | 005202 | | | INC | R2 | | ; ADJUST THE POINTER |
| 6329 | 007724 | 021267 | 170515 | | CMP | (R2), TEMP2+1 | | |
| 6330 | 007730 | 001404 | | | BEQ | 1\$ | | |
| 6331 | 007732 | 012745 | 000213 | | MOV | #213, -(R5) | | |
| (2) | 007736 | 005245 | | | INC | -(R5) | | |
| (2) | 007740 | 000000 | | | HALT | | | ; INSTRUCTIONS FAILED IN MODE 4 |
| 6332 | 007742 | 112742 | 000252 | 1\$: | MOV B | #252, -(R2) | | ; LOAD TEMP2 |
| 6333 | 007746 | 005201 | | | INC | R1 | | ; ADJUST THE POINTERS |
| 6334 | 007750 | 005202 | | | INC | R2 | | |
| 6335 | 007752 | 114241 | | | MOV B | -(R2), -(R1) | | ; TEMP2 ---> TEMP1 |
| 6336 | 007754 | 005200 | | | INC | R0 | | ; ADJUST THE POINTERS |
| 6337 | 007756 | 005202 | | | INC | R2 | | |
| 6338 | 007760 | 114042 | | | MOV B | -(R0), -(R2) | | ; TEMP ---> TEMP2 |
| 6339 | 007762 | 105200 | | | INCB | R0 | | ; ADJUST THE POINTERS |
| 6340 | 007764 | 021067 | 170451 | | CMP | (R0), TEMP+1 | | |
| 6341 | 007770 | 001404 | | | BEQ | 2\$ | | |
| 6342 | 007772 | 012745 | 000214 | | MOV | #214, -(R5) | | |
| (2) | 007776 | 005245 | | | INC | -(R5) | | |
| (2) | 010000 | 000000 | | | HALT | | | ; INSTRUCTIONS FAILED IN MODE 4 |
| 6343 | 010002 | 105201 | | 2\$: | INCB | R1 | | |
| 6344 | 010004 | 114140 | | | MOV B | - R1), -(R0) | | ; TEMP1 ----> TEMP |
| 6345 | 010006 | 122767 | 000252 | 170424 | CMP B | #252, TEMP | | ; CHECK IT |
| 6346 | 010014 | 001003 | | | BNE | 3\$ | | ; FAILED |
| 6347 | 010016 | 105767 | 170422 | | TST B | TEMP2 | | ; CHECK IT |
| 6348 | 010022 | 001404 | | | BEQ | 4\$ | | ; OK, CONTINUE |
| 6349 | 010024 | | | 3\$: | | | | |
| (2) | 010024 | 012745 | 000215 | | MOV | #215, -(R5) | | |
| (2) | 010030 | 005245 | | | INC | -(R5) | | |
| (2) | 010032 | 000000 | | | HALT | | | ; INSTRUCTIONS FAILED IN MODE 4 |
| 6350 | 010034 | 005067 | 170400 | 4\$: | CLR | TEMP | | ; START CLEAN |
| 6351 | 010040 | 012700 | 000440 | | MOV | #TEMP, R0 | | ; LOAD ADDRESSES |
| 6352 | 010044 | 012701 | 000442 | | MOV | #TEMP1, R1 | | |
| 6353 | 010050 | 012702 | 000444 | | MOV | #TEMP2, R2 | | |
| 6354 | 010054 | 005722 | | | TST | (R2)+ | | ; ADJUST THE POINTER |
| 6355 | 010056 | 021267 | 170364 | | CMP | (R2), TEMP2+2 | | |
| 6356 | 010062 | 001404 | | | BEQ | 5\$ | | |
| 6357 | 010064 | 012745 | 000216 | | MOV | #216, -(R5) | | |
| (2) | 010070 | 005245 | | | INC | -(R5) | | |
| (2) | 010072 | 000000 | | | HALT | | | ; INSTRUCTIONS FAILED IN MODE 4 |
| 6358 | 010074 | 012742 | 125252 | 5\$: | MOV | #125252, -(R2) | | ; LOAD TEMP2 |
| 6359 | 010100 | 005721 | | | TST | (R1)+ | | ; ADJUST THE POINTERS |
| 6360 | 010102 | 005722 | | | TST | (R2)+ | | |
| 6361 | 010104 | 014241 | | | MOV | -(R2), -(R1) | | ; TEMP2 ----> TEMP1 |
| 6362 | 010106 | 005720 | | | TST | (R0)+ | | ; ADJUST POINTERS |
| 6363 | 010110 | 005722 | | | TST | (R2)+ | | |
| 6364 | 010112 | 014042 | | | MOV | -(R0), -(R2) | | ; TEMP ---> TEMP2 |
| 6365 | 010114 | 005720 | | | TST | (R0)+ | | ; ADJUST THE POINTERS |
| 6366 | 010116 | 005721 | | | TST | (R1)+ | | |
| 6367 | 010120 | 014140 | | | MOV | -(R1), -(R0) | | ; TEMP1 ----> TEMP |
| 6368 | 010122 | 022767 | 125252 | 170310 | CMP | #125252, TEMP | | ; CHECK IT |
| 6369 | 010130 | 001003 | | | BNE | EMODE4 | | ; FAILED |
| 6370 | 010132 | 005767 | 170306 | | TST | TEMP2 | | ; CHECK IT |
| 6371 | 010136 | 001404 | | | BEQ | MODE5 | | ; OK, CONTINUE |
| 6372 | 010140 | | | EMODE4: | | | | |
| (2) | 010140 | 012745 | 000217 | | MOV | #217, -(R5) | | |
| (2) | 010144 | 005245 | | | INC | -(R5) | | |

B05

DVKAAA MACY11 27.732' 25-AUG-76 13:25 PAGE 54-50
DVKAAA.P11 757 CHECK MODE 4 USING THE MOVE AND MOV INSTRUCTIONS

*** SEQ 0053

21 010146 000000
63.3

HALT

: INSTRUCTIONS FAILED IN MODE 4
: OR WRONG SEQUENCE

C05

DVKAAA MACY11 27(732) 25-AUG-76 13:25 PAGE 54-51
 DVKAAA.P11 T60 CHECK MODE 5 USING THE MOVB AND MOV INSTRUCTIONS

*** SEQ 0054

```

6374 (2)
6375 (2)
6376 (2)
6377 (2)
6378 (2)
6379 (2)
6380 (2)
6381 (2)
6382 (2)
6383 (2)
6384 (2)
6385 (2)
6386 (2)
6387 (2)
6388 (2)
6389 (2)
6390 (2)
6391 (2)
6392 (2)
6393 (2)
6394 (2)
6395 (2)
6396 (2)
6397 (2)
6398 (2)
6399 (2)
6400 (2)
6401 (2)
6402 (2)
6403 (2)
6404 (2)
6405 (2)
6406 (2)
6407 (2)
6408 (2)
6409 (2)
6410 (2)
6411 (2)
6412 (2)
6413 (2)
6414 (2)
6415 (2)
6416 (2)
6417 (2)
6418 (2)
6419 (2)
6420 (2)
  
```

```

*****
: TEST: 60 CHECK MODE 5 USING THE MOVB AND MOV INSTRUCTIONS
*****
  
```

```

MODE5:
CMP (R5),#60
BNE EMODE5 ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
INC (R5)
CLRB TEMP ; START CLEAN
MOV #TEMP,ADR ; LOAD ADDRESSES
MOV #TEMP1,ADR1
MOV #TEMP2,ADR2
MOV #ADR,R0 ; LOAD ADDRESSES OF ADDRESSES
MOV #ADR1,R1
MOV #ADR2,R2
TST (R2)+ ; ADJUST THE POINTER
MOVB #125,(R2) ; LOAD TEMP2
CMP (R1)+,(R2)+ ; ADJUST THE POINTERS
MOVB (R2)+,(R1) ; TEMP2 ---> TEMP1
CMP (R0)+,(R2)+ ; ADJUST THE POINTERS
MOVB (R0)+,(R2) ; TEMP ---> TEMP2
CMPB (R0)+,(R2) ; ADJUST THE POINTERS
BEQ 1$ ; CHECK IT
MOV #220,-(R5)
INC -(R5)
HALT
1$: CMP (R1)+,(R0)+ ; ADJUST THE POINTERS
MOVB (R1)+,(R0) ; TEMP1 ---> TEMP
CMPB #125,TEMP ; CHECK IT
BNE 2$ ; FAILED
TSTB TEMP2 ; CHECK IT
BEQ 3$ ; OK, CONTINUE
2$: MOV #221,-(R5)
INC -(R5)
HALT
3$: CLR TEMP ; INSTRUCTIONS FAILED IN MODE 5
MOV #ADR,R0 ; START CLEAN
MOV #ADR1,R1 ; LOAD ADDRESSES OF ADDRESSES
MOV #ADR2,R2
TST (R2)+ ; ADJUST THE POINTER
MOVB #52525,(R2) ; LOAD TEMP2
CMP (R1)+,(R2)+ ; ADJUST THE POINTERS
MOVB (R2)+,(R1) ; TEMP2 ---> TEMP1
CMP (R0)+,(R2)+ ; ADJUST THE POINTERS
MOVB (R0)+,(R2) ; TEMP ---> TEMP2
CMP (R0)+,(R1)+ ; ADJUST THE POINTERS
MOVB (R1)+,(R0) ; TEMP1 ---> TEMP
CMP #52525,TEMP ; CHECK IT
BNE EMODE5 ; FAILED
TST TEMP2 ; CHECK IT
BEQ MODE6 ; OK, CONTINUE
EMODE5:
MOV #222,-(R5)
INC -(R5)
  
```

(2) 010376 000000
6420
6421
6422
6423
6424
6425
6426
6427
6428
6429
6430
6431
6432
6433
6434
6435
6436
6437
6438
6439
6440
6441
6442
6443
6444
6445
6446
6447
6448
6449
6450

010400 021527 000061
010401 001055
010404 005215
010406 005067 170030
010410 012700 000440
010414 012701 000442
010420 012702 000444
010424 112760 000252 000000
010430 112760 000252 000000
010436 022767 125252 167766
010444 001012
010452 116062 000001 000000
010454 116160 000002 000005
010462 022767 125252 167746
010470 001404
010500
010500 012745 000223
010504 005245
010506 000000
010510 005067 167726
010514 012760 052525 000000
010522 016260 177774 000002
010530 022767 052525 167704
010536 001404
010540
010540 012745 000224
010544 005245
010546 000000

HALT : INSTRUCTIONS FAILED IN MODE 5
: OR WRONG SEQUENCE

*TEST: 61 CHECK MODE 6 USING THE MOVB AND MOV INSTRUCTIONS

MODE6:
CMP (R5),#61
BNE EMODE6 : IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
INC (R5)
CLR TEMP2 : START CLEAN
MOV #TEMP,R0 : LOAD ADDRESSES
MOV #TEMP1,R1
MOV #TEMP2,R2
MOVB #252,0(R0) : LOAD TEMP (LOW BYTE)
MOVB #252,1(R0) : LOAD TEMP (HIGH BYTE)
CMP #125252,TEMP : CHECK IT
BNE IS : FAILED
MOVB 1(R0),0(R2) : TEMP(H) ----> TEMP2(L)
MOVB 2(R1),5(R0) : TEMP2(L) ----> TEMP2(H)
CMP #125252,TEMP2 : CHECK IT
BEQ 25 : OK, CONTINUE

15: MOV #223,-(R5)
INC -(R5)
HALT : INSTRUCTIONS FAILED IN MODE 6
23: CLR TEMP1 : START CLEAN
MOV #52525,0(R0) : LOAD TEMP
MOV -4(R2),2(R0) : TEMP ----> TEMP1
CMP #52525,TEMP1 : CHECK IT
BEQ MODE7 : OK, CONTINUE

EMODE6: MOV #224,-(R5)
INC -(R5)
HALT : INSTRUCTIONS FAILED IN MODE 6
: OR WRONG SEQUENCE

E05

DVKAAA MACY11 27(732)
DVKAAA.P11 T62

25-AUG-76 13:25 PAGE 54-53
CHECK MODE 7 USING THE MOV8 AND MOV INSTRUCTIONS

*** SEQ 0056

6448
(2)
(3)
6449
6450
(2)
6451
6452
6453
6454
6455
6456
6457
6458
6459
6460
6461
6462
6463
6464
(2)
(2)
6465
6466
6467
6468
6469
(2)
(2)
6470

010550 021527 000062
010550 001052
010554 001052
010556 005215
010560 005067 167656
010564 012767 000440 167636
012572 012767 000442 167632
010600 012767 000444 167626
010606 012700 000430
010612 012701 000432
010616 012702 000434
010622 112770 000252 000000
010630 112770 177774 000002
010636 122767 000252 167576
010644 001404
010646 012745 000225
010652 005245
010654 000000
010656 012770 125252 000000
010664 012770 177774 000002
010672 022767 125252 167542
010700 001404
010702
010702 012745 000226
010706 005245
010710 000000

: TEST: 62 CHECK MODE 7 USING THE MOV8 AND MOV INSTRUCTIONS

MODE7:
CMP (R5), #62
BNE EMODE7 : IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
INC (R5)
CLR TEMP1 : START CLEAN
MOV #TEMP, ADR : LOAD ADDRESSES
MOV #TEMP1, ADR1
MOV #TEMP2, ADR2
MOV #ADR, R0 : LOAD ADDRESSES OF ADDRESSES
MOV #ADR1, R1
MOV #ADR2, R2
MOV8 #252, R0 : LOAD TEMP
MOV8 #4(R2), R2 : TEMP ----> TEMP1
CMPB #252, TEMP1 : CHECK IT
BEQ IS : OK, CONTINUE
MOV #225, -(R5)
INC -(R5)
HALT : MODE 7 IS FAILING
IS: MOV #125252, R0 : LOAD TEMP
MOV #4(R2), R2 : TEMP ----> TEMP1
CMPB #125252, TEMP1 : CHECK IT
BEQ TS'B1 : OK, CONTINUE
EMODE7:
MOV #226, -(R5)
INC -(R5)
HALT : INSTRUCTIONS FAILED IN MODE 7
: OR WRONG SEQUENCE

F05

DVKAAA MACY11 27(732) 25-AUG-76 13:25 PAGE 54-54
 DVKAAA.P11 CHECK BYTE INSTRUCTIONS, NOT DESTINATION MODE 0

*** SEQ 0057

6475
 6476
 6477
 6478
 6479
 5480
 6481
 (2)
 (2)
 6482
 6483
 (2)
 6484
 6485
 6486
 6487
 6488
 6489
 6490
 6491
 6492
 6493
 6494
 6495
 6496
 6497
 6498
 6499
 6500
 6501
 6502
 (2)
 (2)
 (2)
 6503
 6504
 6505
 (2)
 (2)
 (2)
 6506
 6507
 6508

```

:      CHECK BYTE INSTRUCTIONS, NOT DESTINATION MODE 0
:-----
:*****
:*TEST: 63      NEW INSTRUCTIONS USED IN THIS SECTION ARE TSTB, CLRB, MOVB
:*****
TSTB1:
      CMP      (R5), #63
      BNE     ETSTB1      ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF TEST
2$:   INC      (R5)
      MOV     #TEMP, R0   ; LOAD ADDRESSES
      MOV     #TEMP1, R1
      SCC
      CLR     (R0)        ; CLEAR THE LOCATION
      JSR     PC, @#SCC4  ; CHECK FOR CC = 4
      TST     (R0)        ; CHECK IT
      JSR     PC, @#SCC4  ; CHECK FOR CC = 4
      MOVB    #377, (R1)  ; LOAD THE LOCATION
      JSR     PC, @#SCC10 ; CHECK FOR CC = 10
      TST     (R1)        ; CHECK IT
      JSR     PC, @#SCC10 ; CHECK FOR CC = 10
      MOV     R0, R2      ; R2 IS NOW POINTING TO LOCATION TEMP
      MOVB    #200, 0(R2) ; PLACE #200 IN LOCATION TEMP
      MOVB    (R2)+, -(R1) ; MOVE #200 TO LOCATION TEMP+1
      CMP     -1(R1), #100200 ; CHECK THE DATA IN LOCATION TEMP
      BEQ     4$
      MOV     #227, -(R5)
      INC     -(R5)
      HALT
4$:   CMP     R1, R2
      BEQ     CMPB1
ETSTB1:
      MOV     #230, -(R5)
      INC     -(R5)
      HALT
      ; MOVB INSTRUCTION FAILED OR WRONG SEQUENCE
  
```

```

010712 021527 000063
010712 001042
010716 005215
010720 012700 000440
010722 012701 000442
010726 000277
010732 105010
010734 004737 017164
010736 105710
010742 004737 017164
010744 112711 000377
010750 004737 017252
010754 105711
010760 004737 017252
010762 010002
010766 112762 000200 000000
010770 112241
010776 026127 177777 100200
011000 001404
011006 012745 000227
011010 005245
011014 000000
011016 020102
011020 001404
011022 011024 000230
011024 012745
011030 005245
011032 000000
  
```

G05

DVKAAA MACY11 27(732)
DVKAAA.P11 T64

25-AUG-76 13:25 PAGE 54-55
NEW INSTRUCTIONS USED IN THIS SECTION ARE CMPB, BISB

*** SEQ 0058

```

6509 (2)
6510 (3)
6511 011034 021527 000064
(2) 011034 001032
6512 011040 005215
6513 011042 012701 000444
6514 011044 012702 000440
6515 011050 012711 000077
6516 011060 112704 000377
6517 011064 150412
6518 011064 150412
6519 011066 004737 017252
6520 011072 120412
6521 011074 001404
6522 011076 012745 000231
(2) 011102 005245
(2) 011104 000000
6523 011106 121112
6524 011110 100004
6525 011112 012745 000232
(2) 011116 005245
(2) 011120 000000
6526 011122 121211
6527 011124 100404
6528 011126
(2) 011126 012745 000233
(2) 011132 005245
(2) 011134 000000

```

```

*****
: TEST: 64 NEW INSTRUCTIONS USED IN THIS SECTION ARE CMPB, BISB
*****

```

```

CMPB1:
      CMP      (R5), #64
      BNE     ECMPB1      ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
1$:   INC      (R5)
      MOV     #TEMP2, R1
      MOV     #TEMP, R2      ; LOAD ADDRESS
      MOV     #77, (R1)      ; PLACE 77 IN LOCATION TEMP2
      MOVB   #377, R4      ; R4 SHOULD CONTAIN #177777
      BISB   R4, (R2)      ; LOAD LOCATION
      JSR    PC, #SCC10    ; CHECK FOR CC = 10
      CMPB   R4, (R2)      ; CHECK COMPARE
      BEQ    2$           ; CONTINUE IF OK
      MOV     #231, -(R5)
      INC     -(R5)
      HALT
2$:   CMPB   (R1), (R2)    ; BISB OR CMPB INSTRUCTION FAILED
      BPL    3$           ; CHECK IT AGAIN
      MOV     #232, -(R5)
      INC     -(R5)
      HALT
3$:   CMPB   (R2), (R1)    ; CMPB INSTRUCTION FAILED [WRONG CC]
      BMI   BICB1        ; ONCE MORE
      BICB1
      MOV     #233, -(R5)
      INC     -(R5)
      HALT

```

```

6529 (2)
6530 (2)
6531 (2)
6532 (2)
6533 (2)
6534 (2)
6535 (2)
6536 (2)
6537 (2)
6538 (2)
6539 (2)
6540 (2)
6541 (2)
6542 (2)
6543 (2)
6544 (2)
6545 (2)
6546 (2)
6547 (2)
6548 (2)

```

```

*****
: TEST: 65 NEW INSTRUCTIONS USED IN THIS SECTION ARE BICB, BITB
*****

```

```

6535 011136 021527 000065
(2) 011142 001404
6536 011144 012745 000234
(2) 011150 005245
(2) 011152 000000
6537 011154 005215
6538 011156 012703 000440
6539 011162 112713 000377
6540 011166 012700 000442
6541 011172 010001
6542 011174 112721 000252
6543 011200 000277
6544 011202 146013 000000
6545 011206 004737 017252
6546 011212 136113 177777
6547 011216 001404
6548 011220 012745 000235
(2) 011224 005245

```

```

BICB1:
      CMP     (R5), #65
      BEQ    2$           ; IF IN WRONG SEQUENCE GO TO HLT BELOW
      MOV     #234, -(R5)
      INC     -(R5)
      HALT
2$:   INC     (R5)
      MOV     #TEMP, R3      ; LOAD ADDRESS
      MOVB   #377, (R3)      ; LOAD LOCATION
      MOV     #TEMP1, R0     ; PLACE THE ADDRESS OF LOCATION TEMP1 IN R0
      MOV     R0, R1        ; AND R1
      MOVB   #252, (R1)+    ; PLACE #252 IN TEMP1
      SCC
      BICB   0(R0), (R3)    ; CLEAR EVERY OTHER BIT
      JSR    PC, #SCC1      ; CHECK FOR CC = 1
      BITB   -1(R1), (R3)  ; CHECK IT
      BEQ    4$           ; CONTINUE IF OK
      MOV     #235, -(R5)
      INC     -(R5)

```

H05

DVAAA MACY11 27(732) 25-AUG-76 13:25 PAGE 54-56
 DVAAA.P11 T65 NEW INSTRUCTIONS USED IN THIS SECTION ARE BICB, BITB

*** SEQ 0059

| | | | | | | | |
|------|--------|--------|--------|------|------|--------------|---|
| 6549 | 011226 | 000000 | | | HALT | | : BICB OR BITB INSTRUCTION FAILED |
| 6550 | 011230 | 132713 | 000125 | 4S: | BITB | #125,(R3) | : CHECK IT |
| 6551 | 011234 | 004737 | 017102 | | JSR | PC,2#SCC1 | : CHECK FOR CC = 1 |
| 6552 | 011240 | 154113 | | | BISB | -(R1),(R3) | : SET THE BITS THAT WERE CLEARED |
| 6553 | 011242 | 100404 | | | BMI | 6S | : CONTINUE IF OK |
| 6554 | 011244 | 012745 | 000236 | | MOV | #236,-(R5) | |
| 6555 | 011250 | 005245 | | | INC | -(R5) | |
| 6556 | 011252 | 000000 | | | HALT | | : BITB OR BISB INSTRUCTION FAILED |
| 6557 | 011254 | 012746 | 000177 | 6S: | MOV | #177,-(SP) | : STORE #177 ON THE STACK |
| 6558 | 011260 | 142613 | | | BICB | (SP)+,(R3) | : CLEAR ALL THE BITS EXCEPT SIGN BIT |
| 6559 | 011262 | 004737 | 017272 | | JSR | PC,2#SCC11 | : CHECK FOR CC = 11 |
| 6560 | 011266 | 132713 | 000377 | | BITB | #377,(R3) | : CHECK IT |
| 6561 | 011272 | 004737 | 017272 | | JSR | PC,2#SCC11 | : CHECK FOR CC = 11 |
| 6562 | 011276 | 010300 | | | MOV | R3,R0 | : PLACE THE ADDRESS OF LOCATION TEMP IN R0 |
| 6563 | 011300 | 012710 | 000442 | | MOV | #TEMP1,(R0) | : PLACE THE ADDRESS OF LOCATION TEMP1 IN TEMP |
| 6564 | 011304 | 012730 | 000377 | | MOV | #377,2(R0)+ | : WRITE A 377 IN LOCATION TEMP1 |
| 6565 | 011310 | 000263 | | | SEVC | | : SET V & C BITS |
| 6566 | 011312 | 145070 | 000000 | | BICB | 2-(R0),2(R0) | : BIT CLEAR THE CONTENTS |
| 6567 | 011316 | 004737 | 017206 | | JSR | PC,2#SCC5 | : OF TEMP1 TO THE CONTENTS OF TEMP1 |
| 6568 | 011322 | 022027 | 000442 | | CMP | (R0)+,#TEMP1 | : CHECK FOR CC = 5 |
| 6569 | 011326 | 001404 | | | BEQ | 8S | : MAKE SURE THAT (R0) IS POINTING TO LOCATION TEMP1 |
| 6570 | 011330 | 012745 | 000237 | | MOV | #237,-(R5) | |
| 6571 | 011334 | 005245 | | | INC | -(R5) | |
| 6572 | 011336 | 000000 | | | HALT | | : BICB OR CMP INSTRUCTION FAILED IN THE SPECIFIC MODE |
| 6573 | 011340 | 005750 | | 8S: | TST | 2-(R0) | : TEST LOCATION TEMP1 |
| 6574 | 011342 | 001404 | | | BEQ | 10S | |
| 6575 | 011344 | 012745 | 000240 | | MOV | #240,-(R5) | |
| 6576 | 011350 | 005245 | | | INC | -(R5) | |
| 6577 | 011352 | 000000 | | | HALT | | : BICB INSTRUCTION FAILED |
| 6578 | 011354 | 000257 | | 10S: | CCC | | |
| 6579 | 011356 | 141010 | | | BICB | (R0),(R0) | : CLEAR THE LOCATION TEMP |
| 6580 | 011360 | 004737 | 017164 | | JSR | PC,2#SCC4 | : CHECK FOR CC = 4 |

```

6575
(2)
(3)
6576
6577 011364 021527 000066
(2) 011364
6578 011370 001067
6579 011372 005215
6580 011374 012704 000440
6581 011400 112714 000177
6582 011404 000261
6583 011406 105214
6584 011410 004737 017334
6585 011414 012714 000376
6586 011420 012700 017272
6587 011424 105224
6588 011426 004720
6589 011430 105744
6590 011432 005746
6591 011434 010426
6592 011436 000241
6593 011440 105256
6594 011442 004737 017164
6595 011446 123634
6596 011450 000261
6597 011452 105264 177777
6598 011456 004737 017102
6599 011462 124427 000001
6600 011466 001404
6601 011470 012745 000241
(2) 011474 005245
(2) 011476 000000
6602 011500 000261
6603 011502 105314
6604 011504 004737 017206
6605 011510 105324
6606 011512 004740
6607 011514 112764 000200 177777
6608 011522 105344
6609 011524 004760 177650
6610 011530 105364 000000
6611 011534 004737 017102
6612 011540 126427 000000 000176
6613 011546 001404
6614 011550
(2) 011550 012745 000242
(2) 011554 005245
(2) 011556 000000
6615
6616
6617
6618
6619
6620
6621
6622

```

```

:*****
:TEST: 66 NEW INSTRUCTIONS USED IN THIS SECTION ARE INCB, DECB
:*****

```

```

INCB1:
CMP (R5),#66
BNE EINCB1 ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
1S: INC (R5)
MOV #TEMP,R4 ; LOAD ADDRESS
MOVB #177,(R4) ; TEMP LOCATION=177
SEC
INCB (R4) ; ADD ONES INTO LOCATION
JSR PC,@#SCC13 ; CHECK FOR CC = 13
MOV #376,(R4)
MOV #SCC11,R0 ; MAKE R0 POINT TO CHECKING ROUTINE FOR CC = 11
INCB (R4)+
JSR PC,(R0)+ ; CHECK FOR CC = 11
TSTB -(R4) ; DECREMENT R4 BY 1
TST -(SP) ; AND SP BY 2
MOV R4,(SP)+ ; PLACE THE ADDRESS OF TEMP ON THE STACK
CLC ; CLEAR C BIT
INCB @-(SP) ; INCREMENT THE CONTENTS OF LOCATION TEMP
JSR PC,@#SCC4 ; CHECK FOR CC = 4
CMPB @-(SP)+,@(R4)+ ; RESTORE STACK POINTER
SEC ; SET C BIT
INCB -1(R4)
JSR PC,@#SCC1 ; CHECK FOR CC = 1
CMPB -(R4),#1 ; CHECK IT
BEQ 2S ; CONTINUE IF OK
MOV #241,-(R5)
INC -(R5)
2S: HALT ; INCB INSTRUCTION FAILED
SEC
DECB (R4) ; SUBTRACT ONES FROM LOCATION
JSR PC,@#SCC5 ; CHECK FOR CC = 5
DECB (R4)+
JSR PC,-(R0) ; CHECK FOR CC = 11
MOVB #200,-1(R4)
DECB -(R4)
JSR PC,SCC3-SCC11(R0) ; CHECK FOR CC = 3
DECB 0(R4)
JSR PC,@#SCC1 ; CHECK FOR CC = 1
CMPB 0(R4),#176
BEQ COMB1
EINCB1:
MOV #242,-(R5)
INC -(R5)
HALT ; DECB INSTRUCTION FAILED OR SEQUENCE ERROR

```

J05

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 DVKAAA.P11 T66 NEW INSTRUCTIONS USED IN THIS SECTION ARE INCB, DECB

*** SEQ 0061

```

6623
6624
(2)
(3)
6625
6626 011560 021527 000067
(2) 011560 001404
6627 011564 012745
6628 011566 012745 000243
(2) 011572 005245
(2) 011574 000000
6629 011576 005215
6630 011600 012703 000440
6631 011604 012704 000442
6632 011610 012714 000252
6633 011614 112413
6634 011616 000277
6635 011620 105113
6636 011622 004737 017102
6637 011626 122713 000125
6638 011632 001404
6639 011634 012745 000244
(2) 011640 005245
(2) 011642 000000
6640 011644 000277
6641 011646 105113
6642 011650 004737 017272
6643 011654 010400
6644 011656 126013 177777
6645 011662 001404
6646 011664 012745 000245
(2) 011670 005245
(2) 011672 000000
6647 011674 112724 000377
6648 011700 114413
6649 011702 000277
6650 011704 105113
6651 011706 004737 017206
  
```

```

:*****
:TEST: 67 NEW INSTRUCTION IN THIS SECTION IS COMB
:*****
COMB1:
      CMP      (R5),#67
      BEQ      1$
      MOV      #243,-(R5)
      INC      -(R5)
      HALT
      ; IF IN WRONG SEQUENCE GO TO HLT
      ; TEST IS IN WRONG SEQUENCE
1$:
      INC      (R5)
      MOV      #TEMP,R3
      MOV      #TEMP1,R4
      MOV      #252,(R4)
      MOVB     (R4)+,(R3)
      SCC
      COMB     (R3)
      JSR     PC,#$CC1
      CMPB    #125,(R3)
      BEQ     2$
      MOV     #244,-(R5)
      INC     -(R5)
      HALT
      ; COMB INSTRUCTION FAILED
2$:
      SCC
      COMB     (R3)
      JSR     PC,#$CC11
      MOV     R4,R0
      CMPB    -1(R0),(R3)
      BEQ     3$
      MOV     #245,-(R5)
      INC     -(R5)
      HALT
      ; COMB INSTRUCTION FAILED
3$:
      MOVB    #377,(R4)+
      MOVB    -(R4),(R3)
      SCC
      COMB     (R3)
      JSR     PC,#$CC5
      ; PLACE #377 IN (R3)
      ; CHECK FOR CC = 5
  
```

K05

DVKAAA MACY11 27(732) 25-AUG-76 13:25 PAGE 54-59
 DVKAAA.P11 T70 NEW INSTRUCTION IN THIS SECTION IS NEGB

*** SEQ 0062

```

6652          ;*****
(2)          ;*TEST: 70      NEW INSTRUCTION IN THIS SECTION IS NEGB
(3)          ;*****
6653
6654 011712   NEG B1:      CMP      (R5), #70
(2) 011712   021527 000070      BNE     ENEGB1      ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
6655 011716   001027      INC     (R5)
6656 011720   005215      MOV     #TEMP, R0      ; LOAD ADDRESS
6657 011722   012700 000440      MOV B #1, (R0)      ; LOAD THE LOCATION
6658 011726   112710 000001      NEGB   (R0)      ; 2'S COMPLEMENT
6659 011732   105410      JSR    PC, @#SCC11    ; CHECK FOR CC = 11
6660 011734   004737 017272      CMP B #377, (R0)    ; CHECK IT
6661 011740   122710 000377      BEQ    2$            ; CONTINUE IF OK
6662 011744   001404      MOV     #246, -(R5)
6663 011746   012745 000246      INC     -(R5)
(2) 011752   005245      HALT
(2) 011754   000000      ; NEG B INSTRUCTION FAILED
6664 011756   012710 000200      2$:  MOV     #200, (R0)
6665 011762   105410      NEGB   (R0)      ; 2'S COMPLEMENT
6666 011764   004737 017334      JSR    PC, @#SCC13    ; CHECK FOR CC = 13
6667 011770   122710 000200      CMP B #200, (R0)    ; CHECK IT
6668 011774   001404      BEQ    ROLB1      ; CONTINUE IF OK
6669 011776   012745 000247      ENEGB1: MOV     #247, -(R5)
(2) 011776   012745 000247      INC     -(R5)
(2) 012002   005245      HALT      ; WRONG RESULT AT TEMP OR WRONG SEQUENCE
(2) 012004   000000
6670
6671
6672
6673          ;*****
(2)          ;*TEST: 71      NEW INSTRUCTION IN THIS SECTION IS ROLB
(3)          ;*****
6674
6675 012006   ROL B1:      CMP      (R5), #71
(2) 012006   021527 000071      BNE     EROLB1      ; IF IN WRONG SEQUENCE GO TO HLT ABOVE
6676 012012   001030      INC     (R5)
6677 012014   005215      MOV     #TEMP1, R1    ; LOAD ADDRESS
6678 012016   012701 000442      MOV B #40, (R1)      ; LOAD LOCATION
6679 012022   112711 000040      CCC
6680 012026   000257      ROL B  (R1)      ; CLEAR FLAGS
6681 012030   106111      ROL B  (R1)      ; SHIFT
6682 012032   106111      ROL B  (R1)
6683 012034   004737 017314      JSR    PC, @#SCC12    ; CHECK FOR CC = 12
6684 012040   122711 000200      CMP B #200, (R1)    ; CHECK IT
6685 012044   001404      BEQ    1$            ; CONTINUE IF OK
6686 012046   012745 000250      MOV     #250, -(R5)
(2) 012052   005245      INC     -(R5)
(2) 012054   000000      HALT      ; ROL B INSTRUCTION FAILED
6687 012056   106111      1$:  ROL B  (R1)
6688 012060   004737 017230      JSR    PC, @#SCC7
6689 012064   106111      ROL B  (R1)      ; SHIFT
6690 012066   122711 000001      ROL B  (R1)      ; SHIFT
6691 012072   001404      CMP B #1, (R1)      ; CHECK IT
6692 012074   001404      BEQ    RORB1      ; CONTINUE IF OK
(2) 012074   012745 000251      EROLB1: MOV     #251, -(R5)
(2) 012100   005245      INC     -(R5)

```

L05

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DVKAAA.P11 T71 NEW INSTRUCTION IN THIS SECTION IS ROLB

*** SEQ 0063

.2' 012102 000000

HALT

; WRONG RESULT AT TEMP1 OR WRONG SEQUENCE

M05

DVKAAA MACY11 27(732) 25-AUG-76 13:25 PAGE 54-61
 DVKAAA.P11 T72 NEW INSTRUCTION IN THIS SECTION IS RORB

*** SEQ 0064

```

6693
(2)
(3)
6694
6695 012104
(2) 012104 021527 000072
6696 012110 001030
6697 012112 005215
6698 012114 012702 000442
6699 012120 112712 000004
6700 012124 000257
6701 012126 106012
6702 012130 106012
6703 012132 122712 000001
6704 012136 001404
6705 012140 012745 000252
(2) 012144 005245
(2) 012146 000000
6706 012150 106012
6707 012152 004737 017230
6708 012156 106012
6709 012160 004737 017314
6710 012164 122712 000200
6711 012170 001404
6712 012172
(2) 012172 012745 000253
(2) 012176 005245
(2) 012200 000000
6713
6714
6715
6716
(2)
(3)
6717
6718 012202
(2) 012202 021527 000073
6719 012206 001404
6720 012210 012745 000254
(2) 012214 005245
(2) 012216 000000
6721 012220 005215
6722 012222 012703 000442
6723 012226 112713 000040
6724 012232 000257
6725 012234 106313
6726 012236 106313
6727 012240 004737 017314
6728 012244 122713 000200
6729 012250 001404
6730 012252 012745 000255
(2) 012256 005245
(2) 012260 000000
6731 012262 106313
6732 012264 004737 017230
6733 012270 106313
  
```

```

*****
*TEST: 72 NEW INSTRUCTION IN THIS SECTION IS RORB
*****
RORB1:
CMP (R5), #72
BNE ERORB1 ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
INC (R5)
MOV #TEMP1, R2 ; LOAD ADDRESS
MOVB #4, (R2) ; LOAD LOCATION
CCC ; CLEAR FLAGS
RORB (R2) ; SHIFT
RORB (R2)
CMPB #1, (R2) ; CHECK IT
BEQ 1$ ; CONTINUE IF OK
MOV #252, -(R5)
INC -(R5)
1$:
RORB (R2) ; RORB INSTRUCTION FAILED
JSR PC, @#SCC7 ; SHIFT
RORB (R2) ; CHECK FOR CC = 7
JSR PC, @#SCC12 ; SHIFT
CMPB #200, (R2) ; CHECK FOR CC = 12
BEQ ASLB1 ; CHECK IT
ERORB1:
MOV #253, -(R5) ; CONTINUE IF OK
INC -(R5)
HALT ; WRONG RESULT AT TEMP1 OR WRONG SEQUENCE
  
```

```

*****
*TEST: 73 NEW INSTRUCTION IN THIS SECTION IS ASLB
*****
ASLB1:
CMP (R5), #73
BEQ 2$ ; IF IN WRONG SEQUENCE GO TO HLT BELOW
MOV #254, -(R5)
INC -(R5)
HALT ; PROGRAM IS IN WRONG SEQUENCE
2$:
INC (R5)
MOV #TEMP1, R3 ; LOAD ADDRESS
MOVB #40, (R3) ; LOAD LOCATION
CCC ; CLEAR FLAGS
ASLB (R3) ; SHIFT
ASLB (R3)
JSR PC, @#SCC12 ; CHECK FOR CC = 12
CMPB #200, (R3) ; CHECK IT
BEQ 4$ ; CONTINUE IF OK
MOV #255, -(R5)
INC -(R5)
HALT ; ASLB INSTRUCTION FAILED
4$:
ASLB (R3) ; SHIFT
JSR PC, @#SCC7 ; CHECK FOR CC = 7
ASLB (R3) ; SHIFT
  
```

N05

DVKAAA MACY11 27(732) 25-AUG-76 13:25 PAGE 54-62
DVKAAA.P11 T73 NEW INSTRUCTION IN THIS SECTION IS ASLB

*** SEQ 0065

6734 012272 004737 017164 JSR PC,28SC04 ; CHECK FOR CC = 4

012400 000000 000000 000000
012401 000000 000000 000000
012402 000000 000000 000000
012403 000000 000000 000000
012404 000000 000000 000000
012405 000000 000000 000000
012406 000000 000000 000000
012407 000000 000000 000000
012408 000000 000000 000000
012409 000000 000000 000000
012410 000000 000000 000000
012411 000000 000000 000000
012412 000000 000000 000000
012413 000000 000000 000000
012414 000000 000000 000000
012415 000000 000000 000000
012416 000000 000000 000000
012417 000000 000000 000000
012418 000000 000000 000000
012419 000000 000000 000000
012420 000000 000000 000000
012421 000000 000000 000000
012422 000000 000000 000000
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012426 000000 000000 000000
012427 000000 000000 000000
012428 000000 000000 000000
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012431 000000 000000 000000
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012437 000000 000000 000000
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012439 000000 000000 000000
012440 000000 000000 000000
012441 000000 000000 000000
012442 000000 000000 000000
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012446 000000 000000 000000
012447 000000 000000 000000
012448 000000 000000 000000
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012451 000000 000000 000000
012452 000000 000000 000000
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012457 000000 000000 000000
012458 000000 000000 000000
012459 000000 000000 000000
012460 000000 000000 000000

: TEST: 74 NEW INSTRUCTION IN THIS SECTION IS ASRB

```

ASRB1:
      CMP      (R5), #74
      BNE     EASRB1      ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST*
18:   INC     (R5)
      MOV     @TEMP1,R4   ; LOAD ADDRESSES
      MOV     @TEMP2,R3   ; LOAD LOCATION
      MOVB    #4,(R4)     ; CLEAR FLAGS
      CCC
      ASRB    (R4)        ; SHIFT
      ASRB    (R4)
      CMPB    #1,(R4)     ; CHECK IT
      BEQ     Z$          ; CONTINUE IF OK
      MOV     #256,-(R5)
      INC     -(R5)
28:   HALT
      ASRB    (R4)        ; ASRB INSTRUCTION FAILED
      JSR     PC,@#5CC7   ; SHIFT
      ASRB    (R4)        ; CHECK FOR CC = 7
      JSR     PC,@#5CC4   ; SHIFT
      MOVB    #202,(R3)   ; CHECK FOR CC = 4
      ASRB    (R3)        ; LOAD LOCATION
      ASRB    (R3)        ; SHIFT
      JSR     PC,@#5CC11  ; CHECK FOR CC = 11
      CMPB    #340,(R3)   ; CHECK IT
      BEQ     ADCB1       ; CONTINUE IF OK
EASRB1:
      MOV     #257,-(R5)
      INC     -(R5)
      HALT                ; WRONG RESULT AT TEMP2 OR WRONG SEQUENCE

```

: TEST: 75 NEW INSTRUCTION IN THIS SECTION IS ADCB

```

ADCB1:
      CMP     (R5), #75
      BEQ     Z$          ; IF IN WRONG SEQUENCE GO TO HLT BELOW
      MOV     #260,-(R5)
      INC     -(R5)
28:   HALT                ; PROGRAM IS IN WRONG SEQUENCE
      INC     (R5)
      MOV     @TEMP2,R0   ; LOAD ADDRESS
      CLRB   (R0)        ; CLEAR THE LOCATION
      CCC
      ADCB   (R0)        ; CLEAR FLAGS
      JSR     PC,@#5CC4   ; ADD C BIT = 0
      SEC
      ADCB   (R0)        ; CHECK FOR CC = 4
      SEC
      ADCB   (R0)        ; ADD C BIT=1
      SEC
      ADCB   (R0)        ; C=1
                        ; AGAIN

```

C06

DVKAAA MACY11 27(732) 25-AUG-76 13:25 PAGE 54-64
 DVKAAA.P11 T75 NEW INSTRUCTION IN THIS SECTION IS AOCB

*** SEG 0067

| | | | | | | | |
|------|--------|--------|--------|------|------|------------|------------------------------|
| 6778 | 012462 | 004737 | 017062 | | JSR | PC,285CC0 | : CHECK FOR CC = 0 |
| 6779 | 012466 | 122710 | 000002 | | CMPB | #2,(R0) | : CHECK IT |
| 6780 | 012472 | 001404 | | | BEQ | 4\$ | : CONTINUE IF OK |
| 6781 | 012474 | 012745 | 000261 | | MOV | #261,-(R5) | |
| (2) | 012500 | 005245 | | | INC | -(R5) | |
| (2) | 012502 | 000000 | | | HALT | | : AOCB INSTRUCTION FAILED |
| 6782 | 012504 | 112710 | 000177 | 4\$: | MOVB | #177,(R0) | : LOAD LARGEST POSITIVE BYTE |
| 6783 | 012510 | 000261 | | | SEC | | : C=1 |
| 6784 | 012512 | 105510 | | | AOCB | (R0) | : ADD C BIT=1 |
| 6785 | 012514 | 004737 | 017314 | | JSR | PC,285CC12 | : CHECK FOR CC = 12 |
| 6786 | 012520 | 122710 | 000200 | | CMPB | #200,(R0) | : CHECK IT |
| 6787 | 012524 | 001404 | | | BEQ | 6\$ | : CONTINUE IF OK |
| 6788 | 012526 | 012745 | 000262 | | MOV | #262,-(R5) | |
| (2) | 012532 | 005245 | | | INC | -(R5) | |
| (2) | 012534 | 000000 | | | HALT | | : AOCB INSTRUCTION FAILED |
| 6789 | 012536 | 112710 | 000377 | 6\$: | MOVB | #377,(R0) | : LOAD -1 |
| 6790 | 012542 | 000261 | | | SEC | | : C=1 |
| 6791 | 012544 | 105510 | | | AOCB | (R0) | : ADD C BIT=1 |
| 6792 | 012546 | 004737 | 017206 | | JSR | PC,285CC5 | : CHECK FOR CC = 5 |

```

012552 021527 000076
012552 001404
012556 012745 000263
012560 005245
012564 000000
012566 005215
012570 012701 000444
012572 112711 000003
012576 000257
012604 105611
012606 004737 017062
012612 122711 000003
012616 001404
012620 012745 000264
012624 005245
012626 000000
012630 000261
012632 105611
012634 000261
012636 105611
012640 004737 017062
012644 122711 000001
012650 001404
012652 012745 000265
012656 005245
012660 000000
012662 000261
012664 105611
012666 004737 017164
012672 000261
012674 105611
012676 004737 017272
012702 122711 000377
012706 001404
012710 012745 000266
012714 005245
012716 000000
012720 112711 000200
012724 000261
012726 105611
012730 004737 017122

```

```

*****
: TEST: 76 NEW INSTRUCTION IN THIS SECTION IS SBCB
*****

```

```

SBCB1:
CMP      (R5),#76
BEQ      1$      ; IF IN WRONG SEQUENCE GO TO HLT
MOV      #263,-(R5)
INC      -(R5)
HALT
1$:      INC      (R5)      ; TEST IS IN WRONG SEQUENCE
MOV      #TEMP2,R1      ; LOAD ADDRESS
MOV      #3,(R1)      ; LOAD LOCATION
CCC
SBCB      (R1)      ; SUBTRACT C BIT=0
JSR      PC,#S0C00      ; CHECK FOR CC = 0
CMP      #3,(R1)      ; CHECK IT
BEQ      2$      ; CONTINUE IF OK
MOV      #264,-(R5)
INC      -(R5)
HALT
2$:      SEC
SBCB      (R1)      ; SBCB INSTRUCTION FAILED
SEC
SBCB      (R1)      ; SUBTRACT C BIT=1
SEC
SBCB      (R1)      ; C=1
JSR      PC,#S0C00      ; CHECK FOR CC = 0
CMP      #1,(R1)      ; CHECK IT
BEQ      3$      ; CONTINUE IF OK
MOV      #265,-(R5)
INC      -(R5)
HALT
3$:      SEC
SBCB      (R1)      ; SBCB INSTRUCTION FAILED
JSR      PC,#S0C04      ; C=1
SEC
SBCB      (R1)      ; SUBTRACT C BIT = 1
JSR      PC,#S0C011      ; CHECK FOR CC = 11
CMP      #377,(R1)      ; CHECK IT
BEQ      4$      ; CONTINUE IF OK
MOV      #266,-(R5)
INC      -(R5)
HALT
4$:      MOV      #200,(R1)      ; SBCB INSTRUCTION FAILED
SEC
SBCB      (R1)      ; LOAD R1
SEC
SBCB      (R1)      ; C=1
JSR      PC,#S0C02      ; SUBTRACT C BIT = 1

```

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...
CHECK WORD INSTRUCTIONS, NOT DESTINATION MODE 0

: *TEST: 77 NEW INSTRUCTIONS USED IN THIS SECTION ARE TST, CLR, MOV
: *****

012734
012734 021527 000077
012740 001404
012742 012745 000267
012746 005245
012750 000000
012752 005215
012754 012701 000440
012760 012700 000442
012764 000277
012766 005010
012770 004737 017164
012774 005720
012776 004737 017164
013002 010040
013004 012730 177777
013010 017011 177776
013014 004737 017252
013020 005711
013022 004737 017252

TST1:
CMP (R5),#77
BEQ 15 ; IF IN WRONG SEQUENCE GO TO HLT
MOV #267, -(R5)
INC -(R5)
HALT ; TEST IS IN A WRONG SEQUENCE
IS:
INC (R5)
MOV #TEMP, R1 ; LOAD ADDRESSES
MOV #TEMP1, R0
SCC
CLR (R0) ; CLEAR THE LOCATION
JSR PC, @#SCC4 ; CHECK FOR CC = 4
TST (R0)+ ; CHECK IT
JSR PC, @#SCC4 ; CHECK FOR CC = 4
MOV R0, -(R0)
MOV #177777, @ (R0)+
MOV @-2(R0), (R1) ; LOAD THE LOCATION
JSR PC, @#SCC10 ; CHECK FOR CC = 10
TST (R1) ; CHECK IT
JSR PC, @#SCC10 ; CHECK FOR CC = 10

F06

DVKA00 MACY11 27(732)
 DVKA00.P11 *100

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 NEW INSTRUCTIONS USED IN THIS SECTION ARE CMP, BIS

*** SEQ 0070

```

6863          :*****
6864          :*TEST: 100   NEW INSTRUCTIONS USED IN THIS SECTION ARE CMP, BIS
6865          :*****
6865 013026    021527 000100    CMP1:      CMP      (R5), #100
6866 013032    001113          BNE     ECMP1      ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE EST
6867 013034    005215    1S:      INC      (R5)
6868 013036    012702 000442    MOV     #TEMP1, R2 ; LOAD ADDRESS
6869 013042    012700 000440    MOV     #TEMP, R0  ; PLACE THE ADDRESS OF TEMP IN R0
6870 013046    012720 177777    MOV     #177777, (R0)+ ; PLACE #177777 IN LOCATION TEMP AND INC. R0 BY 2
6871 013052    054012          BIS     -(R0), (R2) ; LOAD LOCATION
6872 013054    004737 017252    JSR     PC, #100010 ; CHECK FOR CC = 10
6873 013060    022227 177777    CMP     (R2)+, #177777 ; CHECK COMPARE
6874 013064    001404          BEQ     2S        ; CONTINUE IF OK
6875 013066    012745 000270    MOV     #270, -(R5)
6876 013072    005245          INC     -(R5)
6877 013074    000000          HALT
6878 013076    020227 000444    2S:      CMP     R2, #TEMP1+2 ; CMP OR BIS INSTRUCTION FAILED
6879 013102    001404          BEQ     3S        ; CHECK R2 TO CONTAIN ADDRESS OF TEMP1+2
6880 013104    012745 000271    MOV     #271, -(R5)
6881 013110    005245          INC     -(R5)
6882 013112    000000          HALT
6883 013114    022742 000077    3S:      CMP     #77, -(R2) ; NO AUTO INCREMENT
6884 013120    004737 017102    JSR     PC, #10001 ; CHECK IT AGAIN
6885 013124    022722 077777    CMP     #77777, (R2)+ ; CHECK FOR CC = 1
6886 013130    004737 017334    JSR     PC, #100013 ; CHECK FOR CC = 13
6887 013134    024227 077777    CMP     -(R2), #77777 ; ONCE MORE
6888 013140    004737 017252    JSR     PC, #100010 ; CHECK FOR CC = 10
6889 013144    012767 052525 165272    MOV     #52525, TEMP2 ; SET EVERY OTHER BIT IN TEMP2
6890 013152    012767 000444 165262    MOV     #TEMP2, TEMP1 ; PLACE THE ADDRESS OF TEMP2 IN LOCATION TEMP1
6891 013160    012704 000430    MOV     #ADR, R4
6892 013164    012714 000432    MOV     #ADR1, (R4) ; PLACE THE ADDRESS OF ADR1 IN ADR POINTED BY R4
6893 013170    012734 125252    MOV     #125252, 2(R4)+ ; PLACE THE #125252 IN LOCATION ADR1
6894 013174    057432 177776    BIS     2-2(R4), 2(R2)+ ; SET EVERY OTHER BIT AT LOCATION TEMP2
6895          : AND INCREMENT R2 BY 2
6896 013200    010200          MOV     R2, R0 ; PLACE ADDRESS OF TEMP2 IN R0
6897 013202    025027 177777    CMP     2-(R0), #177777 ; TEMP2 SHOULD CONTAIN ALL 1'S
6898 013206    001404          BEQ     4S
6899 013210    012745 000272    MOV     #272, -(R5)
6900 013214    005245          INC     -(R5)
6901 013216    000000          HALT
6902 013220    020227 000444    4S:      CMP     R2, #TEMP1+2 ; CMP OR BIS INSTUCTIONS FAILED IN MODES OTHER THAN 0
6903          : R2 SHOULD CONTAIN THE ADDRESS FOR TEMP2
6904          : I.E. TEMP1+2
6905 013224    001404          BEQ     5S
6906 013226    012745 000273    MOV     #273, -(R5)
6907 013232    005245          INC     -(R5)
6908 013234    000000          HALT
6909 013236    005040          CLR     -(R0) ; MODE 5 IS FAILING
6910 013240    010067 165200    MOV     R0, TEMP2 ; PLACE A 0 IN LOCATION TEMP
6911 013244    022020          CMP     (R0)+, (R0)+ ; PLACE ADDRESS OF TEMP IN LOCATION TEMP2
6912          : BUMP R0 BY 4
6913 013246    055070 000002    BIS     2-(R0), 2(R0) ; PLACE THE CONTENTS OF LOCATION TEMP2 AT TEMP
6914 013252    022767 000440 165160    CMP     #TEMP, TEMP ; LOCATION TEMP SHOULD CONTAIN ITS OWN ADDRESS
6915 013260    001404          BEQ     BIC1
6916 013262          :
6917 013262    012745 000274    ECMP1:   MOV     #274, -(R5)
  
```

013266 005245
013270 000000
69008
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013464
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013474
013500
013504

021527
001122
005215
012703
012713
012704
012714
011334
012700
012710
000277
042013
004737
034013
001404
012745
005245
000000
032713
004737
056013
100404
012745
005245
000000
012720
010002
046213
004737
020027
001404
012745
005245
000000
010020
000263
045000
004737
037413
004737
012746
017423
046643
000000
022327
001404

INC
HALT
- (R5)
BIC1:
CMP (R5), #101
BNE EBIC1
INC (R5)
MOV #TEMP, R3
MOV #177777, (R3)
MOV #ADR, R4
MOV #ADR1, (R4)
MOV (R3), (R4)+
MOV #TEMP1, R0
MOV #125252, (R0)
SCC
BIC (R0)+, (R3)
JSR PC, #SCC1
BIT -(R0), (R3)
BEQ 1\$
MOV #275, -(R5)
INC -(R5)
HALT
1\$: BIT #52525, (R3)
JSR PC, #SCC1
BIS 0(R0), (R3)
BNE 2\$
MOV #276, -(R5)
INC -(R5)
HALT
2\$: MOV #77777, (R0)+
MOV R0, R2
BIC -2(R2), (R3)
JSR PC, #SCC1
CMP R0, #TEMP1+2
BEQ 3\$
MOV #277, -(R5)
INC -(R5)
HALT
3\$: MOV R0, (R0)+
SEVC
BIC 2-(R0), R0
JSR PC, #SCC5
BIT 2-2(R4), (R3)
JSR PC, #SCC11
MOV #125252, -(SP)
MOV 2-2(R4), (R3)+
BIC 0(SP), -(R3)
CMP (R3)+, #52525
BEQ 4\$

: CMP OR BIS INSTRUCTIONS FAILED OR WRONG
: SEQUENCE COUNTER

: TEST: 101 NEW INSTRUCTIONS USED IN THIS SECTION ARE BIC, BIT

: IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST

: LOAD ADDRESS
: LOAD LOCATION
: PLACE THE ADDRESS OF ADR IN R4
: PLACE THE ADDRESS OF ADR1 IN ADR
: LOAD LOCATION ADR1 WITH #177777
: PLACE THE ADDRESS OF TEMP1 IN R0
: SET EVERY OTHER BIT AT LOCATION TEMP1

: CLEAR EVERY OTHER BIT
: CHECK FOR CC = 1
: CHECK IT
: CONTINUE IF OK

: BIC OR BIT INSTRUCTION FAILED
: CHECK IT
: CHECK FOR CC = 1
: SET THE BITS THAT WERE CLEARED
: CONTINUE IF OK

: BIT OR BIS INSTRUCTION FAILED
: SET ALL THE BITS AT LOCATION TEMP1 EXCEPT SIGN BIT

: TRY CLEARING THE OTHER BITS
: CHECK FOR CC = 11
: R0 SHOULD CONTAIN THE ADDRESS OF TEMP1+2

: PLACE THE ADDRESS OF LOCATION TEMP2 IN TEMP2
: SET V & C BITS
: CLEAR R0

: CHECK FOR CC = 5
: CHECK IT
: CHECK FOR CC = 11
: SET EVERY OTHER BIT ON THE STACK
: SET ALL THE BITS AT LOCATION TEMP
: CLEAR EVERY OTHER BIT AT LOCATION TEMP
: TEMP SHOULD CONTAIN # 52525

H06

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 DVAAA.P11 T101 NEW INSTRUCTIONS USED IN THIS SECTION ARE BIC, BIT

*** SEQ 0072

| | | | | | | | |
|------|--------|--------|--------|--------|------|----------------|---|
| 6952 | 013506 | 012745 | 000300 | | MOV | #300, -(R5) | |
| | 013512 | 005245 | | | INC | -(R5) | |
| | 013514 | 000000 | | | HALT | | |
| 6953 | 013516 | 012700 | 000446 | 48: | MOV | #TEMP2+2, R0 | : BIC FAILED IN MODE 6 |
| 6954 | 013522 | 010340 | | | MOV | R3, -(R0) | : PLACE THE ADDRESS OF TEMP2+2 IN R0 |
| 6955 | 013524 | 014330 | | | MOV | -(R3), @ (R0)+ | : PLACE THE ADDRESS OF TEMP1 IN TEMP2 |
| 6956 | 013526 | 000263 | | | SEVC | | : MOVE # 52525 IN LOCATION TEMP1 |
| 6957 | 013530 | 035026 | | | BIT | @-(R0), (SP)+ | : SET V & C BITS |
| 6958 | 013532 | 004737 | 017206 | | JSR | PC, @#SCCS | : BIT TEST TEMP1 WITH STACK AND RESTORE STACK POINTER |
| 6959 | 013536 | 020627 | 000530 | | CMP | SP, #START | : CHECK FOR CC = 5 |
| 6960 | 013542 | 001404 | | | BEQ | INC1 | : MAKE SURE THAT THE SP IS OK |
| 6961 | 013544 | | | EBIC1: | | | |
| | 013544 | 012745 | 000301 | | MOV | #301, -(R5) | |
| | 013550 | 005245 | | | INC | -(R5) | |
| | 013552 | 000000 | | | HALT | | : STACK POINTER FOULED UP OR SEQUENCE ERROR |

6962 (2) (3) *****
: *TEST: 102 NEW INSTRUCTIONS USED IN THIS SECTION ARE INC, DEC
: *****

```

6963 013554
6964 (2) 013554 021527 000102 INC1:  CMP (R5),#102
6965 013560 001404 BEQ 2$ ; IF IN WRONG SEQUENCE GO TO HLT BELOW
6966 013562 012745 000302 MOV #302,-(R5)
(2) 013566 005245 INC -(R5)
(2) 013570 000000 HALT ; PROGRAM IS IN WRONG SEQUENCE
6967 013572 005215 2$: INC (R5)
6968 013574 012704 000442 MOV #TEMP1,R4 ; LOAD ADDRESS
6969 013600 012714 077777 MOV #77777,(R4) ; TEMP1 = 77777
6970 013604 000261 SEC
6971 013606 005214 INC (R4) ; ADD ONES INTO LOCATION
6972 013610 004737 017334 JSR PC,#$CC13 ; CHECK FOR CC = 13
6973 013614 012714 177776 MOV #177776,(R4)
6974 013620 012700 000440 MOV #TEMP,R0 ; R0 IS POINTING TO LOCATION TEMP
6975 013624 012710 017272 MOV #$CC11,(R0) ; PLACE THE ADDRESS OF SUBROUTINE TO CHECK CC = 11
6976 ; IN LOCATION TEMP
6977 013630 005214 INC (R4)
6978 013632 004730 JSR PC,#(R0)+ ; CHECK FOR CC = 11
6979 013634 005214 INC (R4)
6980 013636 004737 017206 JSR PC,#$CC5 ; CHECK FOR CC = 5
6981 013642 005214 INC (R4)
6982 013644 004737 017102 JSR PC,#$CC1 ; CHECK FOR CC = 1
6983 013650 026427 000000 000001 CMP C(R4),#1 ; CHECK IT
6984 013656 001404 BEQ 4$ ; CONTINUE IF OK
6985 013660 012745 000303 MOV #303,-(R5)
(2) 013664 005245 INC -(R5)
(2) 013666 000000 HALT ; INC INSTRUCTION FAILED
6986 013670 000261 4$: SEC
6987 013672 005314 DEC (R4) ; SUBTRACT ONES FROM LOCATION
6988 013674 004737 017206 JSR PC,#$CC5 ; CHECK FOR CC = 5
6989 013700 005314 DEC (R4)
6990 013702 004770 177776 JSR PC,#-2(R0) ; CHECK FOR CC = 11
6991 013706 012714 100000 MOV #100000,(R4)
6992 013712 005314 DEC (R4)
6993 013714 004737 017142 JSR PC,#$CC3 ; CHECK FOR CC = 3
6994 013720 005314 DEC (R4)
6995 013722 004737 017102 JSR PC,#$CC1 ; CHECK FOR CC = 1
6996
6997
6998
6999 (2) (3) *****
: *TEST: 103 NEW INSTRUCTION IN THIS SECTION IS COM
: *****

```

7000 *****
: *TEST: 103 NEW INSTRUCTION IN THIS SECTION IS COM
: *****

```

7001 013726 COM1:  CMP (R5),#103
(2) 013726 021527 000103 BEQ 1$ ; IF IN WRONG SEQUENCE GO TO HLT
7002 013732 001404 MOV #304,-(R5)
7003 013734 012745 000304 INC -(R5)
(2) 013740 005245 HALT ; TEST IS IN WRONG SEQUENCE
(2) 013742 000000 1$: INC (R5)
7004 013744 005215 MOV #TEMP1,R3 ; LOAD ADDRESS
7005 013746 012703

```

JOB

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 DVKAAR.P11 T103 NEW INSTRUCTION IN THIS SECTION IS COM

*** SEQ 0074

| | | | | | | | |
|------|--------|--------|--------|------|------|----------------|--|
| 7006 | 013752 | 012713 | 125252 | | MOV | #125252, (R3) | ; LOAD EVERY OTHER BIT |
| 7007 | 013756 | 000277 | | | SCC | | |
| 7008 | 013760 | 005163 | 000030 | | COM | 0(R3) | ; 1'S COMPLEMENT |
| 7009 | 013764 | 004737 | 017102 | | JSR | PC, 2#SCC1 | ; CHECK FOR CC = 1 |
| 7010 | 013770 | 022713 | 052525 | | CMP | #52525, (R3) | ; CHECK IT |
| 7011 | 013774 | 001404 | | | BEQ | 2\$ | ; CONTINUE IF OK |
| 7012 | 013776 | 012745 | 000305 | | MOV | #305, -(R5) | |
| (2) | 014002 | 005245 | | | INC | -(R5) | |
| (2) | 014004 | 000000 | | | HALT | | ; COM INSTRUCTION FAILED |
| 7013 | 014006 | 000277 | | 2\$: | SCC | | |
| 7014 | 014010 | 005123 | | | COM | (R3)+ | ; COMPLEMENT BACK |
| 7015 | 014012 | 004737 | 017272 | | JSR | PC, 2#SCC11 | ; CHECK FOR CC = 11 |
| 7016 | 014016 | 022743 | 125252 | | CMP | #125252, -(R3) | ; CHECK IT |
| 7017 | 014022 | 001404 | | | BEQ | 3\$ | ; CONTINUE IF OK |
| 7018 | 014024 | 012745 | 000306 | | MOV | #306, -(R5) | |
| (2) | 014030 | 005245 | | | INC | -(R5) | |
| (2) | 014032 | 000000 | | | HALT | | ; COM INSTRUCTION FAILED |
| 7019 | 014034 | 010300 | | 3\$: | MOV | R3, R0 | ; R0 IS NOW POINTING TO LOCATION TEMP1 |
| 7020 | 014036 | 012710 | 177777 | | MOV | #177777, (R0) | |
| 7021 | 014042 | 000277 | | | SCC | | |
| 7022 | 014044 | 005110 | | | COM | (R0) | |
| 7023 | 014046 | 004737 | 017206 | | JSR | PC, 2#SCC5 | ; CHECK FOR CC = 5 |

K06

DVKAAA MACY11 27(732) 25-AUG-76 13:25 PAGE 54-72
 DVKAAA.P11 T104 NEW INSTRUCTION IN THIS SECTION IS NEG

*** SEQ 0075

```

7024 :*****
(2) :*TEST: 104 NEW INSTRUCTION IN THIS SECTION IS NEG
(3) :*****
7025
7026

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```

7026 014052 021527 000104 NEG1:  CMP      (R5), #104
(2) 014052 001033      BNE      ENEG1      ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
7027 014056 005215      INC      (R5)
7028 014060 012704 000442      MOV      #TEMP1, R4      ; LOAD ADDRESS
7029 014062 012704 000001      MOV      #1, (R4)+      ; LOAD THE LOCATION
7030 014066 012724 000001      MOV      R4, R2
7031 014072 010402      MOV      #100000, 0(R2)
7032 014074 012762 100000 000000      NEG      -(R4)      ; 2'S COMPLEMENT
7033 014102 005444      JSR      PC, 2#5CC11      ; CHECK FOR CC = 11
7034 014104 004737 017272      CMP      #177777, (R4)+      ; CHECK IT
7035 014110 022724 177777      BEQ      2$      ; CONTINUE IF OK
7036 014114 001404      MOV      #307, -(R5)
7037 014116 012745 000307      INC      -(R5)
(2) 014122 005245      HALT      ; NEG INSTRUCTION FAILED
(2) 014124 000000      2$:  MOV      0(R4), -(R4)      ; TEMP1 CONTAINS THE LARGEST NEGATIVE NUMBER
7038 014126 016444 000000      NEG      (R4)      ; 2'S COMPLEMENT
7039 014132 005414      JSR      PC, 2#5CC13      ; CHECK FOR CC = 13
7040 014134 004737 017334      CMP      0(R2), (R4)      ; CHECK IT
7041 014140 026214 000000      BEQ      ROL1      ; CONTINUE IF OK
7042 014144 001404      ENEG1:  MOV      #310, -(R5)
7043 014146      INC      -(R5)
(2) 014146 012745 000310      HALT      ; WRONG RESULT IN TEMP2 OR WRONG SEQUENCE
(2) 014152 005245
(2) 014154 000000
7044
7045
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7047

```

```

(2) :*****
(3) :*TEST: 105 NEW INSTRUCTION IN THIS SECTION IS ROL
(3) :*****
7048
7049

```

```

7049 014156 021527 000105 ROL1:  CMP      (R5), #105
(2) 014156 001032      BNE      EROL1      ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
7050 014162 005215      INC      (R5)
7051 014164 012701 000444      MOV      #TEMP2, R1      ; LOAD ADDRESS
7052 014166 012711 020000      MOV      #20000, (R1)      ; LOAD LOCATION
7053 014172 000257      CCC      ; CLEAR FLAGS
7054 014176 006121      ROL      (R1)+      ; SHIFT
7055 014200 006141      ROL      -(R1)
7056 014202 004737 017314      JSR      PC, 2#5CC12      ; CHECK FOR CC = 12
7057 014210 022711 100000      CMP      #100000, (R1)      ; CHECK IT
7058 014214 001404      BEQ      1$      ; CONTINUE IF OK
7059 014216 012745 000311      MOV      #311, -(R5)
7060 014222 005245      INC      -(R5)
(2) 014224 000000      HALT      ; ROL INSTRUCTION FAILED
(2) 014226 006161 000000      1$:  ROL      0(R1)      ; SHIFT
7061 014232 004737 017230      JSR      PC, 2#5CC7      ; CHECK FOR CC = 7
7062 014236 010102      MOV      R1, R2      ; R2 IS NOW POINTING TO LOCATION TEMP2
7063 014240 006112      ROL      (R2)      ; SHIFT
7064 014242 022711 000001      CMP      #1, (R1)      ; CHECK IT
7065 014244 001404      BEQ      ROL1      ; CONTINUE IF OK
7066

```

L06

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DVKAAA.P11 T105 NEW INSTRUCTION IN THIS SECTION IS ROL

*** SEQ 0076

| | | | | | | |
|------|--------|--------|--------|--------|------|-------------|
| 7067 | 014250 | | | EROL1: | | |
| (2) | 014250 | 012745 | 000312 | | MOV | #312, -(R5) |
| (2) | 014254 | 005245 | | | INC | -(R5) |
| (2) | 014256 | 000000 | | | HALT | |

: WRONG RESULT AT TEMP2 OR WRONG SEQUENCE

M06

DVKAAA MACY11 27(732) 25-AUG-76 13:25 PAGE 54-74
 DVKAAA.P11 T106 NEW INSTRUCTION IN THIS SECTION IS ROR

*** SEQ 0077

```

7068
(2)
(3)
7069
7070 014260 021527 000106
(2) 014260
7071 014264 001030
7072 014266 005215
7073 014270 012702 000444
7074 014274 012712 000004
7075 014300 000257
7076 014302 006012
7077 014304 006012
7078 014306 022712 000001
7079 014312 001404
7080 014314 012745 000313
(2) 014320 005245
(2) 014322 000000
7081 014324 006012
7082 014326 004737 017230
7083 014332 006012
7084 014334 004737 017314
7085 014340 022712 100000
7086 014344 001404
7087 014346
(2) 014346 012745 000314
(2) 014352 005245
(2) 014354 000000
7088
7089
7090
7091
(2)
(3)
7092
7093 014356
(2) 014356 021527 000107
7094 014362 001404
7095 014364 012745 000315
(2) 014370 005245
(2) 014372 000000
7096 014374 005215
7097 014376 012703 000444
7098 014402 012713 020000
7099 014406 000257
7100 014410 006313
7101 014412 006313
7102 014414 004737 017314
7103 014420 022713 100000
7104 014424 001404
7105 014426 012745 000316
(2) 014432 005245
(2) 014434 000000
7106 014436 006313
7107 014440 004737 017230
7108 014444 006313
  
```

```

*****
; *TEST: 106 NEW INSTRUCTION IN THIS SECTION IS ROR
*****
  
```

```

ROR1:
      CMP      (R5), #106
      BNE     EROR1      ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
      INC     (R5)
      MOV     #TEMP2,R2  ; LOAD ADDRESS
      MOV     #4,(R2)    ; LOAD LOCATION
      CCC
      ROR     (R2)       ; CLEAR FLAGS
      ROR     (R2)       ; SHIFT
      CMP     #1,(R2)    ; CHECK IT
      BEQ     1$         ; CONTINUE IF OK
      MOV     #313,-(R5)
      INC     -(R5)
      HALT
1$:   ROR     (R2)       ; ROR INSTRUCTION FAILED
      JSR     PC,#$CC7   ; SHIFT
      ROR     (R2)       ; CHECK FOR CC = 7
      JSR     PC,#$CC12  ; SHIFT
      CMP     #100000,(R2) ; CHECK FOR CC = 12
      BEQ     ASL1       ; CHECK IT
      MOV     #314,-(R5) ; CONTINUE IF OK
      INC     -(R5)
      HALT
      ; WRONG RESULT AT TEMP2 OR WRONG SEQUENCE
  
```

```

*****
; *TEST: 107 NEW INSTRUCTION IN THIS SECTION IS ASL
*****
  
```

```

ASL1:
      CMP     (R5), #107
      BEQ     2$         ; IF IN WRONG SEQUENCE GO TO HLT BELOW
      MOV     #315,-(R5)
      INC     -(R5)
      HALT
      ; PROGRAM IS IN WRONG SEQUENCE
2$:   INC     (R5)
      MOV     #TEMP2,R3  ; LOAD ADDRESS
      MOV     #20000,(R3) ; LOAD LOCATION
      CCC
      ASL     (R3)       ; CLEAR FLAGS
      ASL     (R3)       ; SHIFT
      JSR     PC,#$CC12  ; CHECK FOR CC = 12
      CMP     #100000,(R3) ; CHECK IT
      BEQ     4$         ; CONTINUE IF OK
      MOV     #316,-(R5)
      INC     -(R5)
      HALT
4$:   ASL     (R3)       ; ASL INSTRUCTION FAILED
      JSR     PC,#$CC7   ; SHIFT
      ASL     (R3)       ; CHECK FOR CC = 7
      ASL     (R3)       ; SHIFT
  
```

N06

DV:AAA MACY11 27(732) 25-AUG-76 13:25 PAGE 54-75
DV:AAA.P11 T107 NEW INSTRUCTION IN THIS SECTION IS ASL

*** SEQ 0078

7109 014446 004737 017164 JSR PC,20\$CC4 ; CHECK FOR CC = 4

```

7:110 (2) 014440 021527 000110
7:111 (2) 014440 001240
7:112 (2) 014440 005215
7:113 (2) 014440 012704 000444
7:114 (2) 014440 012703 000440
7:115 (2) 014440 012714 000004
7:116 (2) 014476 000257
7:117 (2) 014500 006214
7:118 (2) 014500 006214
7:119 (2) 014504 022714 000001
7:120 (2) 014510 001404
7:121 (2) 014512 012745 000317
7:122 (2) 014516 005245
7:123 (2) 014520 000000
7:124 (2) 014522 006214
7:125 (2) 014524 004737 017230
7:126 (2) 014530 006214
7:127 (2) 014532 004737 017164
7:128 (2) 014536 012713 100002
7:129 (2) 014542 006213
7:130 (2) 014544 006213
7:131 (2) 014546 004737 017272
7:132 (2) 014550 022713 160000
7:133 (2) 014556 001404
7:134 (2) 014560
7:135 (2) 014560 012745 000320
7:136 (2) 014564 005245
7:137 (2) 014566 000000

```

```

*****
: *TEST: 110 NEW INSTRUCTION IN THIS SECTION IS ASR
*****

```

```

ASR1:  CMP      (R5), #110
      BNE     EASR1      ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
2S:   INC     (R5)
      MOV     #TEMP2, R4  ; LOAD ADDRESSES
      MOV     #TEMP, R3   ; LOAD LOCATION
      MOV     #4, (R4)    ; CLEAR FLAGS
      CCC
      ASR     (R4)        ; SHIFT
      ASR     (R4)
      CMP     #1, (R4)    ; CHECK IT
      BEQ     ZS          ; CONTINUE IF OK
      MOV     #317, -(R5)
      INC     -(R5)
      HALT
2S:   ASR     (R4)        ; ASR INSTRUCTION FAILED
      JSR     PC, #SCC7   ; CHECK FOR CC = 7
      ASR     (R4)        ; SHIFT
      JSR     PC, #SCC4   ; CHECK FOR CC = 4
      MOV     #100002, (R3) ; LOAD LOCATION
      ASR     (R3)        ; SHIFT
      ASR     (R3)
      JSR     PC, #SCC11  ; CHECK FOR CC = 11
      CMP     #160000, (R3) ; CHECK IT
      BEQ     ADC1       ; CONTINUE IF OK
EASR1: MOV     #320, -(R5)
      INC     -(R5)
      HALT
      ; WRONG RESULT IN TEMP OR WRONG SEQUENCE

```

```

7:138 (2)
7:139 (2)
7:140 (2) 014570 021527 000111
7:141 (2) 014570 001404
7:142 (2) 014574 001404 000321
7:143 (2) 014576 012745 000321
7:144 (2) 014602 005245
7:145 (2) 014604 000000
7:146 (2) 014606 005215
7:147 (2) 014610 012700 000440
7:148 (2) 014614 005010
7:149 (2) 014616 000257
7:150 (2) 014620 005510
7:151 (2) 014622 004737 017164
7:152 (2) 014626 000261
7:153 (2) 014630 005510
7:154 (2) 014632 000261
7:155 (2) 014634 005510

```

```

*****
: *TEST: 111 NEW INSTRUCTION IN THIS SECTION IS ADC
*****

```

```

ADC1:  CMP      (R5), #111
      BEQ     ZS          ; IF IN WRONG SEQUENCE GO TO HLT BELOW
      MOV     #321, -(R5)
      INC     -(R5)
      HALT
      ; PROGRAM IS IN WRONG SEQUENCE
2S:   INC     (R5)
      MOV     #TEMP, R0   ; LOAD ADDRESS
      CLR     (R0)        ; CLEAR THE LOCATION
      CCC
      ADC     (R0)        ; CLEAR FLAGS
      JSR     PC, #SCC4   ; ADD C BIT = 0
      SEC
      JSR     PC, #SCC4   ; CHECK FOR CC = 4
      SEC
      ADC     (R0)        ; ADD C BIT=1
      SEC
      ADC     (R0)        ; C=1
      ; AGAIN

```


| | | | | | | | | |
|------|--------|--------|--------|-----|------|--------------|---|------------------------------|
| 7153 | 014636 | 004737 | 017062 | | JSR | PC,285000 | : | CHECK FOR CC = 0 |
| 7154 | 014640 | 022710 | 000002 | | CMP | #2,(R0) | : | CHECK IT |
| 7155 | 014646 | 001404 | | | BEG | 45 | : | CONTINUE IF OK |
| 7156 | 014650 | 012745 | 000322 | | MOV | #322,-(R5) | | |
| (2) | 014654 | 005245 | | | INC | -(R5) | | |
| (2) | 014656 | 000000 | | | HALT | | : | ADC INSTRUCTION FAILED |
| 7157 | 014660 | 012710 | 077777 | 45: | MOV | #77777,(R0) | : | LOAD LARGEST POSITIVE NUMBER |
| 7158 | 014664 | 000261 | | | SEC | | : | C=1 |
| 7159 | 014666 | 005510 | | | ADC | (R0) | : | ADD C BIT=1 |
| 7160 | 014670 | 004737 | 017314 | | JSR | PC,2850012 | : | CHECK FOR CC = 12 |
| 7161 | 014674 | 022710 | 100000 | | CMP | #100000,(R0) | : | CHECK IT |
| 7162 | 014700 | 001404 | | | BEG | 65 | : | CONTINUE IF OK |
| 7163 | 014702 | 012745 | 000323 | | MOV | #323,-(R5) | | |
| (2) | 014706 | 005245 | | | INC | -(R5) | | |
| (2) | 014710 | 000000 | | | HALT | | : | ADC INSTRUCTION FAILED |
| 7164 | 014712 | 012710 | 177777 | 65: | MOV | #-1,(R0) | : | LOAD -1 |
| 7165 | 014716 | 000261 | | | SEC | | : | C=1 |
| 7166 | 014720 | 005510 | | | ADC | (R0) | : | ADD C BIT=1 |
| 7167 | 014722 | 004737 | 017206 | | JSR | PC,285005 | : | CHECK FOR CC = 5 |

7168
(2) *TEST: 112 NEW INSTRUCTION IN THIS SECTION IS SBC
(3)
7169

```

7170 014726 021527 000112 SBC1:  CMP      (R5),#112
(2) 014726 001404      BEQ      1$      ; IF IN WRONG SEQUENCE GO TO HLT
7171 014732 001404      MOV      #324,-(R5)
7172 014734 012745 000324      MOV      -(R5)
(2) 014740 005245      INC
(2) 014742 000000      HALT      ; TEST IS IN WRONG SEQUENCE
7173 014744 005215 1$:      INC      (R5)
7174 014746 012701 000440      MOV      #TEMP,R1      ; LOAD ADDRESS
7175 014752 012711 000003      MOV      #3,(R1)      ; LOAD LOCATION
7176 014756 000257      CCC      ; CLEAR FLAGS
7177 014760 005611      SBC      (R1)      ; SUBTRACT C BIT=0
7178 014762 004737 017062      JSR      PC,#$CC0    ; CHECK FOR CC = 0
7179 014766 022711 000003      CMP      #3,(R1)      ; CHECK IT
7180 014772 001404      BEQ      2$      ; CONTINUE IF OK
7181 014774 012745 000325      MOV      #325,-(R5)
(2) 015000 005245      INC      -(R5)
(2) 015002 000000      HALT      ; SBC INSTRUCTION FAILED
7182 015004 000261 2$:      SEC      ; C=1
7183 015006 005611      SBC      (R1)      ; SUBTRACT C BIT=1
7184 015010 000261      SEC      ; C=1
7185 015012 005611      SBC      (R1)
7186 015014 004737 017062      JSR      PC,#$CC0    ; CHECK FOR CC = 0
7187 015020 022711 000001      CMP      #1,(R1)      ; CHECK IT
7188 015024 001404      BEQ      3$
7189 015026 012745 000326      MOV      #326,-(R5)
(2) 015032 005245      INC      -(R5)
(2) 015034 000000      HALT      ; SBC INSTRUCTION FAILED
7190 015036 000261 3$:      SEC      ; C=1
7191 015040 005611      SBC      (R1)      ; SUBTRACT C BIT=1
7192 015042 004737 017164      JSR      PC,#$CC4    ; CHECK FOR CC = 4
7193 015046 000261      SEC      ; C=1
7194 015050 005611      SBC      (R1)      ; SUBTRACT C BIT = 1
7195 015052 004737 017272      JSR      PC,#$CC11   ; CHECK FOR CC = 11
7196 015056 022711 177777      CMP      #-1,(R1)      ; CHECK IT
7197 015052 001404      BEQ      4$      ; CONTINUE IF OK
7198 015064 012745 000327      MOV      #327,-(R5)
(2) 015070 005245      INC      -(R5)
(2) 015072 000000      HALT      ; SBC INSTRUCTION FAILED
7199 015074 012711 100000 4$:      MOV      #100000,(R1) ; LOAD R1
7200 015100 000261      SEC      ; C=1
7201 015102 005611      SBC      (R1)      ; SUBTRACT C BIT = 1
7202 015104 004737 017122      JSR      PC,#$CC2    ; CHECK FOR CC = 2
7203
7204
7205
7206 .....
(2) *TEST: 113 NEW INSTRUCTION IN THIS SECTION IS SXT
(3) .....
7207
7208 015110 021527 000113 SXT1:  CMP      (R5),#113
(2) 015110 001026      BNE      ESXT1      ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
7209 015114

```

E07

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DVKAAA.P11 T113 NEW INSTRUCTION IN THIS SECTION IS SXT

*** SEQ 0082

| | | | | | | | |
|------|--------|--------|--------|--------|------|------------|---|
| 7210 | 015116 | 005215 | | 18: | INC | (R5) | |
| 7211 | 015120 | 012702 | 000442 | | MOV | #TEMP1,R2 | : LOAD ADDRESS |
| 7212 | 015124 | 005012 | | | CLR | (R2) | : CLEAR LOCATIONS |
| 7213 | 015128 | 000277 | | | SCC | | |
| 7214 | 015130 | 000254 | | | CLNZ | | |
| 7215 | 015132 | 006712 | | | SXT | (R2) | : SIGN EXTEND |
| 7216 | 015134 | 004737 | 017206 | | JSR | PC,2#SCC5 | : CHECK FOR CC = 5 |
| 7217 | 015140 | 005712 | | | TST | (R2) | : LOCATION SHOULD STILL BE 0 |
| 7218 | 015142 | 001404 | | | BEQ | 25 | : CONTINUE IF OK |
| 7219 | 015144 | 012745 | 000330 | | MOV | #330,-(R5) | |
| (2) | 015150 | 005245 | | | INC | -(R5) | |
| (2) | 015152 | 000000 | | | HALT | | : SXT INSTRUCTION FAILED |
| (2) | 015154 | 000273 | | 25: | SENV | | : SET N, V & C BITS |
| (2) | 015156 | 006712 | | | SXT | (R2) | : SIGN EXTEND |
| (2) | 015160 | 004737 | 017272 | | JSR | PC,2#SCC11 | : CHECK FOR CC = 11 |
| (2) | 015164 | 022712 | 177777 | | CMP | #-1,(R2) | : LOCATION SHOULD NOW HAVE -1 |
| (2) | 015170 | 001404 | | | BEQ | SWAB1 | : CONTINUE IF OK |
| (2) | 015172 | | | ESXT1: | | | |
| (2) | 015172 | 012745 | 000331 | | MOV | #331,-(R5) | |
| (2) | 015176 | 005245 | | | INC | -(R5) | |
| (2) | 015200 | 000000 | | | HALT | | : WRONG RESULT IN TEMP1 OR WRONG SEQUENCE |

F07

CVK000 MACY11 27(732)
 DVK000.P11 T114

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 NEW INSTRUCTION IN THIS SECTION IS SWAB

*** SEQ 0083

```

7226
(2)
(3)
7227
7228 015202
(2) 015202 021527 000114
7229 015206 001034
7230 015210 005215
7231 015212 012703 000444
7232 015216 012713 125125
7233 015222 000277
7234 015224 000250
7235 015226 000313
7236 015230 004737 017252
7237 015234 022713 052652
7238 015240 001404
7239 015242 012745 000332
(2) 015246 005245
(2) 015250 000000
7240 015252 012713 000377
7241 015256 000277
7242 015260 000244
7243 015262 000363 000000
7244 015266 004737 017164
7245 015272 022713 177400
7246 015276 001404
7247 015300
(2) 015300 012745 000333
(2) 015304 005245
(2) 015306 000000
7248
7249
7250
7251
(2)
(3)
7252
7253 015310
(2) 015310 021527 000115
7254 015314 001041
7255 015316 005215
7256 015320 012704 177777
7257 015324 012767 177777 163110
7258 015332 000277
7259 015334 074467 163102
7260 015340 004737 217206
7261 015344 012767 077777 163070
7262 015352 012700 000442
7263 015356 000263
7264 015360 000244
7265 015362 074410
7266 015364 004737 017272
7267 015370 012701 125252
7268 015374 012720 052525
7269 015400 000277
7270 015402 074140
  
```

```

*****
: TEST: 114 NEW INSTRUCTION IN THIS SECTION IS SWAB
*****
  
```

```

SWAB1:
      CMP      (R5),#114
      BNE     ESWAB1      ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
      INC     (R5)
      MOV     #TEMP2,R3   ; LOAD ADDRESS
      MOV     #125125,(R3) ; LOAD BIT PATTERN INTO LOCATION
      SCC
      CLN
      SWAB    (R3)        ; SWAP BYTES OF LOCATIONS
      JSR     PC,#5CC10   ; CHECK FOR CC = 10
      CMP     #52652,(R3) ; CHECK IT
      BEQ     IS         ; CONTINUE IF OK
      MOV     #332,-(R5)
      INC     -(R5)
      HALT
IS:   MOV     #377,(R3)   ; SWAB INSTRUCTION FAILED
      SCC
      CLZ
      SWAB    0(R3)
      JSR     PC,#5CC4    ; CHECK FOR CC = 4
      CMP     #177400,(R3)
      BEQ     XOR1
ESWAB1:
      MOV     #333,-(R5)
      INC     -(R5)
      HALT
      ; WRONG RESULT IN: TEMP2 OR WRONG SEQUENCE
  
```

```

*****
: TEST: 115 NEW INSTRUCTION IN THIS SECTION IS XOR
*****
  
```

```

XOR1:
      CMP     (R5),#115
      BNE     EXOR1      ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
      INC     (R5)
      MOV     #-1,R4     ; LOAD LOCATIONS
      MOV     #-1,TEMP1
      SCC
      XOR     R4,TEMP1   ; SHOULD PRODUCE 0'S IN TEMP1
      JSR     PC,#5CC5   ; CHECK FOR CC = 5
      MOV     #77777,TEMP1
      MOV     #TEMP1,R0  ; PLACE THE ADDRESS OF TEMP1 IN R0
      SEVC
      CLZ
      XOR     R4,(R0)
      JSR     PC,#5CC11  ; CHECK FOR CC = 11
      MOV     #125252,R1 ; LOAD LOCATIONS
      MOV     #52525,(R0)+
      SCC
      XOR     R1,-R0    ; SHOULD PRODUCE ALL 1'S IN TEMP1
  
```

GO7

DVKAAA MACY11 27(732) 25-AUG-76 13:25 PAGE 54-81
DVKAAA.P11 T115 NEW INSTRUCTION IN THIS SECTION IS XOR

*** SEQ 0084

| | | | | | | | |
|------|--------|--------|---------------|--------|-------------|---|---|
| 7271 | 015404 | 004737 | 017272 | JSR | PC, @SCC11 | : | CHECK FOR CC = 11 |
| 7272 | 015410 | 022737 | 177777 000442 | CMP | #-1, @TEMP1 | : | CHECK IT |
| 7273 | 015416 | 001404 | | BEG | ADD1 | : | CONTINUE IF OK |
| 7274 | 015420 | | | EXOR1: | | | |
| (2) | 015420 | 012745 | 000334 | MOV | #334, -(R5) | | |
| 3 | 015424 | 005245 | | INC | -(R5) | | |
| 4 | 015426 | 000000 | | HALT | | : | WRONG RESULT IN TEMP1 OR WRONG SEQUENCE |

```

7275 (2) (3)
7276
7277 015430 ADD1:
7278 (2) 015430 021527 000116 CMP (R5), #116
7279 015434 001133 BNE EADD1 ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
7280 015436 005215 INC (R5)
7281 015440 012700 000444 MOV #TEMP2, R0 ; LOAD ADDRESSES
7282 015444 012701 000440 MOV #TEMP, R1
7283 015450 012767 021421 162766 MOV #21421, TEMP2 ; LOAD LOCATIONS
7284 015456 011011 MOV (R0), (R1)
7285 015460 061011 ADD (R0), (R1)
7286 015462 004737 017062 JSR PC, #5CC0 ; CHECK FOR CC = 0
7287 015466 022767 043042 162744 CMP #43042, TEMP ; CHECK IT
7288 015474 001404 BEQ 15 ; CONTINUE IF OK
7289 (2) 015476 012745 000335 MOV #335, -(R5)
7290 (2) 015502 005245 INC -(R5)
7291 (2) 015504 000000 HALT ; ADD INSTRUCTION FAILED
7292 015506 005010 15: CLR (R0) ; CLEAR LOCATION TEMP2
7293 015510 060020 ADD R0, (R0)+ ; PLACE THE ADDRESS OF TEMP2 IN LOCATION TEMP2
7294 015512 024027 000444 CMP -(R0), #TEMP2 ; CHECK IT
7295 015516 001404 BEQ 25
7296 015520 012745 000336 MOV #336, -(R5)
7297 (2) 015524 005245 INC -(R5)
7298 (2) 015526 000000 HALT ; ADD INSTRUCTION FAILED IN MODE 2
7299 015530 012767 156357 162706 25: MOV #-21421, TEMP2 ; LOAD LOCATIONS
7300 015536 012011 MOV (R0)+, (R1)
7301 015540 064011 ADD -(R0), (R1) ; ADD
7302 015542 004737 017272 JSR PC, #5CC11 ; CHECK FOR CC = 11
7303 015546 022767 134736 162664 CMP #-43042, TEMP ; CHECK IT
7304 015554 001404 BEQ 35 ; CONTINUE IF OK
7305 015556 012745 000337 MOV #337, -(R5)
7306 (2) 015562 005245 INC -(R5)
7307 (2) 015564 000000 HALT ; ADD INSTRUCTION FAILED
7308 015566 012767 100000 162650 35: MOV #100000, TEMP2 ; LOAD LOCATIONS
7309 015574 011061 000000 MOV (R0), 0(R1)
7310 015600 066011 000000 ADD 0(R0), (R1) ; ADD SHOULD RESULT AS 0'S
7311 015604 004737 017230 JSR PC, #5CC7 ; CHECK FOR CC=7
7312 015610 012767 021421 162624 MOV #21421, TEMP1 ; LOAD LOCATION TEMP1
7313 015616 012760 000442 000000 MOV #TEMP1, 0(R0) ; PLACE THE ADDRESS OF LOCATION TEMP1 IN TEMP2
7314 015624 012711 156357 MOV #-21421, (R1) ; LOAD LOCATION TEMP
7315 015630 010004 MOV R0, R4 ; MAKE R4 POINT TO LOCATION TEMP2
7316 015632 067411 000000 ADD 20(R4), (R1) ; ADD SHOULD RESULT AS 0'S
7317 015636 004737 017206 JSR PC, #5CC5 ; CHECK FOR CC=5
7318 015642 005430 NEG 2(R0)+ ; NEGATE THE CONTENTS OF TEMP1
7319 015644 012746 021421 MOV #21421, -(SP) ; PLACE # 21421 ON THE STACK
7320 015650 065066 000000 ADD 2-(R0), 0(SP) ; ADD, SHOULD=0'S
7321 015654 004737 017206 JSR PC, #5CC5 ; CHECK FOR CC=5
7322 015660 005726 TST (SP)+ ; CHECK THE STACK TO CONTAIN 0. ALSO
7323 015662 001404 BEQ 45 ; RESTORE THE STACK POINTER
7324 015664 012745 000340 MOV #340, -(R5)
7325 (2) 015670 005245 INC -(R5)
7326 (2) 015672 000000 HALT ; ADD INSTRUCTION FAILED IN MODE 5
7327 015674 012767 137777 162542 45: MOV #137777, TEMP2

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```

7320 015702 062767 137777 162534      ADD      #137777,TEMP2
7321 015710 004737 017142                    JSR      PC,2#SCC3      ; CHECK CC=3
7322 015714 022767 077776 162522      CMP      #77776,TEMP2
7323 015722 001404                    BEQ      SUB1
7324 015724                    EADD1:
(2) 015724 012745 000341      MOV      #341,-(R5)
(2) 015730 005245      INC      -(R5)
(2) 015732 000000      HALT                    ; WRONG RESULT AT TEMP OR WRONG SEQUENCE

```

```

:*****
:*TEST: 117      NEW INSTRUCTION IN THIS SECTION IS SUB
:*****

```

```

7329
7330 015734                    SJB1:
(2) 015734 021527 000117      CMP      (R5),#117
7331 015740 001100      BNE     ESUB1            ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
7332 015742 005215      INC      (R5)
7333 015744 012702 000440      MOV      #TEMP,R2      ; LOAD ADDRESSES
7334 015750 012703 000442      MOV      #TEMP1,R3      ;
7335 015754 012767 021421 162456      MOV      #21421,TEMP    ; LOAD LOCATIONS
7336 015762 012767 156357 162452      MOV      #-21421,TEMP1  ;
7337 015770 161213      SUB      (R2),(R3)      ; RESULT SHOULD=-43042
7338 015772 004737 017252      JSR      PC,2#SCC10     ; CHECK FOR CC = 10
7339 015776 022767 134736 162436      CMP      #-43042,TEMP1  ; CHECK IT
7340 016004 001404      BEQ     IS              ; CONTINUE IF OK
7341 016006 012745 000342      MOV      #342,-(R5)
(2) 016012 005245      INC      -(R5)
(2) 016014 000000      HALT                    ; SUB INSTRUCTION FAILED
7342 016016 012767 021421 162416 15:      MOV      #21421,TEMP1   ; LOAD LOCATION
7343 016024 161213      SUB      (R2),(R3)      ; RESULT SHOULD=0
7344 016026 001404      BEQ     25
7345 016030 012745 000343      MOV      #343,-(R5)
(2) 016034 005245      INC      -(R5)
(2) 016036 000000      HALT                    ; SJB INSTRUCTION FAILED
7346 016040 012767 177777 162374 25:      MOV      #-1,TEMP1      ; LOAD LOCATIONS
7347 016046 012767 077777 162354      MOV      #77777,TEMP    ; LOAD LOCATIONS
7348 016054 161312      SUB      (R3),(R2)      ; RESULT SHOULD GIVE 10000 AND OVERFLOW
7349 016056 004737 017334      JSR      PC,2#SCC13     ; CHECK FOR CC = 13
7350 016062 022767 100000 162350      CMP      #100000,TEMP   ; CHECK IT
7351 016070 001404      BEQ     35              ; CONTINUE IF OK
7352 016072 012745 000344      MOV      #344,-(R5)
(2) 016076 005245      INC      -(R5)
(2) 016100 000000      HALT                    ; SUB INSTRUCTION FAILED
7353 016102 012712 177777 35:      MOV      #-1,(R2)
7354 016106 161312      SUB      (R3),(R2)
7355 016110 004737 017164      JSR      PC,2#SCC4      ; CHECK FOR CC = 4
7356 016114 012767 077777 162316      MOV      #77777,TEMP
7357 016122 162767 077777 162310      SLB     #77777,TEMP
7358 016130 004737 017164      JSR      PC,2#SCC4      ; CHECK FOR CC=4
7359 016134 005767 162300      TST     TEMP
7360 016140 001404      BEQ     SOB            ; TEMP SHOULD BE =0
7361 016142                    ESJB1:
(2) 016142 012745 000345      MOV      #345,-(R5)
(2) 016146 005245      INC      -(R5)

```

J07

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DVKAAA.P11 T117 NEW INSTRUCTION IN THIS SECTION IS SUB

*** SEQ 0087

(2) 016150 000000

HALT

; SUB INSTRUCTION FAILED OR SEQUENCE ERROR


```

7362
(2)
(3)
7363
7364 016152 021527 000120
(2) 016152
7365 016156 001042
7366 016160 005215
7367 016162 012700 000012
7368 016166 005001
7369 016170 005201
7370 016172 020127 000012
7371 016176 003404
7372 016200 012745 000346
(2) 016204 005245
(2) 016206 000000
7373 016210 000277
7374 016212 077012
7375 016214 004737 017354
7376 016220 005700
7377 016222 001404
7378 016224 012745 000347
(2) 016230 005245
(2) 016232 000000
7379 016234 022701 000012
7380 016240 001404
7381 016242 012745 000350
(2) 016246 005245
(2) 016250 000000
7382 016252 012704 000010
7383 016256 077401
7384 016260 005704
7385 016262 001404
7386 016264
(2) 016264 012745 000351
(2) 016270 005245
(2) 016272 000000

```

```

*****
*TEST: 120 NEW INSTRUCTION IN THIS SECTION IS SOB
*****
SOB:
CMP (R5),#120
BNE ES0B ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
INC (R5)
MOV #10.,R0 ; LOAD REGISTERS
CLR R1
15: INC R1 ; KEEP COUNT
CMP R1,#10.
BLE 25
MOV #346,-(R5)
INC -(R5)
HALT ; SOB INSTRUCTION FAILED
25: SCC
SOB R0,15 ; SUB. 1 FROM REG. 0, GO BACK TO 15
JSR PC,#SCC17 ; CHECK FOR CC = 17
TST R0 ; REG. 0 = 0 ?
BEQ 35 ; NO, FAILED
MOV #347,-(R5)
INC -(R5)
HALT ; SOB INSTRUCTION FAILED
35: CMP #10.,R1 ; DID IT GO THRU 10 TIMES ?
BEQ 45 ; CONTINUE IF OK
MOV #350,-(R5)
INC -(R5)
HALT ; SOB INSTRUCTION FAILED
45: MOV #10,R4 ; PLACE #10 IN R4
55: SOB R4,55 ; STAY HERE UNTILL R4 = 0
TST R4
BEQ PSWNO ; CONTINUE IF OK
ESOB:
MOV #351,-(R5)
INC -(R5)
HALT ; SOB FAILED OR WRONG SEQUENCE

```

```

7387
7388
7389
7390
(2)
(3)
7391
7392
7393 016274
(2) 016274 021527 000121
7394 016300 001042
7395 016302 005215
7396 016304 012700 000440
7397 016310 012701 000442
7398 016314 012711 177777
7399 016320 005010
7403 016322
(1) 016322 106410
7404 016324 004737 017062

```

```

*****
*TEST: 121 NEW INSTRUCTIONS IN THIS SECTION ARE MTPS & MFPS
*****
PSWNO:
CMP (R5),#121
BNE EPSWNO ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
INC (R5)
MOV #TEMP,R0 ; PUT THE ADDRESS OF TEMP IN R0
MOV #TEMP1,R1 ; PUT THE ADDRESS OF TEMP1 IN R1
MOV #177777,(R1) ; TEMP1 = 177777
CLR (R0) ; TEMP = 0
MTPS (R0) ; PSW = 0
.WORD 106400!..C
JSR PC,#SCC0 ; CHECK FOR CC = 0

```

| | | | | | | | | | |
|------|--------|--------|--------|--------|---------|-------|--------------|---|--|
| 7405 | 016330 | | | | | MFPS | (R1) | : | MOVE PSW TO TEMP1 |
| (1) | 016330 | 106711 | | | | .WORD | 106700!..C | | |
| 7409 | 016332 | 004737 | 017164 | | | JSR | PC,2#5CC4 | : | CHECK FOR CC = 4 |
| 7410 | 016336 | 022711 | 177400 | | | CMP | #177400,(R1) | : | CHECK TEMP1 TO MAKE SURE THAT ONLY |
| 7411 | | | | | | | | : | THE LOWER BYTE WAS AFFECTED BY MFPS |
| 7412 | 016342 | 001404 | | | | BEQ | IS | | |
| 7413 | 016344 | 012745 | 000352 | | | MOV | #352,-(R5) | | |
| (2) | 016350 | 005245 | | | | INC | -(R5) | | |
| (2) | 016352 | 000000 | | | | HALT | | : | MTPS OR MFPS INSTRUCTION FAILED |
| 7414 | 016354 | 005011 | | | IS: | CLR | (R1) | | |
| 7418 | 016356 | | | | | MTPS | #377 | : | SET PSW = 357 SINCE T BIT CAN NOT BE SET BY MTPS |
| (1) | 016356 | 106427 | | | | .WORD | 106400!..C | | |
| 7419 | 016362 | 004737 | 017354 | | | JSR | PC,2#5CC17 | : | CHECK FOR CC = 17 |
| 7420 | 016366 | | | | | MFPS | TEMP1 | : | MOVE PSW TO TEMP1 |
| (1) | 016366 | 106767 | | | | .WORD | 106700!..C | | |
| 7424 | 016372 | 004737 | 017272 | | | JSR | PC,2#5CC11 | : | CHECK FOR CC = 11 [C BIT SHOULD NOT BE EFFECTED BY MFP |
| 7425 | 016376 | 022767 | 000357 | 162036 | | CMP | #357,TEMP1 | | |
| 7426 | 016404 | 001404 | | | | BEQ | BTWRD | | |
| 7427 | 016406 | | | | EPSWNO: | | | | |
| (2) | 016406 | 012745 | 000353 | | | MOV | #353,-(R5) | | |
| (2) | 016412 | 005245 | | | | INC | -(R5) | | |
| (2) | 016414 | 000000 | | | | HALT | | : | MFPS INSTRUCTION FAILED IN MODE 6 |
| 7428 | | | | | | | | : | OR SEQUENCE ERROR |

```

7429 .....
(2) *TEST: 122 BYTE INSTRUCTIONS REQUIRING WORD INST. TO CHECK
(3) .....
7430 .....
7431 .....

```

```

BTWRD:
(2) 016416 021527 000122      CMP      (R5),#122
7432 016422 001124      BNE     EB*WRD      ; IF IN WRONG SEQUENCE GO TO HALT AT THE END OF THE TEST
7433 016424 005215      INC     (R5)
7434 016426 005000      CLR     RO
7435 016430 000277      SCC
7436 016432 112700 000200      MOVB   #200,RO      ; SET THE HIGHEST BIT OF THE
7437                                ; LOWER BYTE
7438 016436 004737 017272      JSR    PC,2#5CC11   ; CHECK FOR CC=11
7439 016442 022700 177600      CMP    #177600,RO  ; CHECK FOR SIGN EXTENSION IN RO
7440 016446 001404      BEQ    1$
7441 016450 012745 000354      MOV    #354,-(R5)
(2) 016454 005245      INC    -(R5)
(2) 016456 000000      HALT
7442 016460 000277      1$:    SCC
7443 016462 012700 177777      MOV    #177777,RO
7444 016466 112700 000000      MOVB   #0,RO      ; CLEAR THE LOWER BYTE OF RO.
7445 016472 004737 017206      JSR    PC,2#5CC5   ; CHECK FOR CC=5
7446 016476 005700      TST   RO          ; CHECK RO FOR SIGN EXTENTION
7447 016500 001404      BEQ    2$
7448 016502 012745 000355      MOV    #355,-(R5)
(2) 016506 005245      INC    -(R5)
(2) 016510 000000      HALT
7449 016512 012704 000444      2$:    MOV    #TEMP2,R4   ; SIGN WAS NOT EXTENDED IN RO.
7450 016516 012714 000377      MOV    #377,(R4)   ; R4 IS POINTING TO TEMP2
7451 016522 012706 000526      MOV    #START-2,R6 ; PLACE #377 IN LOCATION TEMP2
7452 016526 116426 000000      MOVB   0(R4),(R6)+ ; PUSH # 377 ON STACK
7453 016532 022706 000530      CMP    #START,R6
7454 016536 001404      BEQ    3$
7455 016540 012745 000356      MOV    #356,-(R5)
(2) 016544 005245      INC    -(R5)
(2) 016546 000000      HALT
7456                                ; R6 DID NOT GET INCREMENTED
7457 016550 124627 000377      3$:    CMPB  -(R6),#377  ; BY 2 BY A BYTE INSTRUCTION
7458                                ; CHECK LOCATION START-2 TO
7459                                ; CONTAIN PROPER DATA
7459 016554 001404      BEQ    4$
7460 016556 012745 000357      MOV    #357,-(R5)
(2) 016562 005245      INC    -(R5)
(2) 016564 000000      HALT
7461 016566 022706 000526      4$:    CMP    #START-2,R6 ; BYTE INSTRUCTION IS FAILING WITH R6
7462                                ; CHECK THAT R6 WAS DECREMENTED
7463                                ; BY 2 BY A BYTE INSTRUCTION
7463 016572 001404      BEQ    5$
7464 016574 012745 000360      MOV    #360,-(R5)
(2) 016600 005245      INC    -(R5)
(2) 016602 000000      HALT
7465 016604 016467 000000 161626 5$:    MOV    0(R4),TEMP  ; R6 WAS NOT DECREMENTED
7466 016612 005726      TST   (R6)+      ; SET THE LOWER BYTE OF LOCATION TEMP
7467 016614 000277      SCC
7468 016616 114667 161617      MOVB  -(SP),TEMP+1 ; RESTORE STACK POINTER
7469 016622 004737 017272      JSR    PC,2#5CC11 ; SET THE HIGHER BYTE OF LOCATION TEMP
7470 016626 022767 177777 161604      CMP    #177777,TEMP ; CHECK FOR CC=11
7471 016634 001404      BEQ    6$          ; CHECK TEMP FOR THE CORRECT VALUE

```

N07

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DVKAAR.P11 T122 BYTE INSTRUCTIONS REQUIRING WORD INST. TO CHECK

*** SEQ 0091

| | | | | | | | |
|------|--------|--------|--------|---------|------|---------------|---|
| 7472 | 016636 | 012745 | 000361 | | MOV | #361, -(R5) | |
| 2) | 016642 | 005245 | | | INC | -(R5) | |
| 2) | 016644 | 000000 | | | HALT | | : TEMP FOULED UP |
| 7473 | 016646 | 005067 | 161566 | 6S: | CLR | TEMP | |
| 7474 | 016652 | 000241 | | | CLC | | |
| 7475 | 016654 | 105167 | 161561 | | CCMB | TEMP+1 | : WRITE 1'S IN THE HIGHER BYTE OF TEMP |
| 7476 | 016660 | 004737 | 017272 | | JSR | PC, 2*#5CC11 | : CHECK FOR CC=11 |
| 7477 | 016664 | 022767 | 177400 | 161546 | CMP | #177400, TEMP | |
| 7478 | 016672 | 001404 | | | BEQ | NEXT | |
| 7479 | 016674 | | | ESTWRD: | | | |
| 2) | 016674 | 012745 | 000362 | | MOV | #362, -(R5) | |
| 2) | 016700 | 005245 | | | INC | -(R5) | |
| 2) | 016702 | 000000 | | | HALT | | : WRONG VALUE IN TEMP OR WRONG SEQUENCE |

016700
016701
016702
016703
016704
016705
016706
016707
016708
016709
016710
016711
016712
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016789
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016795
016796
016797
016798
016799
017000
017001
017002
017003
017004
017005
017006
017007
017008
017009
017010
017011
017012
017013
017014
017015
017016

END OF PASS

NEXT: CMP (R5),#123
BEQ 25 ; IF IN WRONG SEQUENCE GO TO HLT BELOW
MOV #363,-(R5)
INC -(R5)
HALT ; PROGRAM IS IN WRONG SEQUENCE
25: INC \$PASS
CMPB \$PASS,#1 ; ALLOW THE TYPE OUT OF END OF
; PASS EVERY 377 PASSES
BNE GET42
; TYPE END OF PASS MESSAGE
GET42: MOV #42,R0
SENDAD: BEQ DOAGN
JSA PC,(R0)
NOP
NOP
NOP
DOAGN: CLR \$TESTN ; PREPARE TO START FROM TEST 0
RETURN: JMP START ; START TEST OVER AT BEGINNING

.SBTTL POWER FAIL ROUTINE

PWRDN: MOV #PWRUP,#24 ; GO TO POWER UP ROUTINE AFTER THE POWER COMES BACK
HALT
PWRUP: MOV \$START,SP
MOV #PWRDN,#24
; TYPE POWER
BR DOAGN

017020
017026
017030
017034
017036
017040
017044
017046
017052
017054
017060

.SBTTL TYPE ROUTINE

| | | | | | | | | |
|--------|--------|--------|--------|-------|------|-------------|---|--|
| 017020 | 132737 | 000040 | 000421 | TYPE: | BITB | #40, @SENVN | : | HAS THE CONSOLE OUTPUTS BEEN SUPPRESSED? |
| 017026 | 001012 | | | | BNE | 4\$ | : | IF SO THEN GO TO 4\$ |
| 017030 | 017603 | 000000 | | | MOV | @(SP), R3 | : | GET ADDRESS OF MESSAGE |
| 017034 | 105713 | | | 1\$: | TSTB | (R3) | : | END OF MESSAGE ? |
| 017036 | 001406 | | | | BEG | 4\$ | : | YES, GO WRAP IT UP |
| 017040 | 105777 | 161402 | | 3\$: | TSTB | @TPS | : | READY FOR NEXT CHARACTER ? |
| 017044 | 100375 | | | | BPL | 3\$ | : | NO, WAIT |
| 017046 | 112377 | 161376 | | | MOVB | R3 +, @TPB | : | LOAD AND TYPE THE CHARACTER |
| 017052 | 000770 | | | | BR | 1\$ | : | YES, GET THE NEXT CHARACTER |
| 017054 | 062716 | 000002 | | -\$: | ADD | #2, SP | : | ADJUST THE RETURN PC |
| 017060 | 000006 | | | | RTR | | : | RETURN |

| | | | | | | | |
|------|--------|--------|--------|-------|------|-------------|------------------------------------|
| 7547 | 017062 | 003402 | | SCC0: | BLE | 1S | |
| 7548 | 017064 | 100401 | | | BMI | 1S | |
| 7549 | 017066 | 103004 | | | BCC | 2S | |
| 7550 | 017070 | | | 1S: | | | |
| (2) | 017070 | 012745 | 000364 | | MOV | #364, -(R5) | |
| (2) | 017074 | 005245 | | | INC | -(R5) | |
| (2) | 017076 | 000000 | | | HALT | | ;WRONG CC, IT SHOULD HAVE BEEN = 0 |
| 7551 | 017100 | 000207 | | 2S: | RTS | PC | |
| 7552 | | | | | | | |
| 7553 | 017102 | 003402 | | SCC1: | BLE | 1S | |
| 7554 | 017104 | 100401 | | | BMI | 1S | |
| 7555 | 017106 | 103404 | | | BCS | 2S | |
| 7556 | 017110 | | | 1S: | | | |
| (2) | 017110 | 012745 | 000365 | | MOV | #365, -(R5) | |
| (2) | 017114 | 005245 | | | INC | -(R5) | |
| (2) | 017116 | 000000 | | | HALT | | ;WRONG CC, IT SHOULD HAVE BEEN = 1 |
| 7557 | 017120 | 000207 | | 2S: | RTS | PC | |
| 7558 | | | | | | | |
| 7559 | 017122 | 100402 | | SCC2: | BMI | 1S | |
| 7560 | 017124 | 101401 | | | BLOS | 1S | |
| 7561 | 017126 | 102404 | | | BVS | 2S | |
| 7562 | 017130 | | | 1S: | | | |
| (2) | 017130 | 012745 | 000366 | | MOV | #366, -(R5) | |
| (2) | 017134 | 005245 | | | INC | -(R5) | |
| (2) | 017136 | 000000 | | | HALT | | ;WRONG CC, IT SHOULD HAVE BEEN = 2 |
| 7563 | 017140 | 000207 | | 2S: | RTS | PC | |
| 7564 | | | | | | | |
| 7565 | 017142 | 100403 | | SCC3: | BMI | 1S | |
| 7566 | 017144 | 001402 | | | BEQ | 1S | |
| 7567 | 017146 | 102001 | | | BVC | 1S | |
| 7568 | 017150 | 103404 | | | BCS | 2S | |
| 7569 | 017152 | | | 1S: | | | |
| (2) | 017152 | 012745 | 000367 | | MOV | #367, -(R5) | |
| (2) | 017156 | 005245 | | | INC | -(R5) | |
| (2) | 017160 | 000000 | | | HALT | | ;WRONG CC, IT SHOULD HAVE BEEN = 3 |
| 7570 | 017162 | 000207 | | 2S: | RTS | PC | |
| 7571 | | | | | | | |
| 7572 | 017164 | 100403 | | SCC4: | BMI | 1S | |
| 7573 | 017166 | 001002 | | | BNE | 1S | |
| 7574 | 017170 | 102401 | | | BVS | 1S | |
| 7575 | 017172 | 103004 | | | BCC | 2S | |
| 7576 | 017174 | | | 1S: | | | |
| (2) | 017174 | 012745 | 000370 | | MOV | #370, -(R5) | |
| (2) | 017200 | 005245 | | | INC | -(R5) | |
| (2) | 017202 | 000000 | | | HALT | | ;WRONG CC, IT SHOULD HAVE BEEN = 4 |
| 7577 | 017204 | 000207 | | 2S: | RTS | PC | |
| 7578 | | | | | | | |
| 7579 | 017206 | 100403 | | SCC5: | BMI | 1S | |
| 7580 | 017210 | 001002 | | | BNE | 1S | |
| 7581 | 017212 | 102401 | | | BVS | 1S | |
| 7582 | 017214 | 103404 | | | BCS | 2S | |
| 7583 | 017216 | | | 1S: | | | |
| (2) | 017216 | 012745 | 000371 | | MOV | #371, -(R5) | |
| (2) | 017222 | 005245 | | | INC | -(R5) | |
| (2) | 017224 | 000000 | | | HALT | | ;WRONG CC, IT SHOULD HAVE BEEN = 5 |
| 7584 | 017226 | 000207 | | 2S: | RTS | PC | |

| | | | | | | | | | |
|------|--------|--------|--------|--------|------|-------------|--|--|-------------------------------------|
| 7585 | | | | | | | | | |
| 7586 | | | | | | | | | |
| 7587 | 017230 | 100403 | | SCC7: | BMI | 1S | | | |
| 7588 | 017232 | 001002 | | | BNE | 1S | | | |
| 7589 | 017234 | 102001 | | | BVC | 1S | | | |
| 7590 | 017236 | 103404 | | | BCS | 2S | | | |
| 7591 | 017240 | | | 1S: | | | | | |
| (2) | 017240 | 012745 | 000372 | | MOV | #372, -(R5, | | | |
| (2) | 017244 | 005245 | | | INC | -(R5) | | | |
| (2) | 017246 | 000000 | | | HALT | | | | ;WRONG CC, IT SHOULD HAVE BEEN = 7 |
| 7592 | 017250 | 000207 | | 2S: | RTS | PC | | | |
| 7593 | | | | | | | | | |
| 7594 | 017252 | 100002 | | SCC10: | BPL | 1S | | | |
| 7595 | 017254 | 101401 | | | BLOS | 1S | | | |
| 7596 | 017256 | 102004 | | | BVC | 2S | | | |
| 7597 | 017260 | | | 1S: | | | | | |
| (2) | 017260 | 012745 | 000373 | | MOV | #373, -(R5) | | | |
| (2) | 017264 | 005245 | | | INC | -(R5) | | | |
| (2) | 017266 | 000000 | | | HALT | | | | ;WRONG CC, IT SHOULD HAVE BEEN = 10 |
| 7598 | 017270 | 000207 | | 2S: | RTS | PC | | | |
| 7599 | | | | | | | | | |
| 7600 | 017272 | 100003 | | SCC11: | BPL | 1S | | | |
| 7601 | 017274 | 001402 | | | BEQ | 1S | | | |
| 7602 | 017276 | 102401 | | | BVS | 1S | | | |
| 7603 | 017300 | 103404 | | | BCS | 2S | | | |
| 7604 | 017302 | | | 1S: | | | | | |
| (2) | 017302 | 012745 | 000374 | | MOV | #374, -(R5) | | | |
| (2) | 017306 | 005245 | | | INC | -(R5) | | | |
| (2) | 017310 | 000000 | | | HALT | | | | ;WRONG CC, IT SHOULD HAVE BEEN = 11 |
| 7605 | 017312 | 000207 | | 2S: | RTS | PC | | | |
| 7606 | | | | | | | | | |
| 7607 | 017314 | 100002 | | SCC12: | BPL | 1S | | | |
| 7608 | 017316 | 101401 | | | BLOS | 1S | | | |
| 7609 | 017320 | 102404 | | | BVS | 2S | | | |
| 7610 | 017322 | | | 1S: | | | | | |
| (2) | 017322 | 012745 | 000375 | | MOV | #375, -(R5) | | | |
| (2) | 017326 | 005245 | | | INC | -(R5) | | | |
| (2) | 017330 | 000000 | | | HALT | | | | ;WRONG CC, IT SHOULD HAVE BEEN = 12 |
| 7611 | 017332 | 000207 | | 2S: | RTS | PC | | | |
| 7612 | | | | | | | | | |
| 7613 | 017334 | 100002 | | SCC13: | BPL | 1S | | | |
| 7614 | 017336 | 003401 | | | BLE | 1S | | | |
| 7615 | 017340 | 103404 | | | BCS | 2S | | | |
| 7616 | 017342 | | | 1S: | | | | | |
| (2) | 017342 | 012745 | 000376 | | MOV | #376, -(R5) | | | |
| (2) | 017346 | 005245 | | | INC | -(R5) | | | |
| (2) | 017350 | 000000 | | | HALT | | | | ;WRONG CC, IT SHOULD HAVE BEEN = 13 |
| 7617 | 017352 | 000207 | | 2S: | RTS | PC | | | |
| 7618 | | | | | | | | | |
| 7619 | 017354 | 100003 | | SCC17: | BPL | 1S | | | |
| 7620 | 017356 | 001002 | | | BNE | 1S | | | |
| 7621 | 017360 | 102001 | | | BVC | 1S | | | |
| 7622 | 017362 | 103404 | | | BCS | 2S | | | |
| 7623 | 017364 | | | 1S: | | | | | |
| (2) | 017364 | 012745 | 000377 | | MOV | #377, -(R5) | | | |
| (2) | 017370 | 005245 | | | INC | -(R5) | | | |

F08

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DVKAAA.P11 ROUTINES TO CHECK CONDITION CODES

*** SEQ 00%

| | | | | | |
|------|--------|--------|------|------|----|
| (2) | 017372 | 000000 | | HAL* | |
| 7624 | 017374 | 000207 | 28: | R*S | PC |
| 7625 | | | | | |
| 7626 | | 000001 | .END | | |

;WRONG CC, IT SHOULD HAVE BEEN = 17

M08

| | | | | | | | | | | | | | | |
|--------|----------|-------|-------|-------|-------|-------|-------|-------|-------|-------|-------|-------|-------|-------|
| R6 | =%000006 | 5020# | 5422 | 5428* | 5429* | 5430* | 5431* | 5432* | 5433* | 5434* | 5435 | 5437* | 5439* | 7451* |
| | | 7452* | 7453 | 7457 | 7461 | 7466 | | | | | | | | |
| SBC80 | 004272 | 5700# | | | | | | | | | | | | |
| SBC81 | 012552 | 6795# | | | | | | | | | | | | |
| SBC0 | 006066 | 6009# | | | | | | | | | | | | |
| SBC1 | 014726 | 7170# | | | | | | | | | | | | |
| SEAVC | = 000273 | 5027# | 6054 | 7220 | | | | | | | | | | |
| SEVC | = 000263 | 5026# | 5763 | 5810 | 6096 | 6562 | 6942 | 6956 | 7263 | | | | | |
| SOB | 016152 | 7360 | 7364# | | | | | | | | | | | |
| SP | =%000006 | 5021# | 5085* | 5358* | 5362 | 5366 | 5369 | 5373* | 5376* | 5377* | 5378* | 5379* | 5387 | 5395 |
| | | 5402 | 5404* | 6554* | 6555 | 6590 | 6591* | 6593* | 6595 | 6947* | 6949 | 6957 | 6959 | 7312* |
| | | 7313* | 7315 | 7468 | 7518* | 7530 | 7540* | | | | | | | |
| S*ART | 000530 | 5079 | 5085# | 5358 | 5362 | 5366 | 5387 | 5395 | 6959 | 7451 | 7453 | 7461 | 7507 | 7518 |
| SJ80 | 006710 | 6139 | 6143# | | | | | | | | | | | |
| SJ81 | 015734 | 7323 | 7330# | | | | | | | | | | | |
| SWA80 | 006332 | 6058 | 6065# | | | | | | | | | | | |
| SWA81 | 015202 | 7224 | 7228# | | | | | | | | | | | |
| SXT0 | 006244 | 6043# | | | | | | | | | | | | |
| SXT1 | 015110 | 7208# | | | | | | | | | | | | |
| TEMP | 000440 | 5043# | 5044 | 5277* | 5278* | 5279 | 5286* | 5287 | 5341 | 5346 | 5349* | 5350 | 5422* | 5437 |
| | | 5439 | 5792 | 6231 | 6256 | 6263* | 6268 | 6276* | 6280 | 6289 | 6295* | 6297* | 6301 | 6305* |
| | | 6309* | 6313 | 6324* | 6325 | 6340 | 6345 | 6350* | 6351 | 6368 | 6379* | 6380 | 6398 | 6403* |
| | | 6415 | 6429 | 6434 | 6454 | 6486 | 6515 | 6538 | 6580 | 6630 | 6657 | 6847 | 6869 | 6904 |
| | | 6916 | 6974 | 7116 | 7144 | 7174 | 7281 | 7286 | 7298 | 7333 | 7335* | 7347* | 7350 | 7356* |
| | | 7357* | 7359 | 7396 | 7465* | 7468* | 7470 | 7473* | 7475* | 7477 | | | | |
| TEMP1 | 000442 | 5045# | 5046 | 5308* | 5309 | 5310* | 5314 | 5317* | 5318 | 5319* | 6232 | 6257 | 6290 | 6326 |
| | | 6352 | 6381 | 6430 | 6441* | 6444 | 6453* | 6455 | 6462 | 6467 | 6487 | 6540 | 6560 | 6566 |
| | | 6631 | 6678 | 6698 | 6722 | 6740 | 6848 | 6868 | 6876 | 6886* | 6896 | 6921 | 6938 | 6968 |
| | | 7005 | 7029 | 7211 | 7257* | 7259* | 7261* | 7262 | 7272 | 7305* | 7306 | 7334 | 7336* | 7339 |
| | | 7342* | 7346* | 7397 | 7420* | 7425 | | | | | | | | |
| TEMP2 | 000444 | 5047# | 5048 | 6233 | 6234* | 6238 | 6244 | 6258 | 6291 | 6294* | 6299 | 6304* | 6311 | 6327 |
| | | 6329 | 6347 | 6353 | 6355 | 6370 | 6382 | 6400 | 6417 | 6428* | 6431 | 6438 | 6456 | 6514 |
| | | 6741 | 6769 | 6799 | 6885* | 6886 | 6901* | 6953 | 7052 | 7073 | 7097 | 7115 | 7231 | 7280 |
| | | 7282* | 7291 | 7294* | 7301* | 7319* | 7320* | 7322 | 7449 | | | | | |
| TP8 | 000450 | 5050# | 7537* | | | | | | | | | | | |
| TP5 | 000446 | 5049# | 7535 | | | | | | | | | | | |
| TST80 | 002724 | 5438 | 5451# | | | | | | | | | | | |
| TST81 | 010712 | 6468 | 6483# | | | | | | | | | | | |
| TST0 | 004450 | 5746# | | | | | | | | | | | | |
| TST1 | 012734 | 6843# | | | | | | | | | | | | |
| TYPE | 017020 | 5059 | 7528# | | | | | | | | | | | |
| VBIT | 000670 | 5131 | 5134# | | | | | | | | | | | |
| XOR0 | 006432 | 6082 | 6086# | | | | | | | | | | | |
| XOR1 | 015310 | 7246 | 7253# | | | | | | | | | | | |
| YESCC | 001054 | 5185 | 5192# | | | | | | | | | | | |
| ZBIT | 001004 | 5166 | 5171# | | | | | | | | | | | |
| SAPTHD | 000430 | 5032# | 5034 | | | | | | | | | | | |
| SCC0 | 017062 | 5680 | 5707 | 5715 | 5989 | 6016 | 6024 | 6118 | 6182 | 6778 | 6803 | 6811 | 7153 | 7178 |
| | | 7186 | 7285 | 7404 | 7547# | | | | | | | | | |
| SCC1 | 017102 | 5495 | 5500 | 5527 | 5540 | 5550 | 5796 | 5801 | 5835 | 5848 | 5858 | 6545 | 6550 | 6598 |
| | | 6611 | 6636 | 6880 | 6925 | 6930 | 6982 | 6995 | 7009 | 7553# | | | | |
| SCC10 | 017252 | 5461 | 5463 | 5757 | 5759 | 5776 | 6072 | 6150 | 6494 | 6496 | 6519 | 6857 | 6859 | 6872 |
| | | 6884 | 7236 | 7338 | 7594# | | | | | | | | | |
| SCC11 | 017272 | 5474 | 5505 | 5507 | 5523 | 5535 | 5556 | 5574 | 5662 | 5724 | 5806 | 5809 | 5831 | 5843 |
| | | 5864 | 5882 | 5971 | 6033 | 6056 | 6099 | 6104 | 6124 | 6200 | 6556 | 6558 | 6586 | 6609 |
| | | 6642 | 6660 | 6756 | 6820 | 6937 | 6946 | 6975 | 7015 | 7034 | 7131 | 7195 | 7222 | 7266 |

| | | | | | | | | | | |
|-----|---|--------|-------|-------|-------|-------|-------|-------|-------|-------|
| .SY | = | 000430 | 50320 | | | | | | | |
| ..A | = | 016366 | 61810 | 61930 | 61940 | 61960 | 74030 | 74050 | 74180 | 74200 |
| ..B | = | 016372 | 61810 | 61830 | 61940 | 61960 | 74030 | 74050 | 74180 | 74200 |
| ..C | = | 00006 | 61810 | 61830 | 61940 | 61960 | 74030 | 74050 | 74180 | 74200 |

| | |
|----|----|
| .S | 29 |
| .S | 27 |
| .S | 26 |
| .S | 25 |
| .S | 24 |
| .S | 23 |
| .S | 22 |
| .S | 21 |
| .S | 20 |
| .S | 19 |
| .S | 18 |
| .S | 17 |
| .S | 16 |
| .S | 15 |
| .S | 14 |
| .S | 13 |
| .S | 12 |
| .S | 11 |
| .S | 10 |
| .S | 9 |
| .S | 8 |
| .S | 7 |
| .S | 6 |
| .S | 5 |
| .S | 4 |
| .S | 3 |
| .S | 2 |
| .S | 1 |
| .S | 0 |

| | | | | | | | | | | | | | | | |
|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|
| ROC | 5983 | 5986 | 5988 | 5995 | 6002 | 7147 | 7150 | 7152 | 7159 | 7166 | | | | | |
| ROCB | 5677 | 5677 | 5679 | 5686 | 5693 | 6772 | 6775 | 6777 | 6784 | 6791 | | | | | |
| ROO | 5430 | 5430 | 5431 | 5432 | 5433 | 5434 | 6117 | 6123 | 6129 | 6133 | 6138 | 7284 | 7290 | 7296 | 7303 |
| RS | 5941 | 5942 | 5947 | 5949 | 7100 | 7101 | 7106 | 7108 | | | | | | | |
| RSB | 5632 | 5633 | 5638 | 5640 | 6725 | 6726 | 6731 | 6733 | | | | | | | |
| RSRB | 5960 | 5960 | 5964 | 5966 | 5969 | 5970 | 7119 | 7120 | 7124 | 7126 | 7129 | 7130 | | | |
| RSC | 5650 | 5651 | 5655 | 5657 | 5660 | 5661 | 6744 | 6745 | 6749 | 6751 | 6754 | 6755 | | | |
| RXC | 5181 | 5199 | 5204 | 7549 | 7575 | | | | | | | | | | |
| RXC | 5099 | 5104 | 5126 | 5142 | 5214 | 5251 | 5269 | 5365 | 5383 | 7555 | 7568 | 7582 | 7590 | 7603 | 7615 |
| RE | 5288 | 5101 | 5106 | 5123 | 5140 | 5160 | 5185 | 5218 | 5230 | 5253 | 5271 | 5284 | 5302 | 5315 | 5332 |
| | 5388 | 5387 | 5370 | 5385 | 5388 | 5396 | 5400 | 5411 | 5452 | 5476 | 5488 | 5497 | 5514 | 5529 | 5544 |
| | 5572 | 5558 | 5576 | 5582 | 5595 | 5601 | 5615 | 5622 | 5627 | 5636 | 5653 | 5664 | 5669 | 5682 | 5689 |
| | 7011 | 5709 | 5717 | 5726 | 5747 | 5761 | 5748 | 5798 | 5814 | 5822 | 5837 | 5852 | 5860 | 5866 | 5884 |
| | 5890 | 5903 | 5909 | 5924 | 5931 | 5936 | 5945 | 5962 | 5973 | 5978 | 5991 | 5998 | 6010 | 6018 | 6026 |
| | 6033 | 6052 | 6058 | 6074 | 6082 | 6106 | 6120 | 6126 | 6134 | 6139 | 6144 | 6152 | 6157 | 6164 | 6187 |
| | 6202 | 6222 | 6229 | 6239 | 6245 | 6269 | 6281 | 6302 | 6314 | 6330 | 6341 | 6348 | 6356 | 6371 | 6394 |
| | 6401 | 6418 | 6439 | 6445 | 6463 | 6468 | 6501 | 6504 | 6521 | 6535 | 6547 | 6567 | 6570 | 6600 | 6613 |
| | 6627 | 6638 | 6645 | 6662 | 6668 | 6685 | 6691 | 6704 | 6711 | 6719 | 6729 | 6747 | 6758 | 6766 | 6780 |
| | 6787 | 6796 | 6805 | 6813 | 6822 | 6844 | 6874 | 6877 | 6894 | 6898 | 6905 | 6927 | 6939 | 6951 | 6960 |
| | 6965 | 6984 | 7002 | 7011 | 7017 | 7036 | 7042 | 7059 | 7066 | 7079 | 7086 | 7094 | 7104 | 7122 | 7133 |
| | 7141 | 7155 | 7162 | 7171 | 7180 | 7188 | 7197 | 7218 | 7224 | 7238 | 7246 | 7273 | 7287 | 7292 | 7299 |
| | 7317 | 7323 | 7340 | 7344 | 7351 | 7360 | 7377 | 7380 | 7385 | 7412 | 7425 | 7440 | 7447 | 7454 | 7459 |
| | 7463 | 7471 | 7478 | 7493 | 7501 | 7533 | 7566 | 7601 | | | | | | | |
| BGE | 5124 | 5149 | 5166 | | | | | | | | | | | | |
| BGT | 5125 | 5147 | 5183 | | | | | | | | | | | | |
| BHI | 5078 | 5111 | 5165 | 5184 | 5221 | | | | | | | | | | |
| BIC | 5795 | 5805 | 5811 | 6924 | 6936 | 6943 | 6949 | | | | | | | | |
| BICB | 5494 | 5504 | 6544 | 6555 | 6563 | 6573 | | | | | | | | | |
| BIS | 5775 | 5802 | 6871 | 6890 | 6903 | 6931 | | | | | | | | | |
| BISB | 5473 | 5501 | 6518 | 6551 | | | | | | | | | | | |
| BISB | 5797 | 5800 | 6926 | 6926 | 6929 | 6945 | 6957 | | | | | | | | |
| BISB | 5496 | 5499 | 6546 | 6546 | 6549 | 6557 | 7528 | | | | | | | | |
| BLE | 5109 | 5129 | 5144 | 5164 | 7371 | 7547 | 7553 | 7614 | | | | | | | |
| BLO | 5128 | 5146 | | | | | | | | | | | | | |
| BLOS | 5110 | 5127 | 5145 | 5205 | 7560 | 7595 | 7608 | | | | | | | | |
| BPT | 5108 | 5143 | 5163 | 5182 | | | | | | | | | | | |
| BPI | 5102 | 5107 | 5220 | 5483 | 5502 | 5784 | 5803 | 6527 | 6552 | 6932 | 7548 | 7554 | 7559 | 7565 | 7572 |
| | 7579 | 7587 | | | | | | | | | | | | | |
| BPE | 5096 | 5119 | 5131 | 5135 | 5155 | 5172 | 5178 | 5193 | 5197 | 5202 | 5210 | 5245 | 5251 | 5263 | 5269 |
| | 5293 | 5326 | 5356 | 5365 | 5383 | 5420 | 5436 | 5470 | 5570 | 5587 | 5608 | 5646 | 5772 | 5799 | 5879 |
| | 5895 | 5917 | 5955 | 6044 | 6066 | 6087 | 6114 | 6174 | 6216 | 6254 | 6267 | 6279 | 6287 | 6300 | 6312 |
| | 6322 | 6346 | 6369 | 6377 | 6399 | 6416 | 6426 | 6435 | 6451 | 6484 | 6512 | 6578 | 6655 | 6676 | 6696 |
| | 6738 | 6866 | 6914 | 7027 | 7050 | 7071 | 7113 | 7209 | 7229 | 7254 | 7278 | 7331 | 7365 | 7394 | 7432 |
| | 7498 | 7529 | 7573 | 7580 | 7588 | 7620 | | | | | | | | | |
| BPL | 5122 | 5141 | 5161 | 5179 | 5196 | 5201 | 5223 | 5251 | 5269 | 5365 | 5383 | 5480 | 5781 | 6524 | 7536 |
| | 7594 | 7600 | 7607 | 7613 | 7619 | | | | | | | | | | |
| BP | 5233 | 5235 | 5237 | 5239 | 5241 | 5299 | 5438 | 7521 | 7538 | | | | | | |
| BVC | 5113 | 5139 | 5162 | 5180 | 5198 | 5203 | 5251 | 5269 | 5365 | 5383 | 7567 | 7589 | 7596 | 7621 | |
| BVS | 5100 | 5105 | 5216 | 7561 | 7574 | 7581 | 7602 | 7609 | | | | | | | |
| CCC | 5098 | 5590 | 5611 | 5631 | 5649 | 5673 | 5705 | 5898 | 5920 | 5940 | 5958 | 5982 | 6014 | 6572 | 6680 |
| | 6790 | 6724 | 6743 | 6771 | 6801 | 7054 | 7075 | 7099 | 7118 | 7146 | 7176 | | | | |
| CLC | 5213 | 6592 | 7474 | | | | | | | | | | | | |
| CLM | 5219 | 6070 | 7234 | | | | | | | | | | | | |
| CLR | 5076 | 5428 | 5751 | 5981 | 6046 | 6177 | 6181 | 6183 | 6194 | 6196 | 6234 | 6273 | 6304 | 6350 | 6403 |

| | | | | | | | | | | | | | | | |
|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|
| | 6428 | 6441 | 6453 | 6850 | 6900 | 7145 | 7212 | 7289 | 7368 | 7399 | 7403 | 7405 | 7414 | 7418 | 7420 |
| | 7434 | 7473 | 7506 | | | | | | | | | | | | |
| CLRB | 5456 | 5672 | 6259 | 6294 | 6324 | 6379 | 6489 | 6770 | | | | | | | |
| CLV | 5215 | | | | | | | | | | | | | | |
| 61 | 5217 | 6078 | 6097 | 7242 | 7264 | | | | | | | | | | |
| 66 | 5077 | 5095 | 5118 | 5134 | 5154 | 5171 | 5192 | 5209 | 5229 | 5244 | 5252 | 5262 | 5270 | 5283 | 5292 |
| | 5301 | 5314 | 5325 | 5331 | 5355 | 5362 | 5366 | 5369 | 5384 | 5387 | 5395 | 5399 | 5409 | 5419 | 5435 |
| | 5451 | 5469 | 5487 | 5513 | 5542 | 5569 | 5586 | 5607 | 5626 | 5645 | 5668 | 5700 | 5746 | 5760 | 5771 |
| | 5777 | 5790 | 5783 | 5798 | 5821 | 5836 | 5851 | 5859 | 5865 | 5877 | 5883 | 5899 | 5894 | 5902 | 5908 |
| | 5916 | 5923 | 5930 | 5935 | 5944 | 5954 | 5961 | 5972 | 5977 | 5990 | 5997 | 6009 | 6017 | 6025 | 6034 |
| | 6043 | 6057 | 6065 | 6073 | 6081 | 6086 | 6105 | 6113 | 6119 | 6125 | 6143 | 6151 | 6163 | 6173 | 6201 |
| | 6215 | 6227 | 6244 | 6253 | 6278 | 6286 | 6311 | 6321 | 6329 | 6340 | 6355 | 6368 | 6376 | 6388 | 6390 |
| | 6292 | 6396 | 6409 | 6411 | 6413 | 6415 | 6425 | 6434 | 6438 | 6444 | 6450 | 6467 | 6483 | 6500 | 6503 |
| | 6511 | 6534 | 6556 | 6577 | 6626 | 6654 | 6675 | 6695 | 6718 | 6737 | 6765 | 6795 | 6843 | 6865 | 6873 |
| | 6876 | 6879 | 6881 | 6883 | 6893 | 6896 | 6902 | 6904 | 6913 | 6938 | 6950 | 6959 | 6964 | 6983 | 7001 |
| | 7010 | 7016 | 7026 | 7035 | 7041 | 7049 | 7058 | 7065 | 7070 | 7078 | 7085 | 7093 | 7103 | 7112 | 7121 |
| | 7132 | 7140 | 7154 | 7161 | 7170 | 7179 | 7187 | 7196 | 7208 | 7223 | 7228 | 7237 | 7245 | 7253 | 7272 |
| | 7277 | 7286 | 7291 | 7298 | 7322 | 7330 | 7339 | 7350 | 7364 | 7370 | 7379 | 7393 | 7410 | 7425 | 7431 |
| | 7439 | 7453 | 7461 | 7470 | 7477 | 7492 | | | | | | | | | |
| CMR | 5475 | 5479 | 5482 | 5528 | 5551 | 5557 | 5575 | 5581 | 5594 | 5600 | 5614 | 5621 | 5635 | 5652 | 5663 |
| | 5681 | 5688 | 5708 | 5716 | 5725 | 6221 | 6238 | 6266 | 6299 | 6345 | 6393 | 6398 | 6452 | 6520 | 6523 |
| | 6526 | 6595 | 6599 | 6612 | 6637 | 6644 | 6661 | 6667 | 6684 | 6690 | 6703 | 6710 | 6728 | 6746 | 6757 |
| | 6779 | 6786 | 6804 | 6812 | 6821 | 7457 | 7496 | | | | | | | | |
| | 5857 | 5863 | 5870 | 7008 | 7014 | 7022 | | | | | | | | | |
| | 5549 | 5555 | 5562 | 6635 | 6641 | 6650 | 7475 | | | | | | | | |
| | 5840 | 5842 | 5845 | 5847 | 6987 | 6989 | 6392 | 6994 | | | | | | | |
| | 5532 | 5534 | 5537 | 5539 | 6603 | 6605 | 6608 | 6610 | | | | | | | |
| | 4916 | 5090 | 5112 | 5130 | 5148 | 5167 | 5186 | 5206 | 5222 | 5231 | 5234 | 5236 | 5238 | 5240 | 5250 |
| | 5251 | 5254 | 5257 | 5268 | 5269 | 5272 | 5275 | 5282 | 5285 | 5289 | 5298 | 5300 | 5303 | 5306 | 5312 |
| | 5313 | 5316 | 5321 | 5330 | 5333 | 5336 | 5339 | 5344 | 5348 | 5352 | 5361 | 5364 | 5365 | 5368 | 5371 |
| | 5375 | 5382 | 5383 | 5386 | 5389 | 5392 | 5393 | 5397 | 5401 | 5407 | 5412 | 5415 | 5440 | 5453 | 5477 |
| | 5481 | 5484 | 5489 | 5498 | 5503 | 5515 | 5530 | 5545 | 5553 | 5559 | 5577 | 5583 | 5596 | 5602 | 5616 |
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| | 5782 | 5785 | 5799 | 5804 | 5815 | 5823 | 5838 | 5853 | 5861 | 5867 | 5885 | 5891 | 5904 | 5910 | 5925 |
| | 5932 | 5937 | 5946 | 5963 | 5974 | 5979 | 5992 | 5999 | 6011 | 6019 | 6027 | 6036 | 6053 | 6055 | 6075 |
| | 6083 | 6107 | 6121 | 6127 | 6135 | 6140 | 6145 | 6153 | 6158 | 6165 | 6188 | 6203 | 6223 | 6229 | 6240 |
| | 6246 | 6270 | 6282 | 6303 | 6315 | 6331 | 6342 | 6349 | 6357 | 6372 | 6395 | 6402 | 6419 | 6440 | 6446 |
| | 6464 | 6469 | 6502 | 6505 | 6522 | 6525 | 6528 | 6536 | 6548 | 6553 | 6568 | 6571 | 6601 | 6614 | 6628 |
| | 6639 | 6646 | 6663 | 6669 | 6686 | 6692 | 6705 | 6712 | 6720 | 6730 | 6748 | 6759 | 6767 | 6781 | 6798 |
| | 6797 | 6806 | 6814 | 6823 | 6845 | 6875 | 6878 | 6895 | 6899 | 6906 | 6928 | 6933 | 6940 | 6952 | 6961 |
| | 6966 | 6985 | 7003 | 7012 | 7018 | 7037 | 7043 | 7060 | 7067 | 7080 | 7087 | 7095 | 7105 | 7123 | 7134 |
| | 7142 | 7156 | 7163 | 7172 | 7181 | 7189 | 7198 | 7219 | 7225 | 7239 | 7247 | 7274 | 7299 | 7293 | 7300 |
| | 7318 | 7324 | 7341 | 7345 | 7352 | 7361 | 7372 | 7378 | 7381 | 7386 | 7413 | 7427 | 7441 | 7448 | 7455 |
| | 7460 | 7464 | 7472 | 7479 | 7494 | 7516 | 7550 | 7556 | 7562 | 7569 | 7576 | 7583 | 7591 | 7597 | 7604 |
| | 7610 | 7616 | 7623 | | | | | | | | | | | | |
| INC | 5097 | 5112 | 5120 | 5130 | 5136 | 5148 | 5156 | 5167 | 5173 | 5186 | 5194 | 5206 | 5211 | 5222 | 5231 |
| | 5232 | 5234 | 5236 | 5238 | 5240 | 5246 | 5250 | 5251 | 5254 | 5257 | 5264 | 5268 | 5269 | 5272 | 5275 |
| | 5282 | 5285 | 5289 | 5294 | 5298 | 5300 | 5303 | 5306 | 5312 | 5313 | 5316 | 5321 | 5327 | 5330 | 5333 |
| | 5336 | 5339 | 5344 | 5348 | 5352 | 5357 | 5361 | 5364 | 5365 | 5368 | 5371 | 5375 | 5382 | 5383 | 5386 |
| | 5389 | 5392 | 5393 | 5397 | 5401 | 5407 | 5412 | 5415 | 5421 | 5440 | 5453 | 5454 | 5471 | 5477 | 5481 |
| | 5484 | 5489 | 5490 | 5498 | 5503 | 5515 | 5516 | 5530 | 5545 | 5546 | 5553 | 5559 | 5571 | 5577 | 5583 |
| | 5588 | 5596 | 5602 | 5609 | 5616 | 5623 | 5628 | 5629 | 5637 | 5647 | 5654 | 5665 | 5670 | 5671 | 5683 |
| | 5690 | 5702 | 5703 | 5710 | 5718 | 5727 | 5748 | 5749 | 5762 | 5773 | 5779 | 5782 | 5785 | 5790 | 5799 |
| | 5804 | 5815 | 5823 | 5824 | 5827 | 5830 | 5832 | 5834 | 5838 | 5853 | 5854 | 5861 | 5867 | 5879 | 5885 |
| | 5891 | 5896 | 5904 | 5910 | 5918 | 5925 | 5932 | 5937 | 5938 | 5946 | 5956 | 5963 | 5974 | 5979 | 5980 |
| | 5992 | 5999 | 6011 | 6012 | 6019 | 6027 | 6036 | 6045 | 6053 | 6059 | 6067 | 6075 | 6083 | 6088 | 6107 |

MARK
MOV

| | | | | | | | | | | | | | | |
|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|
| 6115 | 6121 | 6127 | 6135 | 6140 | 6145 | 6146 | 6153 | 6158 | 6165 | 6175 | 6188 | 6203 | 6217 | 6223 |
| 6229 | 6240 | 6246 | 6255 | 6270 | 6282 | 6288 | 6303 | 6315 | 6323 | 6328 | 6331 | 6333 | 6334 | 6336 |
| 6337 | 6342 | 6349 | 6357 | 6372 | 6378 | 6395 | 6402 | 6419 | 6427 | 6440 | 6446 | 6452 | 6464 | 6469 |
| 6485 | 6502 | 6505 | 6513 | 6522 | 6525 | 6528 | 6536 | 6537 | 6548 | 6553 | 6568 | 6571 | 6579 | 6601 |
| 6614 | 6628 | 6629 | 6639 | 6646 | 6656 | 6663 | 6669 | 6677 | 6686 | 6692 | 6697 | 6705 | 6712 | 6720 |
| 6721 | 6730 | 6739 | 6748 | 6759 | 6767 | 6768 | 6781 | 6788 | 6797 | 6798 | 6806 | 6814 | 6823 | 6845 |
| 6846 | 6867 | 6875 | 6878 | 6895 | 6899 | 6906 | 6915 | 6928 | 6933 | 6940 | 6952 | 6961 | 6966 | 6967 |
| 6971 | 6977 | 6979 | 6981 | 6985 | 7003 | 7004 | 7012 | 7018 | 7028 | 7037 | 7043 | 7051 | 7060 | 7067 |
| 7072 | 7080 | 7087 | 7095 | 7096 | 7105 | 7114 | 7123 | 7134 | 7142 | 7143 | 7156 | 7163 | 7172 | 7173 |
| 7181 | 7189 | 7198 | 7210 | 7219 | 7225 | 7230 | 7239 | 7247 | 7255 | 7274 | 7273 | 7288 | 7293 | 7300 |
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| 5965 | 5967 | 5971 | 5984 | 5989 | 5996 | 6003 | 6016 | 6024 | 6030 | 6033 | 6040 | 6050 | 6056 | 6072 |
| 6080 | 6093 | 6099 | 6104 | 6118 | 6124 | 6130 | 6150 | 6162 | 6168 | 6182 | 6189 | 6195 | 6200 | 6490 |
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| .E.EY .IF | 4898 5269 5624 6007 6509 6999 7429 | 4908 5290 5643 6041 6532 7024 | 4918 5323 5666 6063 6575 7047 | 5031 5353 5698 6084 6624 7068 | 5093 5365 5744 6111 6652 7091 | 5116 5383 5769 6141 6673 7110 | 5132 5417 5786 6171 6693 7138 | 5152 5449 5819 6213 6716 7168 | 5169 5467 5849 6251 6735 7206 | 5190 5485 5875 6284 6763 7226 | 5207 5511 5892 6319 6793 7251 | 5227 5541 5914 6374 6841 7275 | 5242 5567 5933 6423 6863 7328 | 5251 5584 5952 6448 6911 7362 | 5260 5605 5975 6481 6962 7390 |
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| .MACRO | 39 1097 1947 | 81 1110 2140 | 168 1131 2228 | 308 1144 2353 | 485 1177 2450 | 515 1226 2528 | 586 1272 2613 | 747 1309 2827 | 801 1356 2919 | 895 1389 2995 | 926 1419 3095 | 974 1475 3223 | 986 1483 3286 | 1030 1535 3348 | 1064 1739 3466 |

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DVKAAA MACY11 27(732) 25-AUG-76 13:25 PAGE 57-6
DVKAAA.P11 CROSS REFERENCE TABLE -- PERMANENT SYMBOLS

*** SEQ 0115

*.DVKAAA DS:ERFZ.CRF=DVKAAA.SML,DVKAAA.P11
RUN-TIME: 37 49 8 SECONDS
RUN-TIME RATIO: 1533/95=16.0
CORE USED: 30K (.59 PAGES)

