

AR11

WRAPAROUND TEST 2
MD-11-DZARC-B

EP-DZARC-B-DL-A
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NOV 1976
digital
MADE IN USA



.REM :

IDENTIFICATION

PRODUCT CODE: MAINDEC-11-DZARC-B
PRODUCT NAME: ARI1 DIAGNOSTIC TEST III
 (WRAPAROUND TEST)
DATE: MAY 21, 1976
MAINTAINER: DIAGNOSTIC GROUP

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1. ABSTRACT

THE WRAPAROUND DIAGNOSTIC ENABLES THE ARI1 TO TEST ITSELF, IN CONJUNCTION WITH A G5036 WRAPAROUND MODULE, WHICH IS CONNECTED TO THE ARI1 THROUGH A BC08-R CABLE. THE TWO D/A CONVERTERS ARE COMBINED IN VARIOUS MANNERS USING RESISTIVE DIVIDERS ON THE G5036 MODULE AND SENT BACK INTO THE A/D INPUTS. CONSEQUENTLY, BOTH X AND Y D/A'S ARE TESTED BY THE A/D CONVERTER, AND THE A/D IS TESTED BY THE D/A CONVERTERS. BECAUSE THE D/A'S ARE DIVIDED DOWN BEFORE GOING INTO THE A/D, TEST RESOLUTION FAR BETTER THAN 1 LSB IS ATTAINED. IN ADDITION, THE ANALOG POWER SUPPLY LEVELS (+/- 14 VOLTS) ARE DIVIDED DOWN AND USED AS INPUTS, ALLOWING TEST OF THE DC-TO-DC CONVERTER; THE FOUR SCOPE CONTROL LOGIC OUTPUTS (ERASE L, NON-STORE L, CH02 L, AND WRITE-THRU L) ARE USED AS INPUTS, ALLOWING PARAMETRIC TESTS OF THEIR HIGH AND LOW OUTPUT LEVELS; THE CH02 L OUTPUT IS FED BACK INTO THE ERASE RET INPUT, ALLOWING TEST OF THE STORAGE SCOPE ERASE RETURN HANDSHAKING LOGIC; AND THE SCOPE INTENSIFY OUTPUT IS FED BACK INTO THE A/D EXTERNAL START INPUT, ALLOWING TESTS OF BOTH FUNCTIONS. SINCE ALL A/D INPUT CHANNELS ARE USED (AT DIFFERENT INPUT VOLTAGE LEVELS), THE WRAPAROUND MODULE ALSO PROVIDES A COMPLETE FUNCTIONAL CHECK ON THE A/D INPUT MULTIPLEXER.

2. REQUIREMENTS

2.1 EQUIPMENT

PDP-11 FAMILY COMPUTER WITH 9K WORDS OF MEMORY
ARI1 HEX OPTION MODULE INSTALLED
G5036 WRAPAROUND MODULE AND BC08-R CABLE
TELETYPE

2.2 STORAGE

THIS PROGRAM USES LESS THAN 9K OF MEMORY.

3. LOADING PROCEDURE

PROCEDURE FOR NORMAL BINARY TAPES SHOULD BE FOLLOWED.

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4. STARTING PROCEDURE

4.1 CONTROL SWITCH SETTINGS

THIS PROGRAM HAS BEEN MODIFIED TO RUN WITH OR WITHOUT A HARDWARE SWITCH REGISTER.

STANDARD PDP-11 FORMAT

SW 15 = 1	HALT ON ERROR
SW 14 = 1	LOOP ON TEST
SW 13 = 1	INHIBIT ERROR TYPEOUTS
SW 12 = 1	FORCED PRINTOUT ENABLE
SW 11 = 1	INHIBIT ITERATIONS
SW 10 = 1	BELL ON ERROR
SW 09 = 1	LOOP ON ERROR
SW 08 = 1	LOOP ON TEST IN SWR (7:0)

REFER TO 9. FOR SOFTWARE SWITCH REGISTER CONTROL

4.2 STARTING ADDRESS OR ADDRESSES

200 IS THE STARTING ADDRESS OF THE WRAPAROUND TEST.
204 IS THE RESTART ADDRESS OF THE WRAPAROUND TEST.
210 IS THE STARTING ADDRESS FOR THE OPTION TEST AREA.

5. OPERATING PROCEDURE

THE OPERATOR MUST INSERT THE CORRECT INFORMATION IN THE SWITCH REGISTER WHEN REQUESTED BY THE PROGRAM OR AN ERROR WILL OCCUR. ONCE STARTED THE TEST WILL RUN IN ITS NORMAL MANNER WITHOUT OPERATOR INTERVENTION OR SWITCH SELECTION.

6. ERRORS

THIS PROGRAM USES THE DIAGNOSTIC 'SYSMAC' PACKAGE FOR ERROR REPORTING AND TYPEOUT. REFER TO THE "ERROR POINTER TABLE" FOR TYPE AND DESCRIPTION OF ERRORS.

7. RESTRICTIONS

SWITCH ON G5036 MUST BE IN THE 'U' POSITION.

THE WRAPAROUND (G5036) MODULE MUST BE CONNECTED AS FOLLOWS:
ARI1 TO BC09R CONNECTION A-A, VV-VV
BC09R TO G5036 CONNECTION "UPSIDE-DOWN" A-VV, VV-A

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%
.TITLE MAINDEC-11-DZARC-B
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.*MAYNARD, MASS. 01754
.*
.*PROGRAM BY RAYMOND SHOOP
.*
.*THIS PROGRAM WAS ASSEMBLED USING THE PCP-11 MAINDEC SYSMAC
.*PACKAGE (MAINDEC-11-DZGAC-B2), NOV 21, 1975.
.*

```

.SBTTL BASIC DEFINITIONS

```

.C01100 .*INITIAL ADDRESS OF THE STACK POINTER *** 1100 ***
STACK= 1100
.EQUIV EMT.ERROR ::BASIC DEFINITION OF ERROR CALL
.EQUIV IOT.SCOPE ::BASIC DEFINITION OF SCOPE CALL

```

.*MISCELLANEOUS DEFINITIONS

```

000011 HT= 11 ::CODE FOR HORIZONTAL TAB
000012 LF= 12 ::CODE FOR LINE FEED
000015 CR= 15 ::CODE FOR CARRIAGE RETURN
000200 CRLF= 200 ::CODE FOR CARRIAGE RETURN-LINE FEED
177776 PS= 177776 ::PROCESSOR STATUS WORD
.EQUIV PS,PSW
177774 STKLMT= 177774 ::STACK LIMIT REGISTER
177772 PIRG= 177772 ::PROGRAM INTERRUPT REQUEST REGISTER
177570 DSWR= 177570 ::HARDWARE SWITCH REGISTER
177570 DDISP= 177570 ::HARDWARE DISPLAY REGISTER

```

.*GENERAL PURPOSE REGISTER DEFINITIONS

```

000000 R0= %0 ::GENERAL REGISTER
000001 R1= %1 ::GENERAL REGISTER
000002 R2= %2 ::GENERAL REGISTER
000003 R3= %3 ::GENERAL REGISTER
000004 R4= %4 ::GENERAL REGISTER
000005 R5= %5 ::GENERAL REGISTER
000006 R6= %6 ::GENERAL REGISTER
000007 R7= %7 ::GENERAL REGISTER
.EQUIV R6,SP ::STACK POINTER
.EQUIV R7,PC ::PROGRAM COUNTER

```

.*PRIORITY LEVEL DEFINITIONS

```

000000 PR0= 0 ::PRIORITY LEVEL 0
000040 PR1= 40 ::PRIORITY LEVEL 1
000100 PR2= 100 ::PRIORITY LEVEL 2
000140 PR3= 140 ::PRIORITY LEVEL 3
000200 PR4= 200 ::PRIORITY LEVEL 4
000240 PR5= 240 ::PRIORITY LEVEL 5
000300 PR6= 300 ::PRIORITY LEVEL 6
000340 PR7= 340 ::PRIORITY LEVEL 7

```

.*"SWITCH REGISTER" SWITCH DEFINITIONS

```

100000 SW15= 100000
040000 SW14= 40000

```

000001
000002
000004
000010
000020
000040
000100
000200
000400
001000
002000
004000
010000
020000
040000
100000

000001
000002
000004
000010
000020
000040
000100
000200
000400
001000
002000
004000
010000
020000
040000
100000

000010
000004
000002
000001

SW13= 20000
SW12= 10000
SW11= 4000
SW10= 2000
SW09= 1000
SW08= 400
SW07= 200
SW06= 100
SW05= 40
SW04= 20
SW03= 10
SW02= 4
SW01= 2
SW00= 1
.EQUIV SW09,SW9
.EQUIV SW08,SW8
.EQUIV SW07,SW7
.EQUIV SW06,SW6
.EQUIV SW05,SW5
.EQUIV SW04,SW4
.EQUIV SW03,SW3
.EQUIV SW02,SW2
.EQUIV SW01,SW1
.EQUIV SW00,SW0

.*DATA BIT DEFINITIONS (BIT00 TO BIT15)

BIT15= 100000
BIT14= 40000
BIT13= 20000
BIT12= 10000
BIT11= 4000
BIT10= 2000
BIT09= 1000
BIT08= 400
BIT07= 200
BIT06= 100
BIT05= 40
BIT04= 20
BIT03= 10
BIT02= 4
BIT01= 2
BIT00= 1
.EQUIV BIT09,BIT9
.EQUIV BIT08,BIT8
.EQUIV BIT07,BIT7
.EQUIV BIT06,BIT6
.EQUIV BIT05,BIT5
.EQUIV BIT04,BIT4
.EQUIV BIT03,BIT3
.EQUIV BIT02,BIT2
.EQUIV BIT01,BIT1
.EQUIV BIT00,BIT0

.*BASIC "CPU" TRAP VECTOR ADDRESSES

ERRVEC= 4 ;:TIME OUT AND OTHER ERRORS
RESVEC= 10 ;:RESERVED AND ILLEGAL INSTRUCTIONS

001 000014
002 000014
003 000014
004 000020
005 000024
006 000030
007 000034
008 000060
009 000064
010 000240
011 170400
012 000340
013 000200

TBITVEC=14
TRTVEC= 14
BPTVEC= 14
IOTVEC= 20
PWRVEC= 24
EMTVEC= 30
TRAPVEC=34
TKVEC= 60
TPVEC= 64
PIRQVEC=240
ABASE=170400
AVECT1=340
APRIOR=200

:: "T" BIT
:: TRACE TRAP
:: BREAKPOINT TRAP (BPT)
:: INPUT/OUTPUT TRAP (IOT) **SCOPE**
:: POWER FAIL
:: EMULATOR TRAP (EMT) **ERROR**
:: "TRAP" TRAP
:: TTY KEYBOARD VECTOR
:: TTY PRINTER VECTOR
:: PROGRAM INTERRUPT REQUEST VECTOR

021
022
023
024
025
026
027
028
029
030
031
032
033
034
035
036
037
038
039
040
041
042

.SBTTL OPERATIONAL SWITCH SETTINGS

```

:*
:* SWITCH USE
:* -----
:* 15 HALT ON ERROR
:* 14 LOOP ON TEST
:* 13 INHIBIT ERROR TYPEOUTS
:* 12 FORCED PRINTOUT
:* 11 INHIBIT ITERATIONS
:* 10 BELL ON ERROR
:* 9 LOOP ON ERROR
:* 8 LOOP ON TEST IN SWR<7:0>

```

.SBTTL TRAP CATCHER

000000

```

.=0
;*ALL UNUSED LOCATIONS FROM 4 - 776 CONTAIN A ".+2,HALT"
;*SEQUENCE TO CATCH ILLEGAL TRAPS AND INTERRUPTS
;*LOCATION 0 CONTAINS 0 TO CATCH IMPROPERLY LOADED VECTORS

```

```

000174
000176
000000
000000

```

```

.=174
DISPREG: .WORD 0 ;;SOFTWARE DISPLAY REGISTER
SWREG: .WORD 0 ;;SOFTWARE SWITCH REGISTER

```

.SBTTL STARTING ADDRESS(ES)

```

000200 000137 001516
000204 000137 001536
000210 000137 001530

```

```

JMP @*BEGIN ;;JUMP TO STARTING ADDRESS OF PROGRAM
JMP BEGIN1 ;RESTART ADDRESS
JMP BEGIN2 ;STARTING ADDRESS OF OPTION TEST AREA

```

343
 344
 345
 346
 347
 348
 349
 350
 351 000046
 352
 353 000052
 354
 355
 356
 357
 358
 359
 360
 361
 362
 363
 364 000024
 365
 366 000044
 367
 368
 369
 370
 371
 372 001000
 373 001000 000000
 374 001002 001200
 375 001004 000020
 376 001006 000030
 377 001010 000120
 378 001012 000052

.SBTTL ACT11 HOOKS

```

:*****
:HOOKS REQUIRED BY ACT11
      $SVPC=.          ;SAVE PC
      . =46
      $ENDAD          ;;1)SET LOC.46 TO ADDRESS OF $ENDAD IN .SECP
      . =52
      .WORD 0          ;;2)SET LOC.52 TO ZERO
      .=$SVPC         ;; RESTORE PC
      . =1000
  
```

.SBTTL APT PARAMETER BLOCK

```

:*****
:SET LOCATIONS 24 AND 44 AS REQUIRED FOR APT
:*****
      . $X=.          ;;SAVE CURRENT LOCATION
      . =24          ;;SET POWER FAIL TO POINT TO START OF PROGRAM
      200           ;;FOR APT START UP
      . =44          ;;POINT TO APT INDIRECT ADDRESS PNTR.
      $APTHDR       ;;POINT TO APT HEADER BLOCK
      . = $X         ;;RESET LOCATION COUNTER
:*****
:SETUP APT PARAMETER BLOCK AS DEFINED IN THE APT-PDP11 DIAGNOSTIC
:INTERFACE SPEC.
  
```

```

$APTHD:
$HIBTS: .WORD 0      ;;TWO HIGH BITS OF 18 BIT MAILBOX ADDR.
$MADR: .WORD $MAIL  ;;ADDRESS OF APT MAILBOX (BITS 0-15)
$STMT: .WORD 20     ;;RUN TIM OF LONGEST TEST
$PASTM: .WORD 30    ;;RUN TIME IN SECS. OF 1ST PASS ON 1 UNIT (QUICK VERIFY)
$UNITM: .WORD 120   ;;ADDITIONAL RUN TIME (SECS) OF A PASS FOR EACH ADDITIONAL UNIT
          .WORD SETEND-$MAIL/2 ;;LENGTH MAILBOX-ETABLE(WORDS)
  
```

379
380
381
382
383
384
385
386 001100
387 001100 000000
388 001100 000
389 001102 000
390 001103 000
391 001104 000000
392 001105 000000
393 001110 000000
394 001112 000000
395 001114 000
396 001115 001
397 001116 000000
398 001120 000000
399 001122 000000
400 001124 000000
401 001126 000000
402 001130 000000
403 001132 000000
404 001134 000000
405 001136 177570
406 001140 177570
407 001142 177560
408 001144 177562
409 001146 177564
410 001150 177566
411 001152 000
412 001153 002
413 001154 012
414 001155 000
415 001156 000000
416
417 001160 000000
418 001162 000000
419 001164 000000
420 001166 000000
421 001170 177607 000377
422 001174 077
423 001175 015
424 001176 000012

.SBTTL COMMON TAGS

::*****
: THIS TABLE CONTAINS VARIOUS COMMON STORAGE LOCATIONS
: USED IN THE PROGRAM.

SCMTAG: . =1100 ;: START OF COMMON TAGS
 .WORD 0 ;: CONTAINS THE TEST NUMBER
 \$STNM: .BYTE 00 ;: CONTAINS ERROR FLAG
 \$ERFLG: .BYTE 00 ;: CONTAINS SUBTEST ITERATION COUNT
 \$ICNT: .WORD 00 ;: CONTAINS SCOPE LOOP ADDRESS
 \$LPADR: .WORD 00 ;: CONTAINS SCOPE RETURN FOR ERRORS
 \$LPERR: .WORD 00 ;: CONTAINS TOTAL ERRORS DETECTED
 \$ERTTL: .WORD 00 ;: CONTAINS ITEM CONTROL BYTE
 \$ITEMB: .BYTE 00 ;: CONTAINS MAX. ERRORS PER TEST
 \$ERMAX: .BYTE 01 ;: CONTAINS PC OF LAST ERROR INSTRUCTION
 \$ERRPC: .WORD 00 ;: CONTAINS ADDRESS OF 'GOOD' DATA
 \$GDADR: .WORD 00 ;: CONTAINS ADDRESS OF 'BAD' DATA
 \$BDADR: .WORD 00 ;: CONTAINS 'GOOD' DATA
 \$GDADR: .WORD 00 ;: CONTAINS 'BAD' DATA
 \$BDDAT: .WORD 00 ;: RESERVED--NOT TO BE USED
 .WORD 00
 .WORD 00
 .WORD 00
 SWR: .WORD DSWR ;: ADDRESS OF SWITCH REGISTER
 DISPLAY: .WORD DDISP ;: ADDRESS OF DISPLAY REGISTER
 \$TKS: 177560 ;: TTY KBD STATUS
 \$TKB: 177562 ;: TTY KBD BUFFER
 \$TPS: 177564 ;: TTY PRINTER STATUS REG. ADDRESS
 \$TPB: 177566 ;: TTY PRINTER BUFFER REG. ADDRESS
 \$NULL: .BYTE 0 ;: CONTAINS NULL CHARACTER FOR FILLS
 \$FILLS: .BYTE 2 ;: CONTAINS * OF FILLER CHARACTERS REQUIRED
 \$FILLC: .BYTE 12 ;: INSERT FILL CHARS. AFTER A "LINE FEED"
 \$TPFLG: .BYTE 0 ;: "TERMINAL AVAILABLE" FLAG (BIT<07>=0=YES)
 \$REGAD: .WORD 0 ;: CONTAINS THE ADDRESS FROM WHICH (\$REGO) WAS OBTAINED
 \$REGO: .WORD 0 ;: CONTAINS ((\$REGAD)+0)
 \$REG1: .WORD 0 ;: CONTAINS ((\$REGAD)+2)
 \$TIMES: 0 ;: MAX. NUMBER OF ITERATIONS
 \$ESCAPE: 0 ;: ESCAPE ON ERROR ADDRESS
 \$BELL: .ASCIZ <207><377><377> ;: CODE FOR BELL
 \$QUES: .ASCIZ /?/ ;: QUESTION MARK
 \$CRLF: .ASCIZ <15> ;: CARRIAGE RETURN
 \$LF: .ASCIZ <12> ;: LINE FEED

425
426
427
428
429
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431 001200
432 001200 000000
433 001202 000000
434 001204 000000
435 001206 000000
436 001210 000000
437 001212 000000
438 001214 000000
439 001216 000000
440 001220
441 001220 000
442 001221 000
443 001222 000000
444 001224 000000
445 001226 000000
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452 001230 000
453 001231 000
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458 001232 000000
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461 001235 000
462 001236 000000
463 001240 000
464 001241 000
465 001242 000000
466 001244 000
467 001245 000
468 001246 000000
469 001250 340
470 001251 000
471 001252 200
472 001253 000
473
474 001254 170400
475 001256 000000
476 001260 000000
477 001262 000000
478 001264 000000
479 001266 000000
480 001270 000000

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:;*****
.SBTTL  APT MAILBOX-ETABLE
:;*****
.EVEN
$MAIL:
$MSGTY: .WORD  AMSGTY  ;; APT MAILBOX
$FATAL: .WORD  AFATAL  ;; MESSAGE TYPE CODE
$TESTN: .WORD  ATESTN  ;; FATAL ERROR NUMBER
$PASS:  .WORD  APASS   ;; TEST NUMBER
$DEVCT: .WORD  ADEVCT  ;; PASS COUNT
$UNIT:  .WORD  AUNIT   ;; DEVICE COUNT
$MSGAD: .WORD  AMSGAD  ;; I/O UNIT NUMBER
$MSGLG: .WORD  AMSGLG  ;; MESSAGE ADDRESS
$ETABLE: ;; MESSAGE LENGTH
$ENV:   .BYTE  AENV    ;; APT ENVIRONMENT TABLE
$ENVM:  .BYTE  AENVM   ;; ENVIRONMENT BYTE
$SWREG: .WORD  ASWREG  ;; ENVIRONMENT MODE BITS
$USWR:  .WORD  AUSWR   ;; APT SWITCH REGISTER
$CPUOP: .WORD  ACPUOP  ;; USER SWITCHES
:; CPU TYPE, OPTIONS
:; BITS 15-11=CPU TYPE
:; 11/04=01, 11/05=02, 11/20=03, 11/40=04, 11/45=05
:; 11/70=06, PDQ=07, Q=10
:; BIT 10=REAL TIME CLOCK
:; BIT 9=FLOATING POINT PROCESSOR
:; BIT 8=MEMORY MANAGEMENT
$MAMS1: .BYTE  AMAMS1  ;; HIGH ADDRESS, M.S. BYTE
$MTYP1: .BYTE  AMTYP1  ;; MEM. TYPE, BLK#1
:; MEM. TYPE BYTE -- (HIGH BYTE)
:; 900 NSEC CORE=001
:; 300 NSEC BIPOLAR=002
:; 500 NSEC MOS=003
$MADR1: .WORD  AMADR1  ;; HIGH ADDRESS, BLK#1
:; MEM. LAST ADDR.=3 BYTES, THIS WORD AND LOW OF "TYPE" ABOVE
$MAMS2: .BYTE  AMAMS2  ;; HIGH ADDRESS, M.S. BYTE
$MTYP2: .BYTE  AMTYP2  ;; MEM. TYPE, BLK#2
$MADR2: .WORD  AMADR2  ;; MEM. LAST ADDRESS, BLK#2
$MAMS3: .BYTE  AMAMS3  ;; HIGH ADDRESS, M.S. BYTE
$MTYP3: .BYTE  AMTYP3  ;; MEM. TYPE, BLK#3
$MADR3: .WORD  AMADR3  ;; MEM. LAST ADDRESS, BLK#3
$MAMS4: .BYTE  AMAMS4  ;; HIGH ADDRESS, M.S. BYTE
$MTYP4: .BYTE  AMTYP4  ;; MEM. TYPE, BLK#4
$MADR4: .WORD  AMADR4  ;; MEM. LAST ADDRESS, BLK#4
$VECT1: .BYTE  AVECT1  ;; INTERRUPT VECTOR#1
$VECT2: .BYTE  AVECT2  ;; INTERRUPT VECTOR#2
$PRIOR: .BYTE  APRIOR  ;; BUS PRIORITY #1, #2
:; .BYTE  0 ;; SPARE, NOT USED
:; .EVEN
$BASE:  .WORD  ABASE   ;; BASE ADDRESS OF EQUIPMENT UNDER TEST
$DEVM:  .WORD  ADF     ;; DEVICE MAP
$CDW1:  .WORD  ACDW1  ;; CONTROLLER DESCRIPTION WORD#1
$CDW2:  .WORD  ACDW2  ;; CONTROLLER DESCRIPTION WORD#2
$DDW0:  .WORD  ADDW0  ;; DEVICE DESCRIPTOR WORD#0
$DDW1:  .WORD  ADDW1  ;; DEVICE DESCRIPTOR WORD#1
$DDW2:  .WORD  ADDW2  ;; DEVICE DESCRIPTOR WORD#2

```


481 001272 000000
482 001274 000000
483 001276 000000
484 001300 000000
485 001302 000000
486 001304 000000
487 001306 000000
488 001310 000000
489 001312 000000
490 001314 000000
491 001316 000000
492 001320 000000
493 001322 000000

\$DDW3: .WORD ADDW3 ;;DEVICE DESCRIPTOR WORD#3
\$DDW4: .WORD ADDW4 ;;DEVICE DESCRIPTOR WORD#4
\$DDW5: .WORD ADDW5 ;;DEVICE DESCRIPTOR WORD#5
\$DDW6: .WORD ADDW6 ;;DEVICE DESCRIPTOR WORD#6
\$DDW7: .WORD ADDW7 ;;DEVICE DESCRIPTOR WORD#7
\$DDW8: .WORD ADDW8 ;;DEVICE DESCRIPTOR WORD#8
\$DDW9: .WORD ADDW9 ;;DEVICE DESCRIPTOR WORD#9
\$DDW10: .WORD ADDW10 ;;DEVICE DESCRIPTOR WORD#10
\$DDW11: .WORD ADDW11 ;;DEVICE DESCRIPTOR WORD#11
\$DDW12: .WORD ADDW12 ;;DEVICE DESCRIPTOR WORD#12
\$DDW13: .WORD ADDW13 ;;DEVICE DESCRIPTOR WORD#13
\$DDW14: .WORD ADDW14 ;;DEVICE DESCRIPTOR WORD#14
\$DDW15: .WORD ADDW15 ;;DEVICE DESCRIPTOR WORD#15

494 001324

SETEND:

.SBTTL ERROR POINTER TABLE

;*THIS TABLE CONTAINS THE INFORMATION FOR EACH ERROR THAT CAN OCCUR.
;*THE INFORMATION IS OBTAINED BY USING THE INDEX NUMBER FOUND IN
;*LOCATION \$ITEMB. THIS NUMBER INDICATES WHICH ITEM IN THE TABLE IS PERTINENT.
;*NOTE1: IF \$ITEMB IS 0 THE ONLY PERTINENT DATA IS (\$ERRPC).
;*NOTE2: EACH ITEM IN THE TABLE CONTAINS 4 POINTERS EXPLAINED AS FOLLOWS:

;* EM ;;POINTS TO THE ERROR MESSAGE
;* DH ;;POINTS TO THE DATA HEADER
;* DT ;;POINTS TO THE DATA
;* DF ;;POINTS TO THE DATA FORMAT

495 001324

\$ERRTB:

496 001324 011736
497 001326 012451
498 001330 014572
499 001332 000000

; ITEM 1
EM1 ;ERROR ON A/D CHANNEL
DH1 ;ERRPC ARADD CHANNEL NOMINAL TOLERANCE ACTUAL
DT1 ;SERRPC ARBADD CHANL \$GDDAT SPREAD \$BDDAT
0

500 001334 011763
501 001336 012530
502 001340 014610
503 001342 000000

; ITEM 2
EM2 ;VC LOGIC SIGNAL HIGH OUTPUT TOO LOW
DH2 ;ERRPC ARADD CHANNEL 3V LEV. OUTPUT
DT2 ;SERRPC ARBADD CHANL \$GDDAT \$BDDAT
0

504 001344 012027
505 001346 012577
506 001350 014624
507 001352 000000

; ITEM 3
EM3 ;VC STATUS REGISTER IN ERROR
DH3 ;ERRPC ARADD GOOD BAD
DT3 ;SERRPC ARBADD \$GDDAT \$BDDAT
0

508 001354 012063
509 001356 012577
510 001360 014624

; ITEM 4
EM4 ;EXTERNAL AD START FAILED
DH3 ;ERRPC ARADD GOOD BAD
DT3 ;SERRPC ARBADD \$GDDAT \$BDDAT

537	001362	000000
538		
539		
540	001364	012120
541	001366	012534
542	001370	014636
543	001372	000000
544		
545		
546	001374	012161
547	001376	012674
548	001400	014624
549	001402	000000

:ITEM

0
S
EMS
DHS
DTS
0

:AND DIFFERENTIAL LINEARITY ERROR
:ERRPC ARADD # OF STATES ALLOWED
:SERRPC ARBADD \$BDDAT \$GDDAT

:ITEM

6
JMS
JMS
JMS
0

:NOISE LEVEL EXCEEDED LIMIT
:ERRPC ARADD LIMIT MEASURED
:SERRPC ARBADD \$GDDAT \$BDDAT

ADDRESS	DATA	DESCRIPTION
0001404	012214	:ITEM 7
0001406	012236	EM7
0001410	014610	DH7
0001412	000000	DT2
		0
0001414	012260	:ITEM 10
0001416	013004	EM10
0001420	014650	DH10
0001422	000000	DT10
		0
0001424	012221	:ITEM 11
0001426	013063	EM11
0001430	014666	DH11
0001432	000000	DT11
		0
0001434	012262	:ITEM 12
0001436	013063	EM12
0001440	014688	DH11
0001442	000000	DT11
		0
0001444	012277	:ITEM 13
0001446	013063	EM13
0001450	014688	DH11
0001452	000000	DT11
		0
0001454	012242	:ITEM 14
0001456	013063	EM14
0001460	014688	DH11
0001462	000000	DT11
		0
0001464	170400	ARBADD: 170400
0001466	000040	ARBYCT: 340
0001470	000000	NMBEXT: 0
0001472	000000	NBEXT: 0
0001474	170400	ADCS: 170400
0001476	170401	ADCS1: 170401
0001500	170402	ADDBP: 170402
0001502	170404	CSR: 170404
0001504	170406	CSB: 170406
0001506	170410	VCSTAT: 170410
0001508	170412	VCXREG: 170412
0001510	170414	VCYREG: 170414
0001512	170416	VSC: 170416
0001514	.SBTTL	.SBTTL
		PROGRAM START-JP
		:VC LOGIC SIGNAL LOW OUTPUT TOO HIGH
		:ERRPC ARADD A/D CHAN +.4V LEV. OUTP.T
		:SERRPC ARBADD CHANL \$GDDAT \$BCDAT
		:D/A DIFFERENTIAL LINEARITY ERROR
		:ERRPC ARADD STEP NOMINAL SPREAD ACTUAL
		:SERRPC ARBADD EDGE \$GDDAT SPREAD \$BCDAT
		:A/D INTER-CHANNEL SETTLING ERROR
		:ERRPC ARADD NOMINAL SPREAD ACTUAL
		:SERRPC ARBADD \$GDDAT SPREAD \$BCDAT
		:OFFSET ERROR
		:ERRPC ARADD NOMINAL SPREAD ACTUAL
		:SERRPC ARBADD \$GDDAT SPREAD \$BCDAT
		:CALIBRATION ERROR
		:ERRPC ARADD NOMINAL SPREAD ACTUAL
		:SERRPC ARBADD \$GDDAT SPREAD \$BCDAT
		:LINEARITY ERROR AT XX00
		:ERRPC ARADD NOMINAL SPREAD ACTUAL
		:SERRPC ARBADD \$GDDAT SPREAD \$BCDAT
		:A TO D STATUS/CONTROL REGISTER
		:A TO D STATUS REGISTER <HIGH BYTE
		:A TO D CONVERTED VALUE <READ
		:CLOCK STATUS REGISTER
		:CLOCK PRESET BUFFER
		:DAC STATUS REGISTER
		:X BUFFER
		:Y BUFFER
		:CLOCK COUNTER

```

001516 005000 BEGIN: CLR R0 ;CLEAR R0
001520 005037 016762 CLR WFTST ;CLEAR TOLERANCE FLAG
001524 000406 BR RBEG
001528 000406 BR RBEG
001532 012737 000001 016762 BEGIN2: MOV #1,WFTST ;SET TOLERANCE FLAG
001536 012700 177777 BEGIN1: MOV #-1,R0 ;LOAD R0
001540 000005 RBEG: RESET
001544 005037 177776 CLR PS
001550 012706 001100 MOV #STACK,SP ;LOAD STACK
001554 012737 001602 000004 MOV #15,R2 ;LOAD BUS ERROR
001558 012702 001254 MOV #BASE,R2 ;LOAD STARTING ADDRESS
001562 005003 CLR R3 ;CLEAR COUNT
001566 005712 25: TST (R2) ;TEST IF EXISTENT
001570 002702 000020 ADD #20,R2 ;EXIST, UPDATE TEST ADDRESS
001574 005203 INC R3 ;UPDATE # OF ARI1'S
001578 000773 BR 25
001582 022626 15: CMP (SP)+,(SP)+ ;POP STACK
001586 005703 TST R3 ;TEST IF FIRST DOES EXIST
001590 001002 BNE HALT ;BR
001594 000000 HALT ;FIRST ARI1 DOES NOT EXIST
001598 000741 BR BEGIN ;CHECK THE PROGRAM DEVICE ADDRESS
001602 005303 35: DEC R3 ;ADJUST R3
001606 010337 001470 MOV R3,NMBEXT ;SAVE THE NUMBER OF ADDITIONAL ARI1'S
001610 012737 000006 000004 MOV #6,R4 ;RESET BUS ERROR
001614 005037 000006 CLR R4
001618 013737 001254 001464 MOV #BASE,ARBADD ;LOAD FIRST ADDRESS
001622 013737 001250 001466 MOV #VECT1,ARBVCT ;LOAD FIRST VECTOR
001626 013737 001470 001472 MOV NMBEXT,NBEXT ;LOAD NUMBER OF ADDITIONAL ARI1'S
001630 000005 RESET
001634 012706 001100 :: CLEAR THE COMMON TAGS (%CMTAG) AREA
001638 005026 MOV #CMTAG,R6 ;:FIRST LOCATION TO BE CLEARED
001642 022706 001126 CLR (R6)+ ;:CLEAR MEMORY LOCATION
001646 001374 BNE -6 ;:DONE?
001650 012706 001100 MOV #STACK,SP ;:SETUP THE STACK POINTER
001654 012737 017172 000020 :: INITIALIZE A FEW VECTORS
001658 012737 000340 000022 MOV #SCOPE,%IOTVEC ;:IOT VECTOR FOR SCOPE ROUTINE
001662 012737 017452 000030 MOV #340,%IOTVEC+2 ;:LEVEL 7
001666 012737 000340 000032 MOV #ERROR,%EMTVEC ;:EMT VECTOR FOR ERROR ROUTINE
001670 012737 021774 000034 MOV #340,%EMTVEC+2 ;:LEVEL 7
001674 012737 000340 000036 MOV #TRAP,%TRAPVEC ;:TRAP VECTOR FOR TRAP CALLS
001678 012737 020576 000024 MOV #340,%TRAPVEC+2 ;:LEVEL 7
001682 012737 000340 000026 MOV #PWRDN,%PWRVEC ;:POWER FAILLRE VECTOR
001686 013737 010404 010376 MOV #340,%PWRVEC+2 ;:LEVEL 7
001690 005037 001164 SENDCT,SEOPCT ;:SETUP END-OF-PROGRAM COUNTER
001694 005037 001166 STIMES ;:INITIALIZE NUMBER OF ITERATIONS
001698 112737 000001 001115 CLR ESCAPE ;:CLEAR THE ESCAPE ON ERROR ADDRESS
001702 002004 MOV #1,SEMAX ;:ALLOW ONE ERROR PER TEST
001706 012737 002004 001105 MOV #. ,SLPADR ;:INITIALIZE THE LOOP ADDRESS FOR SCOPE
001710 012737 002012 001110 MOV #. ,SLPERR ;:SETUP THE ERROR LOOP ADDRESS
001714 012746 000004 :: SIZE FOR A HARDWARE SWITCH REGISTER. IF NOT FOUND OR IT IS
001718 012737 002062 000004 :: EQUAL TO A "-1", SETUP FOR A SOFTWARE SWITCH REGISTER.
001722 012737 177570 001136 MOV #ERRVEC-(SP) ;:SAVE ERROR VECTOR
001726 012737 177570 001140 MOV #64,%ERRVEC ;:SET UP ERROR VECTOR
001730 012737 177570 001136 MOV #DSWR,SWR ;:SETUP FOR A HARDWARE SWICH REGISTER
001734 012737 177570 001140 MOV #DISP,DISPLAY ;:AND A HARDWARE DISPLAY REGISTER

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659 002046 022777 177777 177062      CMP      #-1,DSWR      :: TRY TO REFERENCE HARDWARE SWR
660 002054 001013                    BNE      655          :: BRANCH IF NO TIMEOUT TRAP OCCURRED
661                    002056 005737 000001      TST      0#1          :: AND THE HARDWARE SWR IS NOT = -1
662 002062 012737 000176 00:136 645:    MOV      #SWREG,SWR   :: FORCE A TRAP THROUGH ERRVEC
663 002070 012737 000174 001140      MOV      #DISPREG,DISPLAY :: POINT TO SOFTWARE SWR
664 002076 012716 002104                    MOV      #655,(SP)    :: POINT TO SOFTWARE DISPLAY REG
665 002102 000002                    RTI                          :: REPLACE OLD PC WITH NEW
666 002104 012637 000004 655:    MOV      (SP)+,3#ERRVEC :: RESTORE PC AND PSW
667                    SARG1:
668 002110 005037 001206                    CLR      $PASS        :: CLEAR PASS COUNT
669 002114 132737 000200 001221      BITB     #APTSIZE,$ENVM :: TEST USER SIZE UNDER APT
670 002122 001403                    BEQ      645          :: YES,USE NON-APT SWITCH
671 002124 012737 001222 001136      MOV      #SSWREG,SWR  :: NO,USE APT SWITCH REGISTER
672 002132 000005 645:
673 002134 012732 000232      RBEG2:  RESET
674 002140 012701 000230      MOV      #232,R2      :LOAD R2
675 002144 010221 55:      MOV      #230,R1      :LOAD R1
676 002146 005021      MOV      R2,(R1)+     :LOAD .+2
677 002150 010102      CLR      (R1)+        :LOAD HALT
678 002152 005722      MOV      R1,R2        :LOAD R2
679 002154 020227 001002      TST      (R2)+        :BUMP R2
680 002160 001371      CMP      R2,#1002     :TEST FOR LAST
681 002162 004737 016646      BNE      55           :BR UNTIL DONE
682 002166 005700      JSR      PC,WFADJ     :ADJUST SOME TOLERANCES
683 002170 001402      TST      R0           :TEST R0
684 002172 000137 002476      BEQ      25          :BR IF CLEARED
685 002176 005737 000042      JMP      45          :INHIBIT TYPOUT
686 002202 00:402 25:      TST      0#42        :TEST ACT-11 OR CCP
687 002204 000137 002476      BEQ      35          :BR IF CLEARED
688 002210 104400 35:      JMP      45          :INHIBIT TYPOUT
689 002214 000422      TYPE     #655        :: TYPE ASCIZ STRING
690                    BR      645          :: GET OVER THE ASCIZ
691 655: .ASCIZ <15><12>/AR-11 DIAGNOSTIC WRAPAROUND TEST.
692 645:
693 002262 104400 002270      TYPE     #675        :: TYPE ASCIZ STRING
694 002266 000414      BR      665          :: GET OVER THE ASCIZ
695 675: .ASCIZ <15><12>/MAINDEC-11-DZARC-B.\15><12>
696 665:
697 002320 013746 001470      MOV      NMBEXT,-(SP) :PUSH CN STACK
698 002324 104402      TYPOS    :TYPE OCTAL
699 002326 000002      .WORD    2
700 002330 104400 002336      *TYPE    #695        :: TYPE ASCIZ STRING
701 002334 000420      BR      685          :: GET OVER THE ASCIZ
702 695: .ASCIZ /18) ADDITIONAL ARII'S CONNECTED/
703 685:
704 002376 104400 002404      TYPE     #715        :: TYPE ASCIZ STRING
705 002376 000435      BR      705          :: GET OVER THE ASCIZ
706 715: .ASCIZ <15><12>/ALL AR-11'S MUST HAVE G5036 WRAPAROUND MODULE INSTALLED
707 705:
708 002476 012700 001474 45:    MOV      #ADCS,R0     :LOAD POINTER
709 002502 013720 001464 105:    MOV      ARBADO,R0+   :
710 002506 022700 001516      JMP      #BEGIN,R0    :TEST FOR END

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002512 001373
002514 005237 001476
002520 012700 001500
002524 012701 000002
002530 060130
002532 005721
002534 022701 000020
002540 001373

BNE 105
INC ADCS1
MOV #ADDBR,R0
MOV #2,R1
125: ADD R1,(R0)+
TST (R1)+
CMP #20,R1
BNE 125
.SBTTL A TO D CHANNEL NUMBERS

:BIPOLAR CHANNEL NUMBERS

000000
000001
000002
000003
000004
000005
000006
000007
000010
000011
000012
000013
000014
000015
000016
000017

CH0 = 0
CH1 = 1
CH2 = 2
CH3 = 3
CH4 = 4
CH5 = 5
CH6 = 6
CH7 = 7
CH10 = 10
CH11 = 11
CH12 = 12
CH13 = 13
CH14 = 14
CH15 = 15
CH16 = 16
CH17 = 17

:UNIPOlar CHANNEL NUMBERS

000040
000041
000042
000043
000044
000045
000046
000047
000050
000051
000052
000053
000054
000055
000056
000057

CH40 = 40
CH41 = 41
CH42 = 42
CH43 = 43
CH44 = 44
CH45 = 45
CH46 = 46
CH47 = 47
CH50 = 50
CH51 = 51
CH52 = 52
CH53 = 53
CH54 = 54
CH55 = 55
CH56 = 56
CH57 = 57

.SBTTL
.SBTTL TEST NUMBER
.SBTTL

ABSTRACT

```

765      ::*****
766      :*TEST 1      TEST THAT "ERASE" AND DONE CAN BE SET AND CLEARED
767      :*****
768      TST1:  SCOPE
769      MOV      #10,$TIMES      ;;DO 10 ITERATIONS
770      CLR      @VCSTAT        ;CLEAR
771      MOV      #BIT12,$GDDAT   ;LOAD EXPECTED
772      MOV      $GDDAT,@VCSTAT  ;LOAD REG
773      MOV      @VCSTAT,$BDDAT  ;READ REG
774      CMP      $GDDAT,$BDDAT   ;COMPARE
775      BEQ      IS              ;;BR IF SET
776      ERROR    3                ;ERASE FAILED TO SET, CHECK G5036-
777      ;BCOBR CONNECTION: A-VV, VV-A
778      IS:     MOV      #BIT9,$GDDAT ;LOAD EXPECTED
779      BIS      #BIT9,@VCSTAT      ;SET CH 2 BIT
780      MOV      @VCSTAT,$BDDAT    ;READ REG
781      CMP      $GDDAT,$BDDAT     ;COMPARE
782      BEQ      ZS                ;;BR IF CLEARED
783      ERROR    3                ;ERASE BIT FAILED TO CLEAR BY ERASE
784      ;RETURN (SETTING OF THE CH 2 BIT)
785
786      ZS:     MOV      #BIT7,$GDDAT ;LOAD EXPECTED
787      BIC      #BIT9,@VCSTAT      ;CLEAR CH 02
788      MOV      @VCSTAT,$BDDAT    ;READ REG
789      CMP      $GDDAT,$BDDAT     ;COMPARE
790      BEQ      TST2              ;;BR IF EQUAL
791      ERROR    3                ;READY FAILED TO SET ON THE END
792      ; OF ERASE RETURN (CLEARING CH 2)
793
794      ::*****
795      :*TEST 2      EXTERNAL A TO D START FROM INTENSIFY
796      :*****
797      TST2:  SCOPE
798      MOV      #10,$TIMES      ;;DO 10 ITERATIONS
799      CLR      @VCSTAT        ;CLEAR DONE
800      MOV      @ADDBR,@ADDBR
801      CLR      @ADCS
802      MOV      #BIT7!BIT4,$GDDAT ;LOAD EXPECTED
803      MOV      $GDDAT,@ADCS     ;LOAD EXTERNAL A/D START ENABLE
804      INC      @VCSTAT        ;INTENSIFY
805      TCTB    @VCSTAT          ;WAIT FOR READY
806      BPL     IS
807      MOV      #BIT8,TEMP      ;SET UP A COUNTER
808      DEC     TEMP            ;DELAY
809      BPL     ZS
810      MOV      @ADCS,$BDDAT    ;READ REG.
811      CMP      $GDDAT,$BDDAT   ;COMPARE RESULTS
812      BEQ     IS              ;;BR IF EQUAL
813      ERROR    4                ;INTENSIFY PULSE FAILED TO START
814      MOV     @ADDBR,@ADDBR    ; AN A TO D CONVERSION
815      CLR     @VCSTAT
816      CLR     @ADCS

```

```

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017
018
019 003030 000004
020 003032 012737 000010 001164
021 003040 012737 001000 001124
022 003046 004537 011534
023 003052 000000
024 003054 013737 011656 001126
025 003062 012737 000001 011664
026 003070 004737 011666
027 003074 000401
028 003076 104001
029
030
031
032
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034
035
036
037 003100 000004
038 003102 012737 000010 001164
039 003110 012737 000000 001124
040 003116 004537 011534
041 003122 000040
042 003124 013737 011656 001126
043 003132 012737 000001 011664
044 003140 004737 011666
045 003144 000401
046 003146 104001
047
048
049 003150 000004
050 003152 012737 000010 001164
051 003160 012737 001315 001124
052 003166 004537 011534
053 003172 000002
054 003174 013737 011656 001126
055 003202 012737 000024 011664
056 003210 004737 011666
057 003214 000401
058 003216 104001
059
060

```

```

*****
*TEST 3 TEST THAT CH 0 IS A BIPOLAR GROUND
*****
TST3: SCOPE
MOV #10,$TIMES ;;DO 10 ITERATIONS
MOV #1000,$GDDAT ;LOAD EXPECTED VALUE
JSR R5,CONVRT ;CONVERT
CH0 ;CH 0
MOV ADEND,$BDDAT ;LOAD VALUE READ
MOV #1,SPREAD ;LOAD + OR - COUNT SPREAD
JSR PC,COMPAR ;COMPARE $GDDAT AND $BDDAT
BR TST4 ;;BR IF WITHIN TOLERANCE
ERROR 1 ;CH 0 FAILED TO EQUAL EXPECTED
; VALUE

```

```

*****
*TEST 4 TEST THAT CH 40 IS A UNIPOLAR GROUND
*****
TST4: SCOPE
MOV #10,$TIMES ;;DO 10 ITERATIONS
MOV #0,$GDDAT ;LOAD EXPECTED VALUE
JSR R5,CONVRT ;CONVERT
CH40 ;CH 40
MOV ADEND,$BDDAT ;LOAD VALUE READ
MOV #1,SPREAD ;LOAD + OR - COUNT SPREAD
JSR PC,COMPAR ;COMPARE $GDDAT AND $BDDAT
BR TST5 ;;BR IF WITHIN TOLERANCE
ERROR 1 ;CH 40 FAILED TO EQUAL EXPECTED
; VALUE

```

```

*****
*TEST 5 TEST THAT CH 2 IS AT +1 VOLT BIPOLAR
*****
TST5: SCOPE
MOV #10,$TIMES ;;DO 10 ITERATIONS
MOV #1315,$GDDAT ;LOAD EXPECTED VALUE
JSR R5,CONVRT ;CONVERT
CH2 ;CH 2
MOV ADEND,$BDDAT ;LOAD VALUE READ
MOV #24,SPREAD ;LOAD + OR - COUNT SPREAD
JSR PC,COMPAR ;COMPARE $GDDAT AND $BDDAT
BR TST6 ;;BR IF WITHIN TOLERANCE
ERROR 1 ;CH 2 FAILED TO EQUAL EXPECTED
; VALUE

```


H02

MAINDEC-11-DZARC-B
DZARCB.P11 T6

MACY11 27.732) 21-SEP-76 16:44 PAGE 20
TEST THAT CH 42 IS AT +1 VOLT UNIPOLAR

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863
864 003220 000004
865 003222 012737 000010 001164
866 003230 012737 000315 001124
867 003236 004537 011534
868 003242 000042
869 003244 013737 011656 001126
870 003252 012737 000024 011664
871 003260 004737 011666
872 003264 000401
873 003256 104001
874
875
876
877
878
879
880 003270 000004
881 003272 012737 000010 001164
882 003300 013737 016764 001124
883 003306 004537 011534
884 003312 000003
885 003314 013737 011656 001126
886 003322 013737 016765 011664
887 003330 004737 011666
888 003334 000401
889 003336 104001
890
891
892
893
894
895 003340 000004
896 003342 012737 000010 001164
897 003350 012737 001000 001124
898 003356 004537 011534
899 003362 000043
900 003364 013737 011656 001126
901 003372 013737 016770 011664
902 003400 004737 011666
903 003404 000401
904 003406 104001
905
```

```
*****
*TEST 6 TEST THAT CH 42 IS AT +1 VOLT UNIPOLAR
*****
TST6: SCOPE
      MOV #10,$TIMES ;;DO 10 ITERATIONS
      MOV #315,$GDDAT ;LOAD EXPECTED VALUE
      JSR R5,CONVRT ;CONVERT
      CH42 ;CH 42
      MOV ADEND,$BDDAT ;LOAD VALUE READ
      MOV #24,$SPREAD ;LOAD + OR - COUNT SPREAD
      JSR PC,COMPAR ;COMPARE $GDDAT AND $BDDAT
      BR TST7 ;;BR IF WITHIN TOLERANCE
      ERROR 1 ;CH 42 FAILED TO EQUAL EXPECTED
                ; VALUE

*****
*TEST 7 TEST THAT CH 3 IS AT +2.5 BIPOLAR
*****
TST7: SCOPE
      MOV #10,$TIMES ;;DO 10 ITERATIONS
      MOV V1754,$GDDAT ;LOAD EXPECTED VALUE
      JSR R5,CONVRT ;CONVERT
      CH3 ;CH 3
      MOV ADEND,$BDDAT ;LOAD VALUE READ
      MOV V24,$SPREAD ;LOAD + OR - COUNT SPREAD
      JSR PC,COMPAR ;COMPARE $GDDAT AND $BDDAT
      BR TST10 ;;BR IF WITHIN TOLERANCE
      ERROR 1 ;CH 3 FAILED TO EQUAL EXPECTED
                ; VALUE

*****
*TEST 10 TEST THAT CH 43 IS AT +2.5 UNIPOLAR
*****
TST10: SCOPE
      MOV #10,$TIMES ;;DO 10 ITERATIONS
      MOV #1000,$GDDAT ;LOAD EXPECTED VALUE
      JSR R5,CONVRT ;CONVERT
      CH43 ;CH 43
      MOV ADEND,$BDDAT ;LOAD VALUE READ
      MOV V50,$SPREAD ;LOAD + OR - COUNT SPREAD
      JSR PC,COMPAR ;COMPARE $GDDAT AND $BDDAT
      BR TST11 ;;BR IF WITHIN TOLERANCE
      ERROR 1 ;CH 43 FAILED TO EQUAL EXPECTED
                ; VALUE
```

```

906                                     ::*****
907                                     ;*TEST 11      TEST THAT CH 4 IS AT -2.5 BIPOLAR
908                                     ;*****
909 003410 000004  TST11: SCOPE
910 003412 012737 000010 001164  MOV      #10,$TIMES      ;;DO 10 ITERATIONS
911 003420 013737 016766 001124  MOV      V24,$GDDAT      ;LOAD EXPECTED VALUE
912 003426 004537 011534  JSR      R5,CONVRT      ;CONVERT
913 003432 000004  CH4      ;CH 4
914 003434 013737 011656 001126  MOV      ADEND,$BDDAT      ;LOAD VALUE READ
915 003442 013737 016766 011664  MOV      V24,SPREAD      ;LOAD + OR - COUNT SPREAD
916 003450 004737 011666  JSR      PC,COMPAR      ;COMPARE $GDDAT AND $BDDAT
917 003454 000401  BR       TST12      ;;BR IF WITHIN TOLERANCE
918 003456 104001  ERROR    1      ;CH 4 FAILED TO EQUAL EXPECTED VALUE
919
920                                     ::*****
921                                     ;*TEST 12      TEST THAT CH 57 IS AT +4 VOLTS UNIPOLAR
922                                     ;*****
923 003460 000004  TST12: SCOPE
924 003462 012737 000010 001164  MOV      #10,$TIMES      ;;DO 10 ITERATIONS
925 003470 012737 001463 001124  MOV      #1463,$GDDAT      ;LOAD EXPECTED VALUE
926 003476 004537 011534  JSR      R5,CONVRT      ;CONVERT
927 003502 000057  CH57     ;CH 57
928 003504 013737 011656 001126  MOV      ADEND,$BDDAT      ;LOAD VALUE READ
929 003512 012737 000120 011664  MOV      #120,SPREAD      ;LOAD + OR - COUNT SPREAD
930 003520 004737 011666  JSR      PC,COMPAR      ;COMPARE $GDDAT AND $BDDAT
931 003524 000401  BR       TST13      ;;BR IF WITHIN TOLERANCE
932 003526 104001  ERROR    1      ;CH 57 FAILED TO EQUAL EXPECTED VALUE
933
934                                     ::*****
935                                     ;*TEST 13      TEST THAT CH 41 IS LESS THAN +200 MVOLTS UNIPOLAR
936                                     ;*****
937 003530 000004  TST13: SCOPE
938 003532 012737 000004 001164  MOV      #4,$TIMES      ;;DO 4 ITERATIONS
939 003540 012737 000041 011662  MOV      #CH41,CHANL      ;LOAD CHANNEL # FOR ERROR "YPCU"
940 003546 112777 000041 175722  MOVB     #CH41,ADCS1      ;LOAD MUX
941 003554 013737 016772 001124  MOV      VA24,$GDDAT      ;LOAD EXPECTED VALUE
942 003562 013737 016772 011664  MOV      VA24,SPREAD      ;LOAD + OR - COUNT SPREAD
943 003570 012737 000200 015216  MOV      #BIT7,TEMP      ;LOAD DELAY
944 003576 005337 015216  2$:     DEC      TEMP      ;DELAY
945 003602 001375  BNE     2$
946 003604 005277 175664  INC      ADCS      ;CONVERT CH 41
947 003610 105777 175660  3$:     TSTB     ADCS      ;DONE
948 003614 100375  BPL     3$
949 003616 017737 175656 001126  MOV      @ADDBR,$BDDAT      ;READ RESULTS
950 003624 013737 001126 015164  MOV      $BDDAT,BIASC1      ;SAVE RESULTS
951 003632 004737 011666  JSR      PC,COMPAR      ;COMPARE $GDDAT AND $BDDAT
952 003636 000401  BR       1$      ;;BR IF WITHIN TOLERANCE
953 003640 104001  ERROR    1      ;CH 41 FAILED TO EQUAL EXPECTED
954                                     ; VALUE, BIAS CURRENT TOO HIGH
955 003642 032777 010000 175266  1$:     BIT      #BIT12,@SWR      ;TEST FORCED SW
956 003650 001432  BEQ     TST14      ;;BR IF NOT FORCED PRINTOUT
957 003652 104400 003660  TYPE     65$      ;;TYPE ASCII STRING
958 003656 000412  BR       64$      ;;GET OVER THE ASCII
959                                     ;:65$: .ASCIIZ <15><12><12>/AR-11 ADDRESS =
960 003704 64$:     MOV      ARBADD,-(SP)      ;SAVE ADDRESS
961 003704 013746 001464

```

962	003710	104401		
963	003712	104400	016536	
964	003716	104400	013132	
965	003722	013746	015164	
966	003726	104402		
967	003730	003	000	
968	003732	104400	013146	

```

TYP0C
TYPE ,ACRLF
TYPE ,BIASST
MOV ,BIASCI,-(SP)
TYPOS
.BYTE 3,0
TYPE ,BIASDN

```

```

*****
*TEST 14 TEST THAT ERASE L (CH 53) IS GREATER THAN +3 VOLTS WHEN HIGH
*****

```

973	003736	000004		
974	003740	012737	000010	001164
975	003746	012777	005000	175532
976	003754	012737	001146	001124
977	003762	004537	011534	
978	003766	000053		
979	003770	013737	011656	001126
980	003776	023737	001124	001126
981	004004	003401		
982	004006	104002		

```

†ST14: SCOPE
MOV #10,$TIMES ;;DO 10 ITERATIONS
MOV #BIT11!BIT9,@VCSTAT ;LOAD VC STATUS
MOV #1146,$GDDAT ;LOAD EXPECTED VALUE
JSR R5,CONVRT ;CONVERT
CH53 ;CH 53
MOV ADEND,$BDDAT ;LOAD VALUE READ
CMP $GDDAT,$BDDAT ;COMPARE
BLE TST15 ;;BR IF LESS THAN OR EQUAL
ERROR 2 ;CH 53 FAILED TO EQUAL EXPECTED VALUE

```

```

*****
*TEST 15 TEST THAT ERASE L (CH 53) IS LESS THAN +400 MVOLTS WHEN LOW
*****

```

987	004010	000004		
988	004012	012737	000010	001164
989	004020	005077	175462	
990	004024	012777	012000	175454
991	004032	012737	000120	001124
992	004040	004537	011534	
993	004044	000053		
994	004046	013737	011656	001126
995	004054	023737	001124	001126
996	004062	002001		
997	004064	104007		

```

†ST15: SCOPE
MOV #10,$TIMES ;;DO 10 ITERATIONS
CLR @VCSTAT ;CLEAR VC STATUS
MOV #BIT12!BIT10,@VCSTAT ;SET ERASE
MOV #120,$GDDAT ;LOAD EXPECTED VALUE
JSR R5,CONVRT ;CONVERT
CH53 ;CH 53
MOV ADEND,$BDDAT ;LOAD VALUE READ
CMP $GDDAT,$BDDAT ;COMPARE
BGE TST16 ;;BR IF GREATER THAN OR EQUAL
ERROR 7 ;CH 53 FAILED TO EQUAL EXPECTED VALUE

```

```

*****
*TEST 16 TEST THAT WRITE-THRU (CH 54) IS GREATER THAN +3 VOLTS WHEN HIGH
*****

```

1000				
1001				
1002	004066	000004		
1003	004070	012737	000010	001164
1004	004076	012777	011000	175402
1005	004104	012737	001146	001124
1006	004112	004537	011534	
1007	004116	000054		
1008	004120	013737	011656	001126
1009	004126	023737	001124	001126
1010	004134	003401		
1011	004136	104002		
1012				

```

†ST16: SCOPE
MOV #10,$TIMES ;;DO 10 ITERATIONS
MOV #BIT12!BIT9,@VCSTAT ;LOAD VC STATUS
MOV #1146,$GDDAT ;LOAD EXPECTED VALUE
JSR R5,CONVRT ;CONVERT
CH54 ;CH 54
MOV ADEND,$BDDAT ;LOAD VALUE READ
CMP $GDDAT,$BDDAT ;COMPARE
BLE TST17 ;;BR IF LESS THAN OR EQUAL
ERROR 2 ;CH 54 FAILED TO EQUAL EXPECTED VALUE

```

K02

MAINDEC-11-DZARC-B
DZARC8.P11 T17

MACY11 27(732) 21-SEP-76 16:44 PAGE 23
TEST THAT WRITE-THRU (CH 54) IS LESS THAN +400 MVOLTS WHEN LOW

```

:013
1014
1015
1016 004140 000004
1017 004142 012737 000010 001164
1018 004150 012777 006000 175330
1019 004156 012737 000120 001124
1020 004164 004537 011534
1021 004170 000054
1022 004172 013737 011656 001126
1023 004200 023737 001124 001126
1024 004206 002001
1025 004210 104007
1026
1027
1028
1029
1030
1031 004212 000004
1032 004214 012737 000010 001164
1033 004222 005077 175260
1034 004226 012737 000120 001124
1035 004234 004537 011534
1036 004240 000055
1037 004242 013737 011656 001126
1038 004250 023737 001124 001126
1039 004256 002001
1040 004260 104007
1041
1042
1043
1044
1045
1046 004262 000004
1047 004264 012737 000010 001164
1048 004272 012777 017000 175206
1049 004300 012737 001146 001124
1050 004306 004537 011534
1051 004312 000055
1052 004314 013737 011656 001126
1053 004322 023737 001124 001126
1054 004330 003401
1055 004332 104002
1056
1057

```

```

:*****
:*TEST 17 TEST THAT WRITE-THRU (CH 54) IS LESS THAN +400 MVOLTS WHEN LOW
:*****

```

```

†ST17: SCOPE
MOV #10,$TIMES ;;DO 10 ITERATIONS
MOV #BIT11!BIT10.!JVCSTAT ;SET WRITE-THRU
MOV #120,$GDDAT ;LOAD EXPECTED VALUE
JSR RS,CONVRT ;CONVERT
CH54 ;CH 54
MOV ADEND,$BDDAT ;LOAD VALUE READ
CMP $GDDAT,$BDDAT ;COMPARE
BGE TST20 ;;BR IF GREATER THAN OR EQUAL
ERROR 7 ;CH 54 FAILED TO EQUAL EXPECTED
; VALUE

```

```

:*****
:*TEST 20 TEST THAT NON-STORE (CH 55) IS LESS THAN +400 MVOLTS WHEN LOW
:*****

```

```

†ST20: SCOPE
MOV #10,$TIMES ;;DO 10 ITERATIONS
CLR JVCSTAT ;CLEAR STORE MODE
MOV #120,$GDDAT ;LOAD EXPECTED VALUE
JSR RS,CONVRT ;CONVERT
CH55 ;CH 55
MOV ADEND,$BDDAT ;LOAD VALUE READ
CMP $GDDAT,$BDDAT ;COMPARE
BGE TST21 ;;BR IF GREATER THAN OR EQUAL
ERROR 7 ;CH 54 FAILED TO EQUAL EXPECTED
; VALUE

```

```

:*****
:*TEST 21 TEST THAT NON-STORE L (CH 55) IS GREATER THAN +3 VOLTS WHEN HIGH
:*****

```

```

†ST21: SCOPE
MOV #10,$TIMES ;;DO 10 ITERATIONS
MOV #BIT12!BIT11!BIT10!BIT9.!JVCSTAT ;SET STORE MODE
MOV #1146,$GDDAT ;LOAD EXPECTED VALUE
JSR RS,CONVRT ;CONVERT
CH55 ;CH 55
MOV ADEND,$BDDAT ;LOAD VALUE READ
CMP $GDDAT,$BDDAT ;COMPARE
BLE TST22 ;;BR IF LESS THAN OR EQUAL
ERROR 2 ;CH 55 FAILED TO EQUAL EXPECTED
; VALUE

```


MAINDEC-11-DZARC-B
DZARCB.P11 T22

MACY11 27(732) 21-SEP-76 16:44 PAGE 24
TEST THAT CHANNEL 02 L (CH 56) IS GREATER THAN +3 VOLTS WHEN HIGH

```

1058
1059
1060
1061 004334 000004
1062 004336 012737 000010 001164
1063 004344 012777 014000 175134
1064 004352 012737 001146 001124
1065 004360 004537 011534
1066 004364 000056
1067 004366 013737 011656 001126
1068 004374 023737 001124 001126
1069 004402 003401
1070 004404 104002
1071
1072
1073
1074
1075
1076 004406 000004
1077 004410 012737 000010 001164
1078 004416 012777 003000 175062
1079 004424 012737 000120 001124
1080 004432 004537 011534
1081 004436 000056
1082 004440 013737 011656 001126
1083 004446 023737 001124 001126
1084 004454 002001
1085 004456 104007
1086
1087 004460 005077 175022
1088
1089

```

```

*****
*TEST 22 TEST THAT CHANNEL 02 L (CH 56) IS GREATER THAN +3 VOLTS WHEN HIGH
*****
†ST22: SCOPE
MOV #10,$TIMES ;:DO 10 ITERATIONS
MOV #BIT12!BIT11,@VCSTAT ;LOAD VC STATUS
MOV #1146,$GDDAT ;LOAD EXPECTED VALUE
JSR R5,CONVRT ;CONVERT
CH56 ;CH 56
MOV ADEND,$BDDAT ;LOAD VALUE READ
CMP $GDDAT,$BDDAT ;COMPARE
BLE TST23 ;:BR IF LESS THAN OR EQUAL
ERROR 2 ;CH 56 FAILED TO EQUAL EXPECTED
; VALUE

```

```

*****
*TEST 23 TEST THAT CHANNEL 02 L (CH 56) IS LESS THAN +400 M.VOLTS WHEN LOW
*****
†ST23: SCOPE
MOV #10,$TIMES ;:DO 10 ITERATIONS
MOV #BIT9!BIT10,@VCSTAT ;SET CHANNEL 2
MOV #120,$GDDAT ;LOAD EXPECTED VALUE
JSR R5,CONVRT ;CONVERT
CH56 ;CH 56
MOV ADEND,$BDDAT ;LOAD VALUE READ
CMP $GDDAT,$BDDAT ;COMPARE
BGE 1$ ;:BR IF GREATER THAN
ERROR 7 ;CH 56 FAILED TO EQUAL EXPECTED
; VALUE
1$: CLR @VCSTAT ;CLEAR BIT 9

```

```

1099          ;:*****
1090          ;*TEST 24          A TO D DIFFERENTIAL LINEARITY TEST
1091          ;:*****
1092 004464 000004          TST24: SCOPE
1093 004466 012737 000001 001164      MOV      #1,$TIMES          ;;DO 1 ITERATION
1094          JSR      R5,DIFLIN          ;START TEST
1095 004474 004537 015220          VCXREG          ;X COARSE
1096 004500 001510          VCYREG          ;Y FINE
1097 004502 001512          .BYTE      1,5          ;GO AND CHANNEL 5
1098 004504      001      005
1099          CLR      $GDDAT          ;CLEAR EXPECTED
1100 004506 005037 001124          MOV      SKIPST,$BDDAT      ;LOAD # OF SKIPPED STATES
1101 004512 013737 015740 001126      BEQ      1$          ;;BR IF NO SKIPPED STATES
1102 004520 001401          ERROR      5          ;SKIPPED STATE(S) DETECTED
1103 004522 104005
1104          MOV      EXCESS,$BDDAT      ;LOAD # OF >2LSB STATES
1105 004524 013737 015744 001126 1$:      BEQ      2$          ;;BR IF NO GREATER THAN 2LSB WIDE STATES
1106 004532 001401          ERROR      5          ;> 2LSB WIDE STATE(S) DETECTED
1107 004534 104005
1108          MOV      DIFERR,$BDDAT      ;LOAD # OUTSIDE +- 5/2 LSB
1109 004536 013737 015742 001126 2$:      MOV      #52,$GDDAT      ;LOAD MAX # OF ALLOWABLE STATES OUTSIDE +- 1/2 L
1110 004544 012737 000064 001124      CMP      $GDDAT,$BDDAT      ;ANY ERRORS
1111 004552 023737 001124 001126      BGE      TST25          ;;BR IF NO ERROR
1112 004560 002001          ERROR      5          ;DIFFERENTIAL LINEARITY ERROR, >5% OF STATE
1113 004562 104005          ;WIDTHS OUTSIDE +- 1/2 LSB
1114
1115

```

```

1116 ::*****
1117 :*TEST 25 X D/A DIFFERENTIAL LINEARITY USING CH 5
1118 :*****
1119 004564 000004 TST25: SCOPE
1120 004566 012737 000010 00:164 MOV #10,$TIMES ;;DO 10 ITERATIONS
1121 004574 012701 000001 MOV #1,R1 ;LOAD EDGE VALUE
1122 004600 012702 015022 MOV #RSLT2,R2 ;LOAD RESULT POINTER
1123
1124 004604 010177 174700 18: MOV R1,$VXCXREG ;LOAD X DAC WITH PRESET
1125 004610 010137 004624 MOV R1,10$ ;LOAD EDGE VALUE
1126
1127 004614 004437 010716 JSR R4,SAR ;SOFTWARE SUCCESSIVE APPROX. ROUTINE
1128 004620 001512 VCYREG
1129 004622 000 005 .BYTE 0,$CH5
1130 004624 000000 10$: 0 ;EDGE VALUE
1131 004626 000200 BIT7 ;50-50
1132
1133 004630 013737 015122 004746 MOV DACSAV,12$ ;SAVE VALUE
1134 004636 005377 174646 DEC $VXCXREG ;LOAD X DAC
1135 004642 010137 004656 MOV R1,11$ ;LOAD SAME EDGE
1136
1137 004646 004437 010716 JSR R4,SAR ;SOFTWARE S.A.R.
1138 004652 001512 VCYREG
1139 004654 000 005 .BYTE 0,$CH5
1140 004656 000000 11$: 0
1141 004660 000200 BIT7
1142
1143 004662 013737 015122 004750 MOV DACSAV,13$ ;SAVE VALUE
1144 004670 013737 015122 001126 MOV DACSAV,$BDDAT ;LOAD VALUE INTO $BDDAT
1145 004676 163737 004746 001126 SUB 12$, $BDDAT ;STEP HEIGHT IN $BDDAT
1146 004704 013722 001126 MOV $BDDAT, R2)+ ;SAVE RESULT
1147 004710 012737 000062 001124 MOV #62,$GDDAT ;LOAD EXPECTED VALUE FOR 1 LSB STEP
1148 004716 013737 016774 011664 MOV V62,$SPREAD ;LOAD TOLERANCE
1149 004724 004737 011666 JSR PC,COMPAR ;TEST LIMITS
1150 004730 000401 BR 2$ ;;BR IF WITHIN LIMITS
1151 004732 104010 ERROR 10 ;D/A DIFFERENTIAL LINEARITY ERROR
1152 004734 006301 2$: ASL R1 ;MOVE LEFT
1153 004736 020127 002000 CMP R1,#2000 ;DONE
1154 004742 001320 BNE 1$ ;;BR IF NOT DONE
1155 004744 000402 BR TST26 ;;
1156
1157 004746 000000 12$: 0
1158 004750 000000 13$: 0

```

```

*****
TEST 26      Y D A DIFFERENTIAL LINEARITY USING CH 6
*****
TST26:  SCOPE
        MOV      #10,STIMES      ;;DC 10 ITERATIONS
        MOV      #1,R1           ;LOAD EDGE VALUE
        MOV      @R1,R2          ;LOAD RESULT POINTER
        MOV      @R1,R2          ;LOAD Y DAC WITH PRESET
        MOV      @R1,R2          ;LOAD EDGE VALUE
        JSR      R4,SAR          ;SOFTWARE S.A.R.
        VCVREG   0,CH6          ;EDGE VALUE
        .BYTE    0
        BIT7

105:    MOV      DACSAV,125      ;SAVE VALUE
        DEC      @VCYREG         ;LOAD Y DAC
        MOV      R1,115         ;LOAD EDGE
        JSR      R4,SAR          ;SOFTWARE S.A.R.
        VCVREG   0,CH6
        .BYTE    0
        BIT7

115:    MOV      DACSAV,135      ;SAVE RESULTS
        MOV      DACSAV,@SBCDAT ;LOAD @SBCDAT
        SUB      125,@SBCDAT     ;STEP HEIGHT IN @SBCDAT
        MOV      @SBCDAT,(R2)+   ;SAVE RESULT
        MOV      #62,@SBCDAT    ;LOAD EXPECTED
        MOV      @62,@SPREAD    ;LOAD TOLERANCE
        JSR      @PC,COMPARE     ;TEST IF WITHIN LIMITS
        BR       25              ;;BR IF WITHIN LIMITS
        ERROR   10               ;D A DIFFERENTIAL LINEARITY ERROR
        ASL      R1
        CMP      R1,#2000        ;DONE ?
        BNE     15               ;;BR IF NOT DONE
        BIT      @BIT12,@SWR     ;TEST FORCED PRINTOUT
        BEQ     TST27           ;;BR IF NOT FORCED PRINTOUT
        TYPE    @DI1MSG          ;TYPE A STRING OF OCTAL #
        JSR      @R5,TYPSTG     ;STARTING
        RSLT2   @R5              ;# OF LOCATIONS
        IC      @R5
        TYPE    @DI2MSG          ;TYPE A STRING OF OCTAL #
        JSR      @R5,TYPSTG     ;STARTING
        RSLT3   @R5              ;# OF LOCATIONS
        IC      @R5
        TYPE    @DI3MSG          ;TYPE A STRING OF OCTAL #
        JSR      @R5,TYPSTG     ;STARTING
        RSLT3   @R5              ;# OF LOCATIONS
        IC      @R5
        BR       TST27          ;;
125:    C
135:    C

```

000000	000000	000000	000000
000001	000001	000001	000001
000002	000002	000002	000002
000003	000003	000003	000003
000004	000004	000004	000004
000005	000005	000005	000005
000006	000006	000006	000006
000007	000007	000007	000007
000008	000008	000008	000008
000009	000009	000009	000009
000010	000010	000010	000010
000011	000011	000011	000011
000012	000012	000012	000012
000013	000013	000013	000013
000014	000014	000014	000014
000015	000015	000015	000015
000016	000016	000016	000016
000017	000017	000017	000017
000018	000018	000018	000018
000019	000019	000019	000019
000020	000020	000020	000020
000021	000021	000021	000021
000022	000022	000022	000022
000023	000023	000023	000023
000024	000024	000024	000024
000025	000025	000025	000025
000026	000026	000026	000026
000027	000027	000027	000027
000028	000028	000028	000028
000029	000029	000029	000029
000030	000030	000030	000030
000031	000031	000031	000031
000032	000032	000032	000032
000033	000033	000033	000033
000034	000034	000034	000034
000035	000035	000035	000035
000036	000036	000036	000036
000037	000037	000037	000037
000038	000038	000038	000038
000039	000039	000039	000039
000040	000040	000040	000040
000041	000041	000041	000041
000042	000042	000042	000042
000043	000043	000043	000043
000044	000044	000044	000044
000045	000045	000045	000045
000046	000046	000046	000046
000047	000047	000047	000047
000048	000048	000048	000048
000049	000049	000049	000049
000050	000050	000050	000050
000051	000051	000051	000051
000052	000052	000052	000052
000053	000053	000053	000053
000054	000054	000054	000054
000055	000055	000055	000055
000056	000056	000056	000056
000057	000057	000057	000057
000058	000058	000058	000058
000059	000059	000059	000059
000060	000060	000060	000060
000061	000061	000061	000061
000062	000062	000062	000062
000063	000063	000063	000063
000064	000064	000064	000064
000065	000065	000065	000065
000066	000066	000066	000066
000067	000067	000067	000067
000068	000068	000068	000068
000069	000069	000069	000069
000070	000070	000070	000070
000071	000071	000071	000071
000072	000072	000072	000072
000073	000073	000073	000073
000074	000074	000074	000074
000075	000075	000075	000075
000076	000076	000076	000076
000077	000077	000077	000077
000078	000078	000078	000078
000079	000079	000079	000079
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000091	000091	000091	000091
000092	000092	000092	000092
000093	000093	000093	000093
000094	000094	000094	000094
000095	000095	000095	000095
000096	000096	000096	000096
000097	000097	000097	000097
000098	000098	000098	000098
000099	000099	000099	000099
000100	000100	000100	000100

```

005234 000004
005236 013737 000010 001164
005238 004437 001770 174270
005240 001510 010716
005242 000000 006
005244 001771
005246 000200

005236 013737 015122 005330
005238 004437 010716
005240 001510
005242 000004 006
005244 001771
005246 000200

005260 013737 015122 001126
005262 163737 005330 001126
005264 013737 001126 015160
005302 012737 000000 001124
005310 012737 000077 011554
005318 004437 011655
005326 000103
005334 124011
005342 000401
005350 000000
005358 000000

```

```

*****
*TEST 27      A TO D POSITIVE MULTIFLEXER SETTILING TEST
*****
*STRT: SCOPE
      MOV      #10,STIMES           ::DO 10 ITERATIONS
      MOV      #1770,SVCYREG       :LOAD Y DAC
      JSR      R4,SAR              :SOFTWARE S.A.R.
      VCXREG
      .BYTE   0.6
      .WORD  1771
      BIT?

      MOV      DACSAV,IOS          :SAVE RESULTS
      JSR      R4,SAR              :SOFTWARE S.A.R.
      VCXREG
      .BYTE   CH4,CH5             :-2.5V CH 4 AND CH 5
      .WORD  1771
      BIT?

      MOV      DACSAV,SBDDAT       :SAVE RESULT IN SBDDAT
      SUB      IOS,SBDDAT          :SUB FIRST VALUE
      MOV      SBDDAT,ADPMUX       :SAVE RESULT
      MOV      #0,SBDDAT           :LOAD EXPECTED SETTILING ERROR
      MOV      #77,SPREAD          :LOAD TOLERANCE
      JSR      PC,COMPARE          :TEST IF WITHIN LIMITS
      BR      TS130                ::BR IF WITHIN
      BR      ERROR                :AND POSITIVE SETTILING ERROR
      TS130
      TS130
*****

```

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11300

005332 000004
005334 012737 000010 001164
005342 012777 000010 174142
005350 004437 010716
005354 001510
005356 000 006
005360 000010
005362 000200

005364 013737 015122 005522
005372 004437 010716
005376 001510
005400 000 006
005404 000010
005404 000220

005406 013737 015122 001126
005414 163737 005522 001126
005422 013737 001126 015162
005430 012737 000000 001124
005436 012737 000077 011664
005444 004737 011666
005450 000401
005452 104011
005454 032777 010000 173454 15:
005462 001420
005464 104400 013436
005470 013746 015160
005474 104402
005476 000 000
005500 104400 013525
005504 013746 015162
005510 104402
005512 000 000
005514 104400 016536
005516 000401
005518 000401
005520 000000
005522 000000

```
::*****  
*TEST 30 A TO D NEGATIVE MULTIPLEXER SETTling TEST  
*****  
TST30: SCOPE  
MOV #10,STIMES ::DO 10 ITERATIONS  
MOV #10,DVCYREG ;LOAD Y DAC  
JSR R4,SAR ;SOFTWARE S.A.R.  
VCXREG  
.BYTE 0.6  
LD  
BIT7  
  
MOV DACSAV,10$ ;SAVE RESULTS  
JSR R4,SAR ;SOFTWARE S.A.R.  
VCXREG  
.BYTE CH3,CH6 ;+2.5V CH AND CH 6  
LD ;EDGE  
BIT7  
  
MOV DACSAV,$BDDAT ;SAVE RESULT IN $BDDAT  
SUB 10$, $BDDAT ;SUB FIRST VALUE  
MOV $BDDAT,ADNMUX ;SAVE RESULTS  
MOV #0,$GDDAT ;LOAD EXPECTED ZERO SETTling ERROR  
MOV #77,$SPREAD ;LOAD TOLERANCE  
JSR PC,COMPAR ;TEST IF WITHIN LIMITS  
BR 1$ ::BR IF WITHIN  
ERROR :1 ;A/D NEGATIVE SETTling ERROR  
BIT #BIT12,$SWR ;TEST FOR FORCED TYPEOUT  
BEQ TST31 ::BR IF NOT FORCED  
TYPE ADMSG  
MOV ADPMUX,-(SP) ;SAVE MUX SETTling  
TYPOS  
.BYTE 3,0  
TYPE ADEMSG  
MOV ACNMUX,-(SP) ;SAVE MUX SETTling  
TYPOS  
.BYTE 3,0  
TYPE ACRLF  
BR TST31 ::  
  
10$: 0
```


E03

MA:NDCC-11-DZARC-8
DZARCB.P11 T31

MACY11 27(732) 21-SEP-76 16:44 PAGE 30
A/D RMS NOISE TEST ON CHANNEL 7 - BIPOLAR

120000
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120100

*TEST 31 A/D RMS NOISE TEST ON CHANNEL 7 - BIPOLAR

005524 000004
005526 012737 000010 001164

005534 004437 010716
005540 001512
005542 000 007
005544 001000
005546 000000

005550 010537 001126
005554 010537 015130
005560 013737 015124 001124
005566 023737 001124 001126
005574 002001
005576 104006

↑TST31: SCOPE
MOV #10,\$TIMES ;;DO 10 ITERATIONS

JSR R4,SAR ;S A R ROUTINE
VCYREG ;Y AXIS
.BYTE 0,7 ;CHANNEL 7, FINE Y
1000 ;EDGE VALUE
C ;RMS RESULTS

MOV R5,\$BDDAT ;SAVE RESULTS (2X RMS NOISE, I=1/50 LSB)
MOV R5,CH7RMS ;SAVE RESULTS (RMS NOISE, I = 1/100 LSB)
MOV RMSMAX,\$GDDAT ;LOAD EXPECTED
CMP \$GDDAT,\$BDDAT ;COMPARE RESULTS
BGE TST32 ;;BR IF NOISE WITHIN SPEC
ERROR 6 ;A/D RMS NOISE (BIPOLAR) IS GREATER
; THAN SPEC

*TEST 32 A/D PEAK NOISE TEST ON CHANNEL 7 - BIPOLAR

005600 000004
005602 012737 000010 001164

005610 004437 010716
005614 001512
005616 000 007
005620 001000
005622 100000

005624 010537 001126
005630 010537 015130
005634 013737 015124 001124
005640 023737 001124 001126
005650 002001
005652 104006

↑TST32: SCOPE
MOV #10,\$TIMES ;;DO 10 ITERATIONS

JSR R4,SAR ;S A R ROUTINE
VCYREG ;Y AXIS
.BYTE 0,7 ;CHANNEL 7, FINE Y
1000 ;EDGE VALUE
BIT15 ;PEAK RESULTS

MOV R5,\$BDDAT ;SAVE RESULTS (PEAK NOISE, I = .01 LSB)
MOV R5,CH7PEK ;SAVE RESULTS
MOV PEKMAX,\$GDDAT ;LOAD EXPECTED
CMP \$GDDAT,\$BDDAT ;COMPARE RESULTS
BGE TST33 ;;BR IF NOISE WITHIN SPEC
ERROR 6 ;A/D PEAK NOISE (BIPOLAR) IS GREATER
; THAN SPEC

F03

MAINDEC-11-DZARC-B
DZARC8.P11 T33

MACY11 27(732) 21-SEP-76 16:44 PAGE 31
A/D RMS NOISE TEST ON CHANNEL 47 - UNIPOLAR

```
1324
1325
1326
1327 005654 000004
1328 005656 012737 000010 001164
1329
1330 005664 004437 010716
1331 005670 001512
1332 005672 000 047
1333 005674 000003
1334 005676 000000
1335
1336 005700 010537 001126
1337 005704 010537 015134
1338 005710 013737 015124 001124
1339 005716 023737 001124 001126
1340 005724 002001
1341 005726 104006
1342
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1353
1354
1355
1356 005730 000004
1357 005732 012737 000010 001164
1358
1359 005740 004437 010716
1360 005744 001512
1361 005746 000 047
1362 005750 000003
1363 005752 100000
1364
1365 005754 010537 001126
1366 005760 010537 015136
1367 005764 013737 015126 001124
1368 005772 023737 001124 001126
1369 006000 002001
1370 006002 104006
1371
1372
1373
1374
1375
1376
1377
1378
1379
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1399
1400
```

```
*****
*TEST 33      A/D RMS NOISE TEST ON CHANNEL 47 - UNIPOLAR
*****
†ST33:  SCOPE
        MOV      #10,$TIMES      ;;DO 10 ITERATIONS
        JSR      R4,SAR          ;S A R ROUTINE
        VCYREG   ;Y AXIS
        BYTE    0,CH47         ;CHANNEL 47, FINE Y
        3        ;EDGE VALUE
        0        ;RMS RESULTS
        MOV      R5,$BDDAT      ;SAVE RESULTS (RMS NOISE, 1=.01 LSB)
        MOV      R5,CH47RM      ;SAVE RESULT
        MOV      RM$MAX,$GDDAT  ;LOAD EXPECTED
        CMP      $GDDAT,$BDDAT  ;COMPARE RESULTS
        BGE     TST34          ;;BR IF NOISE WITHIN SPEC
        ERROR   6              ;A/D RMS NOISE (UNIPOLAR) IS GREATER
                               ; THAN SPEC
*****
*TEST 34      A/D PEAK NOISE TEST ON CHANNEL 47 - UNIPOLAR
*****
†ST34:  SCOPE
        MOV      #10,$TIMES      ;;DO 10 ITERATIONS
        JSR      R4,SAR          ;S A R ROUTINE
        VCYREG   ;Y AXIS
        BYTE    0,CH47         ;CHANNEL 47, FINE Y
        3        ;EDGE VALUE
        BIT15    ;PEAK RESULTS
        MOV      R5,$BDDAT      ;SAVE RESULTS (PEAK NOISE, 1 = .01 LSB)
        MOV      R5,CH47PK      ;SAVE RESULT
        MOV      PEKMAX,$GDDAT  ;LOAD EXPECTED
        CMP      $GDDAT,$BDDAT  ;COMPARE RESULTS
        BGE     TST35          ;;BR IF NOISE WITHIN SPEC
        ERROR   6              ;A/D PEAK NOISE (UNIPOLAR) IS GREATER
                               ; THAN SPEC
```

G03

MAINDEC-11-DZARC-B
DZARCB.P11 T35

MACY11 27(732) 21-SEP-76 16:44 PAGE 32
X DAC RMS NOISE TEST ON CHANNEL 5

```

1364
1365
1366
1367 006004 000004
1368 006006 012737 000010 001164
1369 006014 012777 001000 173466
1370
1371 006022 004437 010716
1372 006026 001512
1373 006030 000 005
1374 006032 001000
1375 006034 000000
1376
1377 006036 010537 001126
1378 006042 163737 015130 001126
1379 006050 013737 001126 015142
1380 006056 013737 015124 001124
1381 006064 023737 001124 001126
1382 006072 002001
1383 006074 104006
1384
1385
1386
1387
1388
1389
1390 006076 000004
1391 006100 012737 000010 001164
1392
1393 006106 012777 001000 173374
1394 006114 004437 010716
1395 006120 001512
1396 006122 000 005
1397 006124 001000
1398 006126 100000
1399
1400 006130 010537 001126
1401 006134 163737 015132 001126
1402 006142 013737 001126 015142
1403 006150 013737 015126 001124
1404 006156 023737 001124 001126
1405 006164 002001
1406 006166 104006

```

```

*****
*TEST 35 X DAC RMS NOISE TEST ON CHANNEL 5
*****
†ST35: SCOPE
MOV #10, $TIMES ;;DO 10 ITERATIONS
MOV #1000, @VCXREG ;LOAD X POS
JSR R4, SAR ;S A R ROUTINE
VCYREG ;Y AXIS
.BYTE 0.5 ;CHANNEL 5, COARSE X AND FINE Y
1000 ;EDGE VALUE
0 ;RMS RESULTS
MOV R5, $BDDAT ;SAVE RESULTS (X DAC + A/D RMS NOISE, 1=.01 LSB)
SUB CH7RMS, $BDDAT ;X DAC RMS NOISE ONLY
MOV $BDDAT, XDACRM ;SAVE RESULT
MOV RMSMAX, $GDDAT ;LOAD EXPECTED
CMP $GDDAT, $BDDAT ;COMPARE RESULTS
BGE TST36 ;;BR IF NOISE WITHIN SPEC
ERROR 6 ;X D/A RMS NOISE IS GREATER
; THAN SPEC
*****
*TEST 36 X DAC PEAK NOISE TEST ON CHANNEL 5
*****
†ST36: SCOPE
MOV #10, $TIMES ;;DO 10 ITERATIONS
MOV #1000, @VCXREG ;LOAD X DAC
JSR R4, SAR ;S A R ROUTINE
VCYREG ;Y AXIS
.BYTE 0.5 ;CHANNEL 5, COARSE X AND FINE Y
1000 ;EDGE VALUE
BITIS ;PEAK RESULTS
MOV R5, $BDDAT ;SAVE RESULTS (X DAC + A/D PEAK NOISE, 1=.01 LSB)
SUB CH7PEK, $BDDAT ;X DAC PEAK NOISE ONLY
MOV $BDDAT, XDACPK ;SAVE RESULT
MOV PEKMAX, $GDDAT ;LOAD EXPECTED
CMP $GDDAT, $BDDAT ;COMPARE RESULTS
BGE TST37 ;;BR IF NOISE WITHIN SPEC
ERROR 6 ;X D/A PEAK NOISE IS GREATER
; THAN SPEC

```

H03

MAINDEC-11-DZARC-B
DZARCB.P11 T37

MACY11 27(732) 21-SEP-76 16:44 PAGE 33
Y DAC RMS NOISE TEST ON CHANNEL 6

```

1409          ::*****
1409          :*TEST 37      Y DAC RMS NOISE TEST ON CHANNEL 6
1410          ::*****
1411 006170 000004  †TST37: SCOPE
1412 006172 012737 000010 001164      MOV      #10,$TIMES      ;;DO 10 ITERATIONS
1413
1414 006200 012777 001000 173304      MOV      #1000,2VCYREG      ;LOAD Y REG
1415 006206 004437 010716      JSR      R4,SAR           ;S A R ROUTINE
1416 006212 001510      VCXREG      ;X AXIS
1417 006214      000      006      .BYTE      0.6      ;CHANNEL 6 ,COARSE Y AND FINE X
1418 006216 001000      1000      ;EDGE VALUE
1419 006220 000000      0      ;RMS RESULTS
1420
1421 006222 010537 001126      MOV      R5,$BDDAT      ;SAVE RESULTS (Y DAC + A/D RMS NOISE, 1=.01 LSB)
1422 006226 163737 015130 001126      SUB      CH7RMS,$BDDAT      ;Y DAC RMS NOISE ONLY
1423 006234 013737 001126 015144      MOV      $BDDAT,YDACRM      ;SAVE RESULT
1424 006242 013737 015124 001124      MOV      RMSMAX,$GDDAT      ;LOAD EXPECTED
1425 006250 023737 001124 001126      CMP      $GDDAT,$BDDAT      ;COMPARE RESULTS
1426 006256 002001      BGE      TST40      ::BR IF NOISE WITHIN SPEC
1427 006260 104006      ERRJR   6      ;Y D/A RMS NOISE IS GREATER
1428      ; THAN SPEC
1429
1430          ::*****
1431          :*TEST 40      Y DAC PEAK NOISE TEST ON CHANNEL 6
1432          ::*****
1433 006262 000004  †TST40: SCOPE
1434 006264 012737 000010 001164      MOV      #10,$TIMES      ;;DO 10 ITERATIONS
1435
1436 006272 012777 001000 173212      MOV      #1000,2VCYREG      ;LOAD Y AXIS
1437 006300 004437 010716      JSR      R4,SAR           ;S A R ROUTINE
1438 006304 001510      VCXREG      ;X AXIS
1439 006306      000      006      .BYTE      0.6      ;CHANNEL 6 ,COARSE Y AND FINE X
1440 006310 001000      1000      ;EDGE VALUE
1441 006312 100000      BIT15      ;PEAK RESULTS
1442
1443 006314 010537 001126      MOV      R5,$BDDAT      ;SAVE RESULTS (Y DAC + A/D PEAK NOISE, 1=.01 LSB)
1444 006320 163737 015132 001126      SUB      CH7PEK,$BDDAT      ;Y DAC PEAK NOISE ONLY
1445 006326 013737 001126 015146      MOV      $BDDAT,YDACPK      ;SAVE RESULT
1446 006334 013737 015126 001124      MOV      PEKMAX,$GDDAT      ;LOAD EXPECTED
1447 006342 023737 001124 001126      CMP      $GDDAT,$BDDAT      ;COMPARE RESULTS
1448 006350 002001      BGE      15      ::BR IF NOISE WITHIN SPEC
1449 006352 104006      ERROR    6      ;Y DAC PEAK NOISE IS GREATER
1450      ; THAN SPEC
1451
1452 006354 032777 010000 172554 15:      BIT      #BIT12,2SWR      ;TEST FORCED PRINTOUT
1453 006362 001432      BEQ      TST41      ::BR IF NOT FORCED
1454 006364 104400 013543      TYPE      ,NOIMSG
1455 006370 004537 011466      JSR      R5,BYTW0      ;TYPE 2 OCTAL COL.
1456 006374 015130      CH7RMS
1457 006376 015132      CH7PEK
1458 006400 104400 013664      TYPE      ,NOIMG1
1459 006404 004537 011466      JSR      R5,BYTW0      ;TYPE 2 OCTAL COL.
1460 006410 015134      CH47RM
1461 006412 015136      CH47PK
1462 006414 104400 013707      TYPE      ,NOIMG2
1463 006420 004537 011466      JSR      R5,BYTW0      ;TYPE 2 OCTAL COL.

```

MAINDEC-11-DZARC-B
DZARCB.P11 T40

MAY11 27(732) 21-SEP-76 16:44 PAGE 34
Y DAC PEAK NOISE TEST ON CHANNEL 6

1464	006424	015140		
1465	006426	015142		
1466	006430	104400	013732	
1467	006434	004537	011466	
1468	006440	015144		
1469	006442	015146		
1470	006444	104400	016536	

```

XDACRM
XDACPK
TYPE      NOIMG3
JSR      R5,BY TWO
YDACRM
YDACPK
TYPE      ,ACRLF

```

```

*****
*TEST 41      BIPOLAR A/D OFFSET USING CH 7 WITH WAIT INTERRUPT
*****

```

1475	006450	000004		
1476	006452	012737	000010	001164
1477	006460	004437	010716	
1478	006464	001512		
1479	006466	000	007	
1480	006470	001001		
1481	006472	000200		
1482				
1483	006474	013737	015122	015150
1484	006502	013737	015122	001126
1485	006510	012737	001014	001124
1486	006516	013737	016774	011664
1487	006524	004737	011666	
1488	006530	000401		
1489	006532	104012		

```

†TST41: SCOPE
MOV      #10,$TIMES      ;;DO 10 ITERATIONS
JSR      R4,$AR          ;SOFTWARE S.A.R.
VCYREG
.BYTE    0,CH7           ;CHANNEL 7, FINE Y
1001     ;EDGE 1000/1001 TRANSITION
BIT7     ;50-50

MOV      DACSAV,OFSTBX   ;SAVE A/D OFFSET BIPOLAR
MOV      DACSAV,$BDDAT   ;LOAD VALUE READ
MOV      #1014,$GDDAT    ;LOAD EXPECTED
MOV      V62,$SPREAD     ;LOAD +- VARIABLE, 1LSB
JSR      PC,COMPAR       ;TEST IF WITHIN +- VALUE
BR       TST42           ;;BR IF WITHIN LIMITS
ERROR    12              ;A/D BIPOLAR OFFSET ERROR

```

```

*****
*TEST 42      UNIPOLAR A/D OFFSET USING CH 7 WITH WAIT INTERRUPT
*****

```

1494	006534	000004		
1495	006536	012737	000010	001164
1496	006544	004437	010716	
1497	006550	001512		
1498	006552	000	047	
1499	006554	000001		
1500	006556	000200		
1501				
1502	006560	013737	015122	015152
1503	006566	013737	015122	001126
1504	006574	012737	001014	001124
1505	006602	013737	016774	011664
1506	006610	004737	011666	
1507	006614	000401		
1508	006616	104012		

```

†TST42: SCOPE
MOV      #10,$TIMES      ;;DO 10 ITERATIONS
JSR      R4,$AR          ;SOFTWARE S.A.R.
VCYREG
.BYTE    0,CH47         ;CHANNEL 7, UNIPOLAR FINE Y
1       ;EDGE 0/1 TRANSITION
BIT7     ;50-50

MOV      DACSAV,OFSTUX   ;SAVE A/D OFFSET UNIPOLAR
MOV      DACSAV,$BDDAT   ;LOAD VALUE READ
MOV      #1014,$GDDAT    ;LOAD EXPECTED
MOV      V62,$SPREAD     ;LOAD +- VARIABLE, 1 LSB
JSR      PC,COMPAR       ;TEST IF WITHIN +- VALUE
BR       TST43           ;;BR IF WITHIN LIMITS
ERROR    12              ;A/D UNIPOLAR OFFSET ERROR

```

J03

MAINDEC-11-DZARC-8
DZARCB.P11 T43

MACY11 27(732) 21-SEP-76 16:44 PAGE 35
BIPOLAR A/D OFFSET USING CH 7 WITH WAIT LOOP

```

1509          ::*****
1510          ::*TEST 43      BIPOLAR A/D OFFSET USING CH 7 WITH WAIT LOOP
1511          ::*****
1512 006620 000004          †TST43: SCOPE
1513 006622 012737 000010 001164      MOV      #10,$TIMES      ;;DO 10 ITERATIONS
1514 006630 004437 010716              JSR      R4,$AR          ;SOFTWARE S.A.R.
1515 006634 001512              VCYREG
1516 006636      000      007          .BYTE   0,CH7          ;CHANNEL 7, FINE Y
1517 006640 001001              I001          ;EDGE, 1000/1001 TRANSITION
1518 006642 000201              BIT7!BIT0     ;50-50, WAIT LOOP
1519
1520 006644 013737 015122 015154      MOV      DACSAV,OFSLBX  ;SAVE A/D OFFSET BIPOLAR
1521 006652 013737 015122 001126      MOV      DACSAV,$BDDAT ;LOAD VALUE READ
1522 006660 012737 001014 001124      MOV      #1014,$GDDAT  ;LOAD EXPECTED
1523 006666 013737 016774 011664      MOV      V62,SPREAD    ;LOAD +- VARIABLE, 1 LSB
1524 006674 004737 011666              JSR      PC,COMPARE    ;TEST IF WITHIN +- VALUE
1525 006700 000401              BR       15            ;;BR IF WITHIN LIMITS
1526 006702 104012              ERROR    12           ;A/D BIPOLAR OFFSET ERROR
1527
1528 006704 163737 015150 001126 15:  SUB      OFSTBX,$BDDAT  ;OFFSET DUE TO WAIT LOOP
1529 006712 005037 001124              CLR      $GDDAT
1530 006716 012737 000031 011664      MOV      #31,SPREAD    ;1/2 LSB ALLOWED
1531 006724 004737 011666              JSR      PC,COMPARE    ;WITHIN LIMITS
1532 006730 000401              BR       TST44         ;;BR IF WITHIN LIMITS
1533 006732 104012              ERROR    12           ;A/D BIPOLAR OFFSET ERROR DUE TO WAIT LOOP
1534
1535          ::*****
1536          ::*TEST 44      UNIPOLAR A/D OFFSET USING CH 7 WITH WAIT LOOP
1537          ::*****
1538 006734 000004          †TST44: SCOPE
1539 006736 012737 000010 001164      MOV      #10,$TIMES    ;;DO 10 ITERATIONS
1540 006744 004437 010716              JSR      R4,$AR        ;SOFTWARE S.A.R.
1541 006750 001512              VCYREG
1542 006752      000      047          .BYTE   0,CH47        ;CHANNEL 7, UNIPOLAR, FINE Y
1543 006754 000001              I          ;EDGE, 0/1 TRANSITION
1544 006756 000201              BIT7!BIT0     ;50-50, WAIT LOOP
1545
1546 006760 013737 015122 015156      MOV      DACSAV,OFSLUX ;SAVE A/D OFFSET UNIPOLAR
1547 006766 013737 015122 001126      MOV      DACSAV,$BDDAT ;LOAD VALUE READ
1548 006774 012737 001014 001124      MOV      #1014,$GDDAT  ;LOAD EXPECTED
1549 007002 013737 016774 011664      MOV      V62,SPREAD    ;LOAD +- VARIABLE, 1 LSB
1550 007010 004737 011666              JSR      PC,COMPARE    ;TEST IF WITHIN +- VALUE
1551 007014 000401              BR       15            ;;BR IF WITHIN LIMITS
1552 007016 104012              ERROR    12           ;A/D UNIPOLAR OFFSET ERROR
1553
1554 007020 163737 015152 001126 15:  SUB      OFSTUX,$BDDAT  ;OFFSET DUE TO WAIT LOOP
1555 007026 005037 001124              CLR      $GDDAT
1556 007032 012737 000031 011664      MOV      #31,SPREAD    ;1/2 LSB ALLOWED
1557 007040 004737 011666              JSR      PC,COMPARE    ;WITHIN LIMITS
1558 007044 000401              BR       TST45         ;;BR IF WITHIN LIMITS
1559 007046 104012              ERROR    12           ;A/D UNIPOLAR OFFSET ERROR DUE TO WAIT LOOP

```


K03

MAINDEC-11-DZARC-B
DZARCB.P11 T45

MACY11 27(732) 21-SEP-76 16:44 PAGE 36
X D A OFFSET USING CH 5

```
1560 ::*****
1561 :*TEST 45 X D/A OFFSET USING CH 5
1562 :*****
1563 007050 000004 †ST45: SCOPE
1564 007052 012737 000010 001164 MOV #10, $TIMES ;;DO 10 ITERATIONS
1565 007060 012777 001000 172422 MOV #1000, @VCXREG ;LOAD X REG
1566 007066 004437 010716 JSR R4, SAR ;SOFTWARE S.A.R.
1567 007072 001512 VCYREG ;Y AXIS
1568 007074 000 005 .BYTE 0, CH5 ;CHANNEL 5, COARSE X AND FINE Y
1569 007076 001001 1001 ;EDGE VALUE, 1000/1001 TRANSITION
1570 007100 000200 BIT7 ;50-50
1571 007102 013737 015122 001126 MOV DACSAV, $BDDAT ;SAVE RESULT (X DAC + A/D OFFSET)
1572 007110 163737 015150 001126 SUB OFSTBX, $BDDAT ;OFFSET DUE TO X DAC
1573 007116 005037 001124 CLR $GDDAT ;CLEAR EXPECTED
1574 007122 013737 016776 011664 MOV V77, SPREAD ;TOLERANCE DUE TO X DAC
1575 007130 004737 011666 JSR PC, COMPAR ;TEST IF WITHIN +- LIMITS
1576 007134 000401 BR TST46 ;;BR IF WITHIN LIMITS
1577 007136 104012 ERROR 12 ;X DAC OFFSET ERROR
1578
1579
1580 ::*****
1581 :*TEST 46 Y D/A OFFSET USING CH 6
1582 :*****
1583 007140 000004 †ST46: SCOPE
1584 007142 012737 000010 001164 MOV #10, $TIMES ;;DO 10 ITERATIONS
1585 007150 012777 001000 172334 MOV #1000, @VCYREG ;LOAD Y REG
1586 007156 004437 010716 JSR R4, SAR ;SOFTWARE S.A.R.
1587 007164 000 006 VCXREG ;X AXIS
1588 007166 001001 .BYTE 0, CH6 ;CHANNEL 6, COARSE Y AND FINE X
1589 007170 000200 1001 ;EDGE VALUE, 1000/1001 TRANSITION
1590 007172 013737 015122 001126 MOV DACSAV, $BDDAT ;SAVE RESULT (Y DAC + A/D OFFSET)
1591 007200 163737 015150 001126 SUB OFSTBX, $BDDAT ;OFFSET DUE TO Y DAC
1592 007206 005037 001124 CLR $GDDAT ;CLEAR EXPECTED
1593 007212 013737 016776 011664 MOV V77, SPREAD ;TOLERANCE DUE TO Y DAC
1594 007220 004737 011666 JSR PC, COMPAR ;TEST IF WITHIN +- LIMITS
1595 007224 000401 BR TST47 ;;BR IF WITHIN LIMITS
1596 007226 104012 ERROR 12 ;Y DAC OFFSET ERROR
```

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1599
1600 007230 000004
1601 007232 012737 000010 001164
1602 007240 012777 001300 172242
1603 007246 012737 001600 001124
1604 007254 004537 011534
1605 007260 000011
1606 007262 013737 011656 001126
1607 007270 012737 000003 011664
1608 007276 004737 011666
1609 007302 000401
1610 007304 104013
1611 007306 162777 000100 172174
1612 007314 162737 000200 001124
1613 007322 100354
1614
1615
1616
1617
1618 007324 000004
1619 007326 012737 000010 001164
1620 007334 012777 001300 172150
1621 007342 012737 001600 001124
1622 007350 004537 011534
1623 007354 000012
1624 007356 013737 011656 001126
1625 007364 012737 000003 011664
1626 007372 004737 011666
1627 007376 000401
1628 007400 104013
1629 007402 162777 000100 172102
1630 007410 162737 000200 001124
1631 007416 100354

```

```

*****
*TEST 47 CALIBRATION USING CHANNEL 11 - X DAC VS. A/D
*****
↑ST47: SCOPE
MOV #10, $TIMES ;:DO 10 ITERATIONS
MOV #1300, @VCXREG ;LOAD X DAC
MOV #1600, $GDDAT ;LOAD EXPECTED
JSR R5, CONVRT ;CONVERT 32X AND AVERAGE
CH11
MOV ADEND, $BDDAT ;READ VALUE OBTAINED
MOV #3, $SPREAD ;LOAD DEVIATION
JSR PC, COMPAR ;TEST IF WITHIN
BR 15 ;BR IF WITHIN
ERROR 13 ;CALIBRATION ERROR - X DAC VS. A/D
SUB #100, @VCXREG ;DECREMENT DAC
SUB #200, $GDDAT ;DEC. EXPECTED
BPL 25 ;BR UNTIL DONE

*****
*TEST 50 CALIBRATION USING CHANNEL 12 - Y DAC VS. A/D
*****
↑ST50: SCOPE
MOV #10, $TIMES ;:DO 10 ITERATIONS
MOV #1300, @VCYREG ;LOAD Y DAC
MOV #1600, $GDDAT ;LOAD EXPECTED
JSR R5, CONVRT ;CONVERT 32X AND AVERAGE
CH12
MOV ADEND, $BDDAT ;READ VALUE OBTAINED
MOV #3, $SPREAD ;LOAD DEVIATION
JSR PC, COMPAR ;TEST IF WITHIN
BR 15 ;BR IF WITHIN
ERROR 13 ;CALIBRATION ERROR - Y DAC VS. A/D
SUB #100, @VCYREG ;DECREMENT DAC
SUB #200, $GDDAT ;DEC. EXPECTED
BPL 25 ;BR UNTIL DONE

```

M03

MAINDEC-11-DZARC-B
DZARCB.P11 T51

MACY11 27(732) 21-SEP-76 16:44 PAGE 39
LINEARITY TEST USING CH 6 - Y DAC VS. A/D

```

1632                                     ::*****
1633                                     ;*TEST 51          LINEARITY TEST USING CH 6 - Y DAC VS. A/D
1634                                     ;*****
1635 007420 000004 TST51: SCOPE
1636 007422 012737 000010 001164 MOV #10, $TIMES ;DO 10 ITERATIONS
1637 007430 012700 014702 MOV #NUMBF2, R0 ;LOAD EDGE VALUE PCINTER
1638 007434 012702 014726 MOV #RSLTO, R2 ;LOAD RESULT POINTER
1639
1640 007440 011037 007460 1$: MOV (R0), 10$ ;LOAD EDGE EXPECTED
1641 007444 012077 172042 MOV (R0)+, @VCYREG ;LOAD DAC
1642 007450 004437 010716 JSR R4, SAR ;SOFTWARE S.A.R.
1643 007454 001510 VCYREG
1644 007456 000 006 .BYTE 0, CH6 ;CHANNEL 6, COARSE Y AND FINE X
1645 007460 000000 10$: 0 ;EDGE VALUE
1646 007462 000200 BIT7 ;50-50
1647
1648 007464 013722 015122 MOV DACSAV, (R2)+ ;SAVE RESULTS
1649 007470 005710 TST (R0) ;LAST RESULT?
1650 007472 100362 BPL 1$ ;BR UNTIL DONE
1651 007474 005037 001124 CLR $GDDAT ;ZERO NOMINAL LINEARITY ERROR
1652 007500 013737 016776 011664 MOV V77, SPREAD ;TOLERANCE FOR A/D + DAC
1653 007506 013700 014746 MOV RSLTO+20, R0 ;GET LAST VALUE
1654 007512 163700 014726 SUB RSLTO, R0 ;SUBTRACT FIRST VALUE
1655 007516 006200 ASR R0
1656 007520 006200 ASR R0
1657 007522 006200 ASR R0
1658 007524 005500 ADC R0 ;AVERAGE DIFFERENCE IN R0
1659 007526 012701 000002 MC, #2, R1
1660 007532 005037 014746 CLR RSLTO+20 ;START RUNNING SUM OF ERRORS
1661 007536 060037 014726 2$: ADD R0, RSLTO ;COMPUTE NOM. VALUE ON LINE BETW. END PTS.
1662 007542 163761 014726 014726 SUB RSLTO, RSLTO(R1) ;COMPUTE ERROR WITH RESP. TO END PT. LINE
1663 007550 066137 014726 014746 ADD RSLTO(R1), RSLTO+20 ;KEEP RUNNING SUM
1664 007556 005721 TST (R1)+ ;BUMP R1
1665 007560 020127 000020 CMP R1, #20 ;DONE?
1666 007564 001364 BNE 2$ ;BR IF NOT
1667 007566 006237 014746 ASR RSLTO+20
1668 007572 006237 014746 ASR RSLTO+20
1669 007576 006237 014746 ASR RSLTO+20
1670 007602 005537 014746 ADC RSLTO+20 ;AVERAGE ERROR WITH RESP. TO END PT. LINE
1671 007606 005037 014726 CLR RSLTO
1672 007612 005001 CLR R1
1673 007614 163761 014746 014726 3$: SUB RSLTO+20, RSLTO(R1) ;ERROR WITH RESP. TO "BEST STRAIGHT LINE"
1674
1675 007622 016137 014726 001126 MOV RSLTO(R1), $BDDAT
1676 007630 004737 011666 JSR PC, COMPAR ;COMPARE LINEARITY ERROR WITH + OR - TOLERANCE
1677 007634 000422 BR 4$
1678 007636 010102 MOV R1, R2
1679 007640 042702 177770 BIC #177770, R2 ;MASK BITS 0-2
1680 007644 062702 000060 ADD #60, R2 ;CONVERT TO ASCII
1681 007650 110237 012445 MOVB R2, EM14A
1682 007654 010102 MOV R1, R2 ;LOAD R2
1683 007656 006202 ASR R2
1684 007660 006202 ASR R2
1685 007662 006202 ASR R2
1686 007664 042702 177770 BIC #177770, R2 ;MASK
1687 007670 062702 000060 ADD #60, R2 ;MAKE ASCII

```

N03

MAINDEC-11-DZARC-B
DZARC8.P11

TS1

MACY11 27(732) 21-SEP-76 16:44 PAGE 39
LINEARITY TEST USING CH 6 - Y DAC VS. A/D

```

1688 007674 110237 012444      MOV      R2,EM14B      ;SAVE #
1689 007700 104014      ERROR    14           ;LINEARITY ERROR USING CH 6 - YDAC VS. A/D
1690 007702 005721      4S:     TST      (R1)+    ;BUMP R1
1691 007704 020127 000020      CMP      R1,#20       ;DONE ?
1692 007710 001341      BNE     3S           ;;BR IF NOT DONE
1693
1694
1695 ;:*****
1696 ;:TEST 52      LINEARITY TEST USING CH 5 - X DAC VS. A/D
1697 ;:*****
1697 007712 000004      †ST52:  SCOPE
1698 007714 012737 000010 001164      MOV      #10,STIMES   ;:DO 10 ITERATIONS
1699 007722 012700 014702      MOV      #NUMBF2,R0   ;LOAD EDGE VALUE POINTER
1700 007726 012702 014764      MOV      #RSLT1,R2    ;LOAD RESULT POINTER
1701
1702 007732 011037 007752      1S:     MOV      (R0),10S    ;LOAD EDGE EXPECTED
1703 007736 012077 171546      MOV      (R0)+,2VCXREG ;LOAD DAC
1704 007742 004437 010716      JSR     R4,SAR        ;SOFTWARE S.A.R.
1705 007746 001512
1706 007750      000      005      VCYREG
1707 007752 000000      10S:    .BYTE 0,CH5    ;CHANNEL 5, COARSE & FINE Y
1708 007754 000200      BIT7    ;EDGE VALUE
1709
1710
1711 007756 013722 015122      MOV      DACSAV,(R2)+ ;SAVE RESULTS
1712 007762 005710      TST     (R0)         ;LAST RESULT ?
1713 007764 100362      BPL     1S          ;BR UNTIL DONE
1714 007766 005037 001124      CLR     $CCDAT      ;ZERO NOMINAL LINEARITY ERROR
1715 007772 013737 016776 011664      MOV     V77,SPREAD  ;TOLERANCE FOR A/D + DAC
1716 010000 013700 015004      MOV     RSLT1+20,R0 ;GET LAST VALUE
1717 010004 163700 014764      SUB     RSLT1,R0     ;SUBTRACT FIRST VALUE
1718 010010 006200
1719 010012 006200      ASR     R0
1720 010014 005200      ASR     R0
1721 010016 005500      ASR     R0
1722 010020 012701 000002      ADC     R0
1723 010024 005037 015004      MOV     #2,R1
1724 010030 062037 014764      CLR     RSLT1+20    ;START RUNNING SUM OF ERRORS
1725 010034 163761 014764 014764      ADD     R0,RSLT1    ;COMPUTE NOM. VALUE ON LINE BETW. ENC PTS.
1726 010042 066137 014764 015004      SUB     RSLT1,RSLT1(R1) ;COMPUTE ERROR WITH RESP. TO END PT. LINE
1727 010050 005721 014764 015004      ADD     RSLT1(R1),RSLT1+20 ;KEEP RUNNING SUM
1728 010052 020127 000020      TST     (R1)+       ;BUMP R1
1729 010056 001364      CMP     R1,#20       ;DONE ?
1730 010060 006237 015004      BNE     3S          ;:BR IF NOT
1731 010064 006237 015004      ASR     RSLT1+20
1732 010070 006237 015004      ASR     RSLT1+20
1733 010074 005537 015004      ASR     RSLT1+20
1734 010100 005037 014764      ADC     RSLT1+20    ;AVERAGE ERROR WITH RESP. TO ENC PT. LINE
1735 010104 005001      CLR     RSLT1
1736 010106 163761 015004 014764      CLR     R1
1737 010114 016137 014764 001125      SUB     RSLT1+20,RSLT1(R1) ;ERROR WITH RESP. TO "BEST STRAIGHT LINE"
1738 010122 004737 011666      MOV     RSLT1,R1), $CCDAT
1739 010126 000422      JSR     PC,COMPAR   ;COMPARE LINEARITY ERROR WITH + OR - TOLERANCE
1740 010130 010102      BR     4S
1741 010132 042702 177770      MOV     R1,R2
1742 010136 062702 000060      BIC     #177770,R2  ;MASK BITS 0-2
1743 010142 110237 012445      ADD     #60,R2      ;CONVERT TO ASCII
1744 010146 010102      MOVB   R2,EM14A    ;LOAD R2

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010716 012437 015120
010722 012437 015166
010726 012437 015170
010732 012437 015172
010736 010046
010740 010146
010742 010246
010744 010346
010746 113737 015167 011662
010754 005037 011660
010760 113737 015167 011661
010766 005237 011660
010772 017737 004122 015120
011000 012777 011132 170460
011006 005037 015174
011012 005737 015172
011016 100003
011020 012703 000002
011024 000407
011026 012703 000121 103:
011032 105737 015172
011036 100002
011040 012703 000400
011044 012702 001000 155:
011050 005077 004044
011054 005001 145:
011056 012700 001000
011062 060277 004032
011066 032737 000001 015172 65:
011074 001407
011076 013777 011660 170370
011104 105777 170364 275:
011110 100375
011112 000410
011114 052737 000100 011660 205:
011122 013777 011660 170344
011130 000001
011132 022626 115:
011134 027737 170340 015170 125:
011142 002401
011144 005201
011146 105737 015166 15:
011152 001420

```
:S A R SUBROUTINE
:JSR R4,SAR
:AXIS POINTER
:CH, IN HIGH BYTE / DUMMY CH. IN LOW BYTE
:EDGE VALUE
:RESULT,MODE
:RETURN WITH R5 = DIFFERENCE

:BIT15 = 0      RMS
:BIT15 = 1      PEAK
:BIT7  = 1      SO-50
:BITC  = 1      :WAIT LOOP

SAR:  MOV (R4)+,ADAC      :SAVE DAC ADDRESS POINTER
      MOV (R4)+,SARCHN  :SAVE CHANNEL
      MOV (R4)+,EDGE    :SAVE VALUE EDGE
      MOV (R4)+,CONS    :SAVE RMS/PEAK SW
      MOV R0,-(SP)      :SAVE GPR
      MOV R1,-(SP)
      MOV R2,-(SP)
      MOV R3,-(SP)
      MOVSB SARCHN+1,CHANL
      CLR FCHANL
      MOVSB SARCHN+1,FCHANL+1
      INC FCHANL
      MOV @ADAC,ADAC    :SET START BIT
      MOV @15,SARBYCT  :GET BUS ADDRESS OF ACTIVE DAC
      CLR FINS          :LOAD VECTOR
      TST CONS         :CLEAR 2 PASS COUNTER FLAG
      BPL 10$          :TEST IF RMS
      BR 10$          :BR IF RMS
      MOV @2,R3        :LOAD A = .4% OF 512
      BR 15$
      MOV @121,R3     :LOAD A = 16% OF 512
      TSTB CONS      :TEST FOR SO-50
      BPL 15$
      MOV @400,R3    :LOAD SO-50, A = 50% COUNT
      MOV @1000,R2   :SET UP MSB
      CLR @ADAC     :CLEAR DAC
      CLR R1        :SET HIGH = 0
      MOV @1000,R0  :SET UP 512 CONVERSIONS
      ADD R2,@ADAC  :NEXT BIT OF ACTIVE DAC
      BIT @BITC,CONS :TEST BIT C
      BEQ 20$
      MOV FCHANL,@ADCS :START CONVERSION
      TSTB @ADCS
      BPL 27$
      BR 12$
      BIS @BIT6,FCHANL :SET INTR. ENABLE
      MOV FCHANL,@ADCS :START CONVERSION
      WAIT
      CMP (SP)+,(SP)+ 115:
      CMP @ADCS,EDGE 125:
      BLT 15
      INC R1           :NO, INC HIGH FOR RESULT > OR = EDGE VALUE
      TSTB SARCHN    :TEST IF DUMMY CH. IS SET
      BEQ 35         :BR IF NOT
```

```

1953 011154 012777 011176 170304      MOV      #45, DARBVCT      ;LOAD VECTOR
1954 011162 113777 015166 170306      MOVB     SARCHN, DADCS1   ;LOAD MUX
1955 011170 005277 170300      INC      DADCS           ;CONVERT
1956 011174 000001      WAIT
1957 011176 022636      4$:      CMP      (SP)+, (SP)+     ;POP STACK
1958 011200 027777 170274 170272 13$:     CMP      DADDBR, DADDBR   ;FAKE READ
1959 011206 012777 011132 170252      MOV      #115, DARBVCT   ;RESET VECTOR
1960 011214 005300      3$:      DEC      R0              ;DONE 512 TIMES ?
1961 011216 001323      BNE
1962 011220 020103      CMP      R1, R3          ;IS HIGH > OR = A ?
1963 011222 002402      BLT
1964 011224 160277 003670      SUB      R2, DADAC       ;NO LEAVE BIT ON
1965 011230 022702 000001      2$:      CMP      #1, R2          ;YES TAKE IT OUT
1966 011234 001402      BEQ      #1, R2          ;TEST FOR Z = 1. S.A.R. DONE ?
1967 011236 006202      ASR      R2              ;BR IF DONE
1968 011240 000705      BR       14$            ;NO SET Z /2
1969 011242 017737 003652 015122 21$:     MOV      DADAC, DACSAV   ;TRY AGAIN
1970 011250 005737 015174      TST     FINS            ;FIRST OR SECOND TIME ?
1971 011254 001020      BNE
1972 011256 017705 003636      MOV      DADAC, R5       ;SECOND BR
1973 011262 005237 015174      INC     FINS            ;READ VALUE
1974 011266 005737 015172      TST     CONS            ;SET FLAG
1975 011272 100003      BPL      41$            ;TEST IF PEAK/RMS
1976 011274 012703 000776      MOV      #776, R3        ;BR IF RMS
1977 011300 000405      BR       42$            ;LOAD 99.6% OF 512
1978 011302 012703 000657 41$:      MOV      #657, R3        ;SET A = 84% OF 512
1979 011306 105737 015172 43$:     TSTB    CONS            ;DO MORE TESTING
1980 011312 100404      BMI
1981 011314 000653 42$:      BR       15$            ;SUBTRACT DIFF
1982 011316 167705 003576 45$:     SJB     DADAC, R5
1983 011322 005405      NEG     R5
1984 011324 005077 170144 55$:     CLR     DADCS           ;RETURN WITH 2 X NOISE IN R5
1985 011330 012603      MOV     (SP)+, R3
1986 011332 012602      MOV     (SP)+, R2
1987 011334 012601      MOV     (SP)+, R1
1988 011336 012600      MOV     (SP)+, R0
1989 011340 000204      RTS      R4              ;EXIT

```

.SBTTL MISC. SUBROUTINES

```

;CONVERT R2 INTO DECIMAL DIGITS
1992
1993
1994 011342 J12737 177774 011446 DECPRT: MOV #-4,DIGCNT ;LOAD COUNT
1995 011350 010146 MOV R1,-(SP) ;SAVE R1
1996 011352 012701 011462 MOV #DIGT1-1,R1 ;LOAD POINTER
1997 011356 012737 011452 011450 MOV #DECPNT+2,DECPNT ;LOAD POINTER
1998 011364 012737 177777 011444 1$: MOV #-1,DIGIT ;LOAD #
1999 011372 005237 011444 2$: INC DIGIT ;UPDATE IT
2000 011376 167702 000046 SJB @DECPNT,R2 ;SUB MAGNITUDE
2001 011402 100373 BPL 2$ ;BR IF +
2002 011404 067702 000040 ADD @DECPNT,R2 ;RESTORE
2003 011410 052737 000060 011444 BIS #60,DIGIT ;MAKE A #
2004 011416 113721 011444 MOV#B DIGIT,(R1),+ ;LOAD INTO STRING
2005 011422 005237 011446 INC DIGCNT ;UPDATE COUNT
2006 011425 001002 BNE 3$ ;BR IF NOT DONE
2007 011430 012601 MOV (SP)+,R1 ;RESTORE R1
2008 011432 000207 RTS PC ;EXIT
2009 011434 062737 000002 011450 3$: ADD #2,DECPNT ;UPDATE POINTER
2010 011442 000750 BR 1$ ;BR BACK

2011
2012 011444 000000 DIGIT: 0
2013 011446 000000 DIGCNT: 0
2014 011450 011452 DECPNT: .+2
2015 011452 001750 1000.
2016 011454 000144 100.
2017 011456 000012 10.
2018 011460 000001 1.

2019
2020 011462 000 DIGT0: .BYTE 0
2021 011463 000 DIGT1: .BYTE 0
2022 011464 000 DIGT2: .BYTE 0
2023 011465 000 DIGT3: .BYTE 0

2024
2025 011466 013546 BYTWO: MOV @ (R5)+,-(SP) ;SAVE ON STACK
2026 011470 104402 TYPOS
2027 011472 004 CO1 .BYTE 4,1
2028 011474 104400 014541 TYPE MULTSP
2029 011500 013546 MOV @ (R5)+,-(SP) ;SAVE ON STACK
2030 011502 104402 TYPOS
2031 011504 004 CO1 .BYTE 4,1
2032 011506 000205 RTS R5 ;EXIT

2033
2034 011510 012500 TYPSTG: MOV (R5)+,R0 ;GET ADDRESS POINTER
2035 011512 012501 MOV (R5)+,R1 ;LOAD # OF LOC.
2036 011514 012046 1$: MOV (R0)+,-(SP) ;SAVE ON STACK
2037 011516 104402 TYPOS
2038 011520 004 CO1 .BYTE 4,1
2039 011522 104400 016543 TYPE SP3MSG
2040 011526 005301 DEC R1 ;DONE
2041 011530 001371 BNE 1$
2042 011532 000205 2$: RTS R5 ;EXIT

```

2043 ;SUBROUTINE TO GET AVERAGE OF 32 CONVERSIONS

```

2044
2045 011534 012537 011652 CONVRT: MOV (R5)+,10$
2046 011540 013737 011652 011662 MOV 10$,CHANL
2047 011546 000337 011652 SLAB 10$
2048 011552 C12737 000040 011654 MOV #32,11$
2049 011560 005037 011656 CLR ADEND
2050 011564 013777 011652 167702 MOV 10$,2ADCS
2051 011572 005277 167676 2$: INC 2ADCS
2052 011576 105777 167672 1$: TSTB 2ADCS
2053 011602 100375 BPL 1$
2054 011604 067737 167670 011656 ADD 2ADDBR,ADEND
2055 011612 003337 011654 DEC 11$
2056 011616 001365 BNE 2$
2057 011620 006237 011656 ASR ADEND
2058 011624 006237 011656 ASR ADEND
2059 011630 006237 011656 ASR ADEND
2060 011634 006237 011656 ASR ADEND
2061 011640 006237 011656 ASR ADEND
2062 011644 005537 011656 ADC ADEND
2063 011650 000205 RTS RS ;ROUND UP

```

```

2065 011652 000000 10$: 0
2066 011654 000000 11$: 0
2067 011656 000000 ADEND: 0
2068 011660 000000 FCHANL: 0
2069 011662 000000 CHANL: 0
2070 011664 000000 SPREAD: 0

```

```

2072 011666 010046 COMPAR: MOV R0,-(SP)
2073 011670 010146 MOV R1,-(SP)
2074 011672 013700 001124 MOV $GDAT,R0 ;LOAD R0
2075 011676 013701 001126 MOV $BCDAT,R1 ;LOAD R1
2076 011702 160100 SUB R1,R0 ;SUBTRACT
2077 011704 100001 BPL 2$ ;
2078 011706 005400 NEG R0 ;
2079 011710 020037 011664 8$: CMP R0,SPREAD ;MAGNITUDE OF DIFF. IN RC
2080 011714 003405 BLE 10$
2081 011716 012601 9$: MOV (SP)+,R1
2082 011720 012600 MOV (SP)+,R0
2083 011722 062716 000002 ADD #2,(SP)
2084 011726 000207 RTS PC

```

```

2086 011730 012601 10$: MOV (SP)+,R1
2087 011732 012600 MOV (SP)+,R0
2088 011734 000207 RTS PC
2089 .SBTTL ASCII MESSAGES AND ERROR POINTERS

```


2090					
2091	011736	051105	047522	020122	EM1: .ASCIZ \ERROR ON A/D CHANNEL\
2092	011744	047117	040440	042057	
2093	011752	041440	040510	047116	
2094	011760	046105	000		
2095	011763	126	020103	047514	EM2: .ASCIZ /VC LOGIC SIGNAL HIGH OUTPUT TOO LOW/
2096	011770	044507	020103	044523	
2097	011776	047107	046101	044040	
2098	012004	043511	020110	052517	
2099	012012	050124	052125	052040	
2100	012020	047517	046040	053517	
2101	012026	000			
2102	012027	126	020103	052123	EM3: .ASCIZ /VC STATUS REGISTER IN ERROR/
2103	012034	052101	051525	051040	
2104	012042	043505	051511	042524	
2105	012050	020122	047111	042440	
2106	012056	051122	051117	000	
2107	012063	105	052130	051105	EM4: .ASCIZ /EXTERNAL A TO D START FAILED/
2108	012070	040516	020114	020101	
2109	012076	047524	042040	051440	
2110	012104	040524	052122	043040	
2111	012112	044501	042514	000104	
2112	012120	027501	020104	044504	EM5: .ASCIZ \A/D DIFFERENTIAL LINEARITY ERROR\
2113	012126	043106	051105	047105	
2114	012134	044524	046101	046040	
2115	012142	047111	040505	044522	
2116	012150	054524	042440	051122	
2117	012156	051117	000		
2118	012161	116	044517	042523	EM6: .ASCIZ /NOISE LEVEL EXCEEDED LIMIT/
2119	012166	046040	053105	046105	
2120	012174	042440	041530	042505	
2121	012202	042504	020104	044514	
2122	012210	044515	000124		
2123	012214	041526	046040	043517	EM7: .ASCIZ /VC LOGIC SIGNAL LOW OUTPUT TOO HIGH/
2124	012222	041511	051440	043511	
2125	012230	040516	020114	047514	
2126	012236	020127	052517	050124	
2127	012244	052125	052040	047517	
2128	012252	044040	043511	000110	
2129	012260	027504	020101	044504	EM10: .ASCIZ \D/A DIFFERENTIAL LINEARITY ERROR\
2130	012266	043106	051105	047105	
2131	012274	044524	046101	046040	
2132	012302	047111	040505	044522	
2133	012310	054524	042440	051122	
2134	012316	051117	000		
2135	012321	101	042057	044440	EM11: .ASCIZ \A/D INTER-CHANNEL SETTLING ERROR\
2136	012326	052116	051105	041455	
2137	012334	040510	047116	046105	
2138	012342	051440	052105	046124	
2139	012350	047111	020107	051105	
2140	012356	047522	000122		
2141	012362	043117	051506	052105	EM12: .ASCIZ /OFFSET ERROR/
2142	012370	042440	051122	051117	
2143	012376	000			
2144	012377	103	046101	041111	EM13: .ASCIZ /CALIBRATION ERROR/
2145	012404	040522	044524	047117	

K04

2146	012412	042440	051122	051117						
2147	012420	000								
2148	012421	114	047111	040505	EM14:	.ASCII	/LINEARITY ERROR AT /			
2149	012426	044522	054524	042440						
2150	012434	051122	051117	040440						
2151	012442	020124								
2152	012444	060			EM14B:	.BYTE	60			
2153	012445	060	060	060	EM14A:	.BYTE	60,60,60,0			
2154	012450	000								
2155	012451	105	051122	041520	DH1:	.ASCIZ	/ERRPC ARADD CHANL NOMINAL SPREAD ACTUAL/			
2156	012456	020040	040440	040522						
2157	012464	042104	020040	041440						
2158	012472	040510	046116	020040						
2159	012500	047040	046517	047111						
2160	012506	046101	051440	051120						
2161	012514	040505	020104	040440						
2162	012522	052103	040525	000114						
2163	012530	051105	050122	020103	DH2:	.ASCIZ	/ERRPC ARADD CHANL 3V LEV. OUTPUT/			
2164	012536	020040	051101	042101						
2165	012544	020104	020040	041440						
2166	012552	040510	046116	020040						
2167	012560	053063	046040	053105						
2168	012566	020056	052517	050124						
2169	012574	052125	000							
2170	012577	105	051122	041520	DH3:	.ASCIZ	ERRPC ARADD GOOD BAD/			
2171	012604	020040	040440	040522						
2172	012612	042104	020040	020040						
2173	012620	047507	042117	020040						
2174	012626	020040	040502	000104						
2175	012634	051105	050122	020103	DH5:	.ASCIZ	/ERRPC ARADD # OF ST. ALLOWED/			
2176	012642	020040	051101	042101						
2177	012650	020104	021440	047440						
2178	012656	020106	052123	020056						
2179	012664	046101	047514	042527						
2180	012672	000104								
2181	012674	051105	050122	020103	DH6:	.ASCIZ	/ERRPC ARADD LIMIT MEASURED/			
2182	012702	020040	051101	042101						
2183	012710	020104	020040	044514						
2184	012716	044515	020124	020040						
2185	012724	042515	051501	051125						
2186	012732	042105	000							
2187	012735	105	051122	041520	DH7:	.ASCIZ	/ERRPC ARADD A/D CH. .4 LEV OUTPUT\			
2188	012742	020040	040440	040522						
2189	012750	042104	020040	040440						
2190	012756	042057	041440	027110						
2191	012764	027040	020064	042514						
2192	012772	020126	047440	052125						
2193	013000	052520	000124							
2194	013004	051105	050122	020103	DH10:	.ASCIZ	/ERRPC ARADD STEP NOMINAL SPREAD ACTUAL\			
2195	013012	020040	051101	042101						
2196	013020	020104	020040	051440						
2197	013026	042524	020120	020040						
2198	013034	047516	044515	040516						
2199	013042	020114	050123	042522						
2200	013050	042101	020040	041501						
2201	013056	052524	046101	000						

Line	Address	Code	Value	Value	Value	Value	Value	Value	Value
2202	013063	105	051122	041520	DH11:	.ASCIZ	/ERRPC	ARADD	NOMINAL SPREAD ACTUAL/
2203	013070	020040	040440	040522					
2204	013076	042104	020040	047040					
2205	013104	046517	047111	046101					
2206	013112	051440	051120	040505					
2207	013120	020104	040440	052103					
2208	013126	040525	000114						
2209	013132	005015	020111	044502	BIASST:	.ASCIZ	<15><12>/I	BIAS = /	
2210	013140	051501	036440	000040					
2211	013146	024040			BIASDN:	.ASCII	/ (/		
2212	013150	027062			BASBT1:	.ASCII	/2./		
2213	013152	020060	044515	051103	BASBT2:	.ASCII	/0 MICROAMP LIMIT = /		
2214	013150	040517	050115	046040					
2215	013166	046511	052111	036440					
2216	013174	040							
2217	013175	065	024460	005015	BASBT3:	.ASCIZ	/50)/<15><12>		
2218	013202	000							
2219	013203	015	005012	027504	DIFMSG:	.ASCII	<15><12><12>\D/A DIFFERENTIAL LINEARITY\		
2220	013210	020101	044504	043106					
2221	013216	051105	047105	044524					
2222	013224	046101	046040	047111					
2223	013232	040505	044522	054524					
2224	013240	005015	052123	050105	.ASCII	<15><12>\STEP 0/1	1/2	3/4	7/10 17/20 37/40\
2225	013246	020040	030457	020040					
2226	013254	020040	027461	020062					
2227	013262	020040	031440	032057					
2228	013270	020040	020040	027467					
2229	013276	030061	020040	030440					
2230	013304	027467	030062	020040					
2231	013312	033463	032057	060					
2232	013317	040	033467	030457	.ASCII	\ 77/100 177/200 377/400 777/1000\			
2233	013324	030060	030440	033467					
2234	013332	031057	030060	031440					
2235	013340	033467	032057	030060					
2236	013346	033440	033467	030457					
2237	013354	030060	060						
2238	013357	015	020012	020130	.ASCIZ	<15><12>\ X			
2239	013364	020040	000						
2240	013367	015	020012	020131	DIYMSG:	.ASCIZ	<15><12>/ Y		
2241	013374	020040	000						
2242	013377	015	024012	047516	DIEMSG:	.ASCIZ	<15><12>/<NOMINAL STEP HEIGHT = 62><15><12>		
2243	013404	044515	040516	020114					
2244	013412	052123	050105	044040					
2245	013420	044505	044107	020124					
2246	013426	020075	031066	006451					
2247	013434	000012							
2248	013436	005015	027501	020104	ADSMMSG:	.ASCII	<15><12>\A/D INTER-CHANNEL SETTLING (1 LSB = 62)\		
2249	013444	047111	042524	026522					
2250	013452	044103	047101	042516					
2251	013460	020114	042523	052124					
2252	013466	044514	043516	024040					
2253	013474	020061	051514	020102					
2254	013502	020075	031066	051					
2255	013507	015	050012	051517	.ASCIZ	<15><12>/POSITIVE = /			
2256	013514	052111	053111	020105					
2257	013522	020075	000						

2314	014170	020104	044504	043106	
2315	014176	051105	047105	044524	
2316	014204	046101	046040	047111	
2317	014212	040505	044522	054524	
2318	014220	006472	000012		
2319	014224	052123	052101	026505	ADDIF: .ASCIZ STATE-WIDTH<(15)>(12)
2320	014232	044527	052104	006510	
2321	014240	000012			
2322	014242	034050	020051	045523	SKPMSG: .ASCIZ (8) SKIPPED STATE(S)<(15)>(12)
2323	014244	050111	042520	020104	
2324	014250	052123	052101	024105	
2325	014256	024523	005015	000	
2326	014264	0250	024470	037040	GRTMSG: .ASCIZ (8) > 2 LSB STATE(S)<(15)>(12)
2327	014271	031040	046040	041123	
2328	014280	051440	040524	042524	
2329	014312	051450	006451	000012	
2330	014320	034050	020051	020074	NARMSG: .ASCIZ (8) < 1/2 LSB NARROW STATE(S)<(15)>(12)
2331	014326	027461	020062	051514	
2332	014334	020102	040516	051122	
2333	014342	052517	051440	040524	
2334	014350	042524	051450	006451	
2335	014356	000012			
2336	014360	034050	02	020076	WIDMSG: .ASCIZ (9) 1 1/2 LSB WIDE STATE(S)<(15)>(12)
2337	014366	020051	0207	020062	
2338	014380	051514	020111	044527	
2339	014402	042524	051440	040524	
2340	014410	042524	051450	006451	
2341	014416	000012			
2342	014424	054056	054056	020045	FEH_F: .ASCIZ \XX.X% OF STATES WITHIN 1/2 LSB (SPEC = 95%)<(15)>(12)
2343	014432	043117	051440	040524	
2344	014440	042524	020123	044527	
2345	014448	044111	047111	030440	
2346	014456	031040	046040	041123	
2347	014464	051450	051450	042520	
2348	014472	020076	032771	000	
2349	014480	005015	000		
2350	014488	027130	022530		PERFRT: .ASCIZ \XX.XX OF STATES WITHIN 1/4 LSB <(15)>(12)
2351	014496	020106	052123		
2352	014504	051505	053440		
2353	014512	044510	020116		
2354	014520	020064	051514		
2355	014528	005015	000		
2356	014536	020040	020040		MULTSP: .ASCIZ /
2357	014544	020040	020040		
2358	014552	020040	020040		
2359	014560	020040	020040		
2360	014568	020040	020040		
2361	014576	001116	001464	011662	DT1: .EVEN SERRPC,ARBADD,CHANL,SGDOAT,SPREAD,SBOCAT,0
2362	014584	001126	011664	001126	
2363	014592	000000			
2364	014600	001116	001464	011662	DT2: SERRPC,ARBADD,CHANL,SGDOAT,SBOCAT,0
2365	014608	001126	001126	000000	
2366	014616	001116	001464	001124	DT3: SERRPC,ARBADD,SGDOAT,SBOCAT,0
2367	014624	001126	000000		
2368	014632	001116	001464	001126	DT5: SERRPC,ARBADD,SBOCAT,SGDOAT,0
2369	014640	001116	001464	001126	


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012537 016640 DIFLIN: MOV (R5)+,VCREG1 ;LOAD COARSE REGISTER ADDRESS
012537 016642 MOV (R5)+,VCREG2 ;LOAD FINE ADDRESS
012537 016644 MOV (R5)+,DIFCHN ;LOAD CHANNEL
010546 MOV R5, -(SP)
005077 164244 CLR JVCSTAT
017737 001272 016640 MOV JVCREG1,VCREG1 ;GET BUS ADDRESS
017737 001366 016642 MOV JVCREG2,VCREG2
012777 000000 001354 MOV #0,JVCREG1 ;CLEAR COARSE
012777 000000 001350 MOV #0,JVCREG2 ;CLEAR FINE
012701 022040 MOV #ADBUFF,%1
005000 CLR %0
005021 15: CLR (1)+ ;CLEAR MEMORY
022701 024504 CMP #LAST,%1
001374 BNE 15

015310 004737 016340 CONVR: JSR PC,CONVT
015314 001410 BEQ 25 ;BRANCH IF RESULT = 0
015316 020500 CMP R5,RO ;COMPARE RESULT AND POINT
015320 100406 BMT 25 ;BRANCH FOR RESULT < POINT
015322 105265 022040 INCB ADBUFF(%5) ;INCREMENT CONTENTS OF 2
015326 103003 BCC 25
015330 112765 000377 022040 MOVB #377,ADBUFF(R5)
015336 062777 000001 001276 25: ADD #1,JVCREG2 ;UPDATE FINE REG.
015344 005777 TST JVCREG2
015350 001357 BNE CONVR ;BRANCH UNTIL FINISH
015352 022777 001774 001260 CMP #1774,JVCREG1 ;TEST COARSE REG.
015356 001422 BEQ READ
015360 062777 000014 001250 ADD #14,JVCREG1 ;UPDATE COARSE REG.
015370 005077 001246 CLR JVCREG2 ;CLEAR FINE REG.
015374 004737 016340 JSR PC,CONVT
015400 062705 000004 ADD #4,R5 ;(4+RESULT) IN R5
010500 MOV #5,%0 ;LOAD POINT
062705 022040 ADD #ADBUFF,%5
012704 000010 MOV #10,%4
15: CLR B(R5)+ ;CLEAR B. MEM BYTE LOCATIONS
005025 DEF 2
005024 BNE 15
000721 BR CONVR
032777 010000 163502 READ: BIT #BIT12,DSWR
001402 BEQ 135
104400 014163 TYPE ADDIFM
135: CLR RO
012702 001776 MOV #1776,%2
012703 022041 MOV #ADBUFF+1,%3
012737 000001 015216 MOV #1,TEMP
005037 015206 CLR QRTOK
005037 015742 CLR DIFERR
005037 015210 CLR NARROW
005037 015212 CLR WIDE
005037 015736 CL 205
005037 015740 CLR SKIPST
005037 015744 CLR EXCESS
25: MOVB (R3),RO ;TEST IF STATE WIDTH > 2LSB
020027 000144 CMP RO,#144
003034 BNE 15

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015526 105260 024102 INCB ABUFF4(RO) :UPDATE STATE-WIDTH BUFFER
015528 020037 015202 CMP RO,HILIM1 :TEST IF STATE WIDTH > 1 1/2 LSB
015530 003021 BGT 3$ :YES BR
015532 020037 015204 CMP RO,HILIM2 :TEST IF STATE WIDTH > 1 1/4 LSB
015534 003061 BGT 4$ :YES BR
015536 020037 017000 CMP RO,VI :IS IT A SKIPPED STATE?
015538 103416 SLO 10$ :YES BR
015540 020037 015176 CMP RO,LOLIM1 :TEST IF STATE WIDTH < 1/2 LSB
015542 002415 BLT 11$ :YES BR
015544 020037 015200 CMP RO,LOLIM2 :TEST IF STATE WIDTH < 3/4 LSB
015546 002450 BLT 4$ :YES BR
015548 005237 015206 INC JRTCK :STATE WIDTH BETWEEN 3/4 AND 1 1/4 LSB
015550 003445 BR 4$
015552 005237 015744 1$: INC EXCESS :UPDATE > 2 LSB WIDE STATE COUNT
015554 005237 015212 3$: INC WIDE :UPDATE > 1 1/2 LSB WIDE STATE COUNT
015556 000406 BR 12$
015558 005237 015740 10$: INC SKIPST :UPDATE SKIPPED STATE COUNT
015560 105700 11$: TSTB RO
015562 100767 BMI 1$
015564 005237 015200 INC NARROW :UPDATE < 1/2 LSB NARROW STATE COUNT
015566 005237 015742 12$: INC DIFERR :UPDATE OUTSIDE + OR - 1/2 LSB COUNT
015568 022777 010000 5$: BIT #BIT12,3SWR :TEST FOR FORCED TYPEOLT
015570 001424 BEQ 4$ ::BR IF NOT
015572 005737 015736 TST 20$ :TEST IF FIRST TIME ?
015574 001004 BNE 6$ :BR IF YES
015576 005237 015736 INC 20$
015578 104400 TYPE ADDIF
015580 012746 6$: MOV TEMP,-(SP) :SAVE CURRENT STATE
015582 104402 TYPOS
015584 004 .BYTE 4,1
015586 104400 016552 TYPE .TYANXX
015588 042700 177400 BIC #177400,RO
015590 010046 MOV RO,-(SP)
015592 104402 TYPOS
015594 003 .BYTE 3,1
015596 104400 016536 TYPE ACRLF
015598 005237 015216 4$: INC TEMP
015600 005723 TSTB (R3)+
015602 005302 DEC %2
015604 001276 BNE 2$
015606 032777 010000 163206 BIT #BIT12,3SWR :TEST BIT 12
015608 001006 BNE SWDIST :BR IF FORCED PRINTOUT
015610 012605 MOV (SP)+,R5
015612 000205 RTS R5
015614 000000 20$:
015616 000000 SKIPST: 0
015618 000000 DIFERR: 0
015620 000000 EXCESS: 0
015622 012703 024102 SWDIST: MOV #ABUFF4,:3
015624 005037 015216 CLR TEMP
015626 104400 016536 TYPE ACRLF
015628 012746 015740 MOV SKIPST,-(SP) :SAVE SKIPPED STATES
015630 104402 TYPOS
015632 003 .BYTE 3,0
015634 104400 014242 TYPE .SKPMMSG

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2546	015776	013746	015744		MOV	EXCESS,-(SP)		;SAVE EXCESSIVE WIDE STATES
2547	016002	104402			TYPOS			
2548	016004	003	000		.BYTE	3,0		
2549	016006	104400	014271		TYPE	,GRT2MG		
2550	016012	013746	015210		MOV	NARROW,-(SP)		
2551	016016	104402			TYPOS			
2552	016020	003	000		.BYTE	3,0		
2553	016022	104400	014320		TYPE	,NARMSG		
2554	016026	013746	015212		MOV	WIDE,-(SP)		;SAVE WIDE STATES
2555	016032	104402			TYPOS			
2556	016034	003	000		.BYTE	3,0		
2557	016036	104400	014360		TYPE	,WIDMSG		
2558	016042	012700	001776		MOV	#1022, R0		
2559	016046	163700	015212		SUB	WIDE, R0		
2560	016052	163700	015210		SUB	NARROW, R0		
2561	016056	004537	017044		JSR	RS, PRCNT		;CONVERT TO PERCENT
2562	016062	014420			PERHLF			
2563	016064	104400	014420		TYPE	,PERHLF		
2564	016070	013700	015206		MOV	ORTOK, R0		
2565	016074	004537	017044		JSR	RS, PRCNT		
2566	016100	014477			PERQRT			
2567	016102	104400	014477		TYPE	,PERQRT		
2568	016106	104400			TYPE			
2569	016110	016554			STDMSG			
2570	016112	013702	015216		MOV	TEMP, R2		
2571	016116	006302		65:	ASL	R2		
2572	016120	004737	011342		JSR	PC, DECPRT		;CONVERT R2 TO DEC.
2573	016124	113737	011463	016400	MOVB	DIGT1, RSV1		
2574	016132	113737	011464	016402	MOVB	DIGT2, RSV2		
2575	016140	113737	011465	016403	MOVB	DIGT3, RSV3		
2576								
2577	016146	111302			MOVB	(R3), R2		
2578	016150	004737	011342		JSR	PC, DECPRT		;CONVERT R2 TO DEC.
2579	016154	113737	011462	016405	MOVB	DIGT0, RSX1		
2580	016162	113737	011463	016406	MOVB	DIGT1, RSX2		
2581	016170	113737	011464	016407	MOVB	DIGT2, RSX3		
2582	016176	113737	011465	016410	MOVB	DIGT3, RSX4		
2583	016204	104400			TYPE			
2584	016206	016376			RSVMSG			
2585	016210	111305			MOVB	(%3), R5		
2586	016212	005305		25:	DEC	R5		
2587	016214	100403			BM:	15		
2588	016216	104400			TYPE			
2589	016220	016541			TYANX			
2590	016222	000773			BR	25		
2591	016224	023737	015176	015216	15:	LOLIM1, TEMP		
2592	016232	001413			BEQ	45		
2593	016234	023737	015202	015216	CMF	HILIM1, TEMP		
2594	016242	001412			BEQ	55		
2595	016244	023737	015214	015216	CMF	AMEAN, TEMP		
2596	016252	001010			BNE	35		
2597	016254	104400	016472		TYPE	,TYMEAN		
2598	016260	000405			BR	35		
2599	016262	104400	016414		45:	TYLOW		
2600	016266	000402			BR	35		
2601	016270	104400	016442		55:	,TYHIGH		

2602	016274	005237	015216	35:	INC	TEMP	
2603	016300	105723			TSTB	(3)+	
2604	016302	022737	000145 015216		CMP	#145,TEMP	
2605	016310	001300			BNE	65	
2606	016212	104400			TYPE		
2607	016314	016536			ACRLF		
2608	016316	104400	016516		TYPE	OVERNG	
2609	016322	013746	015744		MOV	EXCESS,-(SP)	
2610	016326	104402			TYPOS		
2611	016330	004	001		.BYTE	4,1	
2612	016332	012605			MOV	(SP)+,R5	
2613	016334	000205			RTS	R5	
2614							
2615	016336	000000		125:	0		
2616							
2617	016340	005277	163142	CONVT:	INC	QVCSTAT	
2618	016344	105777	163136	25:	TSTB	QVCSTAT	
2619	016350	100375			BPL	25	
2620	016352	005005			CLR	%5	
2621	016354	013777	016644 163112		MOV	DIFCHN,QADCS	;CONVERT
2622	016362	105777	163106	15:	TSTB	QADCS	;WAIT FOR DONE
2623	016366	100375			BPL	15	
2624	016370	017705	163104		MOV	QADCBP,R5	;READ VALUE
2625	016374	000207			RTS	PC	
2626							
2627	016376	015	012	RSVMSG:	.BYTE	15,12	
2628	016400	060	056	RSV1:	.BYTE	60,56	
2629	016402	060		RSV2:	.BYTE	60	
2630	016403	060	055	RSV3:	.BYTE	60,55	
2631	016405	060		RSX1:	.BYTE	60	
2632	016406	060		RSX2:	.BYTE	60	
2633	016407	060		RSX3:	.BYTE	60	
2634	016410	060	040 111	RSX4:	.BYTE	60,40,111,0	
2635	016412	000					
2636	016414	026455	026455 026455	TYLOW:	.ASCIZ	"----- (1/2 LSB)"	
2637	016422	026455	026455 020040				
2638	016430	030450	031057 046040				
2639	016436	041123	000051				
2640	016442	026455	026455 026455	TYHIGH:	.ASCIZ	"----- (1 1/2 LSB)"	
2641	016450	026455	026455 020040				
2642	016456	030450	030440 031057				
2643	016464	046040	041123 000051				
2644	016472	026455	026455 026455	TYMEAN:	.ASCIZ	"----- (1 LSB)"	
2645	016500	026455	026455 020040				
2646	016506	030450	046040 041123				
2647	016514	000051					
2648	016516	052517	020124 043117	OVERNG:	.ASCIZ	/OUT OF RANGE -	
2649	016524	051040	047101 042507				
2650	016532	026440	000040				
2651	016536	015	012 000	ACRLF:	.BYTE	15,12,0	
2652	016541	052	000	TYANX:	.BYTE	52,0	
2653	016543	040	020040 000	SP3MSG:	.ASCIZ	/	
2654	016547	000	000 000		.BYTE	0,0,0	
2655	016552	055	000	TYANXX:	.BYTE	55,0	
2656	016554	015	012	STCMMSG:	.BYTE	15,12	
2657	016556	052123	052101 026505		.ASCII	/STATE-WIDTH DISTRIBUTION	

```

2658 016564 044527 052104 020110
2659 016572 044504 052123 044522
2660 016600 052502 044524 047117
2661 016606 015 012
2662 016610 044527 052104 026510
2663 016616 052516 041115 051105
2664 016624 047440 020106 052123
2665 016632 052101 051505 000
2666 016640 000000
2667 016640 000000
2668 016642 000000
2669 016644 000000
2670
2671
2672 016646 012701 016764
2673 016652 005737 016762
2674 016656 001017
2675 016660 012702 017004
2676 016664 112737 000062 013150
2677 016672 112737 000060 013152
2678 016700 112737 000065 013175
2679 016706 112737 000060 013176
2680 016714 000416
2681 016716 012702 017022
2682 016722 112737 000061 013150
2683 016730 112737 000070 013152
2684 016736 112737 000064 013175
2685 016744 112737 000064 013176
2686 016752 012221
2687 016754 005711
2688 016756 100375
2689 016760 000207
2690
2691 016762 000000
2692
2693 016764 001754
2694 016766 000024
2695 016770 000050
2696 016772 000024
2697 016774 000062
2698 016776 000077
2699 017000 000001
2700 017002 100000
2701
2702 017004 001754
2703 017006 000024
2704 017010 000050
2705 017012 000024
2706 017014 000062
2707 017016 000077
2708 017020 000001
2709
2710 017022 001760
2711 017024 000020
2712 017026 000040
2713 017030 000022

```

```

.BYTE 15,12
.ASCIZ WIDTH-NUMBER OF STATES/

```

```

.EVEN
VCREG1: 0
VCREG2: 0
DIFCHN: 0

```

```

.SBTTL PARAMETER ADJUSTMENT ROUTINE
WFADJ: MOV #V1754,R1 ;LOAD PARM. POINTER
TST WFTST ;TEST IF OPTION TEST AREA
BNE IS ;BR IF IT WAS
MOV #VARL1,R2 ;LOAD "STANDARD" PARM. VALUES
MOVB #'2,BASBT1 ;LOAD BIAS MESSAGE
MOVB #'0,BASBT2
MOVB #'5,BASBT3 ;LOAD BIAS LIMIT
MOVB #'0,BASBT3+1
BR 2S
1S: MOV #VARL2,R2 ;LOAD "OPTION TEST AREA" PARM. VALUES
MOVB #'1,BASBT1 ;LOAD BIAS MESSAGE
MOVB #'8,BASBT2
MOVB #'4,BASBT3 ;LOAD BIAS LIMIT
MOVB #'4,BASBT3+1
2S: MOV (R2)+,(R1)+ ;LOAD INTO PARM. LIST
TST (R1) ;TEST FOR LAST
BPL 2S ;BR IF NOT
RTS PC ;EXIT

```

```

WFTST: 0
V1754: 1754 ;1760 IF OPTION TEST AREA SELECTED
V24: 24 ;20
V50: 50 ;40
VAR24: 24 ;20
V62: 62 ;40
V77: 77 ;40
V1: 1 ;15
BIT15 RM.

```

```

VARL1: 1754
24
50
24
62
77
1
VARL2: 1760
20
40
22

```

```

017032 000045 45
017034 000045 45
017036 000015 15
      .SBTTL SUBROUTINE TO CONVERT R0 TO DECIMAL PERCENTAGE OF 1022.
017040 000000 MSGPNT: 0
017042 000000 PCT: 0
017044 012537 017040 PRCNT: MOV (R5)+,MSGPNT ;GET MESSAGE POINTER
017050 010037 017042 MOV R0,PCT ;
017054 006200 ASR R1 ;
017056 006200 ASR R0 ;
017060 006200 ASR R0 ;
017062 006200 ASR R0 ;
017064 006200 ASR R0 ;
017066 006200 ASR R0 ;
017070 160037 017042 SUB R0,PCT ;
017074 006200 ASR R0 ;
017076 160037 017042 SUB R0,PCT ;PCT CONTAINS OCTAL %
017102 013702 017042 MOV PCT,R2 ;LOAD R2 WITH PERCENTAGE
017106 004737 011342 JSR PC,DECPRT ;CONVERT TO DEC.
017112 013700 017040 MOV MSGPNT,R0 ;LOAD MSG. POINTER
017116 022737 001750 017042 CMP #1000.,PCT ;TEST FOR 100 %
017124 001411 BEQ 15 ;BR IF 100 %
017126 113720 011463 MOVB DIGT1,(R0)+ ;LOAD A DIGIT
017132 113720 011464 MOVB DIGT2,(R0)+ ;
017136 112720 000056 MOVB #56,(R0)+ ;LOAD "."
017142 113720 011465 MOVB DIGT3,(R0)+ ;LOAD 1/10 % DIGIT
017146 000205 RTS R5 ;EXIT

017150 112720 000040 15: MOVB #40,(R0)+ ;LOAD "SPACE"
017154 112720 000061 MOVB #'1,(R0)+ ;LOAD '1' 100%
017160 112720 000060 MOVB #'0,(R0)+ ;
017164 112720 000060 MOVB #'0,(R0)+ ;
017170 000205 RTS R5 ;EXIT

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2800
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017172
017172 104405
017174 032777 040000 161734
017202 001114

017204 000416

017206 013746 000004
017212 012737 017232 000004
017220 005737 177060
017224 012637 000004
017230 000463
017232 022626
017234 012637 000004
017240 000423
017242
017242 032777 000400 161666
017250 001404
017252 127737 161660 001102
017260 001465
017262 105737 001103
017266 001421
017270 123737 001115 001103
017276 101015
017300 032777 001000 161630
017306 001404
017310 013737 001110 001106
017316 000446
017320 105037 001107
017324 005037 001154
017330 000415
017332 032777 004000 161576
017340 001011
017342 005737 001206
017346 001406
017350 005237 001104
017354 023737 001164 001104
017362 002024
017364 012737 000001 001104
017372 013737 017450 001164
017400 105237 001102

```
.SBTTL SCOPE HANDLER ROUTINE

:*****
:*THIS ROUTINE CONTROLS THE LOOPING OF SUBTESTS. IT WILL INCREMENT
:*AND LOAD THE TEST NUMBER($TSTNM) INTO THE DISPLAY REG.(DISPLAY<7:0>)
:*AND LOAD THE ERROR FLAG ($ERFLG) INTO DISPLAY<15:08>
:*THE SWITCH OPTIONS PROVIDED BY THIS ROUTINE ARE:
:*SW14=1 LOOP ON TEST
:*SW11=1 INHIBIT ITERATIONS
:*SW09=1 LOOP ON ERROR
:*SW08=1 LOOP ON TEST IN SWR<7:0>
:*CALL
:* SCOPE ::SCOPE=IOT

$SCOPE:
    CKSWR
    BIT #BIT14,$SWR ;;LOOP ON PRESENT TEST?
    BNE $OVER ;;YES IF SW14=1
:*****START OF CODE FOR THE XOR TESTER*****
$XTSTR: BR $S ;;IF RUNNING ON THE "XOR" TESTER CHANGE
    MOV @ERRVEC,-(SP) ;;THIS INSTRUCTION TO A "NOP" (NOP=240)
    MOV #S,$ERRVEC ;;SAVE THE CONTENTS OF THE ERROR VECTOR
    TST @177060 ;;SET FOR TIMEOUT
    MOV (SP)+,@ERRVEC ;;TIME OUT ON XOR?
    BR $SVLAD ;;RESTORE THE ERROR VECTOR
    CMP (SP)+,(SP)+ ;;GO TO THE NEXT TEST
    MOV (SP)+,@ERRVEC ;;CLEAR THE STACK AFTER A TIME OUT
    BR 7S ;;RESTORE THE ERROR VECTOR
    ;;LOOP ON THE PRESENT TEST
5S:;*****END OF CODE FOR THE XOR TESTER*****
    BIT #BIT08,$SWR ;;LOOP ON SPEC. TEST?
    BEQ 2S ;;BR IF NO
    CMPB $SWR,$TSTNM ;;ON THE RIGHT TEST? SWR<7:0>
    BEQ $OVER ;;BR IF YES
    TSTB $ERFLG ;;HAS AN ERROR OCCURRED?
    BEQ 3S ;;BR IF NO
    CMPB $ERMAX,$ERFLG ;;MAX. ERRORS FOR THIS TEST OCCURRED?
    BHI 3S ;;BR IF NO
    BIT #BIT09,$SWR ;;LOOP ON ERROR?
    BEQ 4S ;;BR IF NO
    MOV $LPERR,$LPADR ;;SET LOOP ADDRESS TO LAST SCOPE
    BR $OVER
    CLRB $ERFLG ;;ZERO THE ERROR FLAG
    CLR $TIMES ;;CLEAR THE NUMBER OF ITERATIONS TO MAKE
    BR 1S ;;ESCAPE TO THE NEXT TEST
    BIT #BIT11,$SWR ;;INHIBIT ITERATIONS?
    BNE 1S ;;BR IF YES
    TST $PASS ;;IF FIRST PASS OF PROGRAM
    BEQ 1S ;;INHIBIT ITERATIONS
    INC $ICNT ;;INCREMENT ITERATION COUNT
    CMP $TIMES,$ICNT ;;CHECK THE NUMBER OF ITERATIONS MADE
    BGE $OVER ;;BR IF MORE ITERATION REQUIRED
    MOV #1,$ICNT ;;REINITIALIZE THE ITERATION COUNTER
    MOV $MXCNT,$TIMES ;;SET NUMBER OF ITERATIONS TO DO
    $SVLAD: INCB $TSTNM ;;COUNT TEST NUMBERS
```


K05

```

2805 017404 113737 001102 001204      MOVB  $STSTNM,$STSTN  ;; SET TEST NUMBER IN APT MAILBOX
2806 017412 011637 001106           MOV   (SP), $LPADR    ;; SAVE SCOPE LOOP ADDRESS
2807 017416 011637 001110           MOV   (SP), $LPERR    ;; SAVE ERROR LOOP ADDRESS
2808 017422 005037 001166           CLR   $ESCAPE        ;; CLEAR THE ESCAPE FROM ERROR ADDRESS
2809 017426 112737 000001 001115     MOVB  #1,$E=MAX      ;; ONLY ALLOW ONE(1) ERROR ON NEXT TEST
2810 017434 013777 001102 161476 $OVER: MOV  $STSTNM,$DISPLAY ;; DISPLAY TEST NUMBER
2811 017442 013716 001106           MOV   $LPADR,(SP)    ;; FUDGE RETURN ADDRESS
2812 017446 000002           RTI                  ;; FIXES PS
2813 017450 003720 $MXCNT: 2000.        ;; MAX. NUMBER OF ITERATIONS

.SBTTL  ERROR HANDLER - .JTINE

;;*****
;;*THIS ROUTINE WILL INCREMENT THE ERROR FLAG AND THE ERROR COUNT.
;;*SAVE THE ERROR ITEM NUMBER AND THE ADDRESS OF THE ERROR CALL
;;*AND GO TO $ERRTYP ON ERROR
;;*THE SWITCH OPTIONS PROVIDED BY THIS ROUTINE ARE:
;;*SW15=1      HALT ON ERROR
;;*SW13=1      INHIBIT ERROR TYPEOUTS
;;*SW10=1      BELL ON ERROR
;;*SW09=1      LOOP ON ERROR
;;*CALL
;;*      ERJCR  N      ;;ERROR=EMT AND N=ERROR ITEM NUMBER

2829 017452 $ERROR:
2830 017452 105237 001103 75:      INCB  $ERFLG          ;; SET THE ERROR FLAG
2831 017456 001775      BEQ   75             ;; DON'T LET THE FLAG GO TO ZERO
2832 017460 013777 001102 161452     MOV   $STSTNM,$DISPLAY ;; DISPLAY TEST NUMBER AND ERROR FLAG
2833 017456 032777 002000 161442     BIT   #BIT10,$SWR     ;; BELL ON ERROR?
2834 017474 001402      BEQ   15             ;; NO - SKIP
2835 017476 104400 001170      TYPE  $SBELL         ;; RING BELL
2836 017502 005237 001112 15:      INC   $ERTTL         ;; COUNT THE NUMBER OF ERRORS
2837 017506 011637 001116     MOV   (SP), $ERRPC   ;; GET ADDRESS OF ERROR INSTRUCTION
2838 017512 162737 000002 001116     SUB   #2,$ERRPC
2839 017520 117737 161372 001114     MOVB  $ERRPC,$ITEMB  ;; STRIP AND SAVE THE ERROR ITEM CODE
2840 017526 032777 020000 161402     BIT   #BIT13,$SWR    ;; SKIP TYPEOUT IF SET
2841 017534 001004      BNE   205           ;; SKIP TYPEOUTS
2842 017536 004737 020442     JSR   PC,$ERRTYP     ;; GO TO USER ERROR ROUTINE
2843 017542 104400 001175      TYPE  , $CALF

2844 017546 205:
2845 017546 122737 000001 001220     CMPB  #APTENV,$ENV   ;; RUNNING IN APT MODE
2846 017554 001007      BNE   25             ;; NO SKIP APT ERROR REPORT
2847 017556 113737 001114 017570     MOVB  $ITEMB,215     ;; SET ITEM NUMBER AS ERROR NUMBER
2848 017564 004737 021544     JSR   PC,$ATY4      ;; REPORT FATAL ERROR TO APT

2849 017570      COO
2850 017571      COO
2851 017572 000777 225:      BR    225           ;; APT ERROR LOOP
2852 017574 005777 161336 23:      TST   $SWR          ;; HALT ON ERROR
2853 017600 100001      BPL   35            ;; SKIP IF CONTINUE
2854 017602 000000      HALT                ;; HALT ON ERROR!
2855 017604 032777 001000 161324 35:      BIT   #BIT09,$SWR   ;; LOOP ON ERROR SWITCH SET?
2856 017612 001402      BEQ   45             ;; BR IF NO
2857 017614 013716 001110     MOV   $LPERR,(SP)   ;; FUDGE RETURN FOR LOOPING
2858 017620 005737 001166 45:      TST   $ESCAPE       ;; CHECK FOR AN ESCAPE ADDRESS
2859 017624 001402      BEQ   55             ;; BR IF NONE
2860 017626 013716 001166     MOV   $ESCAPE,(SP)  ;; FUDGE RETURN ADDRESS FOR ESCAPE

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2861 017632 5$:      CMP      #SENDAD, @#42    ;;ACT-11 AUTO-ACCEPT?
2862 017632 022737 010436 000042      BNE      6$                ;;BRANCH IF NO
2863 017640 001001                                HALT                    ;;YES
2864 017642 000000
2865 017644 6$:      RTI                        ;;RETURN
2866 017544 000002
2867
2868 .SBTTL  TTY INPUT ROUTINE
2869
2870 *****
2871 *SOFTWARE SWITCH REGISTER CHANGE ROUTINE.
2872 *ROUTINE IS ENTERED FROM THE TRAP HANDLER, AND WILL
2873 *SERVICE THE TEST FOR CHANGE IN SOFTWARE SWITCH REGISTER TRAP CALL
2874 *WHEN OPERATING IN TTY FLAG MODE.
2875 017646 022737 000176 001136 $CKSWR: CMP      #SWREG, SWR    ;; IS THE SOFT-SWR SELECTED?
2876 017654 001073      BNE      14$                ;; BRANCH IF NO
2877 017656 105777 161260      TSTB    @STKS                ;; CHAR THERE?
2878 017662 100070      BPL      14$                ;; IF NO, DON'T WAIT AROUND
2879 017664 117746 161254 2$:      MOVB    @STKB, -(SP)    ;; SAVE THE CHAR
2880 017670 042716 177600      BIC     #1C177, (SP)    ;; STRIP-OFF THE ASCII
2881 017674 022726 000007      CMP     #7, (SP)+        ;; IS IT A CONTROL G?
2882 017700 001061      BNE     14$                ;; NO, RETURN TO USER
2883 017702 104400 020311      TYPE    .SCNTLG          ;; YES, ECHO CONTROL G
2884
2885 017706 104400 020316 6$:      TYPE    $MSWR            ;; TYPE CURRENT CONTENTS
2886 017712 013746 000176      MOV     SWREG, -(SP)    ;; SAVE SWREG FOR TYPEOUT
2887 017716 104401      TYPOC  ;; GO TYPE--OCTAL ASCII(ALL DIGITS)
2888 017720 104400 020327      TYPE    .SMNEW          ;; PROMPT FOR NEW SWR
2889 017724 005046      CLR     -(SP)            ;; CLEAR COUNTER
2890 017726 005046      CLR     -(SP)            ;; THE NEW SWR
2891 017730 104406 7$:      RDCHR  ;; GET NEXT CHAR
2892
2893 017732 022716 000025 8$:      CMP     #25, (SP)        ;; IS IT A CONTROL U?
2894 017736 001005      BNE     9$                ;; BRANCH IF NO
2895 017740 104400 020304      TYPE    .SCNTLU         ;; YES, ECHO IT
2896 017744 062706 000006      ADD     #6, SP           ;; IGNORE PREVIOUS INPUT
2897 017750 000756      BR     6$                ;; LET'S TRY IT AGAIN
2898
2899 017752 022716 000015 9$:      CMP     #15, (SP)        ;; IS IT A <CR>?
2900 017756 001011      BNE     11$                ;; BRANCH IF NO
2901 017760 005766 000004      TST     4(SP)            ;; YES, IS IT THE FIRST CHAR?
2902 017764 001403      BEQ     10$                ;; BRANCH IF YES
2903 017766 016677 000002 161142      MOV     2(SP), @SWR      ;; SAVE NEW SWR
2904 017774 062706 000006 10$:      ADD     #5, SP           ;; CLEAR UP STACK
2905 020000 000417      BR     13$                ;; RETURN TO USER
2906 020002 022716 000012 11$:      CMP     #12, (SP)        ;; IS IT A <LF>?
2907 020006 001017      BNE     15$                ;; BRANCH IF NO
2908 020010 005766 000004      TST     4(SP)            ;; YES, IS IT THE FIRST CHAR?
2909 020014 001403      BEQ     12$                ;; YES
2910 020016 016677 000002 161112      MOV     2(SP), @SWR      ;; SAVE NEW SWR
2911 020024 062706 000006 12$:      ADD     #6, SP           ;; CLEAR UP STACK
2912 020030 013716 000046      MOV     @#46, (SP)       ;; GET RESTART
2913 020034 062716 000010      ADD     #10, (SP)        ;; ADDRESS
2914 020040 104400 001175 13$:      TYPE    .SCRLF          ;; ECHO <CR> AND <LF>
2915 020044 000002 14$:      RTI                        ;; RETURN
2916 020046 004737 021456 15$:      JSR     FC, $TYPEFC     ;; ECHO CHAR

```

M05

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 DZARCB.P11 TTY INPUT ROUTINE

```

2917 020052 042726 177770      BIC      #177770,(SP)+   ;;RESTRICT TO 0-7
2918 020056 005766 000002      TST      2(SP)         ;;IS THIS THE FIRST CHAR
2919 020062 001403          BEQ      16$          ;;BRANCH IF YES
2920 020064 006316          ASL      (SP)         ;;NO, SHIFT PRESENT
2921 020066 006316          ASL      (SP)         ;;CHAR OVER TO MAKE
2922 020070 006316          ASL      (SP)         ;;ROOM FOR NEW ONE.
2923 020072 005266 000002      16$: INC      2(SP)         ;;KEEP COUNT OF CHAR
2924 020076 056616 177776      BIS      -2(SP),(SP)  ;;SET IN NEW CHAR
2925 020102 000712          BR       7$          ;;GET THE NEXT ONE
2926
2927 *****
2928 *THIS ROUTINE WILL INPUT A SINGLE CHARACTER FROM THE TTY
2929 *CALL:
2930 *      RDCHR          ;;INPUT A SINGLE CHARACTER FROM THE TTY
2931 *      RETURN HERE   ;;CHARACTER IS ON THE STACK
2932 *                  ;;WITH PARITY BIT STRIPPED OFF
2933 ;
2934
2935 $RDCHR: MOV      (SP),-(SP)   ;;PUSH DOWN THE PC
2936 020106 016666 000004 000002  MOV      4(SP),2(SP)   ;;SAVE THE PS
2937 020114 105777 161022 161022  16$: TSTB   2$TKS        ;;WAIT FOR
2938 020120 100375          BPL      1$          ;;A CHARACTER
2939 020122 117766 161010 000004  MOVB    2$TKB,4(SP)   ;;READ THE TTY
2940 020130 042766 177600 000004  BIC     #1C<17>,4(SP) ;;GET RID OF JUNK IF ANY
2941 020136 026627 000004 000140  CMP     4(SP),#140    ;;IS IT UPPER CASE?
2942 020144 002407          BLT      2$          ;;BRANCH IF YES
2943 020146 026627 000004 000175  CMP     4(SP),#175    ;;IS IT A SPECIAL CHAR?
2944 020154 003003          BGT      2$          ;;BRANCH IF YES
2945 020156 042766 000040 000004  BIC     #40,4(SP)     ;;MAKE IT UPPER CASE
2946 020164 000002          RTI          ;;GO BACK TO USER
2947
2948 *****
2949 *THIS ROUTINE WILL INPUT A STRING FROM THE TTY
2950 *CALL:
2951 *      RDLIN         ;;INPUT A STRING FROM THE TTY
2952 *      RETURN HERE  ;;ADDRESS OF FIRST CHARACTER WILL BE ON THE STACK
2953 *                  ;;TERMINATOR WILL BE A BYTE OF ALL 0'S
2954
2955 $RDLIN: MOV      R3, -(SP)   ;;SAVE R3
2956 020170 012703 020274 020274  16$: MOV     #1$TTYIN,R3   ;;GET ADDRESS
2957 020174 022703 020304 020304  2$: CMP     #1$TTYIN+8,R3  ;;BUFFER FULL?
2958 020200 101405          BLOS    4$          ;;BR IF YES
2959 020202 104406          RDCHR   ;;GO READ ONE CHARACTER FROM THE TTY
2960 020204 112613          MOVB   (SP)+,R3      ;;GET CHARACTER
2961 020206 122713 000177 10$: CMPB   #177,R3     ;;IS IT A RUBOUT
2962 020212 001003          BNE    3$          ;;SKIP IF NOT
2963 020214 104400 001174 4$: TYPE   1$QJES     ;;TYPE A '?'
2964 020220 000763          BR     1$          ;;CLEAR THE BUFFER AND LOOP
2965 020222 111337 020272 3$: MOVB   (R3),9$     ;;ECHO THE CHARACTER
2966 020226 104400 020272 3$: TYPE   3$          ;;
2967 020232 122723 000015 3$: CMPB   #15,(R3)+   ;;CHECK FOR RETURN
2968 020236 001356          BNE    2$          ;;LOOP IF NOT RETURN
2969 020240 105053 177777 3$: CLRB   -1(R3)     ;;CLEAR RETURN (THE 15)
2970 020244 104400 001176 3$: TYPE   $LF        ;;TYPE A LINE FEED
2971 020250 012603          MOV    (SP)+,R3     ;;RESTORE R3
2972 020252 011645          MOV    (SP),-(SP)   ;;ADJUST THE STACK AND PUT ADDRESS OF THE
2973 020254 016666 000004 000002  MOV     4(SP),2(SP)  ;;FIRST ASCII CHARACTER ON IT
2974 020262 012766 020274 000004  MOV     #1$TTYIN,4(SP)

```


.SBTTL POWER DOWN AND UP ROUTINES

::*****
:POWER DOWN ROUTINE

```

020576 012737 020742 000024 $PWRDN: MOV $SILLUP,2,$PWRVEC ::SET FOR FAST UP
020604 012737 000340 000026 MOV #340,2,$PWRVEC+2 ::PRIO:7
020612 010046 MOV RC,-(SP) ::PUSH RC ON STACK
020614 010046 MOV R1,-(SP) ::PUSH R1 ON STACK
020616 010046 MOV R2,-(SP) ::PUSH R2 ON STACK
020620 010046 MOV R3,-(SP) ::PUSH R3 ON STACK
020622 010046 MOV R4,-(SP) ::PUSH R4 ON STACK
020624 010046 MOV R5,-(SP) ::PUSH R5 ON STACK
020626 017746 MOV @SWR,-(SP) ::PUSH @SWR ON STACK
020632 010637 160304 SP,$SAVR6 ::SAVE SP
020638 012737 020746 000024 MOV $PWRUP,2,$PWRVEC ::SET UP VECTOR
020644 000000 HALT
020646 000776 BR -2 ::HANG UP

```

::*****
:POWER UP ROUTINE

```

020650 012737 020742 000024 $PWRUP: MOV $SILLUP,2,$PWRVEC ::SET FOR FAST DOWN
020656 013706 020746 MOV $SAVR6,SP ::GET SP
020662 005037 020746 CLR $SAVR6 ::WAIT LOOP FOR THE TTY
020666 005237 020746 IS: INC $SAVR6 ::WAIT FOR THE INC
020672 001375 BNE IS OF WORD
020674 012677 160236 MOV (SP)+,@SWR ::POP STACK INTO @SWR
020680 012605 MOV (SP)+,R5 ::POP STACK INTO R5
020682 012604 MOV (SP)+,R4 ::POP STACK INTO R4
020684 012603 MOV (SP)+,R3 ::POP STACK INTO R3
020686 012602 MOV (SP)+,R2 ::POP STACK INTO R2
020690 012601 MOV (SP)+,R1 ::POP STACK INTO R1
020692 012600 MOV (SP)+,RC ::POP STACK INTO RC
020694 012737 020576 000024 MOV $PWRDN,2,$PWRVEC ::SET UP THE POWER DOWN VECTOR
020696 012737 000340 000026 MOV #340,2,$PWRVEC+2 ::PRIO:7
020698 004400 TYPE ::REPORT THE POWER FAILURE
020700 020750 $PWRMG: .WORD PWRMSG ::POWER FAIL MESSAGE POINTER
020702 002716 MOV (PC)+,(SP) ::RESTART AT BEGIN
020704 001516 $PWRAD: .WORD BEGIN ::RESTART ADDRESS
020706 000002 RTI
020708 000000 $SILLUP: HALT ::THE POWER UP SEQUENCE WAS STARTED
020710 000776 BR -2 ::BEFORE THE POWER DOWN WAS COMPLETE
020712 000000 $SAVR6: 0 PUT THE SP HERE
020714 005015 042522 052123 PWRMSG: .ASCIZ '(5) 12 RESTARTING AFTER A POWER FAILURE (15) (12) (12)'
020716 0044524 0513516
020718 0052106 0511055
020720 0050040 0503517
020722 0430400 0445001
020724 042522 050015

```

.E.E'1

.SBTTL BINARY TO OCTAL (AS.II) AND TYPE

```

*****
*THIS ROUTINE IS USED TO CHANGE A 16-BIT BINARY NUMBER TO A 6-DIGIT
*OCTAL (ASCII) NUMBER AND TYPE IT.
*STYPOS---ENTER HERE TO SETUP SUPPRESS ZEROS AND NUMBER OF DIGITS TO TYPE
*CALL:
*      MOV      NUM, -(SP)      ;; NUMBER TO BE TYPED
*      TYPPOS   ;; CALL FOR TYPEOUT
*      .BYTE    N                ;; N=1 TO 6 FOR NUMBER OF DIGITS TO TYPE
*      .BYTE    M                ;; M=1 OR 0
*                                  ;; 1=TYPE LEADING ZEROS
*                                  ;; 0=SUPPRESS LEADING ZEROS
*STYPON---ENTER HERE TO TYPE OUT WITH THE SAME PARAMETERS AS THE LAST
*STYPOS OR STYPOC
*CALL:
*      MOV      NUM, -(SP)      ;; NUMBER TO BE TYPED
*      TYPON    ;; CALL FOR TYPEOUT
*STYPOC---ENTER HERE FOR TYPEOUT OF A 16 BIT NUMBER
*CALL:
*      MOV      NUM, -(SP)      ;; NUMBER TO BE TYPED
*      TYPOC    ;; CALL FOR TYPEOUT

```

000000	017646	000000	
000001	116637	000001	021241
000002	112637	021243	
000003	062716	000002	
000004	000406		
000005	112737	000001	021241
000006	112737	000006	021243
000007	112737	000005	021240
000008	010346		
000009	010446		
000010	010546		
000011	113704	021243	
000012	005404		
000013	062704	000006	
000014	110437	021242	
000015	113704	021241	
000016	016605	000012	
000017	005003		
000018	006105		
000019	000404		
000020	006105		
000021	006105		
000022	006105		
000023	010503		
000024	006103		
000025	105337	021242	
000026	100016		
000027	042703	177770	
000028	001002		

```

STYPOS: MOV      0(SP), -(SP)      ;; PICKUP THE MODE
        MOVVB   1(SP), $OFILL    ;; LOAD ZERO FILL SWITCH
        MOVVB   (SP)+, $OMODE+1  ;; NUMBER OF DIGITS TO TYPE
        ADD     #2, (SP)         ;; ADJUST RETURN ADDRESS
        BR      STYPON
STYPOC: MOVVB   #1, $OFILL       ;; SET THE ZERO FILL SWITCH
        MOVVB   #6, $OMODE+1    ;; SET FOR SIX(6) DIGITS
STYPON: MOVVB   #5, $OCONT      ;; SET THE ITERATION COUNT
        MOV     R3, -(SP)        ;; SAVE R3
        MOV     R4, -(SP)        ;; SAVE R4
        MOV     R5, -(SP)        ;; SAVE R5
        MOVVB   $OMODE+1, R4     ;; GET THE NUMBER OF DIGITS TO TYPE
        NEG     R4
        ADD     #6, R4          ;; SUBTRACT IT FOR MAX. ALLOWED
        MOVVB   R4, $OMODE      ;; SAVE IT FOR USE
        MOVVB   $OFILL, R4     ;; GET THE ZERO FILL SWITCH
        MOV     12(SP), R5      ;; PICKUP THE INPUT NUMBER
        CLR     R3             ;; CLEAR THE OUTPUT WORD
        ROL    R5             ;; ROTATE MSB INTO "0"
        BR     25:            ;; GO DO MSB
        ROL    R5             ;; FORM THIS DIGIT
        ROL    R5
        ROL    R5
        ROL    R5
        MOV     R5, R3
        ROL    R3             ;; GET LSB OF THIS DIGIT
        DECB   $OMODE         ;; TYPE THIS DIGIT
        BPL   75:            ;; BR IF NO
        BIC   #177770, R3     ;; GET RID OF JUNK
        BNE   45:            ;; TEST FOR 0

```

021154	005704		
021156	001403		
021160	005204		
021162	022060	000060	
021166	022040	000040	
021172	110337	021236	
021176	104400	021236	
021182	105337	021240	
021190	003347		
021196	002402		
021200	005204		
021206	000744		
021214	012605		
021220	012604		
021222	012603		
021224	016666	000002	000004
021226	012616		
021228	000002		
021230	000		
021232	000		
021234	000		
021236	000		
021238	000		
021240	000		
021242	000		
021244	000002		

```

TST R4          ;; SUPPRESS THIS 0?
BEQ 5$          ;; BR IF YES
INC R4          ;; DON'T SUPPRESS ANYMORE 0'S
BIS #0,R3       ;; MAKE THIS DIGIT ASCII
BIS #1,R3       ;; MAKE ASCII IF NOT ALREADY
MOV R3,5$       ;; SAVE FOR TYPING
TYPE 5$         ;; GO TYPE THIS DIGIT
DECB $COUNT   ;; COUNT BY 1
BGT 2$         ;; BR IF MORE TO DO
BLT 6$         ;; BR IF DONE
INC R4          ;; INSURE LAST DIGIT ISN'T A BLANK
BR 2$          ;; GO DO THE LAST DIGIT
5$: MOV (SP)+,R5  ;; RESTORE R5
   MOV (SP)+,R4  ;; RESTORE R4
   MOV (SP)+,R3  ;; RESTORE R3
   MOV 2(SP),4(SP) ;; SET THE STACK FOR RETURNING
   MOV (SP)+,(SP)
RTI            ;; RETURN
5$: .BYTE 0      ;; STORAGE FOR ASCII DIGIT
   .BYTE 0      ;; TERMINATOR FOR TYPE ROUTINE
$COUNT: .BYTE 0 ;; OCTAL DIGIT COUNTER
$OFILL: .BYTE 0  ;; ZERO FILL SWITCH
$OMODE: .WORD 0  ;; NUMBER OF DIGITS TO TYPE

```

.SBTTL TYPE ROUTINE

```

*****
*ROUTINE TO TYPE ASCII MESSAGE. MESSAGE MUST TERMINATE WITH A 0 BYTE.
*THE ROUTINE WILL INSERT A NUMBER OF NULL CHARACTERS AFTER A LINE FEED.
*NOTE1: $NULL CONTAINS THE CHARACTER TO BE USED AS THE FILLER CHARACTER.
*NOTE2: $FILLS CONTAINS THE NUMBER OF FILLER CHARACTERS REQUIRED.
*NOTE3: $FILLC CONTAINS THE CHARACTER TO FILL AFTER.
*
*CALL:
*1) USING A TRAP INSTRUCTION
*   TYPE ,MESADR          ;;MESADR IS FIRST ADDRESS OF AN ASCII STRING
*OR
*   TYPE
*   MESADR
*

```

021244	105737	001155	
021250	100002		
021252	000000		
021254	000430		
021256	010046		
021260	017600	000002	
021264	122737	000001	001220
021272	001011		
021274	132737	000100	001221
021302	001495		
021304	010037	021314	
021310	004737	021534	
021314	000000		
021316	132737	000040	001221
021324	001003		

```

$TYPE: TSTB $TFPLG  ;; IS THERE A TERMINAL?
      BPL 1$        ;; BR IF YES
      HALT          ;; HALT HERE IF NO TERMINAL
1$: BR 3$          ;; LEAVE
   MOV R0,(SP)     ;; SAVE R0
   MOV 22(SP),R0   ;; GET ADDRESS OF ASCII STRING
   CMPB #APTENV,$ENV  ;; RUNNING IN APT MODE
   BNE 62$         ;; NO GO CHECK FOR APT CONSOLE
   BITB #APTPOOL,$ENVM  ;; SPOOL MESSAGE TO APT
   BEQ 62$         ;; NO GO CHECK FOR CONSOLE
   MOV R0,61$      ;; SET UP MESSAGE ADDRESS FOR APT
   JSR PC,$ATY3    ;; SPOOL MESSAGE TO APT
   .WORD 0         ;; MESSAGE ADDRESS
61$: .WORD 0
62$: BITB #APTCONS,$ENVM  ;; APT CONSOLE SUPPRESSED
      BNE 60$      ;; YES, SKIP TYPE OUT

```



```

3235 021326 112046 25:   MOVB   (RO)+, -(SP)   ;; PUSH CHARACTER TO BE TYPED ONTC STACK
3236 021330 001005      BNE    45             ;; BR IF IT ISN'T THE TERMINATOR
3237 021332 005726      TST    (SP)+         ;; IF TERMINATOR POP IT OFF THE STACK
3238 021334 012600 60$:   MOV    (SP)+, RO     ;; RESTORE RO
3239 021336 062716 000002 35:   ADD    #2, (SP)      ;; ADJUST RETURN PC
3240 021342 000002      RTI                    ;; RETURN
3241 021344 122716 000011 45:   CMPB  #HT, (SP)     ;; BRANCH IF <HT>
3242 021350 001430      BEQ    85             ;;
3243 021352 122716 000200      CMPB  #CRLF, (SP)   ;; BRANCH IF NOT <CRLF>
3244 021356 001006      BNE    55             ;;
3245 021360 005726      TST    (SP)+         ;; POP <CR>, <LF> EQUIV
3246 021362 104400      TYPE  TYPE          ;; TYPE A CR AND LF
3247 021364 001175      SCRLF
3248 021366 105037 021522      CLRB  $CHARCNT     ;; CLEAR CHARACTER COUNT
3249 021372 000755      BR    25             ;; GET NEXT CHARACTER
3250 021374 004737 021456 55:   JSR   PC, $TYPEC    ;; GO TYPE THIS CHARACTER
3251 021400 123726 001154 55:   CMPB  $FILLC, (SP)+ ;; IS IT TIME FOR FILLER CHARS.?
3252 021404 001350      BNE    25             ;; IF NO GO GET NEXT CHAR.
3253 021406 013746 001152      MOV   $NULL, -(SP)  ;; GET # OF FILLER CHARS. NEEDED
3254                                ;; AND THE NULL CHAR.
3255 021412 105366 000001 75:   DECB  1(SF)         ;; DOES A NULL NEED TO BE TYPED?
3256 021416 002770      BLT   65             ;; BR IF NO--GO POP THE NULL OFF OF STACK
3257 021420 004737 021456      JSR   PC, $TYPEC    ;; GO TYPE A NULL
3258 021424 105337 021522      DECB  $CHARCNT     ;; DO NOT COUNT AS A COUNT
3259 021430 000770      BR    75             ;; LOOP

```

; HORIZONTAL TAB PROCESSOR

```

3263 021432 112716 000040 85:   MOVB  #' (SP)      ;; REPLACE TAB WITH SPACE
3264 021436 004737 021456 95:   JSR   PC, $TYPEC  ;; TYPE A SPACE
3265 021442 132737 000007 021522 BITB  #7, $CHARCNT   ;; BRANCH IF NOT AT
3266 021450 001372      BNE    95          ;; TAB STOP
3267 021452 005726      TST   (SP)+       ;; POP SPACE OFF STACK
3268 021454 000724      BR    25          ;; GET NEXT CHARACTER
3269 021456 105777 157464 $TYPEC: TSTB  $STPB      ;; WAIT UNTIL PRINTER IS READY
3270 021462 100375      BPL   $TYPEC
3271 021464 116677 000002 157456 MOVB  2(SF), $STPB ;; LOAD CHAR TO BE TYPED INTO DATA REG.
3272 021472 122766 000015 000002 CMFB  #CR, 2(SF)   ;; IS CHARACTER A CARRIAGE RETURN?
3273 021500 001003      BNE    15          ;; BRANCH IF NO
3274 021502 105037 021522      CLRB  $CHARCNT   ;; YES--CLEAR CHARACTER COUNT
3275 021506 000406      BR    $TYPEX     ;; EXIT
3276 021510 122766 000012 000002 15:   CMPB  #LF, 2(SF)  ;; IS CHARACTER A LINE FEED?
3277 021516 001402      BEQ   $TYPEX     ;; BRANCH IF YES
3278 021520 105227      INCB  (PC)+       ;; COUNT THE CHARACTER
3279 021522 000000 $CHARCNT: .WORD 0 ;; CHARACTER COUNT STORAGE
3280 021524 000207 $TYPEX: RTS      PC

```

.SBTTL APT COMMUNICATIONS ROUTINE

```

3286 021526 112737 000001 021772 $ATY1: MOVB  #1, $FFLG  ;; TO REPORT FATAL ERROR
3287 021534 112737 000001 021770 $ATY3: MOVB  #1, $MFLG  ;; TO TYPE A MESSAGE
3288 021542 000403      BR    $ATYC
3289 021544 112737 000001 021772 $ATY4: MOVB  #1, $FFLG  ;; TO ONLY REPORT FATAL ERROR
3290 021552 $ATYC:

```

```

3291 021552 010046 MOV RO,-(SP) ;; PUSH RO ON STACK
3292 021554 010146 MOV R1,-(SP) ;; PUSH R1 ON STACK
3293 021556 105737 021770 TSTB $MFLG ;; SHOULD TYPE A MESSAGE?
3294 021562 001450 BEQ 55 ;; IF NOT: BR
3295 021564 122737 000001 001220 CMPB #APTENV,$ENV ;; OPERATING UNDER APT?
3296 021572 001031 BNE 35 ;; IF NOT: BR
3297 021574 132737 000100 001221 9ITB #APTPOOL,$ENVM ;; SHOULD SPOOL MESSAGES?
3298 021602 001425 BEQ 35 ;; IF NOT: BR
3299 021604 017600 000004 MOV #4(SP),RO ;; GET MESSAGE ADDR.
3300 021610 062766 000002 000004 ADD #2,4(SP) ;; BUMP RETURN ADDR.
3301 021616 005737 001200 15: TST $MSGTYPE ;; SEE IF DONE W/ LAST XMISSION?
3302 021622 001375 BNE 15 ;; IF NOT: WAIT
3303 021624 010037 001214 MOV RO,$MSGAD ;; PUT ADDR IN MAILBOX
3304 021630 105720 25: TSTB (RO)+ ;; FIND END OF MESSAGE
3305 021632 001375 BNE 25
3306 021634 163700 001214 SUB $MSGAD,RO ;; SUB START OF MESSAGE
3307 021640 006200 ASR RO ;; GET MESSAGE LNTH IN WORDS
3308 021642 010037 001216 MOV RO,$MSGLGT ;; PUT LENGTH IN MAILBOX
3309 021646 012737 000004 001200 MOV #4,$MSGTYPE ;; TELL APT TO TAKE MSG.
3310 021654 000413 BR 55
3311 021656 017637 000004 021702 35: MOV #4(SP),45 ;; PUT MSG ADDR IN JSR LINKAGE
3312 021664 062766 000002 000004 ADD #2,4(SP) ;; BUMP RETURN ADDRESS
3313 021672 013746 177776 MOV 177776,-(SP) ;; PUSH 177776 ON STACK
3314 021676 004737 021244 JSR PC,$TYPE ;; CALL TYPE MACRO
3315 021702 000000 45: .WORD 0
3316 021704 55:
3317 021704 105737 021772 105: TSTB $FFLG ;; SHOULD REPORT FATAL ERROR?
3318 021710 001416 BEQ 125 ;; IF NOT: BR
3319 021712 005737 001220 TST $ENV ;; RUNNING UNDER APT?
3320 021716 001413 BEQ 125 ;; IF NOT: BR
3321 021722 005737 001200 115: TST $MSGTYPE ;; FINISHED LAST MESSAGE?
3322 021724 001375 BNE 115 ;; IF NOT: WAIT
3323 021726 017637 000004 001202 MOV #4(SP),$FATAL ;; GET ERROR #
3324 021734 062766 000002 000004 ADD #2,4(SP) ;; BUMP RETURN ADDR.
3325 021742 005237 001200 INC $MSGTYPE ;; TELL APT TO TAKE ERROR
3326 021746 105037 021772 125: CLRB $FFLG ;; CLEAR FATAL FLAG
3327 021752 105037 021771 CLRB $LFLG ;; CLEAR LOG FLAG
3328 021756 105037 021770 CLRB $MFLG ;; CLEAR MESSAGE FLAG
3329 021762 012601 MOV (SP)+,R1 ;; POP STACK INTO R1
3330 021764 012600 MOV (SP)+,RO ;; POP STACK INTO RO
3331 021766 000207 RTS PC ;; RETURN
3332 021770 000 $MFLG: .BYTE 0 ;; MESSG. FLAG
3333 021771 000 $LFLG: .BYTE 0 ;; LOG FLAG
3334 021772 000 $FFLG: .BYTE 0 ;; FATAL FLAG
3335 021774 .EVEN
3336 000200 APTSIZE=200
3337 000001 APTENV=001
3338 000100 APTSPCOL=100
3339 000040 APTCSUP=040

```

.SBTTL TRAP DECODER

```

*****
*THIS ROUTINE WILL PICKUP THE LOWER BYTE OF THE "TRAP" INSTRUCTION
*AND USE IT TO INDEX THROUGH THE TRAP TABLE FOR THE STARTING ADDRESS
*OF THE DESIRED ROUTINE. THEN USING THE ADDRESS OBTAINED IT WILL

```

```

3347
3348
3349 021774 010046
3350 021776 016500 000002
3351 022002 005740
3352 022004 111000
3353 022006 006300
3354 022010 016000 022016
3355 022014 000200
3356
3357
3358
3359
3360
3361
3362
3363
3364
3365 022016
3366 022016 021244
3367 022020 021042
3368 022022 021016
3369 022024 021056
3370 022026 010472
3371 022030 017646
3372 022032 020104
3373 022034 020166
3374 022036 020340
3375
3376 022040 000000
3377 022042 001020
3378 024102 000000
3379 024104 000200
3380 024504 000000
3381 000001

```

:*GO TO THAT ROUTINE.

```

$TRAP:  MOV    RO, -(SP)      ;; SAVE RO
        MOV    2(SP),RO      ;; GET TRAP ADDRESS
        TST   -(RO)          ;; BACKUP BY 2
        MOVB  (RO),RO        ;; GET RIGHT BYTE OF TRAP
        ASL   RO              ;; POSITION FOR INDEXING
        MOV   $TRAPD(RO),RO  ;; INDEX TO TABLE
        RTS   RO              ;; GO TO ROUTINE

```

.SBTTL TRAP TABLE

:*THIS TABLE CONTAINS THE STARTING ADDRESSES OF THE ROUTINES CALLED
:*BY THE "TRAP" INSTRUCTION.

```

; ROUTINE
; -----
$TRAPD:
$TYPE   ;;CALL=TYPE      TRAP+0(104400) TTY TYPEOUT ROUTINE
$TYPC  ;;CALL=TYPC      TRAP+1(104401) TYPE OCTAL NUMBER (WITH LEADING ZERCS.
$TYPOS ;;CALL=TYPOS     TRAP+2(104402) TYPE OCTAL NUMBER (NO LEADING ZERCS.
$TYPON ;;CALL=TYPON     TRAP+3(104403) TYPE OCTAL NUMBER (AS PER LAST CALL)
$TYPDS ;;CALL=TYPDS     TRAP+4(104404) TYPE DECIMAL NUMBER (WITH SIGN)
$CKSWR ;;CALL=CKSWR     TRAP+5(104405) TEST FOR CHANGE IN SOFT-SWR
$RDCHR ;;CALL=RDCHR     TRAP+6(104406) TTY TYPEIN CHARACTER ROUTINE
$RDLIN ;;CALL=RDLIN     TRAP+7(104407) TTY TYPEIN STRING ROUTINE
$RDOCT ;;CALL=RDOCT     TRAP+10(104410) READ AN OCTAL NUMBER FROM TTY

```

```

ACBUFF: 0
        .BLKW 1020
ABUFF4: 0
        .BLKW 200
LAST: 0
        .END

```


.HEADE	190#	
.SETUP	190#	632
.SWRHI	190#	315
.SWRLO	328#	
.SACTI	190#	344
.SAPT8	190#	426#
.SAPTH	190#	356
.SAPTY	190#	3282
.SCATC	190#	328
.SCMTA	190#	379
.SEOP	190#	1786
.SERRO	190#	2814
.SERRT	190#	3023
.SPARM	190#	
.SPOWE	190#	3072
.SRDCC	190#	2983
.SREAD	190#	2867
.SSAVE	190#	
.SSCOP	190#	2749
.SSPAC	190#	
.SSWDO	190#	
.STRAP	190#	3340
.STYPD	190#	1828
.STYPE	190#	3202
.STYPO	190#	2124

K07

ADC	1658	1670	1720	1732	2062										
ADD	616	719	1661	1663	1690	1687	1723	1725	1741	1748	1773	1774	1861	1937	2002
	2009	2054	2083	2457	2462	2465	2467	2896	2904	2911	2913	3012	3046	3153	3163
	3239	3300	3312	3324											
ASL	1152	1195	2571	2920	2921	2922	3005	3007	3009	3043	3044	3045	3353		
ASLB	1866														
ASR	1655	1656	1657	1667	1668	1669	1683	1684	1685	1717	1718	1719	1729	1730	1731
	1744	1745	1746	1967	2057	2058	2059	2060	2061	2724	2725	2726	2727	2728	2729
	2731	3307													
BCC	1867	2455													
BEG	672	686	689	775	782	790	811	956	1102	1106	1199	1270	1453	1755	1772
	1913	1939	1952	1956	2451	2461	2474	2512	2592	2594	2737	2781	2793	2785	2789
	3279	2831	2834	2856	2859	2902	2909	2919	3004	3048	3053	3066	3180	3229	3242
	3277	3294	3298	3318	3320										
BGF	996	1024	1039	1084	1112	1300	1320	1340	1360	1382	1404	1426	1449	2801	
BGTY	1804	1875	2489	2492	2494	2943	3187								
BHT	787	1679	1686	1740	1747	1801	2521	2880	2917	2939	2944	3011	3177		
BIS	779	1869	1870	1944	2003	2924	3182	3193							
BISB	3025														
BIT	955	1198	1269	1452	1754	1938	2473	2511	2530	2766	2780	2789	2795	2833	2840
	2855														
BITB	671	3228	3233	3265	3297										
BLE	991	1010	1054	1069	2090										
BLO	2496														
BLOS	2496														
BLY	1958	1874	1949	1963	2499	2500	2941	3188	3256						
BMT	1865	1980	2453	2508	2587										
BNE	621	636	660	683	715	722	945	1154	1197	1666	1692	1728	1753	1863	1961
	1971	2006	2041	2056	2448	2459	2471	2514	2529	2531	2596	2635	2674	2767	2796
	2084	2046	2063	2076	2082	2094	2900	2907	2960	2966	3036	3059	3097	3178	3227
	3234	3236	3244	3252	3266	3273	3296	3302	3305	3322					
BP	805	808	948	1613	1631	1650	1712	1849	1879	1926	1931	1942	1975	2001	2053
	2077	2619	2623	2688	2853	2878	2937	3176	3221	3270					
BR	605	606	618	623	693	697	704	708	827	842	857	872	887	902	917
	931	952	958	1150	1155	1193	1209	1237	1239	1267	1290	1488	1507	1525	1532
	1551	1558	1576	1595	1603	1627	1677	1738	1776	1860	1977	1939	1943	1963	1977
	1981	2010	2472	2502	2505	2590	2598	2600	2680	2769	2775	2778	2791	2794	2851
	3299	2905	2925	2962	3013	3041	3068	3089	3113	3154	3169	3190	3223	3249	3259
	3269	3275	3288	3310											
CLR	989	604	610	614	627	634	648	649	670	673	770	798	800	814	815
	1734	1033	1087	1100	1529	1555	1573	1592	1651	1660	1671	1672	1713	1722	1733
	3047	1798	1799	1852	1855	1919	1924	1934	1935	1984	2049	2439	2445	2446	2463
	3002	2480	2481	2482	2483	2484	2485	2486	2540	2620	2793	2809	2899	2990	3001
	1881	2469	2792	2967	3248	3274	3326	3327	3328						
CLP	619	635	659	682	714	721	774	781	789	810	990	995	1009	1023	1038
	1053	1068	1083	1111	1153	1196	1299	1319	1339	1359	1381	1403	1435	1447	1465
	1691	1727	1752	1873	1947	1948	1957	1958	1962	1965	2019	2047	2053	2057	2065
	2049	2093	2095	2097	2499	2591	2593	2595	2604	2736	2776	2800	2862	2975	2991
	2093	2099	2066	2940	2942	2955	3226	3241	3243	3251	3272	3295			
	2782	2786	2845	2959	2965	3226	3241	3243	3251	3272	3295				
	624	807	944	1134	1177	1775	1802	1960	2040	2055	2470	2529	2596	3042	
	2186	2186	2255	3258											
	20	222	2854	2864	3088	3112	3221								

2969	2977	2983	2914	2821	2822	2823	2824	2825	2830	2855
831	846	861	876	891	906	920	934	970	984	999
1003	1009	1116	1159	1212	1242	1284	1304	1324	1344	1364
1491	1509	1533	1560	1579	1597	1615	1632	1694	1766	1842
3034	3084	3099	3099	3291	3292	3313	3329	3330	3366	3370
417	416	418	419	426	430	430	433	435	439	436
900	924	938	938	950	970	974	984	988	999	999
1033	1047	1058	1062	1077	1077	1089	1093	1116	1120	1100
1129	1137	1138	1130	1130	1132	1132	1144	1148	1147	1159
1160	1155	1161	1176	1191	1195	1209	1213	1225	1239	1220
1601	1619	1619	1632	1636	1694	1698	1766	1770	1798	1814
3366	3367	3368	3379	3370	3371	3372	3373	3374	3375	3375
419	419	418	419	426	430	632	653	695	699	706
831	846	861	876	891	906	920	934	970	984	999
1003	1009	1116	1159	1212	1242	1284	1304	1324	1344	1364
1491	1509	1533	1560	1579	1597	1615	1632	1694	1766	1842
3034	3084	3099	3099	3291	3292	3313	3329	3330	3366	3370
417	416	418	419	426	430	430	433	435	439	436
900	924	938	938	950	970	974	984	988	999	999
1033	1047	1058	1062	1077	1077	1089	1093	1116	1120	1100
1129	1137	1138	1130	1130	1132	1132	1144	1148	1147	1159
1160	1155	1161	1176	1191	1195	1209	1213	1225	1239	1220
1601	1619	1619	1632	1636	1694	1698	1766	1770	1798	1814
3366	3367	3368	3379	3370	3371	3372	3373	3374	3375	3375
419	419	418	419	426	430	632	653	695	699	706
831	846	861	876	891	906	920	934	970	984	999
1003	1009	1116	1159	1212	1242	1284	1304	1324	1344	1364
1491	1509	1533	1560	1579	1597	1615	1632	1694	1766	1842
3034	3084	3099	3099	3291	3292	3313	3329	3330	3366	3370
417	416	418	419	426	430	430	433	435	439	436
900	924	938	938	950	970	974	984	988	999	999
1033	1047	1058	1062	1077	1077	1089	1093	1116	1120	1100
1129	1137	1138	1130	1130	1132	1132	1144	1148	1147	1159
1160	1155	1161	1176	1191	1195	1209	1213	1225	1239	1220
1601	1619	1619	1632	1636	1694	1698	1766	1770	1798	1814
3366	3367	3368	3379	3370	3371	3372	3373	3374	3375	3375
419	419	418	419	426	430	632	653	695	699	706
831	846	861	876	891	906	920	934	970	984	999
1003	1009	1116	1159	1212	1242	1284	1304	1324	1344	1364
1491	1509	1533	1560	1579	1597	1615	1632	1694	1766	1842
3034	3084	3099	3099	3291	3292	3313	3329	3330	3366	3370
417	416	418	419	426	430	430	433	435	439	436
900	924	938	938	950	970	974	984	988	999	999
1033	1047	1058	1062	1077	1077	1089	1093	1116	1120	1100
1129	1137	1138	1130	1130	1132	1132	1144	1148	1147	1159
1160	1155	1161	1176	1191	1195	1209	1213	1225	1239	1220
1601	1619	1619	1632	1636	1694	1698	1766	1770	1798	1814
3366	3367	3368	3379	3370	3371	3372	3373	3374	3375	3375

REF ID: A68820
 FILED: SEP 21 1976
 FBI - MEMPHIS

Sampler runtime 14 Seconds, 64 KCS, 379 disk reads, 3 disk writes, 92 pages
 Date 12-04-78 10:12:25 Host for 730-0 0270 (100) 000000
 000111111111111111111111111
 000000011111111122222222223333333344444444445555555555666666666677777777778888888888999999999900000000011111111112222222222333312
 00066600000000000000000000000000000000001111111111111111111111
 00000001111111112222222222333333333344444444445555555555666666666677777777778888888888999999999900000000011111111112222222222333312

1.39		000020	...F1	2555	016032	104402	...F5
1.40		000240	...H1	2611	016330	004	...G5
1.41			...I1	2667	016640	000000	...H5
1.42		000052	...J1	2723	017050	010037	...I5
1.43			...K1	2758			...J5
1.44	001100	000000	...L1	2814	017450	003720	...K5
1.45	001204	000000	...M1	2870			...L5
1.46	001314	000000	...N1	2926			...M5
1.47	001374	012161		2982	020334	036440	...N5
5.60	001420	014650	...B2	3038	020460	013746	...B6
6.12	001554	012737	...C2	3080	020614	010146	...C6
6.68	002104	012637	...D2	3132			...D6
7.24			...E2	3188	021210	002402	...E6
7.74	002600	023737	...F2	3244	021356	001006	...F6
8.22	003062	012737	...G2	3300	021610	062766	...G6
8.70	003252	012737	...H2	3356	022014	000200	...H6
9.15	003442	013737	...I2				...I6
9.71			...J2	ARBYCT	001466		...J6
10.22	004172	013737	...K2	CH16 =	000016		...K6
10.67	004366	013737	...L2	DIFERR	C15742		...L6
10.98	004504	001	...M2	NBEXT	001472		...M6
1.25	004610	010137	...N2	RSX2	016406		...N6
11.68	004776	010137	...B3				...B7
12.21	005232	001771	...C3	TST13	003530		...C7
12.51	005360	000010	...D3	VARLT1	017004		...D7
12.93	005544	001000	...E3	SDDWO	001264		...E7
13.33	005674	000003	...F3	SITEMB	001114		...F7
13.73	006030	000	...G3				...G7
14.17	006214	000	...H3	\$40CAT=	***** U		...H7
14.73			...I3	NEWST	311#	755	...I7
15.08	006642	000201	...J3	.SCMTA	190#	379#RE	...J7
15.51	007076	001001	...K3	BCC	1867	2455	...K7
16.06	007262	013737	...L3		1540	1550	...L7
16.41	007444	012077	...M3	TSTB	804	947	...M7
16.97	007712	000004	...N3		645	647	...N7
17.33	010202	001341	...B4		891	895	...B8
17.74			...C4	**END**	USER DAVIES, TOM		...C8
18.36			...D4				
18.92	010700	001750	...E4				
19.06			...F4				
19.62	011220	020103	...G4				
20.01	011402	000000	...H4				
20.52	011576	000000	...I4				
20.99	012012	050124	...J4				
21.55	012451	105	...K4				
22.11	013146	024040	...L4				
22.67	013606	020051	...M4				
23.23	014250	050111	...N4				