

# AR11

WRAPAROUND TEST 2  
MD-11-DZARC-B

EP-DZARC-B-DL-A  
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FICHE 1 OF 1

NOV 1976  
**digital**  
MADE IN USA





.REM %

IDENTIFICATION

PRODUCT CODE:	MAINDEC-11-DZARC-B
PRODUCT NAME:	ARI1 DIAGNOSTIC TEST III (WRAPAROUND TEST)
DATE:	MAY 21, 1976
MAINTAINER:	DIAGNOSTIC GROUP

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MAINDEC-11-DZARC-B  
MAY 21 1976















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%  
:TITLE MAINDEC-11-DZARC-B  
:\*COPYRIGHT (C) 1976  
:\*DIGITAL EQUIPMENT CORP.  
:\*MAYNARD, MASS. 01754  
:\*  
:\*PROGRAM BY RAYMOND SHOOP  
:\*  
:\*THIS PROGRAM WAS ASSEMBLED USING THE PDP-11 MAINDEC SYSMAC  
:\*PACKAGE (MAINDEC-11-DZGAC-B2), NOV 21, 1975.  
:\*

.SBTTL BASIC DEFINITIONS

001100

:\*INITIAL ADDRESS OF THE STACK POINTER \*\*\* 1100 \*\*\*  
STACK= 1100  
.EQUIV EMT,ERROR ;;BASIC DEFINITION OF ERROR CALL  
.EQUIV IOT,SCOPE ;;BASIC DEFINITION OF SCOPE CALL

.\*MISCELLANEOUS DEFINITIONS

000011  
000012  
000015  
000200  
177776  
  
177774  
177772  
177570  
177570

HT= 11 ;;CODE FOR HORIZONTAL TAB  
LF= 12 ;;CODE FOR LINE FEED  
CR= 15 ;;CODE FOR CARRIAGE RETURN  
CRLF= 200 ;;CODE FOR CARRIAGE RETURN-LINE FEED  
PS= 177776 ;;PROCESSOR STATUS WORD  
.EQUIV PS,PSW  
STKLMT= 177774 ;;STACK LIMIT REGISTER  
PIRG= 177772 ;;PROGRAM INTERRUPT REQUEST REGISTER  
DSWR= 177570 ;;HARDWARE SWITCH REGISTER  
DDISP= 177570 ;;HARDWARE DISPLAY REGISTER

.\*GENERAL PURPOSE REGISTER DEFINITIONS

000000  
000001  
000002  
000003  
000004  
000005  
000006  
000007

R0= %0 ;;GENERAL REGISTER  
R1= %1 ;;GENERAL REGISTER  
R2= %2 ;;GENERAL REGISTER  
R3= %3 ;;GENERAL REGISTER  
R4= %4 ;;GENERAL REGISTER  
R5= %5 ;;GENERAL REGISTER  
R6= %6 ;;GENERAL REGISTER  
R7= %7 ;;GENERAL REGISTER  
.EQUIV R6,SP ;;STACK POINTER  
.EQUIV R7,PC ;;PROGRAM COUNTER

.\*PRIORITY LEVEL DEFINITIONS

000000  
000040  
000100  
000140  
000200  
000240  
000300  
000340

PR0= 0 ;;PRIORITY LEVEL 0  
PR1= 40 ;;PRIORITY LEVEL 1  
PR2= 100 ;;PRIORITY LEVEL 2  
PR3= 140 ;;PRIORITY LEVEL 3  
PR4= 200 ;;PRIORITY LEVEL 4  
PR5= 240 ;;PRIORITY LEVEL 5  
PR6= 300 ;;PRIORITY LEVEL 6  
PR7= 340 ;;PRIORITY LEVEL 7

.\*"SWITCH REGISTER" SWITCH DEFINITIONS

100000  
040000

SW15= 100000  
SW14= 40000



020000  
010000  
004000  
002000  
001000  
000400  
000200  
000100  
000040  
000020  
000010  
000004  
000002  
000001

SW13= 20000  
SW12= 10000  
SW11= 4000  
SW10= 2000  
SW09= 1000  
SW08= 400  
SW07= 200  
SW06= 100  
SW05= 40  
SW04= 20  
SW03= 10  
SW02= 4  
SW01= 2  
SW00= 1  
.EQUIV SW09,SW9  
.EQUIV SW08,SW8  
.EQUIV SW07,SW7  
.EQUIV SW06,SW6  
.EQUIV SW05,SW5  
.EQUIV SW04,SW4  
.EQUIV SW03,SW3  
.EQUIV SW02,SW2  
.EQUIV SW01,SW1  
.EQUIV SW00,SW0

.\*DATA BIT DEFINITIONS (BIT00 TO BIT15)

BIT15= 100000  
BIT14= 40000  
BIT13= 20000  
BIT12= 10000  
BIT11= 4000  
BIT10= 2000  
BIT09= 1000  
BIT08= 400  
BIT07= 200  
BIT06= 100  
BIT05= 40  
BIT04= 20  
BIT03= 10  
BIT02= 4  
BIT01= 2  
BIT00= 1  
.EQUIV BIT09,BIT9  
.EQUIV BIT08,BIT8  
.EQUIV BIT07,BIT7  
.EQUIV BIT06,BIT6  
.EQUIV BIT05,BIT5  
.EQUIV BIT04,BIT4  
.EQUIV BIT03,BIT3  
.EQUIV BIT02,BIT2  
.EQUIV BIT01,BIT1  
.EQUIV BIT00,BIT0

.\*BASIC "CPU" TRAP VECTOR ADDRESSES

ERRVEC= 4 ;:TIME OUT AND OTHER ERRORS  
RESVEC= 10 ;:RESERVED AND ILLEGAL INSTRUCTIONS

100000  
040000  
020000  
010000  
004000  
002000  
001000  
000400  
000200  
000100  
000040  
000020  
000010  
000004  
000002  
000001

000004  
000010



301 000014  
 302 000014  
 303 000014  
 304 000020  
 305 000024  
 306 000030  
 307 000034  
 308 000060  
 309 000064  
 310 000240  
 311 170400  
 312 000340  
 313 000200

TBITVEC=14  
 TRTVEC= 14  
 BPTVEC= 14  
 IOTVEC= 20  
 PWRVEC= 24  
 EMTVEC= 30  
 TRAPVEC=34  
 TKVEC= 60  
 TPVEC= 64  
 PIRQVEC=240  
 ABASE=170400  
 AVECT1=340  
 APRIOR=200

:: "T" BIT  
 :: TRACE TRAP  
 :: BREAKPOINT TRAP (BPT)  
 :: INPUT/OUTPUT TRAP (IOT) \*\*SCOPE\*\*  
 :: POWER FAIL  
 :: EMULATOR TRAP (EMT) \*\*ERROR\*\*  
 :: "TRAP" TRAP  
 :: TTY KEYBOARD VECTOR  
 :: TTY PRINTER VECTOR  
 :: PROGRAM INTERRUPT REQUEST VECTOR



214  
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242

.SBTTL OPERATIONAL SWITCH SETTINGS

```

:*
:* SWITCH USE
:* -----
:* 15 HALT ON ERROR
:* 14 LOOP ON TEST
:* 13 INHIBIT ERROR TYPEOUTS
:* 12 FORCED PRINTOUT
:* 11 INHIBIT ITERATIONS
:* 10 BELL ON ERROR
:* 9 LOOP ON ERROR
:* 8 LOOP ON TEST IN SWR<7:0>

```

.SBTTL TRAP CATCHER

000000

```

.=0
;*ALL UNUSED LOCATIONS FROM 4 - 776 CONTAIN A ".+2,HALT"
;*SEQUENCE TO CATCH ILLEGAL TRAPS AND INTERRUPTS
;*LOCATION 0 CONTAINS 0 TO CATCH IMPROPERLY LOADED VECTORS

```

```

000174 000174
000176 000000

```

```

.=174
DISPREG: .WORD 0 ;;SOFTWARE DISPLAY REGISTER
SWREG: .WORD 0 ;;SOFTWARE SWITCH REGISTER

```

.SBTTL STARTING ADDRESS(ES)

```

000200 000137 001516
000204 000137 001536
000210 000137 001530

```

```

JMP @#BEGIN ;;JUMP TO STARTING ADDRESS OF PROGRAM
JMP BEGIN1 ;RESTART ADDRESS
JMP BEGIN2 ;STARTING ADDRESS OF OPTION TEST AREA

```



343  
344  
345  
346  
347  
348  
349  
350  
351 000046  
352  
353 000052  
354  
355  
356  
357  
358  
359  
360  
361  
362  
363  
364 000024  
365  
366 000044  
367  
368  
369  
370  
371  
372 001000  
373 001000 000000  
374 001002 001200  
375 001004 000020  
376 001006 000030  
377 001010 000120  
378 001012 000052

.SBTTL ACT11 HOOKS

```

;*****
;HOOKS REQUIRED BY ACT11
      $SVPC=.          ;SAVE PC
      .=46
      $ENDAD          ;;1)SET LOC.46 TO ADDRESS OF $ENDAD IN .SEOP
      .=52
      .WORD 0         ;;2)SET LOC.52 TO ZERO
      .=$SVPC        ;; RESTORE PC
      .=1000

```

.SBTTL APT PARAMETER BLOCK

```

;*****
;SET LOCATIONS 24 AND 44 AS REQUIRED FOR APT
;*****
      .$X=.          ;;SAVE CURRENT LOCATION
      .=24          ;;SET POWER FAIL TO POINT TO START OF PROGRAM
      200           ;;FOR APT START UP
      .=44          ;;POINT TO APT INDIRECT ADDRESS PNTR.
      $APTHDR       ;;POINT TO APT HEADER BLOCK
      .=.$X         ;;RESET LOCATION COUNTER
;*****
;SETUP APT PARAMETER BLOCK AS DEFINED IN THE APT-PDP11 DIAGNOSTIC
;INTERFACE SPEC.

```

```

$APTHD:
$HIBTS: .WORD 0      ;;TWO HIGH BITS OF 18 BIT MAILBOX ADDR.
$MADR: .WORD $MAIL  ;;ADDRESS OF APT MAILBOX (BITS 0-15)
$STMT: .WORD 20     ;;RUN TIM OF LONGEST TEST
$PASTM: .WORD 30    ;;RUN TIME IN SECS. OF 1ST PASS ON 1 UNIT (QUICK VERIFY)
$UNITM: .WORD 120   ;;ADDITIONAL RUN TIME (SECS) OF A PASS FOR EACH ADDITIONAL UNIT
          .WORD $ETEND-$MAIL/2 ;;LENGTH MAILBOX-ETABLE(WORDS)

```



```

379
380
381
382
383
384
385
386 001100
387 001100
388 001100 000000
389 001102 000
390 001103 000
391 001104 000000
392 001106 000000
393 001110 000000
394 001112 000000
395 001114 000
396 001115 001
397 001116 000000
398 001120 000000
399 001122 000000
400 001124 000000
401 001126 000000
402 001130 000000
403 001132 000000
404 001134 000000
405 001136 177570
406 001140 177570
407 001142 177560
408 001144 177562
409 001146 177564
410 001150 177566
411 001152 000
412 001153 002
413 001154 012
414 001155 000
415 001156 000000
416
417 001160 000000
418 001162 000000
419 001164 000000
420 001166 000000
421 001170 177607 000377
422 001174 077
423 001175 015
424 001176 000012

```

.SBTTL COMMON TAGS

```

*****
*THIS TABLE CONTAINS VARIOUS COMMON STORAGE LOCATIONS
*USED IN THE PROGRAM.

```

```

.=1100
$CMTAG: .WORD 0 ;; START OF COMMON TAGS
$STNM: .BYTE 000 ;; CONTAINS THE TEST NUMBER
$ERFLG: .BYTE 000 ;; CONTAINS ERROR FLAG
$ICNT: .WORD 000 ;; CONTAINS SUBTEST ITERATION COUNT
$LPADR: .WORD 000 ;; CONTAINS SCOPE LOOP ADDRESS
$LPERR: .WORD 000 ;; CONTAINS SCOPE RETURN FOR ERRORS
$ERTTL: .WORD 000 ;; CONTAINS TOTAL ERRORS DETECTED
$ITEMB: .BYTE 000 ;; CONTAINS ITEM CONTROL BYTE
$ERMAX: .BYTE 001 ;; CONTAINS MAX. ERRORS PER TEST
$ERRPC: .WORD 000 ;; CONTAINS PC OF LAST ERROR INSTRUCTION
$GDADR: .WORD 000 ;; CONTAINS ADDRESS OF 'GOOD' DATA
$BDADR: .WORD 000 ;; CONTAINS ADDRESS OF 'BAD' DATA
$GDDAT: .WORD 000 ;; CONTAINS 'GOOD' DATA
$BDDAT: .WORD 000 ;; CONTAINS 'BAD' DATA
        .WORD 000 ;; RESERVED--NOT TO BE USED
        .WORD 000
        .WORD 0
SWR: .WORD DSWR ;; ADDRESS OF SWITCH REGISTER
DISPLAY: .WORD DDISP ;; ADDRESS OF DISPLAY REGISTER
$TKS: 177560 ;; TTY KBD STATUS
$TKB: 177562 ;; TTY KBD BUFFER
$TPS: 177564 ;; TTY PRINTER STATUS REG. ADDRESS
$TPB: 177566 ;; TTY PRINTER BUFFER REG. ADDRESS
$NULL: .BYTE 0 ;; CONTAINS NULL CHARACTER FOR FILLS
$FILLS: .BYTE 2 ;; CONTAINS # OF FILLER CHARACTERS REQUIRED
$FILLC: .BYTE 12 ;; INSERT FILL CHARS. AFTER A "LINE FEED"
$TPFLG: .BYTE 0 ;; "TERMINAL AVAILABLE" FLAG (BIT<07>=0=YES)
$REGAD: .WORD 0 ;; CONTAINS THE ADDRESS FROM
        WHICH ($REGD) WAS OBTAINED
$REGD: .WORD 0 ;; CONTAINS (($REGAD)+0)
$REGI: .WORD 0 ;; CONTAINS (($REGAD)+2)
$TIMES: 0 ;; MAX. NUMBER OF ITERATIONS
$ESCAPE: 0 ;; ESCAPE ON ERROR ADDRESS
$BELL: .ASCIZ <207><377><377> ;; CODE FOR BELL
$QUES: .ASCII /?/ ;; QUESTION MARK
$CRLF: .ASCII <15> ;; CARRIAGE RETURN
$LF: .ASCIZ <12> ;; LINE FEED

```





481 001272 000000  
 482 001274 000000  
 483 001276 000000  
 484 001300 000000  
 485 001302 000000  
 486 001304 000000  
 487 001306 000000  
 488 001310 000000  
 489 001312 000000  
 490 001314 000000  
 491 001316 000000  
 492 001320 000000  
 493 001322 000000

\$DDW3: .WORD ADDW3 ;;DEVICE DESCRIPTOR WORD#3  
 \$DDW4: .WORD ADDW4 ;;DEVICE DESCRIPTOR WORD#4  
 \$DDW5: .WORD ADDW5 ;;DEVICE DESCRIPTOR WORD#5  
 \$DDW6: .WORD ADDW6 ;;DEVICE DESCRIPTOR WORD#6  
 \$DDW7: .WORD ADDW7 ;;DEVICE DESCRIPTOR WORD#7  
 \$DDW8: .WORD ADDW8 ;;DEVICE DESCRIPTOR WORD#8  
 \$DDW9: .WORD ADDW9 ;;DEVICE DESCRIPTOR WORD#9  
 \$DDW10: .WORD ADDW10 ;;DEVICE DESCRIPTOR WORD#10  
 \$DDW11: .WORD ADDW11 ;;DEVICE DESCRIPTOR WORD#11  
 \$DDW12: .WORD ADDW12 ;;DEVICE DESCRIPTOR WORD#12  
 \$DDW13: .WORD ADDW13 ;;DEVICE DESCRIPTOR WORD#13  
 \$DDW14: .WORD ADDW14 ;;DEVICE DESCRIPTOR WORD#14  
 \$DDW15: .WORD ADDW15 ;;DEVICE DESCRIPTOR WORD#15

494 001324

SETEND:

495  
 496  
 497  
 498  
 499  
 500  
 501  
 502  
 503  
 504  
 505  
 506  
 507  
 508  
 509  
 510  
 511  
 512

.SBTTL ERROR POINTER TABLE

;\*THIS TABLE CONTAINS THE INFORMATION FOR EACH ERROR THAT CAN OCCUR.  
 ;\*THE INFORMATION IS OBTAINED BY USING THE INDEX NUMBER FOUND IN  
 ;\*LOCATION \$ITEMB. THIS NUMBER INDICATES WHICH ITEM IN THE TABLE IS PERTINENT.  
 ;\*NOTE1: IF \$ITEMB IS 0 THE ONLY PERTINENT DATA IS (\$ERRPC).  
 ;\*NOTE2: EACH ITEM IN THE TABLE CONTAINS 4 POINTERS EXPLAINED AS FOLLOWS:

;\* EM ;;POINTS TO THE ERROR MESSAGE  
 ;\* DH ;;POINTS TO THE DATA HEADER  
 ;\* DT ;;POINTS TO THE DATA  
 ;\* DF ;;POINTS TO THE DATA FORMAT

513 001324

\$ERRTB:

516 001324 011736  
 517 001326 012451  
 518 001330 014572  
 519 001332 000000

; ITEM 1  
 EM1 ;ERROR ON A/D CHANNEL  
 DH1 ;ERRPC ARADD CHANNEL NOMINAL TOLERANCE ACTUAL  
 DT1 ;\$ERRPC ARBADD CHANL \$GDDAT SPREAD \$BDDAT  
 0

522 001334 011763  
 523 001336 012530  
 524 001340 014610  
 525 001342 000000

; ITEM 2  
 EM2 ;VC LOGIC SIGNAL HIGH OUTPUT TOO LOW  
 DH2 ;ERRPC ARADD CHANNEL 3V LEV. OUTPUT  
 DT2 ;\$ERRPC ARBADD CHANL \$GDDAT \$BDDAT  
 0

528 001344 012027  
 529 001346 012577  
 530 001350 014624  
 531 001352 000000

; ITEM 3  
 EM3 ;VC STATUS REGISTER IN ERROR  
 DH3 ;ERRPC ARADD GOOD BAD  
 DT3 ;\$ERRPC ARBADD \$GDDAT \$BDDAT  
 0

534 001354 012063  
 535 001356 012577  
 536 001360 014624

; ITEM 4  
 EM4 ;EXTERNAL AD START FAILED  
 DH3 ;ERRPC ARADD GOOD BAD  
 DT3 ;\$ERRPC ARBADD \$GDDAT \$BDDAT

537	001362	000000
538		
539		
540	001364	012120
541	001366	012634
542	001370	014636
543	001372	000000
544		
545		
546	001374	012161
547	001376	012674
548	001400	014624
549	001402	000000
550		

:ITEM

0  
5  
DHS  
DTS  
0

:A/D DIFFERENTIAL LINEARITY ERROR  
:ERRPC ARADD # OF STATES ALLOWED  
:SERRPC ARBADD \$BDDAT \$GDDAT

:ITEM

6  
DHS  
DTS  
0

:NOISE LEVEL EXCEEDED LIMIT  
:ERRPC ARADD LIMIT MEASURED  
:SERRPC ARBADD \$GDDAT \$BDDAT



001404	012214	:ITEM 7	EM7	:VC LOGIC SIGNAL LOW OUTPUT TOO HIGH
001406	012735		DH7	:ERRPC ARADD A/D CHAN +.4V LEV. OUTPUT
001410	014610		DT2	:SERRPC ARBADD CHANL \$GDDAT \$BDDAT
001412	000000		0	
001414	012260	:ITEM 10	EM10	:D/A DIFFERENTIAL LINEARITY ERROR
001416	013004		DH10	:ERRPC ARADD STEP NOMINAL SPREAD ACTUAL
001420	014650		DT10	:SERRPC ARBADD EDGE \$GDDAT SPREAD \$BDDAT
001422	000000		0	
001424	012321	:ITEM 11	EM11	:A/D INTER-CHANNEL SETTling ERROR
001426	013063		DH11	:ERRPC ARADD NOMINAL SPREAD ACTUAL
001430	014666		DT11	:SERRPC ARBADD \$GDDAT SPREAD \$BDDAT
001432	000000		0	
001434	012362	:ITEM 12	EM12	:OFFSET ERROR
001436	013063		DH11	:ERRPC ARADD NOMINAL SPREAD ACTUAL
001440	014666		DT11	:SERRPC ARBADD \$GDDAT SPREAD \$BDDAT
001442	000000		0	
001444	012377	:ITEM 13	EM13	:CALIBRATION ERROR
001446	013063		DH11	:ERRPC ARADD NOMINAL SPREAD ACTUAL
001450	014666		DT11	:SERRPC ARBADD \$GDDAT SPREAD \$BDDAT
001452	000000		0	
001454	012421	:ITEM 14	EM14	:LINEARITY ERROR AT XX00
001456	013063		DH11	:ERRPC ARADD NOMINAL SPREAD ACTUAL
001460	014666		DT11	:SERRPC ARBADD \$GDDAT SPREAD \$BDDAT
001462	000000		0	
001464	170400	ARBADD:	170400	
001466	000340	ARBVCT:	340	
001470	000000	NMBEXT:	0	
001472	000000	NBEXT:	0	
001474	170400	ADCS:	170400	:A TO D STATUS/CONTROL REGISTER
001476	170401	ADCS1:	170401	:A TO D STATUS REGISTER <HIGH BYTE>
001500	170402	ADDBR:	170402	:A TO D CONVERTED VALUE <READ>
001502	170404	CSR:	170404	:CLOCK STATUS REGISTER
001504	170406	CSB:	170406	:CLOCK PRESET BUFFER
001506	170410	VCSTAT:	170410	:DAC STATUS REGISTER
001510	170412	VCXREG:	170412	:X BUFFER
001512	170414	VCYREG:	170414	:Y BUFFER
001514	170416	CSC:	170416	:CLOCK COUNTER
		.SBTTL		PROGRAM START-UP

```

00000000 001516 005000 BEGIN: CLR R0 ;CLEAR R0
00000004 001520 005037 016762 CLR WFTST ;CLEAR TOLERANCE FLAG
00000008 001524 000406 BR RBEG
00000012 001528 000406 BR RBEG
00000016 001532 012737 000001 016762 BEGIN2: MOV #1,WFTST ;SET TOLERANCE FLAG
00000020 001536 012700 177777 BEGIN1: MOV #-1,R0 ;LOAD R0
00000024 001540 000005 RBEG: RESET
00000028 001544 005037 177776 CLR PS
00000032 001548 012706 001100 MOV #STACK,SP ;LOAD STACK
00000036 001552 012737 001602 000004 MOV #15,0#4 ;LOAD BUS ERROR
00000040 001556 012702 001254 MOV $BASE,R2 ;LOAD STARTING ADDRESS
00000044 001560 005003 CLR R3 ;CLEAR COUNT
00000048 001564 005712 25: TST (R2) ;TEST IF EXISTENT
00000052 001568 062702 000020 ADD #20,R2 ;EXIST, UPDATE TEST ADDRESS
00000056 001572 005203 INC R3 ;UPDATE # OF AR11'S
00000060 001600 000773 15: BR 25
00000064 001604 022626 CMP (SP)+,(SP)+ ;POP STACK
00000068 001608 005703 TST R3 ;TEST IF FIRST DOES EXIST
00000072 001612 001002 BNE 35 ;BR
00000076 001616 000000 HALT ;FIRST AR11 DOES NOT EXIST
00000080 001620 000741 BR BEGIN ;CHECK THE PROGRAM DEVICE ADDRESS
00000084 001624 005303 35: DEC R3 ;ADJUST R3
00000088 001628 010337 001470 MOV R3,NMBEXT ;SAVE THE NUMBER OF ADDITIONAL AR11'S
00000092 001632 012737 000006 000004 MOV #6,0#4 ;RESET BUS ERROR
00000096 001636 005037 000006 CLR 0#6
00000100 001640 013737 001254 001464 MOV $BASE,ARBADD ;LOAD FIRST ADDRESS
00000104 001644 013737 001250 001466 MOV $VECT1,ARBVCT ;LOAD FIRST VECTOR
00000108 001648 013737 001470 001472 MOV NMBEXT,NBEXT ;LOAD NUMBER OF ADDITIONAL AR11'S
00000112 001652 000005 RESET
00000116 001660 012706 001100 ;;CLEAR THE COMMON TAGS ($CMTAG) AREA
00000120 001664 005026 MOV #CMTAG,R6 ;;FIRST LOCATION TO BE CLEARED
00000124 001668 022706 001126 CLR (R6)+ ;;CLEAR MEMORY LOCATION
00000128 001672 001374 CMP #SBDDAT,R6 ;;DONE?
00000132 001676 012706 001100 BNE -6 ;;LOOP BACK IF NO
00000136 001700 012737 017172 000020 MOV #SCOPE,0#IOTVEC ;;IOT VECTOR FOR SCOPE ROUTINE
00000140 001704 012737 000340 000022 MOV #340,0#IOTVEC+2 ;;LEVEL 7
00000144 001708 012737 017452 000030 MOV #ERROR,0#EMTVEC ;;EMT VECTOR FOR ERROR ROUTINE
00000148 001712 012737 000340 000032 MOV #340,0#EMTVEC+2 ;;LEVEL 7
00000152 001716 012737 021774 000034 MOV #TRAP,0#TRAPVEC ;;TRAP VECTOR FOR TRAP CALLS
00000156 001720 012737 000340 000036 MOV #340,0#TRAPVEC+2 ;;LEVEL 7
00000160 001724 012737 020576 000024 MOV #PWRDN,0#PWRVEC ;;POWER FAILURE VECTOR
00000164 001728 012737 000340 000026 MOV #340,0#PWRVEC+2 ;;LEVEL 7
00000168 001732 013737 010404 010376 MOV $ENDCT,$EOPCT ;;SETUP END-OF-PROGRAM COUNTER
00000172 001736 005037 001164 CLR $TIMES ;;INITIALIZE NUMBER OF ITERATIONS
00000176 001740 005037 001166 CLR $ESCAPE ;;CLEAR THE ESCAPE ON ERROR ADDRESS
00000180 001744 112737 000001 001115 MOVB #1,$ERMAX ;;ALLOW ONE ERROR PER TEST
00000184 001748 012737 002004 001106 MOV #,$SLPADR ;;INITIALIZE THE LOOP ADDRESS FOR SCOPE
00000188 001752 012737 002012 001110 MOV #,$SLPERR ;;SETUP THE ERROR LOOP ADDRESS
00000192 002020 013746 000004 ;;SIZE FOR A HARDWARE SWITCH REGISTER. IF NOT FOUND OR IT IS
00000196 002024 012737 002062 000004 ;;EQUAL TO A "-1", SETUP FOR A SOFTWARE SWITCH REGISTER.
00000200 002028 012737 177570 001136 MOV 0#ERRVEC,-(SP) ;;SAVE ERROR VECTOR
00000204 002032 012737 177570 001140 MOV #64,$0#ERRVEC ;;SET UP ERROR VECTOR
00000208 002036 012737 177570 001140 MOV #DSWR,SWR ;;SETUP FOR A HARDWARE SWICH REGISTER
00000212 002040 012737 177570 001140 MOV #DDISP,DISPLAY ;;AND A HARDWARE DISPLAY REGISTER

```





715	002512	001373	
716	002514	005237	001476
717	002520	012700	001500
718	002524	012701	000002
719	002530	060120	
720	002532	005721	
721	002534	022701	000020
722	002540	001373	

```

BNE 10$
INC ADCS1
MOV #ADDBR,R0
MOV #2,R1
12$: ADD R1,(R0)+
TST (R1)+
CMP #20,R1
BNE 12$
.SBTTL A TO D CHANNEL NUMBERS

```

:BIPOLAR CHANNEL NUMBERS

723	000000
724	000001
725	000002
726	000003
727	000004
728	000005
729	000006
730	000007
731	000010
732	000011
733	000012
734	000013
735	000014
736	000015
737	000016
738	000017

```

CH0= 0
CH1= 1
CH2= 2
CH3= 3
CH4= 4
CH5= 5
CH6= 6
CH7= 7
CH10= 10
CH11= 11
CH12= 12
CH13= 13
CH14= 14
CH15= 15
CH16= 16
CH17= 17

```

:UNIPOLAR CHANNEL NUMBERS

745	000040
746	000041
747	000042
748	000043
749	000044
750	000045
751	000046
752	000047
753	000050
754	000051
755	000052
756	000053
757	000054
758	000055
759	000056
760	000057

```

CH40= 40
CH41= 41
CH42= 42
CH43= 43
CH44= 44
CH45= 45
CH46= 46
CH47= 47
CH50= 50
CH51= 51
CH52= 52
CH53= 53
CH54= 54
CH55= 55
CH56= 56
CH57= 57

```

```

.SBTTL
.SBTTL TEST NUMBER ABSTRACT
.SBTTL

```



F02

MAINDEC-11-DZARC-B  
DZARCB.P11 T1

MACY11 27(732) 21-SEP-76 16:44 PAGE 18  
TEST THAT "ERASE" AND DONE CAN BE SET AND CLEARED

```

765      ::*****
766      :*TEST 1      TEST THAT "ERASE" AND DONE CAN BE SET AND CLEARED
767      :******
768      TST1:  SCOPE
769      MOV      #10,$TIMES      ;;DO 10 ITERATIONS
770      CLR      @VCSTAT        ;CLEAR
771      MOV      #BIT12,$GDDAT   ;LOAD EXPECTED
772      MOV      $GDDAT,@VCSTAT  ;LOAD REG
773      MOV      @VCSTAT,$BDDAT  ;READ REG
774      CMP      $GDDAT,$BDDAT   ;COMPARE
775      BEQ      1$              ;;BR IF SET
776      ERROR   3                ;ERASE FAILED TO SET, CHECK G5036-
777      ;BCOBR CONNECTION: A-VV, VV-A
778      1$:  MOV      #BIT9,$GDDAT ;LOAD EXPECTED
779      BIS      #BIT9,@VCSTAT    ;SET CH 2 BIT
780      MOV      @VCSTAT,$BDDAT  ;READ REG
781      CMP      $GDDAT,$BDDAT   ;COMPARE
782      BEQ      2$              ;;BR IF CLEARED
783      ERROR   3                ;ERASE BIT FAILED TO CLEAR BY ERASE
784      ;RETURN (SETTING OF THE CH 2 BIT)
785
786      2$:  MOV      #BIT7,$GDDAT ;LOAD EXPECTED
787      BIC      #BIT9,@VCSTAT    ;CLEAR CH 02
788      MOV      @VCSTAT,$BDDAT  ;READ REG
789      CMP      $GDDAT,$BDDAT   ;COMPARE
790      BEQ      TST2            ;;BR IF EQUAL
791      ERROR   3                ;READY FAILED TO SET ON THE END
792      ; OF ERASE RETURN (CLEARING CH 2)
793
794      :*TEST 2      EXTERNAL A TO D START FROM INTENSIFY
795      :******
796      TST2:  SCOPE
797      MOV      #10,$TIMES      ;;DO 10 ITERATIONS
798      CLR      @VCSTAT        ;CLEAR DONE
799      MOV      @ADDBR,@ADDBR
800      CLR      @ADCS
801      MOV      #BIT7!BIT4,$GDDAT ;LOAD EXPECTED
802      MOV      $GDDAT,@ADCS    ;LOAD EXTERNAL A/D START ENABLE
803      INC      @VCSTAT        ;INTENSIFY
804      TSTB   @VCSTAT          ;WAIT FOR READY
805      BPL      1$
806      MOV      #BIT8,TEMP      ;SET UP A COUNTER
807      DEC     TEMP            ;DELAY
808      BPL     2$
809      MOV     @ADCS,$BDDAT    ;READ REG.
810      CMP     $GDDAT,$BDDAT  ;COMPARE RESULTS
811      BEQ     3$              ;;BR IF EQUAL
812      ERROR  4                ;INTENSIFY PULSE FAILED TO START
813      MOV     @ADDBR,@ADDBR  ; AN A TO D CONVERSION
814      CLR     @VCSTAT
815      CLR     @ADCS

```

```

016
017
018
019 003030 000004
020 003032 012737 000010 001164
021 003040 012737 001000 001124
022 003046 004537 011534
023 003052 000000
024 003054 013737 011656 001126
025 003062 012737 000001 011664
026 003070 004737 011666
027 003074 000401
028 003076 104001
029
030
031
032
033
034 003100 000004
035 003102 012737 000010 001164
036 003110 012737 000000 001124
037 003116 004537 011534
038 003122 000040
039 003124 013737 011656 001126
040 003132 012737 000001 011664
041 003140 004737 011666
042 003144 000401
043 003146 104001
044
045
046
047
048
049 003150 000004
050 003152 012737 000010 001164
051 003160 012737 001315 001124
052 003166 004537 011534
053 003172 000002
054 003174 013737 011656 001126
055 003202 012737 000024 011664
056 003210 004737 011666
057 003214 000401
058 003216 104001
059
060

```

```

*****
*TEST 3 TEST THAT CH 0 IS A BIPOLAR GROUND
*****
TST3: SCOPE
MOV #10,$TIMES ;;DO 10 ITERATIONS
MOV #1000,$GDDAT ;LOAD EXPECTED VALUE
JSR R5,CONVRT ;CONVERT
CH0 ;CH 0
MOV ADEND,$BDDAT ;LOAD VALUE READ
MOV #1,SPREAD ;LOAD + OR - COUNT SPREAD
JSR PC,COMPAR ;COMPARE $GDDAT AND $BDDAT
BR TST4 ;;BR IF WITHIN TOLERANCE
ERROR 1 ;CH 0 FAILED TO EQUAL EXPECTED
; VALUE

```

```

*****
*TEST 4 TEST THAT CH 40 IS A UNIPOLAR GROUND
*****
TST4: SCOPE
MOV #10,$TIMES ;;DO 10 ITERATIONS
MOV #0,$GDDAT ;LOAD EXPECTED VALUE
JSR R5,CONVRT ;CONVERT
CH40 ;CH 40
MOV ADEND,$BDDAT ;LOAD VALUE READ
MOV #1,SPREAD ;LOAD + OR - COUNT SPREAD
JSR PC,COMPAR ;COMPARE $GDDAT AND $BDDAT
BR TST5 ;;BR IF WITHIN TOLERANCE
ERROR 1 ;CH 40 FAILED TO EQUAL EXPECTED
; VALUE

```

```

*****
*TEST 5 TEST THAT CH 2 IS AT +1 VOLT BIPOLAR
*****
TST5: SCOPE
MOV #10,$TIMES ;;DO 10 ITERATIONS
MOV #1315,$GDDAT ;LOAD EXPECTED VALUE
JSR R5,CONVRT ;CONVERT
CH2 ;CH 2
MOV ADEND,$BDDAT ;LOAD VALUE READ
MOV #24,SPREAD ;LOAD + OR - COUNT SPREAD
JSR PC,COMPAR ;COMPARE $GDDAT AND $BDDAT
BR TST6 ;;BR IF WITHIN TOLERANCE
ERROR 1 ;CH 2 FAILED TO EQUAL EXPECTED
; VALUE

```



861  
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902  
903  
904  
905

003220	000004		
003222	012737	000010	001164
003230	012737	000315	001124
003236	004537	011534	
003242	000042		
003244	013737	011656	001126
003252	012737	000024	011664
003260	004737	011666	
003264	000401		
003266	104001		
003270	000004		
003272	012737	000010	001164
003300	013737	016764	001124
003306	004537	011534	
003312	000003		
003314	013737	011656	001126
003322	013737	016766	011664
003330	004737	011666	
003334	000401		
003336	104001		
003340	000004		
003342	012737	000010	001164
003350	012737	001000	001124
003356	004537	011534	
003362	000043		
003364	013737	011656	001126
003372	013737	016770	011664
003400	004737	011666	
003404	000401		
003406	104001		

```

*****
*TEST 6 TEST THAT CH 42 IS AT +1 VOLT UNIPOLAR
*****
TST6: SCOPE
MOV #10,$TIMES ;;DO 10 ITERATIONS
MOV #315,$GDDAT ;LOAD EXPECTED VALUE
JSR R5,CONVRT ;CONVERT
CH42 ;CH 42
MOV ADEND,$BDDAT ;LOAD VALUE READ
MOV #24,SPREAD ;LOAD + OR - COUNT SPREAD
JSR PC,COMPAR ;COMPARE $GDDAT AND $BDDAT
BR TST7 ;;BR IF WITHIN TOLERANCE
ERROR 1 ;CH 42 FAILED TO EQUAL EXPECTED
; VALUE

*****
*TEST 7 TEST THAT CH 3 IS AT +2.5 BIPOLAR
*****
TST7: SCOPE
MOV #10,$TIMES ;;DO 10 ITERATIONS
MOV V1754,$GDDAT ;LOAD EXPECTED VALUE
JSR R5,CONVRT ;CONVERT
CH3 ;CH 3
MOV ADEND,$BDDAT ;LOAD VALUE READ
MOV V24,SPREAD ;LOAD + OR - COUNT SPREAD
JSR PC,COMPAR ;COMPARE $GDDAT AND $BDDAT
BR TST10 ;;BR IF WITHIN TOLERANCE
ERROR 1 ;CH 3 FAILED TO EQUAL EXPECTED
; VALUE

*****
*TEST 10 TEST THAT CH 43 IS AT +2.5 UNIPOLAR
*****
TST10: SCOPE
MOV #10,$TIMES ;;DO 10 ITERATIONS
MOV #1000,$GDDAT ;LOAD EXPECTED VALUE
JSR R5,CONVRT ;CONVERT
CH43 ;CH 43
MOV ADEND,$BDDAT ;LOAD VALUE READ
MOV V50,SPREAD ;LOAD + OR - COUNT SPREAD
JSR PC,COMPAR ;COMPARE $GDDAT AND $BDDAT
BR TST11 ;;BR IF WITHIN TOLERANCE
ERROR 1 ;CH 43 FAILED TO EQUAL EXPECTED
; VALUE

```

```

906
907
908
909 003410 000004
910 003412 012737 000010 001164
911 003420 013737 016766 001124
912 003426 004537 011534
913 003432 000004
914 003434 013737 011656 001126
915 003442 013737 016766 011664
916 003450 004737 011666
917 003454 000401
918 003456 104001

```

```

*****
*TEST 11 TEST THAT CH 4 IS AT -2.5 BIPOLAR
*****
TST11: SCOPE
MOV #10,$TIMES ;;DO 10 ITERATIONS
MOV V24,$GDDAT ;LOAD EXPECTED VALUE
JSR R5,CONVRT ;CONVERT
CH4 ;CH 4
MOV ADEND,$BDDAT ;LOAD VALUE READ
MOV V24,SPREAD ;LOAD + OR - COUNT SPREAD
JSR PC,COMPAR ;COMPARE $GDDAT AND $BDDAT
BR TST12 ;;BR IF WITHIN TOLERANCE
ERROR 1 ;CH 4 FAILED TO EQUAL EXPECTED VALUE

```

```

919
920
921
922
923 003460 000004
924 003462 012737 000010 001164
925 003470 012737 001463 001124
926 003476 004537 011534
927 003502 000057
928 003504 013737 011656 001126
929 003512 012737 000120 011664
930 003520 004737 011666
931 003524 000401
932 003526 104001

```

```

*****
*TEST 12 TEST THAT CH 57 IS AT +4 VOLTS UNIPOLAR
*****
TST12: SCOPE
MOV #10,$TIMES ;;DO 10 ITERATIONS
MOV #1463,$GDDAT ;LOAD EXPECTED VALUE
JSR R5,CONVRT ;CONVERT
CH57 ;CH 57
MOV ADEND,$BDDAT ;LOAD VALUE READ
MOV #120,SPREAD ;LOAD + OR - COUNT SPREAD
JSR PC,COMPAR ;COMPARE $GDDAT AND $BDDAT
BR TST13 ;;BR IF WITHIN TOLERANCE
ERROR 1 ;CH 57 FAILED TO EQUAL EXPECTED VALUE

```

```

933
934
935
936
937 003530 000004
938 003532 012737 000004 001164
939 003540 012737 000041 011662
940 003546 112777 000041 175722
941 003554 013737 016772 001124
942 003562 013737 016772 011664
943 003570 012737 000200 015216
944 003576 005337 015216
945 003602 001375
946 003604 005277 175664
947 003610 105777 175660
948 003614 100375
949 003616 017737 175656 001126
950 003624 013737 001126 015164
951 003632 004737 011666
952 003636 000401
953 003640 104001
954
955 003642 032777 010000 175266
956 003650 001432
957 003652 104400 003660
958 003656 000412
959
960 003704
961 003704 013746 001464

```

```

*****
*TEST 13 TEST THAT CH 41 IS LESS THAN +200 MVOLTS UNIPOLAR
*****
TST13: SCOPE
MOV #4,$TIMES ;;DO 4 ITERATIONS
MOV #CH41,CHANL ;LOAD CHANNEL # FOR ERROR TYP0UT
MOVB #CH41,ADCS1 ;LOAD MUX
MOV VA24,$GDDAT ;LOAD EXPECTED VALUE
MOV VA24,SPREAD ;LOAD + OR - COUNT SPREAD
MOV #BIT7,TEMP ;LOAD DELAY
2$: DEC TEMP ;DELAY
BNE 2$
INC ADCS ;CONVERT CH 41
3$: TSTB ADCS ;DONE ?
BPL 3$
MOV ADDBR,$BDDAT ;READ RESULTS
MOV $BDDAT,BIASCI ;SAVE RESULTS
JSR PC,COMPAR ;COMPARE $GDDAT AND $BDDAT
BR 1$ ;;BR IF WITHIN TOLERANCE
ERROR 1 ;CH 41 FAILED TO EQUAL EXPECTED
; VALUE, BIAS CURRENT TOO HIGH
; TEST FORCED SW
1$: BIT #BIT12,ASWR
BEQ TST14 ;;BR IF NOT FORCED PRINTOUT
TYPE 65$ ;TYPE ASCIZ STRING
BR 64$ ;GET OVER THE ASCIZ
;;65$: .ASCIZ <15><12><12>/AR-11 ADDRESS = /
64$: MOV ARBADD,-(SP) ;SAVE ADDRESS

```



962	003710	104401		
963	003712	104400	016536	
964	003716	104400	013132	
965	003722	013746	015164	
966	003726	104402		
967	003730	003	000	
968	003732	104400	013146	

```

TYP0C
TYPE ,ACRLF
TYPE ,BIASST
MOV ,BIASCI,-(SP)
TYPOS
.BYTE 3,0
TYPE ,BIASDN

```

```

*****
*TEST 14 TEST THAT ERASE L (CH 53) IS GREATER THAN +3 VOLTS WHEN HIGH
*****

```

973	003736	000004		
974	003740	012737	000010	001164
975	003746	012777	005000	175532
976	003754	012737	001146	001124
977	003762	004537	011534	
978	003766	000053		
979	003770	013737	011656	001126
980	003776	023737	001124	001126
981	004004	003401		
982	004006	104002		

```

TST14: SCOPE
MOV #10,$TIMES ;;DO 10 ITERATIONS
MOV #BIT11!BIT9,@VCSTAT ;LOAD VC STATUS
MOV #1146,$GDDAT ;LOAD EXPECTED VALUE
JSR R5,CONVRT ;CONVERT
CH53 ;CH 53
MOV ADEND,$BDDAT ;LOAD VALUE READ
CMP $GDDAT,$BDDAT ;COMPARE
BLE TST15 ;;BR IF LESS THAN OR EQUAL
ERROR 2 ;CH 53 FAILED TO EQUAL EXPECTED VALUE

```

```

*****
*TEST 15 TEST THAT ERASE L (CH 53) IS LESS THAN +400 MVOLTS WHEN LOW
*****

```

987	004010	000004		
988	004012	012737	000010	001164
989	004020	005077	175462	
990	004024	012777	012000	175454
991	004032	012737	000120	001124
992	004040	004537	011534	
993	004044	000053		
994	004046	013737	011656	001126
995	004054	023737	001124	001126
996	004062	002001		
997	004064	104007		

```

TST15: SCOPE
MOV #10,$TIMES ;;DO 10 ITERATIONS
CLR @VCSTAT ;CLEAR VC STATUS
MOV #BIT12!BIT10,@VCSTAT ;SET ERASE
MOV #120,$GDDAT ;LOAD EXPECTED VALUE
JSR R5,CONVRT ;CONVERT
CH53 ;CH 53
MOV ADEND,$BDDAT ;LOAD VALUE READ
CMP $GDDAT,$BDDAT ;COMPARE
BGE TST16 ;;BR IF GREATER THAN OR EQUAL
ERROR 7 ;CH 53 FAILED TO EQUAL EXPECTED VALUE

```

```

*****
*TEST 16 TEST THAT WRITE-THRU (CH 54) IS GREATER THAN +3 VOLTS WHEN HIGH
*****

```

1002	004066	000004		
1003	004070	012737	000010	001164
1004	004076	012777	011000	175402
1005	004104	012737	001146	001124
1006	004112	004537	011534	
1007	004116	000054		
1008	004120	013737	011656	001126
1009	004126	023737	001124	001126
1010	004134	003401		
1011	004136	104002		

```

TST16: SCOPE
MOV #10,$TIMES ;;DO 10 ITERATIONS
MOV #BIT12!BIT9,@VCSTAT ;LOAD VC STATUS
MOV #1146,$GDDAT ;LOAD EXPECTED VALUE
JSR R5,CONVRT ;CONVERT
CH54 ;CH 54
MOV ADEND,$BDDAT ;LOAD VALUE READ
CMP $GDDAT,$BDDAT ;COMPARE
BLE TST17 ;;BR IF LESS THAN OR EQUAL
ERROR 2 ;CH 54 FAILED TO EQUAL EXPECTED VALUE

```

K02

MAINDEC-11-DZARC-B  
DZARCB.F11 T17

MACY11 27(732) 21-SEP-76 16:44 PAGE 23  
TEST THAT WRITE-THRU (CH 54) IS LESS THAN +400 MVOLTS WHEN LOW

1013				
1014				
1015				
1016	004140	000004		
1017	004142	012737	000010	001164
1018	004150	012777	006000	175330
1019	004156	012737	000120	001124
1020	004164	004537	011534	
1021	004170	000054		
1022	004172	013737	011656	001126
1023	004200	023737	001124	001126
1024	004206	002001		
1025	004210	104007		
1026				
1027				
1028				
1029				
1030				
1031	004212	000004		
1032	004214	012737	000010	001164
1033	004222	005077	175260	
1034	004226	012737	000120	001124
1035	004234	004537	011534	
1036	004240	000055		
1037	004242	013737	011656	001126
1038	004250	023737	001124	001126
1039	004256	002001		
1040	004260	104007		
1041				
1042				
1043				
1044				
1045				
1046	004262	000004		
1047	004264	012737	000010	001164
1048	004272	012777	017000	175206
1049	004300	012737	001146	001124
1050	004306	004537	011534	
1051	004312	000055		
1052	004314	013737	011656	001126
1053	004322	023737	001124	001126
1054	004330	003401		
1055	004332	104002		
1056				
1057				

```

*****
*TEST 17 TEST THAT WRITE-THRU (CH 54) IS LESS THAN +400 MVOLTS WHEN LOW
*****

```

```

†TST17: SCOPE
MOV #10,$TIMES ;;DO 10 ITERATIONS
MOV #BIT11!BIT10,!JVCSTAT ;SET WRITE-THRU
MOV #120,$GDDAT ;LOAD EXPECTED VALUE
JSR R5,CONVRT ;CONVERT
CH54 ;CH 54
MOV ADEND,$BDDAT ;LOAD VALUE READ
CMP $GDDAT,$BDDAT ;COMPARE
BGE TST20 ;;BR IF GREATER THAN OR EQUAL
ERROR 7 ;CH 54 FAILED TO EQUAL EXPECTED
; VALUE

```

```

*****
*TEST 20 TEST THAT NON-STORE (CH 55) IS LESS THAN +400 MVOLTS WHEN LOW
*****

```

```

†TST20: SCOPE
MOV #10,$TIMES ;;DO 10 ITERATIONS
CLR JVCSTAT ;CLEAR STORE MODE
MOV #120,$GDDAT ;LOAD EXPECTED VALUE
JSR R5,CONVRT ;CONVERT
CH55 ;CH 55
MOV ADEND,$BDDAT ;LOAD VALUE READ
CMP $GDDAT,$BDDAT ;COMPARE
BGE TST21 ;;BR IF GREATER THAN OR EQUAL
ERROR 7 ;CH 54 FAILED TO EQUAL EXPECTED
; VALUE

```

```

*****
*TEST 21 TEST THAT NON-STORE L (CH 55) IS GREATER THAN +3 VOLTS WHEN HIGH
*****

```

```

†TST21: SCOPE
MOV #10,$TIMES ;;DO 10 ITERATIONS
MOV #BIT12!BIT11!BIT10!BIT9,!JVCSTAT ;SET STORE MODE
MOV #1146,$GDDAT ;LOAD EXPECTED VALUE
JSR R5,CONVRT ;CONVERT
CH55 ;CH 55
MOV ADEND,$BDDAT ;LOAD VALUE READ
CMP $GDDAT,$BDDAT ;COMPARE
BLE TST22 ;;BR IF LESS THAN OR EQUAL
ERROR 2 ;CH 55 FAILED TO EQUAL EXPECTED
; VALUE

```



L02

MAINDEC-11-DZARC-B  
DZARCB.P11 T22

MACY11 27(732) 21-SEP-76 16:44 PAGE 24  
TEST THAT CHANNEL 02 L (CH 56) IS GREATER THAN +3 VOLTS WHEN HIGH

```

1058
1059
1060
1061 004334 000004
1062 004336 012737 000010 001164
1063 004344 012777 014000 175134
1064 004352 012737 001146 001124
1065 004360 004537 011534
1066 004364 000056
1067 004366 013737 011656 001126
1068 004374 023737 001124 001126
1069 004402 003401
1070 004404 104002
1071
1072
1073
1074
1075
1076 004406 000004
1077 004410 012737 000010 001164
1078 004416 012777 003000 175062
1079 004424 012737 000120 001124
1080 004432 004537 011534
1081 004436 000056
1082 004440 013737 011656 001126
1083 004446 023737 001124 001126
1084 004454 002001
1085 004456 104007
1086
1087 004460 005077 175022
1088

```

```

*****
*TEST 22 TEST THAT CHANNEL 02 L (CH 56) IS GREATER THAN +3 VOLTS WHEN HIGH
*****
†ST22: SCOPE
MOV #10,$TIMES ;;DO 10 ITERATIONS
MOV #BIT12!BIT11,@VCSTAT ;LOAD VC STATUS
MOV #1146,$GDDAT ;LOAD EXPECTED VALUE
JSR R5,CONVRT ;CONVERT
CH56 ;CH 56
MOV ADEND,$BDDAT ;LOAD VALUE READ
CMP $GDDAT,$BDDAT ;COMPARE
BLE TST23 ;;BR IF LESS THAN OR EQUAL
ERROR 2 ;CH 56 FAILED TO EQUAL EXPECTED
; VALUE

```

```

*****
*TEST 23 TEST THAT CHANNEL 02 L (CH 56) IS LESS THAN +400 MVOLTS WHEN LOW
*****
†ST23: SCOPE
MOV #10,$TIMES ;;DO 10 ITERATIONS
MOV #BIT9!BIT10,@VCSTAT ;SET CHANNEL 2
MOV #120,$GDDAT ;LOAD EXPECTED VALUE
JSR R5,CONVRT ;CONVERT
CH56 ;CH 56
MOV ADEND,$BDDAT ;LOAD VALUE READ
CMP $GDDAT,$BDDAT ;COMPARE
BGE 1$ ;;BR IF GREATER THAN
ERROR 7 ;CH 56 FAILED TO EQUAL EXPECTED
; VALUE
1$: CLR @VCSTAT ;CLEAR BIT 9

```

```

1089                                     ;*****
1090                                     ;*TEST 24          A TO D DIFFERENTIAL LINEARITY TEST
1091                                     ;*****
1092 004464 000004 TST24: SCOPE
1093 004466 012737 000001 001164 MOV #1,$TIMES ;;DO 1 ITERATION
1094
1095 004474 004537 015220 JSR R5,DIFLIN ;START TEST
1096 004500 001510 VCXREG ;X COARSE
1097 004502 001512 VCYREG ;Y FINE
1098 004504 001 005 .BYTE 1,5 ;GO AND CHANNEL 5
1099
1100 004506 005037 001124 CLR $GDDAT ;CLEAR EXPECTED
1101 004512 013737 015740 001126 MOV SKIPST,$BDDAT ;LOAD # OF SKIPPED STATES
1102 004520 001401 BEQ 1$ ;;BR IF NO SKIPPED STATES
1103 004522 104005 ERROR 5 ;SKIPPED STATE(S) DETECTED
1104
1105 004524 013737 015744 001126 1$: MOV EXCESS,$BDDAT ;LOAD # OF >2LSB STATES
1106 004532 001401 BEQ 2$ ;;BR IF NO GREATER THAN 2LSB WIDE STATES
1107 004534 104005 ERROR 5 ;> 2LSB WIDE STATE(S) DETECTED
1108
1109 004536 013737 015742 001126 2$: MOV DIFERR,$BDDAT ;LOAD # OUTSIDE +- 5/2 LSB
1110 004544 012737 000064 001124 MOV #52,$GDDAT ;LOAD MAX # OF ALLOWABLE STATES OUTSIDE +- 1/2 L
1111 004552 023737 001124 001126 CMP $GDDAT,$BDDAT ;ANY ERRORS
1112 004560 002001 BGE TST25 ;;BR IF NO ERROR
1113 004562 104005 ERROR 5 ;DIFFERENTIAL LINEARITY ERROR, >5% OF STATE
1114 ;WIDTHS OUTSIDE +- 1/2 LSB
1115

```



```

1116      ;:*****
1117      ;*TEST 25      X D/A DIFFERENTIAL LINEARITY USING CH 5
1118      ;:*****
1119      004564 000004      TST25: SCOPE
1120      004566 012737 000010 001164      MOV      #10,$TIMES      ;;DO 10 ITERATIONS
1121      004574 012701 000001      MOV      #1,R1      ;LOAD EDGE VALUE
1122      004600 012702 015022      MOV      #RSLT2,R2      ;LOAD RESULT POINTER
1123
1124      004604 010177 174700      1$:      MOV      R1,$VCXREG      ;LOAD X DAC WITH PRESET
1125      004610 010137 004624      MOV      R1,10$      ;LOAD EDGE VALUE
1126
1127      004614 004437 010716      JSR      R4,SAR      ;SOFTWARE SUCCESSIVE APPROX. ROUTINE
1128      004620 001512      VCYREG
1129      004622      000      005      .BYTE 0,$CH5
1130      004624 000000      10$:      0      ;EDGE VALUE
1131      004626 000200      BIT7      ;50-50
1132
1133      004630 013737 015122 004746      MOV      DACSAV,12$      ;SAVE VALUE
1134      004636 005377 174646      DEC      $VCXREG      ;LOAD X DAC
1135      004642 010137 004656      MOV      R1,11$      ;LOAD SAME EDGE
1136
1137      004646 004437 010716      JSR      R4,SAR      ;SOFTWARE S.A.R.
1138      004652 001512      VCYREG
1139      004654      000      005      .BYTE 0,$CH5
1140      004656 000000      11$:      0
1141      004660 000200      BIT7
1142
1143      004662 013737 015122 004750      MOV      DACSAV,13$      ;SAVE VALUE
1144      004670 013737 015122 001126      MOV      DACSAV,$BDDAT      ;LOAD VALUE INTO $BDDAT
1145      004676 163737 004746 001126      SUB      12$,$BDDAT      ;STEP HEIGHT IN $BDDAT
1146      004704 013722 001126      MOV      $BDDAT,(R2)+      ;SAVE RESULT
1147      004710 012737 000062 001124      MOV      #62,$GDDAT      ;LOAD EXPECTED VALUE FOR 1 LSB STEP
1148      004716 013737 016774 011664      MOV      V62,$SPREAD      ;LOAD TOLERANCE
1149      004724 004737 011666      JSR      PC,COMPAR      ;TEST LIMITS
1150      004730 000401      BR      2$      ;;BR IF WITHIN LIMITS
1151      004732 104010      ERROR 10      ;D/A DIFFERENTIAL LINEARITY ERROR
1152      004734 006301      2$:      ASL      R1      ;MOVE LEFT
1153      004736 020127 002000      CMP      R1,#2000      ;DONE
1154      004742 001320      BNE     1$      ;;BR OF NOT DONE
1155      004744 000402      BR      TST26      ;;
1156
1157      004746 000000      12$:      0
1158      004750 000000      13$:      0

```

```

115 *****
116 :+TEST 26      Y D/A DIFFERENTIAL LINEARITY USING CH 6
117 *****
118 TST26: SCOPE
119          MOV      #10, $TIMES      ;; DO 10 ITERATIONS
120          MOV      #1, R1           ;LOAD EDGE VALUE
121          MOV      @RSLT3, R2       ;LOAD RESULT POINTER
122
123 10:        MOV      R1, @VCYREG     ;LOAD Y DAC WITH PRESET
124          MOV      R1, 10$         ;LOAD EDGE VALUE
125
126          JSR      R4, SAR          ;SOFTWARE S.A.R.
127          VCXREG
128          .BYTE   0, CH6
129
130 10$:      0
131          BIT7
132
133          MOV      DACSAV, 12$      ;SAVE VALUE
134          DEC      @VCYREG          ;LOAD Y DAC
135          MOV      R1, 11$         ;LOAD EDGE
136
137          JSR      R4, SAR          ;SOFTWARE S.A.R.
138          VCXREG
139          .BYTE   0, CH6
140
141 11$:      0
142          BIT7
143
144          MOV      DACSAV, 13$      ;SAVE RESULTS
145          MOV      DACSAV, $BDDAT   ;LOAD $BDDAT
146          SUB      12$, $BDDAT     ;STEP HEIGHT IN $BDDAT
147          MOV      $BDDAT, (R2)+   ;SAVE RESULT
148          MOV      #62, $GDDAT     ;LOAD EXPECTED
149          MOV      V62, SPREAD     ;LOAD TOLERANCE
150          JSR      PC, COMPAR      ;TEST IF WITHIN LIMITS
151          BR       2$              ;; BR IF WITHIN LIMITS
152          ERROR   10              ;D/A DIFFERENTIAL LINEARITY ERROR
153
154 2$:      ASL      R1
155          CMP      R1, #2000        ;DONE ?
156          BNE     1$              ;; BR IF NOT DONE
157          BIT      #BIT12, $SWR    ;TEST FORCED PRINTOUT
158          BEQ     TST27           ;; BR IF NOT FORCED PRINTOUT
159          TYPE    DIFMSG
160          JSR      R5, TYPSTG      ;TYPE A STRING OF OCTAL #
161          RSLT2
162          IO
163          TYPE    DIYMSG
164          JSR      R5, TYPSTG      ;TYPE A STRING OF OCTAL #
165          RSLT3
166          IO
167          TYPE    DIEMSG
168          BR      †TST27          ;;
169
170 12$:      0
171 13$:      0

```











# F03

MAINDEC-11-DZARC-B  
DZARCB.P11 T33

MACY11 27(732) 21-SEP-76 16:44 PAGE 31  
A/D RMS NOISE TEST ON CHANNEL 47 - UNIPOLAR

```

1324
1325
1326
1327 005654 000004
1328 005656 012737 000010 001164
1329
1330 005664 004437 010716
1331 005670 001512
1332 005672 000 047
1333 005674 000003
1334 005676 000000
1335
1336 005700 010537 001126
1337 005704 010537 015134
1338 005710 013737 015124 001124
1339 005716 023737 001124 001126
1340 005724 002001
1341 005726 104006
1342
1343
1344
1345
1346
1347 005730 000004
1348 005732 012737 000010 001164
1349
1350 005740 004437 010716
1351 005744 001512
1352 005746 000 047
1353 005750 000003
1354 005752 100000
1355
1356 005754 010537 001126
1357 005760 010537 015136
1358 005764 013737 015126 001124
1359 005772 023737 001124 001126
1360 006000 002001
1361 006002 104006
1362
1363

```

```

*****
*TEST 33      A/D RMS NOISE TEST ON CHANNEL 47 - UNIPOLAR
*****
TST33: SCOPE
MOV      #10,$TIMES      ;;DO 10 ITERATIONS
JSR      R4,SAR          ;S A R ROUTINE
VCYREG   ;Y AXIS
.BYTE    0,CH47         ;CHANNEL 47, FINE Y
3        ;EDGE VALUE
0        ;RMS RESULTS
MOV      R5,$BDDAT      ;SAVE RESULTS (RMS NOISE, 1=.01 LSB)
MOV      R5,CH47RM     ;SAVE RESULT
MOV      RMSMAX,$GDDAT ;LOAD EXPECTED
CMP      $GDDAT,$BDDAT ;COMPARE RESULTS
BGE      TST34          ;;BR IF NOISE WITHIN SPEC
ERROR    6              ;A/D RMS NOISE (UNIPOLAR) IS GREATER
                          ; THAN SPEC
*****
*TEST 34      A/D PEAK NOISE TEST ON CHANNEL 47 - UNIPOLAR
*****
TST34: SCOPE
MOV      #10,$TIMES      ;;DO 10 ITERATIONS
JSR      R4,SAR          ;S A R ROUTINE
VCYREG   ;Y AXIS
.BYTE    0,CH47         ;CHANNEL 47, FINE Y
3        ;EDGE VALUE
BIT15    ;PEAK RESULTS
MOV      R5,$BDDAT      ;SAVE RESULTS (PEAK NOISE, 1 = .01 LSB)
MOV      R5,CH47PK     ;SAVE RESULT
MOV      PEKMAX,$GDDAT ;LOAD EXPECTED
CMP      $GDDAT,$BDDAT ;COMPARE RESULTS
BGE      TST35          ;;BR IF NOISE WITHIN SPEC
ERROR    6              ;A/D PEAK NOISE (UNIPOLAR) IS GREATER
                          ; THAN SPEC

```



```

1364
1365
1366
1367 006004 000004
1368 006006 012737 000010 001164
1369 006014 012777 001000 173466
1370
1371 006022 004437 010716
1372 006026 001512
1373 006030 000 005
1374 006032 001000
1375 006034 000000
1376
1377 006036 010537 001126
1378 006042 163737 015130 001126
1379 006050 013737 001126 015140
1380 006056 013737 015124 001124
1381 006064 023737 001124 001126
1382 006072 002001
1383 006074 104006
1384
1385
1386
1387
1388
1389 006076 000004
1390 006100 012737 000010 001164
1391
1392 006106 012777 001000 173374
1393 006114 004437 010716
1394 006120 001512
1395 006122 000 005
1396 006124 001000
1397 006126 100000
1398
1399 006130 010537 001126
1400 006134 163737 015132 001126
1401 006142 013737 001126 015142
1402 006150 013737 015126 001124
1403 006156 023737 001124 001126
1404 006164 002001
1405 006166 104006
1406
1407

*****
*TEST 35 X DAC RMS NOISE TEST ON CHANNEL 5
*****
†ST35: SCOPE
MOV #10,$TIMES ;;DO 10 ITERATIONS
MOV #1000,$VCXREG ;LOAD X POS
JSR R4,SAR ;S A R ROUTINE
VCYREG ;Y AXIS
.BYTE 0,5 ;CHANNEL 5, COARSE X AND FINE Y
1000 ;EDGE VALUE
0 ;RMS RESULTS
MOV R5,$BDDAT ;SAVE RESULTS (X DAC + A/D RMS NOISE, 1=.01 LSB)
SUB CH7RMS,$BDDAT ;X DAC RMS NOISE ONLY
MOV $BDDAT,$XDACRM ;SAVE RESULT
MOV RMSMAX,$GDDAT ;LOAD EXPECTED
CMP $GDDAT,$BDDAT ;COMPARE RESULTS
BGE TST36 ;;BR IF NOISE WITHIN SPEC
ERROR 6 ;X D/A RMS NOISE IS GREATER
; THAN SPEC

*****
*TEST 36 X DAC PEAK NOISE TEST ON CHANNEL 5
*****
†ST36: SCOPE
MOV #10,$TIMES ;;DO 10 ITERATIONS
MOV #1000,$VCXREG ;LOAD X DAC
JSR R4,SAR ;S A R ROUTINE
VCYREG ;Y AXIS
.BYTE 0,5 ;CHANNEL 5, COARSE X AND FINE Y
1000 ;EDGE VALUE
BIT15 ;PEAK RESULTS
MOV R5,$BDDAT ;SAVE RESULTS (X DAC + A/D PEAK NOISE, 1=.01 LSB)
SUB CH7PEK,$BDDAT ;X DAC PEAK NOISE ONLY
MOV $BDDAT,$XDACPK ;SAVE RESULT
MOV PEKMAX,$GDDAT ;LOAD EXPECTED
CMP $GDDAT,$BDDAT ;COMPARE RESULTS
BGE TST37 ;;BR IF NOISE WITHIN SPEC
ERROR 6 ;X D/A PEAK NOISE IS GREATER
; THAN SPEC

```

# H03

MAINDEC-11-DZARC-B  
DZARCB.P11 T37

MACY11 27(732) 21-SEP-76 16:44 PAGE 33  
Y DAC RMS NOISE TEST ON CHANNEL 6

```
1409          ;:*****
1409          ;*TEST 37      Y DAC RMS NOISE TEST ON CHANNEL 6
1410          ;:*****
1411 006170 000004          †TST37: SCOPE
1412 006172 012737 000010 001164      MOV      #10,$TIMES      ;;DO 10 ITERATIONS
1413
1414 006200 012777 001000 173304      MOV      #1000,$VCYREG      ;LOAD Y REG
1415 006206 004437 010716      JSR      R4,SAR           ;S A R ROUTINE
1416 006212 001510          VCXREG      ;X AXIS
1417 006214      000      006      .BYTE 0,6      ;CHANNEL 6 ,COARSE Y AND FINE X
1418 006216 001000          1000      ;EDGE VALUE
1419 006220 000000          0      ;RMS RESULTS
1420
1421 006222 010537 001126          MOV      R5,$BDDAT      ;SAVE RESULTS (Y DAC + A/D RMS NOISE, 1=.01 LSB)
1422 006226 163737 015130 001126      SUB      CH7RMS,$BDDAT  ;Y DAC RMS NOISE ONLY
1423 006234 013737 001126 015144      MOV      $BDDAT,YDACRM ;SAVE RESULT
1424 006242 013737 015124 001124      MOV      RMSMAX,$GDDAT ;LOAD EXPECTED
1425 006250 023737 001124 001126      CMP      $GDDAT,$BDDAT ;COMPARE RESULTS
1426 006256 002001          BGE      TST40          ;;BR IF NOISE WITHIN SPEC
1427 006260 104006          ERROR    6              ;Y D/A RMS NOISE IS GREATER
1428
1429
1430          ;:*****
1431          ;*TEST 40      Y DAC PEAK NOISE TEST ON CHANNEL 6
1432          ;:*****
1433 006262 000004          †TST40: SCOPE
1434 006264 012737 000010 001164      MOV      #10,$TIMES      ;;DO 10 ITERATIONS
1435
1436 006272 012777 001000 173212      MOV      #1000,$VCYREG      ;LOAD Y AXIS
1437 006300 004437 010716      JSR      R4,SAR           ;S A R ROUTINE
1438 006304 001510          VCXREG      ;X AXIS
1439 006306      000      006      .BYTE 0,6      ;CHANNEL 6 ,COARSE Y AND FINE X
1440 006310 001000          1000      ;EDGE VALUE
1441 006312 100000          BIT15     ;PEAK RESULTS
1442
1443 006314 010537 001126          MOV      R5,$BDDAT      ;SAVE RESULTS (Y DAC + A/D PEAK NOISE, 1=.01 LSB)
1444 006320 163737 015132 001126      SUB      CH7PEK,$BDDAT  ;Y DAC PEAK NOISE ONLY
1445 006326 013737 001126 015146      MOV      $BDDAT,YDACPK ;SAVE RESULT
1446 006334 013737 015126 001124      MOV      PEKMAX,$GDDAT ;LOAD EXPECTED
1447 006342 023737 001124 001126      CMP      $GDDAT,$BDDAT ;COMPARE RESULTS
1448 006350 002001          BGE      1$           ;;BR IF NOISE WITHIN SPEC
1449 006352 104006          ERROR    6              ;Y DAC PEAK NOISE IS GREATER
1450
1451
1452 006354 032777 010000 172554 1$: BIT      #BIT12,$SWR      ;TEST FORCED PRINTOUT
1453 006362 001432          BEQ      TST41          ;;BR IF NOT FORCED
1454 006364 104400 013543          TYPE     NOIMSG
1455 006370 004537 011466          JSR      R5,BY TWO     ;TYPE 2 OCTAL COL.
1456 006374 015130          CH7RMS
1457 006376 015132          CH7PEK
1458 006400 104400 013664          TYPE     NOIMG1
1459 006404 004537 011466          JSR      R5,BY TWO     ;TYPE 2 OCTAL COL.
1460 006410 015134          CH47RM
1461 006412 015136          CH47PK
1462 006414 104400 013707          TYPE     NOIMG2
1463 006420 004537 011466          JSR      R5,BY TWO     ;TYPL 2 OCTAL COL.
```



MAINDEC-11-DZARC-B  
DZARCB.P11 T40

MACY11 27(732) 21-SEP-76 16:44 PAGE 34  
Y DAC PEAK NOISE TEST ON CHANNEL 6

1464	006424	015140		
1465	006426	015142		
1466	006430	104400	013732	
1467	006434	004537	011466	
1468	006440	015144		
1469	006442	015146		
1470	006444	104400	016536	

```

XDACRM
XDACPK
TYPE      NOIMG3
JSR      R5,BY TWO
YDACRM
YDACPCK
TYPE      ,ACRLF

```

1471				
1472				
1473				
1474				

```

*****
*TEST 41      BIPOLAR A/D OFFSET USING CH 7 WITH WAIT INTERRUPT
*****

```

1475	006450	000004		
1476	006452	012737	000010	001164
1477	006460	004437	010716	
1478	006464	001512		
1479	006466	000	007	
1480	006470	001001		
1481	006472	000200		

```

†TST41:  SCOPE
          MOV      #10,$TIMES      ;;DO 10 ITERATIONS
          JSR      R4,$AR          ;SOFTWARE S.A.R.
          VCYREG
          .BYTE   0,CH7           ;CHANNEL 7, FINE Y
          1001                    ;EDGE, 1000/1001 TRANSITION
          BIT7                      ;50-50

```

1482				
1483	006474	013737	015122	015150
1484	006502	013737	015122	001126
1485	006510	012737	001014	001124
1486	006516	013737	016774	011664
1487	006524	004737	011666	
1488	006530	000401		
1489	006532	104012		

```

          MOV      DACSAV,OFSTBX   ;SAVE A/D OFFSET BIPOLAR
          MOV      DACSAV,$BDDAT   ;LOAD VALUE READ
          MOV      #1014,$GDDAT    ;LOAD EXPECTED
          MOV      V62,$SPREAD     ;LOAD +- VARIABLE, 1LSB
          JSR      PC,COMPAR        ;TEST IF WITHIN +- VALUE
          BR       TST42           ;;BR IF WITHIN LIMITS
          ERROR    12              ;A/D BIPOLAR OFFSET ERROR

```

1490				
1491				
1492				
1493				

```

*****
*TEST 42      UNIPOLAR A/D OFFSET USING CH 7 WITH WAIT INTERRUPT
*****

```

1494	006534	000004		
1495	006536	012737	000010	001164
1496	006544	004437	010716	
1497	006550	001512		
1498	006552	000	047	
1499	006554	000001		
1500	006556	000200		

```

†TST42:  SCOPE
          MOV      #10,$TIMES      ;;DO 10 ITERATIONS
          JSR      R4,$AR          ;SOFTWARE S.A.R.
          VCYREG
          .BYTE   0,CH47          ;CHANNEL 7, UNIPOLAR FINE Y
          1                    ;EDGE, 0/1 TRANSITION
          BIT7                      ;50-50

```

1501				
1502	006560	013737	015122	015152
1503	006566	013737	015122	001126
1504	006574	012737	001014	001124
1505	006602	013737	016774	011664
1506	006610	004737	011666	
1507	006614	000401		
1508	006616	104012		

```

          MOV      DACSAV,OFSTUX   ;SAVE A/D OFFSET UNIPOLAR
          MOV      DACSAV,$BDDAT   ;LOAD VALUE READ
          MOV      #1014,$GDDAT    ;LOAD EXPECTED
          MOV      V62,$SPREAD     ;LOAD +- VARIABLE, 1 LSB
          JSR      PC,COMPAR        ;TEST IF WITHIN +- VALUE
          BR       TST43           ;;BR IF WITHIN LIMITS
          ERROR    12              ;A/D UNIPOLAR OFFSET ERROR

```

```

1509          ;:*****
1510          ;*TEST 43      BIPOLAR A/D OFFSET USING CH 7 WITH WAIT LOOP
1511          ;:*****
1512 006620 000004          TST43: SCOPE
1513 006622 012737 000010 001164      MOV      #10,$TIMES      ;;DO 10 ITERATIONS
1514 006630 004437 010716              JSR      R4,$AR          ;SOFTWARE S.A.R.
1515 006634 001512              VCYREG
1516 006636          000          .BYTE   0,CH7          ;CHANNEL 7, FINE Y
1517 006640 001001              1001          ;EDGE, 1000/1001 TRANSITION
1518 006642 000201              BIT7!BIT0        ;50-50, WAIT LOOP
1519
1520 006644 013737 015122 015154      MOV      DACSAV,OFSLBX  ;SAVE A/D OFFSET BIPOLAR
1521 006652 013737 015122 001126      MOV      DACSAV,$BDDAT ;LOAD VALUE READ
1522 006660 012737 001014 001124      MOV      #1014,$GDDAT  ;LOAD EXPECTED
1523 006666 013737 016774 011664      MOV      V62,$SPREAD   ;LOAD +- VARIABLE, 1 LSB
1524 006674 004737 011666              JSR      PC,COMPAR     ;TEST IF WITHIN +- VALUE
1525 006700 000401              BR       1$           ;;BR IF WITHIN LIMITS
1526 006702 104012              ERROR    12          ;A/D BIPOLAR OFFSET ERROR
1527
1528 006704 163737 015150 001126 1$:   SUB      OFSTBX,$BDDAT  ;OFFSET DUE TO WAIT LOOP
1529 006712 005037 001124              CLR      $GDDAT
1530 006716 012737 000031 011664      MOV      #31,$SPREAD   ;1/2 LSB ALLOWED
1531 006724 004737 011666              JSR      PC,COMPAR     ;WITHIN LIMITS
1532 006730 000401              BR       TST44        ;;BR IF WITHIN LIMITS
1533 006732 104012              ERROR    12          ;A/D BIPOLAR OFFSET ERROR DUE TO WAIT LOOP
1534
1535          ;:*****
1536          ;*TEST 44      UNIPOLAR A/D OFFSET USING CH 7 WITH WAIT LOOP
1537          ;:*****
1538 006734 000004          TST44: SCOPE
1539 006736 012737 000010 001164      MOV      #10,$TIMES      ;;DO 10 ITERATIONS
1540 006744 004437 010716              JSR      R4,$AR          ;SOFTWARE S.A.R.
1541 006750 001512              VCYREG
1542 006752          000          .BYTE   0,CH47       ;CHANNEL 7, UNIPOLAR, FINE Y
1543 006754 000001              1          ;EDGE, 0/1 TRANSITION
1544 006756 000201              BIT7!BIT0        ;50-50, WAIT LOOP
1545
1546 006760 013737 015122 015156      MOV      DACSAV,OFSLUX ;SAVE A/D OFFSET UNIPOLAR
1547 006766 013737 015122 001126      MOV      DACSAV,$BDDAT ;LOAD VALUE READ
1548 006774 012737 001014 001124      MOV      #1014,$GDDAT  ;LOAD EXPECTED
1549 007002 013737 016774 011664      MOV      V62,$SPREAD   ;LOAD +- VARIABLE, 1 LSB
1550 007010 004737 011666              JSR      PC,COMPAR     ;TEST IF WITHIN +- VALUE
1551 007014 000401              BR       1$           ;;BR IF WITHIN LIMITS
1552 007016 104012              ERROR    12          ;A/D UNIPOLAR OFFSET ERROR
1553
1554 007020 163737 015152 001126 1$:   SUB      OFSTUX,$BDDAT ;OFFSET DUE TO WAIT LOOP
1555 007026 005037 001124              CLR      $GDDAT
1556 007032 012737 000031 011664      MOV      #31,$SPREAD   ;1/2 LSB ALLOWED
1557 007040 004737 011666              JSR      PC,COMPAR     ;WITHIN LIMITS
1558 007044 000401              BR       TST45        ;;BR IF WITHIN LIMITS
1559 007046 104012              ERROR    12          ;A/D UNIPOLAR OFFSET ERROR DUE TO WAIT LOOP

```



```

1560
1561
1562
1563 007050 000004
1564 007052 012737 000010 001164
1565 007060 012777 001000 172422
1566 007066 004437 010716
1567 007072 001512
1568 007074 000 005
1569 007076 001001
1570 007100 000200
1571 007102 013737 015122 001126
1572 007110 163737 015150 001126
1573 007116 005037 001124
1574 007122 013737 016776 011664
1575 007130 004737 011666
1576 007134 000401
1577 007136 104012
1578
1579
1580
1581
1582 007140 000004
1583 007142 012737 000010 001164
1584 007150 012777 001000 172334
1585 007156 004437 010716
1586 007162 001510
1587 007164 000 006
1588 007166 001001
1589 007170 000200
1590 007172 013737 015122 001126
1591 007200 163737 015150 001126
1592 007206 005037 001124
1593 007212 013737 016776 011664
1594 007220 004737 011666
1595 007224 000401
1596 007226 104012

```

```

*****
*TEST 45 X D/A OFFSET USING CH 5
*****
†ST45: SCOPE
MOV #10,$TIMES ;;DO 10 ITERATIONS
MOV #1000,@VCXREG ;LOAD X REG
JSR R4,SAR ;SOFTWARE S.A.R.
VCYREG ;Y AXIS
.BYTE 0,CH5 ;CHANNEL 5, COARSE X AND FINE Y
1001 ;EDGE VALUE, 1000/1001 TRANSITION
BIT7 ;50-50
MOV DACSAV,$BDDAT ;SAVE RESULT (X DAC + A/D OFFSET)
SUB OFSTBX,$BDDAT ;OFFSET DUE TO X DAC
CLR $GDDAT ;CLEAR EXPECTED
MOV V77,SPREAD ;TOLERANCE DUE TO X DAC
JSR PC,COMPAR ;TEST IF WITHIN +- LIMITS
BR TST46 ;;BR IF WITHIN LIMITS
ERROR 12 ;X DAC OFFSET ERROR

```

```

*****
*TEST 46 Y D/A OFFSET USING CH 6
*****
†ST46: SCOPE
MOV #10,$TIMES ;;DO 10 ITERATIONS
MOV #1000,@VCYREG ;LOAD Y REG
JSR R4,SAR ;SOFTWARE S.A.R.
VCXREG ;X AXIS
.BYTE 0,CH6 ;CHANNEL 6, COARSE Y AND FINE X
1001 ;EDGE VALUE, 1000/1001 TRANSITION
BIT7 ;50-50
MOV DACSAV,$BDDAT ;SAVE RESULT (Y DAC + A/D OFFSET)
SUB OFSTBX,$BDDAT ;OFFSET DUE TO Y DAC
CLR $GDDAT ;CLEAR EXPECTED
MOV V77,SPREAD ;TOLERANCE DUE TO Y DAC
JSR PC,COMPAR ;TEST IF WITHIN +- LIMITS
BR TST47 ;;BR IF WITHIN LIMITS
ERROR 12 ;Y DAC OFFSET ERROR

```

```

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1598
1599
1600 007230 000004
1601 007232 012737 000010 001164
1602 007240 012777 001300 172242
1603 007246 012737 001600 001124
1604 007254 004537 011534
1605 007260 000011
1606 007262 013737 011656 001126
1607 007270 012737 000003 011664
1608 007276 004737 011666
1609 007302 000401
1610 007304 104013
1611 007306 162777 000100 172174
1612 007314 162737 000200 001124
1613 007322 100354
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1618 007324 000004
1619 007326 012737 000010 001164
1620 007334 012777 001300 172150
1621 007342 012737 001600 001124
1622 007350 004537 011534
1623 007354 000012
1624 007356 013737 011656 001126
1625 007364 012737 000003 011664
1626 007372 004737 011666
1627 007376 000401
1628 007400 104013
1629 007402 162777 000100 172102
1630 007410 162737 000200 001124
1631 007416 100354

```

```

*****
*TEST 47 CALIBRATION USING CHANNEL 11 - X DAC VS. A/D
*****
†ST47: SCOPE
MOV #10, $TIMES ;;DO 10 ITERATIONS
MOV #1300, @VCXREG ;LOAD X DAC
MOV #1600, $GDDAT ;LOAD EXPECTED
2$: JSR R5, CONVRT ;CONVERT 32X AND AVERAGE
CH11
MOV ADEND, $BDDAT ;READ VALUE OBTAINED
MOV #3, SPREAD ;LOAD DEVIATION
JSR PC, COMPAR ;TEST IF WITHIN
BR 1$ ;BR IF WITHIN
ERROR 13 ;CALIBRATION ERROR - X DAC VS. A/D
1$: SUB #100, @VCXREG ;DECREMENT DAC
SUB #200, $GDDAT ;DEC. EXPECTED
BPL 2$ ;BR UNTIL DONE

```

```

*****
*TEST 50 CALIBRATION USING CHANNEL 12 - Y DAC VS. A/D
*****
†ST50: SCOPE
MOV #10, $TIMES ;;DO 10 ITERATIONS
MOV #1300, @VCYREG ;LOAD Y DAC
MOV #1600, $GDDAT ;LOAD EXPECTED
2$: JSR R5, CONVRT ;CONVERT 32X AND AVERAGE
CH12
MOV ADEND, $BDDAT ;READ VALUE OBTAINED
MOV #3, SPREAD ;LOAD DEVIATION
JSR PC, COMPAR ;TEST IF WITHIN
BR 1$ ;BR IF WITHIN
ERROR 13 ;CALIBRATION ERROR - Y DAC VS. A/D
1$: SUB #100, @VCYREG ;DECREMENT DAC
SUB #200, $GDDAT ;DEC. EXPECTED
BPL 2$ ;BR UNTIL DONE

```



# M03

MAINDEC-11-DZARC-B  
DZARCB.P11 T51

MACY11 27(732) 21-SEP-76 16:44 PAGE 38  
LINEARITY TEST USING CH 6 - Y DAC VS. A/D

```

1632                                     ;*****
1633                                     ;*TEST 51 LINEARITY TEST USING CH 6 - Y DAC VS. A/D
1634                                     ;*****
1635 007420 000004 TST51: SCOPE
1636 007422 012737 000010 001164 MOV #10,$TIMES ;DO 10 ITERATIONS
1637 007430 012700 014702 MOV #NUMBF2,R0 ;LOAD EDGE VALUE PCINTER
1638 007434 012702 014726 MOV #RSLTO,R2 ;LOAD RESULT POINTER
1639
1640 007440 011037 007460 1$: MOV (R0),10$ ;LOAD EDGE EXPECTED
1641 007444 012077 172042 MOV (R0)+,AVCYREG ;LOAD DAC
1642 007450 004437 010716 JSR R4,SAR ;SOFTWARE S.A.R.
1643 007454 001510 VCYREG
1644 007456 000 006 .BYTE 0,CH6 ;CHANNEL 6, COARSE Y AND FINE X
1645 007460 000000 10$: 0 ;EDGE VALUE
1646 007462 000200 BIT7 ;50-50
1647
1648 007464 013722 015122 MOV DACSAV,(R2)+ ;SAVE RESULTS
1649 007470 005710 TST (R0) ;LAST RESULT ?
1650 007472 100362 BPL 1$ ;BR UNTIL DONE
1651 007474 005037 001124 CLR $GDDAT ;ZERO NOMINAL LINEARITY ERROR
1652 007500 013737 016776 011664 MOV V77,SPREAD ;TOLERANCE FOR A/D + DAC
1653 007506 013700 014746 MOV RSLTO+20,R0 ;GET LAST VALUE
1654 007512 163700 014726 SUB RSLTO,R0 ;SUBTRACT FIRST VALUE
1655 007516 006200 ASR R0
1656 007520 006200 ASR R0
1657 007522 006200 ASR R0
1658 007524 005500 ADC R0 ;AVERAGE DIFFERENCE IN R0
1659 007526 012701 000002 MOV #2,R1
1660 007532 005037 014746 CLR RSLTO+20 ;START RUNNING SUM OF ERRORS
1661 007536 060037 014726 2$: ADD R0,RSLTO ;COMPUTE NOM. VALUE ON LINE BETW. END PTS.
1662 007542 163761 014726 014726 SUB RSLTO,RSLTO(R1) ;COMPUTE ERROR WITH RESP. TO END PT. LINE
1663 007550 066137 014726 014746 ADD RSLTO(R1),RSLTO+20 ;KEEP RUNNING SUM
1664 007556 005721 TST (R1)+ ;BUMP R1
1665 007560 020127 000020 CMP R1,#20 ;DONE ?
1666 007564 001364 BNE 2$ ;BR IF NOT
1667 007566 006237 014746 ASR RSLTO+20
1668 007572 006237 014746 ASR RSLTO+20
1669 007576 006237 014746 ASR RSLTO+20
1670 007602 005537 014746 ADC RSLTO+20 ;AVERAGE ERROR WITH RESP. TO END PT. LINE
1671 007606 005037 014726 CLR RSLTO
1672 007612 005001 CLR R1
1673 007614 163761 014746 014726 3$: SUB RSLTO+20,RSLTO(R1) ;ERROR WITH RESP. TO "BEST STRAIGHT LINE"
1674
1675 007622 016137 014726 001126 MOV RSLTO(R1),$BDDAT
1676 007630 004737 011666 JSR PC,COMPAR ;COMPARE LINEARITY ERROR WITH + OR - TOLERANCE
1677 007634 000422 BR 4$
1678 007636 010102 MOV R1,R2
1679 007640 042702 177770 BIC #177770,R2 ;MASK BITS 0-2
1680 007644 062702 000060 ADD #60,R2 ;CONVERT TO ASCII
1681 007650 110237 012445 MOVB R2,$M14A
1682 007654 010102 MOV R1,R2 ;LOAD R2
1683 007656 006202 ASR R2
1684 007660 006202 ASR R2
1685 007662 006202 ASR R2
1686 007664 042702 177770 BIC #177770,R2 ;MASK
1687 007670 062702 000060 ADD #60,R2 ;MAKE ASCII

```

```

1688 007674 110237 012444      MOVB    R2,EM14B      ;SAVE #
1689 007700 104014      ERROR   14           ;LINEARITY ERROR USING CH 6 - YDAC VS. A/D
1690 007702 005721      4$:    TST    (R1)+     ;BUMP R1
1691 007704 020127 000020      CMP     R1,#20      ;DONE ?
1692 007710 001341      BNE     3$          ;:BR IF NOT DONE
1693
1694      ;:*****
1695      ;:TEST 52          LINEARITY TEST USING CH 5 - X DAC VS. A/D
1696      ;:*****
1697 007712 000004      †ST52: SCOPE
1698 007714 012737 000010 001164      MOV     #10,$TIMES   ;:DO 10 ITERATIONS
1699 007722 012700 014702      MOV     #NUMBF2,R0   ;:LOAD EDGE VALUE POINTER
1700 007726 012702 014764      MOV     #RSLT1,R2    ;:LOAD RESULT POINTER
1701
1702 007732 011037 007752      1$:    MOV     (R0),10$  ;:LOAD EDGE EXPECTED
1703 007736 012077 171546      MOV     (R0)+,AVCXREG ;:LOAD DAC
1704 007742 004437 010716      JSR     R4,SAR       ;:SOFTWARE S.A.R.
1705 007746 001512      VCYREG
1706 007750 000 005      .BYTE  0,CH5        ;:CHANNEL 5, COARSE X AND FINE Y
1707 007752 000000      10$:   0             ;:EDGE VALUE
1708 007754 000200      BIT7          ;:50-50
1709
1710 007756 013722 015122      MOV     DACSAV,(R2)+ ;:SAVE RESULTS
1711 007762 005710      TST     (R0)         ;:LAST RESULT ?
1712 007764 100362      BPL     1$          ;:BR UNTIL DONE
1713 007766 005037 001124      CLR     $GDDAT       ;:ZERO NOMINAL LINEARITY ERROR
1714 007772 013737 016776 011664      MOV     V77,SPREAD   ;:TOLERANCE FOR A/D + DAC
1715 010000 013700 015004      MOV     RSLT1+20,R0  ;:GET LAST VALUE
1716 010004 163700 014764      SUB     RSLT1,R0     ;:SUBTRACT FIRST VALUE
1717 010010 006200      ASR     R0
1718 010012 006200      ASR     R0
1719 010014 006200      ASR     R0
1720 010016 005500      ADC     R0           ;:AVERAGE DIFFERENCE IN R0
1721 010020 012701 000002      MOV     #2,R1
1722 010024 005037 015004      CLR     RSLT1+20     ;:START RUNNING SUM OF ERRORS
1723 010030 060037 014764      2$:    ADD     R0,RSLT1   ;:COMPUTE NOM. VALUE ON LINE BETW. END PTS.
1724 010034 163761 014764 014764      SUB     RSLT1,RSLT1(R1) ;:COMPUTE ERROR WITH RESP. TO END PT. LINE
1725 010042 066137 014764 015004      ADD     RSLT1(R1),RSLT1+20 ;:KEEP RUNNING SUM
1726 010050 005721      TST     (R1)+       ;:BUMP R1
1727 010052 020127 000020      CMP     R1,#20      ;:DONE ?
1728 010056 001364      BNE     2$          ;:BR IF NOT
1729 010060 006237 015004      ASR     RSLT1+20
1730 010064 006237 015004      ASR     RSLT1+20
1731 010070 006237 015004      ASR     RSLT1+20
1732 010074 005537 015004      ADC     RSLT1+20     ;:AVERAGE ERROR WITH RESP. TO END PT. LINE
1733 010100 005037 014764      CLR     RSLT1
1734 010104 005001      CLR     R1
1735 010106 163761 015004 014764 3$:    SUB     RSLT1+20,RSLT1(R1) ;:ERROR WITH RESP. TO "BEST STRAIGHT LINE"
1736 010114 016137 014764 001126      MOV     RSLT1(R1),$GDDAT ;:COMPARE LINEARITY ERROR WITH + OR - TOLERANCE
1737 010122 004737 011666      JSR     PC,COMPAR
1738 010126 000422      BR     4$
1739 010130 010102      MOV     R1,R2
1740 010132 042702 177770      BIC     #177770,R2   ;:MASK BITS 0-2
1741 010136 062702 000060      ADD     #60,R2       ;:CONVERT TO ASCII
1742 010142 110237 012445      MOVB   R2,EM14A
1743 010146 010102      MOV     R1,R2       ;:LOAD R2

```



```

1774 010150 006202
1775 010152 006202
1776 010154 006202
1777 010156 042703 177770
1778 010162 062702 000060
1779 010166 110237 012444
1780 010172 104014
1781 010174 005721 4S:
1782 010176 020127 000020
1783 010200 001341
1784 010204 032777 010000 170724
1785 010212 001416
1786 010214 104400 013755
1787 010220 004537 011510
1788 010224 014726
1789 010226 000010
1790 010230 104400 014140
1791 010234 004537 011510
1792 010240 014764
1793 010242 000010
1794 010244 104400 016536

```

```

ASR R2
ASR R2
ASR R2
BIC #177770,R2 :MASK
ADD #60,R2 :MAKE ASCII
MOV8 R2,EM148 :SAVE #
ERROR 14 :LINEARITY ERROR USING CH 5 - XDAC VS. A/D
TST (R1)+ :BUMP RI
CMP R1,#20 :DONE ?
BNE 3S :;BR IF NOT DONE
BIT #BIT12,DSWR :TEST IF FORCED TYPEOUT
BEQ TST53 :;BR IF NOT FORCED
TYPE LINMSG
JSR R5,TYPSTG :TYPE OCTAL STRING
RSLTO :STARTING AT
B. : # OF LOCATIONS
TYPE LINMG1
JSR R5,TYPSTG :TYPE OCTAL STRING
RSLT1 :STARTING AT
B. : # OF LOCATIONS
TYPE ,ACRLF

*****
*TEST 53 DETERMINE IF MORE AR11'S ARE TO BE TESTED
*****
TST53: SCOPE
MOV #1,STIMES :;DO 1 ITERATION
TST NBEXT :TEST IF ANY
BEQ 1S :BR IF NONE
ADD #20,ARBADD :UPDATE DEVICE ADDRESS
ADD #20,ARBVCT :UPDATE DEVICE VECTOR
DEC NBEXT :ANOTHER ONE ?
BR BYPAS1 :BR IF ANOTHER
MOV $BASE,ARBADD :RELOAD ADDRESS
MOV $VECT1,ARBVCT :RELOAD VECTOR
MOV NMBEXT,NBEXT :RELOAD NUMBER
RESET
JMP SEOP :DONE
BYPAS1: MOV #-1,R0
JMP RBEG2 :TEST ANOTHER UNIT
.SBTL

```





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.SBTTL CONVERT BINARY TO DECIMAL AND TYPE ROUTINE  
  
\*\*\*\*\*  
\*THIS ROUTINE IS USED TO CHANGE A 16-BIT BINARY NUMBER TO A 5-DIGIT  
\*SIGNED DECIMAL (ASCII) NUMBER AND TYPE IT. DEPENDING ON WHETHER THE  
\*NUMBER IS POSITIVE OR NEGATIVE A SPACE OR A MINUS SIGN WILL BE TYPED  
\*BEFORE THE FIRST DIGIT OF THE NUMBER. LEADING ZEROS WILL ALWAYS BE  
\*REPLACED WITH SPACES.  
\*CALL:  
\*     MOV        NUM,-(SP)         ::PUT THE BINARY NUMBER ON THE STACK  
\*     TYPDS         ::GO TO THE ROUTINE  
  
\$TYPDS:  
MOV        R0,-(SP)         ::PUSH R0 ON STACK  
MOV        R1,-(SP)         ::PUSH R1 ON STACK  
MOV        R2,-(SP)         ::PUSH R2 ON STACK  
MOV        R3,-(SP)         ::PUSH R3 ON STACK  
MOV        R5,-(SP)         ::PUSH R5 ON STACK  
MOV        #20200,-(SP)       ::SET BLANK SWITCH AND SIGN  
MOV        20(SP),R5         ::GET THE INPUT NUMBER  
BPL        1\$                ::BR IF INPUT IS POS.  
NEG        R5                ::MAKE THE BINARY NUMBER POS.  
MOVB       #'-,1(SP)         ::MAKE THE ASCII NUMBER NEG.  
1\$: CLR        R0             ::ZERO THE CONSTANTS INDEX  
MOV        #SDBLK,R3         ::SETUP THE OUTPUT POINTER  
MOVB       #',(R3)+         ::SET THE FIRST CHARACTER TO A BLANK  
2\$: CLR        R2             ::CLEAR THE BCD NUMBER  
MOV        \$DTBL(R0),R1       ::GET THE CONSTANT  
3\$: SUB        R1,R5         ::FORM THIS BCD DIGIT  
BLT        4\$                ::BR IF DONE  
INC        R2                ::INCREASE THE BCD DIGIT BY 1  
BR         3\$  
4\$: ADD        R1,R5         ::ADD BACK THE CONSTANT  
TST        R2                ::CHECK IF BCD DIGIT=0  
BNE        5\$                ::FALL THROUGH IF 0  
TSTB       (SP)             ::STILL DOING LEADING 0'S?  
BMI        7\$                ::BR IF YES  
5\$: ASLB       (SP)         ::MSD?  
BCC        6\$                ::BR IF NO  
MOVB       1(SP),-1(R3)       ::YES--SET THE SIGN  
6\$: BIS        #'0,R2         ::MAKE THE BCD DIGIT ASCII  
7\$: BIS        #',R2         ::MAKE IT A SPACE IF NOT ALREADY A DIGIT  
MOVB       R2,(R3)+         ::PUT THIS CHARACTER IN THE OUTPUT BUFFER  
TST        (R0)+             ::JUST INCREMENTING  
CMP        R0,#10            ::CHECK THE TABLE INDEX  
BLT        2\$                ::GO DO THE NEXT DIGIT  
BGT        9\$                ::GO TO EXIT  
MOV        R5,R2             ::GET THE LSD  
BR         6\$                ::GO CHANGE TO ASCII  
8\$: TSTB       (SP)+         ::WAS THE LSD THE FIRST NON-ZERO?  
BPL        9\$                ::BR IF NO  
MOVB       -1(SP),-2(R3)       ::YES--SET THE SIGN FOR TYPING  
9\$: CLRB       (R3)         ::SET THE TERMINATOR  
MOV        (SP)+,R5         ::POP STACK INTO R5

010472 010046  
010472 010146  
010474 010246  
010476 010346  
010500 010546  
010502 012746 020200  
010504 016605 000020  
010510 100004  
010514 005405  
010516 112766 000055 000001  
010520 005000 1\$:  
010526 012703 010706  
010530 112723 000040  
010534 005002 2\$:  
010540 016001 010676  
010542 160105 3\$:  
010546 002402  
010550 005202  
010552 000774  
010554 060105 4\$:  
010556 005702  
010560 001002  
010562 105716  
010564 100407  
010566 105316 5\$:  
010570 103003  
010572 116663 000001 177777  
010574 052702 000060  
010602 052702 000040 6\$:  
010612 110223 7\$:  
010614 005720  
010616 020027 000010  
010622 002746  
010624 003002  
010626 010502  
010630 000764  
010632 105726 8\$:  
010634 100003  
010636 116663 177777 177776  
010644 105013 9\$:  
010646 012605

1883	010650	012603			MOV	(SP)+,R3	::POP STACK INTO R3
1884	010652	012602			MOV	(SP)+,R2	::POP STACK INTO R2
1885	010654	012601			MOV	(SP)+,R1	::POP STACK INTO R1
1886	010656	012600			MOV	(SP)+,R0	::POP STACK INTO R0
1887	010660	104400	010706		TYPE	\$DBLK	::NOW TYPE THE NUMBER
1888	010664	016666	000002	000004	MOV	2(SP),4(SP)	::ADJUST THE STACK
1889	010672	012616			MOV	(SP)+,(SP)	
1890	010674	000002			RTI		::RETURN TO USER
1891	010676	023420			\$DTBL:	1000.	
1892	010700	001750				1000.	
1893	010702	000144				100.	
1894	010704	000012				10.	
1895	010706	000004			\$DBLK:	.BLKW 4	
1896					.SBTTL	SUCCESSIVE APPROX. SUBROUTINE	



1897					:S A R SUBROUTINE	
1898					JSR R4,SAR	
1899					AXIS POINTER	
1900					CH. IN HIGH BYTE / DUMMY CH. IN LOW BYTE	
1901					EDGE VALUE	
1902					RESULT/MODE	
1903					RETURN WITH R5 = DIFFERENCE	
1904						
1905					BIT15 = 0	RMS
1906					BIT15 = 1	PEAK
1907					BIT7 = 1	50-50
1908					BIT0 = 1	;WAIT LOOP
1909						
1910	010716	012437	015120		SAR: MOV (R4)+,ADAC	;SAVE DAC ADDRESS POINTER
1911	010722	012437	015166		MOV (R4)+,SARCHN	;SAVE CHANNEL
1912	010726	012437	015170		MOV (R4)+,EDGE	;SAVE VALUE EDGE
1913	010732	012437	015172		MOV (R4)+,CONS	;SAVE RMS/PEAK SW
1914	010736	010046			MOV R0,-(SP)	;SAVE GPR
1915	010740	010146			MOV R1,-(SP)	
1916	010742	010246			MOV R2,-(SP)	
1917	010744	010346			MOV R3,-(SP)	
1918	010746	113737	015167	011662	MOVB SARCHN+1,CHANL	
1919	010754	005037	011660		CLR FCHANL	
1920	010760	113737	015167	011661	MOVB SARCHN+1,FCHANL+1	
1921	010766	005237	011660		INC FCHANL	;SET START BIT
1922	010772	017737	004122	015120	MOV @ADAC,ADAC	;GET BUS ADDRESS OF ACTIVE DAC
1923	011000	012777	011132	170460	MOV #115,@ARBVCT	;LOAD VECTOR
1924	011006	005037	015174		CLR FINS	;CLEAR 2 PASS COUNTER FLAG
1925	011012	005737	015172		TST CONS	;TEST IF RMS
1926	011016	100003			BPL 10\$	;BR IF RMS
1927	011020	012703	000002		MOV #2,R3	;LOAD A = .4% OF 512
1928	011024	000407			BR 15\$	
1929	011026	012703	000121	10\$:	MOV #121,R3	;LOAD A = 16% OF 512
1930	011032	105737	015172		TSTB CONS	;TEST FOR 50-50
1931	011036	100002			BPL 15\$	
1932	011040	012703	000400		MOV #400,R3	;LOAD 50-50, A = 50% COUNT
1933	011044	012702	001000	15\$:	MOV #1000,R2	;SET UP MSB
1934	011050	005077	004044		CLR @ADAC	;CLEAR DAC
1935	011054	005001		14\$:	CLR R1	;SET HIGH = 0
1936	011056	012700	001000		MOV #1000,R0	;SET UP 512 CONVERSIONS
1937	011062	060277	004032		ADD R2,@ADAC	;NEXT BIT OF ACTIVE DAC
1938	011066	032737	000001	015172	BIT #BIT0,CONS	;TEST BIT 0
1939	011074	001407			BEQ 20\$	
1940	011076	013777	011660	170370	MOV FCHANL,@ADCS	;START CONVERSION
1941	011104	105777	170364	27\$:	TSTB @ADCS	
1942	011110	100375			BPL 27\$	
1943	011112	000410			BR 12\$	
1944	011114	052737	000100	011660	BIS #BIT6,FCHANL	;SET INTR. ENABLE
1945	011122	013777	011660	170344	MOV FCHANL,@ADCS	;START CONVERSION
1946	011130	000001			WAIT	
1947	011132	022626			11\$: CMP (SP)+,(SP)+	
1948	011134	027737	170340	015170	12\$: CMP @ADDBR,EDGE	;IS RESULT < EDGE VALUE ?
1949	011142	002401			BLT 1\$	;YES, BR
1950	011144	005201			INC R1	;NO, INC HIGH FOR RESULT > OR = EDGE VALUE
1951	011146	105737	015166	1\$:	TSTB SARCHN	;TEST IF DUMMY CH. IS SET
1952	011152	001420			BEQ 3\$	;BR IF NOT

G04

1953	011154	012777	011176	170304		MOV	#4\$,DARBVCT		:LOAD VECTOR
1954	011162	113777	015166	170306		MOVB	SARCHN,DADCS1		:LOAD MUX
1955	011170	005277	170300			INC	DADCS		:CONVERT
1956	011174	000001				WAIT			
1957	011176	022626			4\$:	CMP	(SP)+,(SP)+		:POP STACK
1958	011200	027777	170274	170272	13\$:	CMP	DADDBR,DADDBR		:FAKE READ
1959	011206	012777	011132	170252		MOV	#11\$,DARBVCT		:RESET VECTOR
1960	011214	005300			3\$:	DEC	R0		:DONE 512 TIMES ?
1961	011216	001323				BNE	6\$		:NO
1962	011220	020103				CMP	R1,R3		:IS HIGH > OR = A ?
1963	011222	002402				BLT	2\$		:NO LEAVE BIT ON
1964	011224	160277	003670			SUB	R2,DADAC		:YES TAKE IT OUT
1965	011230	022702	000001		2\$:	CMP	#1,R2		:TEST FOR Z = 1, S.A.R. DONE ?
1966	011234	001402				BEQ	21\$		:BR IF DONE
1967	011236	006202				ASR	R2		:NO SET Z /2
1968	011240	000705				BR	14\$		:TRY AGAIN
1969	011242	017737	003652	015122	21\$:	MOV	DADAC,DACSAV		
1970	011250	005737	015174			TST	FINS		:FIRST OR SECOND TIME ?
1971	011254	001020				BNE	45\$		:SECOND BR
1972	011256	017705	003636			MOV	DADAC,R5		:READ VALUE
1973	011262	005237	015174			INC	FINS		:SET FLAG
1974	011266	005737	015172			TST	CONS		:TEST IF PEAK/RMS
1975	011272	100003				BPL	41\$		:BR IF RMS
1976	011274	012703	000776			MOV	#776,R3		:LOAD 99.6% OF 512
1977	011300	000405				BR	42\$		
1978	011302	012703	000657		41\$:	MOV	#657,R3		:SET A = 84% OF 512
1979	011306	105737	015172		43\$:	TSTB	CONS		
1980	011312	100404				BMI	5\$		
1981	011314	000653			42\$:	BR	15\$		:DO MORE TESTING
1982	011316	167705	003576		45\$:	SUB	DADAC,R5		:SUBTRACT DIFF
1983	011322	005405				NEG	R5		
1984	011324	005077	170144		5\$:	CLR	DADCS		:RETURN WITH 2 X NOISE IN R5
1985	011330	012603				MOV	(SP)+,R3		
1986	011332	012602				MOV	(SP)+,R2		
1987	011334	012601				MOV	(SP)+,R1		
1988	011336	012600				MOV	(SP)+,R0		
1989	011340	000204				RTS	R4		:EXIT
1990									
1991						.SBTTL	MISC. SUBROUTINES		



H04

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1992                                     ;CONVERT R2 INTO DECIMAL DIGITS
1993
1994 011342 012737 177774 011446 DECPRT: MOV      #-4,DIGCNT      ;LOAD COUNT
1995 011350 010146                MOV      R1,-(SP)      ;SAVE R1
1996 011352 012701 011462                MOV      #DIGT1-1,R1  ;LOAD POINTER
1997 011356 012737 011452 011450                MOV      #DECPNT+2,DECPNT ;LOAD POINTER
1998 011364 012737 177777 011444 1$: MOV      #-1,DIGIT    ;LOAD #
1999 011372 005237 011444                2$: INC      DIGIT      ;UPDATE IT
2000 011376 167702 000046                SUB      @DECPNT,R2    ;SUB MAGNITUDE
2001 011402 100373                BPL      2$           ;BR IF +
2002 011404 067702 000040                ADD      @DECPNT,R2    ;RESTORE
2003 011410 052737 000060 011444                BIS      #60,DIGIT    ;MAKE A #
2004 011416 113721 011444                MOV      DIGIT,(R1)+   ;LOAD INTO STRING
2005 011422 005237 011446                INC      DIGCNT       ;UPDATE COUNT
2006 011426 001002                BNE      3$           ;BR IF NOT DONE
2007 011430 012601                MOV      (SP)+,R1     ;RESTORE R1
2008 011432 000207                RTS      PC           ;EXIT
2009 011434 062737 000002 011450 3$: ADD      #2,DECPNT    ;UPDATE POINTER
2010 011442 000750                BR       1$          ;BR BACK
2011
2012 011444 000000                DIGIT: 0
2013 011446 000000                DIGCNT: 0
2014 011450 011452                DECPNT: .+2
2015 011452 001750                .1000.
2016 011454 000144                .100.
2017 011456 000012                .10.
2018 011460 000001                .1.
2019
2020 011462 000                DIGT0: .BYTE 0
2021 011463 000                DIGT1: .BYTE 0
2022 011464 000                DIGT2: .BYTE 0
2023 011465 000                DIGT3: .BYTE 0
2024
2025 011466 013546                BYTWO: MOV      @R5+,-(SP) ;SAVE ON STACK
2026 011470 104402                TYPOS
2027 011472 004 001                .BYTE 4,1
2028 011474 104400 014541                TYPE  ,MULTSP
2029 011500 013546                MOV      @R5+,-(SP)    ;SAVE ON STACK
2030 011502 104402                TYPOS
2031 011504 004 001                .BYTE 4,1
2032 011506 000205                RTS      R5           ;EXIT
2033
2034 011510 012500                TYPSTG: MOV      (R5)+,R0 ;GET ADDRESS POINTER
2035 011512 012501                MOV      (R5)+,R1     ;LOAD # OF LOC.
2036 011514 012046                1$: MOV      (R0)+,-(SP) ;SAVE ON STACK
2037 011516 104402                TYPOS
2038 011520 004 001                .BYTE 4,1
2039 011522 104400 016543                TYPE  ,SP3MSG
2040 011526 005301                DEC      R1           ;DONE ?
2041 011530 001371                BNE      1$
2042 011532 000205                2$: RTS      R5           ;EXIT

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2043 ;SUBROUTINE TO GET AVERAGE OF 32 CONVERSIONS
2044
2045 011534 012537 011652 CONVRT: MOV (R5)+,10$
2046 011540 013737 011652 011662 MOV 10$,CHANL
2047 011546 000337 011652 SWAB 10$
2048 011552 012737 000040 011654 MOV #32.,11$
2049 011560 005037 011656 CLR ADEND
2050 011564 013777 011652 167702 MOV 10$,JADCS
2051 011572 005277 167676 2$: INC JADCS
2052 011576 105777 167672 1$: TSTB JADCS
2053 011602 100375 BPL 1$
2054 011604 067737 167670 011656 ADD JADDBR,ADEND
2055 011612 005337 011654 DEC 11$
2056 011616 001365 BNE 2$
2057 011620 006237 011656 ASR ADEND
2058 011624 006237 011656 ASR ADEND
2059 011630 006237 011656 ASR ADEND
2060 011634 006237 011656 ASR ADEND
2061 011640 006237 011656 ASR ADEND
2062 011644 005537 011656 ADC ADEND ;ROUND UP
2063 011650 000205 RTS R5
2064
2065 011652 000000 10$: 0
2066 011654 000000 11$: 0
2067 011656 000000 ADEND: 0
2068 011660 000000 FCHANL: 0
2069 011662 000000 CHANL: 0
2070 011664 000000 SPREAD: 0
2071
2072 011666 010046 COMPAR: MOV R0,-(SP)
2073 011670 010146 MOV R1,-(SP)
2074 011672 013700 001124 MOV $GDAT,R0 ;LOAD R0
2075 011676 013701 001126 MOV $BDDAT,R1 ;LOAD R1
2076 011702 160100 SUB R1,R0 ;SUBTRACT
2077 011704 100001 BPL 8$ ;
2078 011706 005400 NEG R0
2079 011710 020037 011664 8$: CMP R0,SPREAD ;MAGNITUDE OF DIFF. IN R0
2080 011714 003405 BLE 10$
2081 011716 012601 9$: MOV (SP)+,R1
2082 011720 012600 MOV (SP)+,R0
2083 011722 062716 000002 ADD #2,(SP)
2084 011726 000207 RTS PC
2085
2086 011730 012601 10$: MOV (SP)+,R1
2087 011732 012600 MOV (SP)+,R0
2088 011734 000207 RTS PC
2089 .SBTTL ASCII MESSAGES AND ERROR POINTERS
    
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2090						
2091	011736	051105	047522	020122	EM1:	.ASCIZ \ERROR ON A/D CHANNEL\
2092	011744	047117	040440	042057		
2093	011752	041440	040510	047116		
2094	011760	046105	000			
2095	011763	126	020103	047514	EM2:	.ASCIZ /VC LOGIC SIGNAL HIGH OUTPUT TOO LOW/
2096	011770	044507	020103	044523		
2097	011776	047107	046101	044040		
2098	012004	043511	020110	052517		
2099	012012	050124	052125	052040		
2100	012020	047517	046040	053517		
2101	012026	000				
2102	012027	126	020103	052123	EM3:	.ASCIZ /VC STATUS REGISTER IN ERROR/
2103	012034	052101	051525	051040		
2104	012042	043505	051511	042524		
2105	012050	020122	047111	042440		
2106	012056	051122	051117	000		
2107	012063	105	052130	051105	EM4:	.ASCIZ /EXTERNAL A TO D START FAILED/
2108	012070	040516	020114	020101		
2109	012076	047524	042040	051440		
2110	012104	040524	052122	043040		
2111	012112	044501	042514	000104		
2112	012120	027501	020104	044504	EM5:	.ASCIZ \A/D DIFFERENTIAL LINEARITY ERROR\
2113	012126	043106	051105	047105		
2114	012134	044524	046101	046040		
2115	012142	047111	040505	044522		
2116	012150	054524	042440	051122		
2117	012156	051117	000			
2118	012161	116	044517	042523	EM6:	.ASCIZ /NOISE LEVEL EXCEEDED LIMIT/
2119	012166	046040	053105	046105		
2120	012174	042440	041530	042505		
2121	012202	042504	020104	044514		
2122	012210	044515	000124			
2123	012214	041526	046040	043517	EM7:	.ASCIZ /VC LOGIC SIGNAL LOW OUTPUT TOO HIGH/
2124	012222	041511	051440	043511		
2125	012230	040516	020114	047514		
2126	012236	020127	052517	050124		
2127	012244	052125	052040	047517		
2128	012252	044040	043511	000110		
2129	012260	027504	020101	044504	EM10:	.ASCIZ \D/A DIFFERENTIAL LINEARITY ERROR\
2130	012266	043106	051105	047105		
2131	012274	044524	046101	046040		
2132	012302	047111	040505	044522		
2133	012310	054524	042440	051122		
2134	012316	051117	000			
2135	012321	101	042057	044440	EM11:	.ASCIZ \A/D INTER-CHANNEL SETTLING ERROR\
2136	012326	052116	051105	041455		
2137	012334	040510	047116	046105		
2138	012342	051440	052105	046124		
2139	012350	047111	020107	051105		
2140	012356	047522	000122			
2141	012362	043117	051506	052105	EM12:	.ASCIZ /OFFSET ERROR/
2142	012370	042440	051122	051117		
2143	012376	000				
2144	012377	103	046101	041111	EM13:	.ASCIZ /CALIBRATION ERROR/
2145	012404	040522	044524	047117		

K04

2146	012412	042440	051122	051117						
2147	012420	000								
2148	012421	114	047111	040505	EM14:	.ASCII	/LINEARITY ERROR AT /			
2149	012426	044522	054524	042440						
2150	012434	051122	051117	040440						
2151	012442	020124								
2152	012444	060			EM14B:	.BYTE	60			
2153	012445	060	060	060	EM14A:	.BYTE	60,60,60,0			
2154	012450	000								
2155	012451	105	051122	041520	DH1:	.ASCIZ	/ERRPC	ARADD	CHANL	NOMINAL SPREAD ACTUAL/
2156	012456	020040	040440	040522						
2157	012464	042104	020040	041440						
2158	012472	040510	046116	020040						
2159	012500	047040	046517	047111						
2160	012506	046101	051440	051120						
2161	012514	040505	020104	040440						
2162	012522	052103	040525	000114						
2163	012530	051105	050122	020103	DH2:	.ASCIZ	/ERRPC	ARADD	CHANL	3V LEV. OUTPUT/
2164	012536	020040	051101	042101						
2165	012544	020104	020040	041440						
2166	012552	040510	046116	020040						
2167	012560	053063	046040	053105						
2168	012566	020056	052517	050124						
2169	012574	052125	000							
2170	012577	105	051122	041520	DH3:	.ASCIZ	/ERRPC	ARADD	GOOD	BAD/
2171	012604	020040	040440	040522						
2172	012612	042104	020040	020040						
2173	012620	047507	042117	020040						
2174	012626	020040	040502	000104						
2175	012634	051105	050122	020103	DH5:	.ASCIZ	/ERRPC	ARADD	# OF ST. ALLOWED/	
2176	012642	020040	051101	042101						
2177	012650	020104	021440	047440						
2178	012656	020106	052123	020056						
2179	012664	046101	047514	042527						
2180	012672	000104								
2181	012674	051105	050122	020103	DH6:	.ASCIZ	/ERRPC	ARADD	LIMIT	MEASURED/
2182	012702	020040	051101	042101						
2183	012710	020104	020040	044514						
2184	012716	044515	020124	020040						
2185	012724	042515	051501	051125						
2186	012732	042105	000							
2187	012735	105	051122	041520	DH7:	.ASCIZ	\ERRPC	ARADD	A/D CH. .4 LEV	OUTPUT\
2188	012742	020040	040440	040522						
2189	012750	042104	020040	040440						
2190	012756	042057	041440	027110						
2191	012764	027040	020064	042514						
2192	012772	020126	047440	052125						
2193	013000	052520	000124							
2194	013004	051105	050122	020103	DH10:	.ASCIZ	\ERRPC	ARADD	STEP	NOMINAL SPREAD ACTUAL\
2195	013012	020040	051101	042101						
2196	013020	020104	020040	051440						
2197	013026	042524	020120	020040						
2198	013034	047516	044515	040516						
2199	013042	020114	050123	042522						
2200	013050	042101	020040	041501						
2201	013056	052524	046101	000						









2314	014170	020104	044504	043106	
2315	014176	051105	047105	044524	
2316	014204	046101	046040	047111	
2317	014212	040505	044522	054524	
2318	014220	006472	000012		
2319	014224	052123	052101	026505	ADDIF: .ASCIZ /STATE-WIDTH/<15><12>
2320	014232	044527	052104	006510	
2321	014240	000012			
2322	014242	034050	020051	045523	SKPMSG: .ASCIZ /(8) SKIPPED STATE(S)/<15><12>
2323	014250	050111	042520	020104	
2324	014256	052123	052101	024105	
2325	014264	024523	005015	000	
2326	014271	050	024470	037040	GRT2MG: .ASCIZ /(8) > 2 LSB STATE(S)/<15><12>
2327	014276	031040	046040	041123	
2328	014304	051440	040524	042524	
2329	014312	051450	006451	000012	
2330	014320	034050	020051	020074	NARMSG: .ASCIZ \ (8) < 1/2 LSB NARROW STATE(S)\<15><12>
2331	014326	027461	020062	051514	
2332	014334	020102	040516	051122	
2333	014342	053517	051440	040524	
2334	014350	042524	051450	006451	
2335	014356	000012			
2336	014360	034050	020051	020076	WIDMSG: .ASCIZ \ (9) > 1 1/2 LSB WIDE STATE(S)\<15><12>
2337	014366	020061	027461	020062	
2338	014374	051514	020102	044527	
2339	014402	042504	051440	040524	
2340	014410	042524	051450	006451	
2341	014416	000012			
2342	014420	054130	054056	020045	PERHLF: .ASCIZ \XX.X% OF STATES WITHIN 1/2 LSB (SPEC = 95%)\<15><12>
2343	014426	043117	051440	040524	
2344	014434	042524	020123	044527	
2345	014442	044124	047111	030440	
2346	014450	031057	046040	041123	
2347	014456	020040	051450	042520	
2348	014464	020103	020075	032471	
2349	014472	024445	005015	000	
2350	014477	130	027130	022530	PERQRT: .ASCIZ \XX.X% OF STATES WITHIN 1/4 LSB \<15><12>
2351	014504	047440	020106	052123	
2352	014512	052101	051505	053440	
2353	014520	052111	044510	020116	
2354	014526	027461	020064	051514	
2355	014534	020102	005015	000	
2356	014541	040	020040	020040	MULTSP: .ASCIZ /
2357	014546	020040	020040	020040	
2358	014554	020040	020040	020040	
2359	014562	020040	020040	020040	
2360	014570	000040			
2361					
2362	014572	001116	001464	011662	DT1: .EVEN SERRPC,ARBADD,CHANL,SGDDAT,SPREAD,SBDDAT,0
2363	014600	001124	011664	001126	
2364	014606	000000			
2365	014610	001116	001464	011662	DT2: SERRPC,ARBADD,CHANL,SGDDAT,SBDDAT,0
2366	014616	001124	001126	000000	
2367	014624	001116	001464	001124	DT3: SERRPC,ARBADD,SGDDAT,SBDDAT,0
2368	014632	001126	000000		
2369	014636	001116	001464	001126	DT5: SERRPC,ARBADD,SBDDAT,SGDDAT,0

014644	001124	000000			
014650	001116	001464	015170	DT10:	\$ERRPC,ARBADD,EDGE,\$GDDAT,SPREAD,\$BDDAT,0
014656	001124	011664	001126		
014664	000000				
014666	001116	001464	001124	DT11:	.RFP0,ARBADD,\$GDDAT,SPREAD,\$BDDAT,0
014674	011664	001126	000000		
014702	000004	000203	00402	NUMBF2:	4,203,402,601,1000,1177,1376,1575,1774,BIT15
014710	000601	001000	001177		
014716	001376	001575	001774		
014724	100000				
014726	000000	000000	000000	RSLT0:	0,0,0,0,0,0,0,0,0,0,0,0,0,0,0
014734	000000	000000	000000		
014742	000000	000000	000000		
014750	000000	000000	000000		
014756	000000	000000	000000		
014764	000000	000000	000000	RSLT1:	0,0,0,0,0,0,0,0,0,0,0,0,0,0,0
014772	000000	000000	000000		
015000	000000	000000	000000		
015006	000000	000000	000000		
015014	000000	000000	000000		
015022	000000	000000	000000	RSLT2:	0,0,0,0,0,0,0,0,0,0,0,0,0,0,0
015030	000000	000000	000000		
015036	000000	000000	000000		
015044	000000	000000	000000		
015052	000000	000000	000000		
015060	000000	000000	000000	RSLT3:	0,0,0,0,0,0,0,0,0,0,0,0,0,0,0
015066	000000	000000	000000		
015074	000000	000000	000000		
015102	000000	000000	000000		
015110	000000	000000	000000		
015116	000000	000000	000000		

.SBTTL DIFFERENTIAL LINEARITY SUBROUTINE









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2490 015526 105260 024102 INCB ABUFF4(RO) ;UPDATE STATE-WIDTH BUFFER
2491 015532 020037 015202 CMP RO,HILIM1 ;TEST IF STATE WIDTH > 1 1/2 LSB
2492 015536 003021 BGT 3$ ;YES, BR
2493 015540 020037 015204 CMP RO,HILIM2 ;TEST IF STATE WIDTH > 1 1/4 LSB
2494 015544 003061 BGT 4$ ;YES, BR
2495 015546 020037 017000 CMP RO,V1 ;IS IT A SKIPPED STATE?
2496 015552 103416 BLO 10$ ;YES, BR
2497 015554 020037 015176 CMP RO,LOLIM1 ;TEST IF STATE WIDTH < 1/2 LSB
2498 015560 002415 BLT 11$ ;YES, BR
2499 015562 020037 015200 CMP RO,LOLIM2 ;TEST IF STATE WIDTH < 3/4 LSB
2500 015566 002450 BLT 4$ ;YES, BR
2501 015570 005237 015206 INC QRTOK ;STATE WIDTH BETWEEN 3/4 AND 1 1/4 LSB
2502 015574 000445 BR 4$
2503 015576 005237 015744 1$: INC EXCESS ;UPDATE > 2 LSB WIDE STATE COUNT
2504 015602 005237 015212 3$: INC WIDE ;UPDATE > 1 1/2 LSB WIDE STATE COUNT
2505 015606 000406 BR 12$
2506 015610 005237 015740 10$: INC SKIPST ;UPDATE SKIPPED STATE COUNT
2507 015614 105700 11$: TSTB RO
2508 015616 100767 BMI 1$
2509 015620 005237 015210 INC NARROW ;UPDATE < 1/2 LSB NARROW STATE COUNT
2510 015624 005237 015742 12$: INC DIFERR ;UPDATE OUTSIDE + OR - 1/2 LSB COUNT
2511 015630 032777 010000 163300 5$: BIT #BIT12,DSWR ;TEST FOR FORCED TYPEOUT
2512 015636 001424 BEQ 4$ ;;BR IF NOT
2513 015640 005737 015736 TST 20$ ;TEST IF FIRST TIME ?
2514 015644 001004 BNE 6$ ;BR IF YES
2515 015646 005237 015736 INC 20$
2516 015652 104400 014224 TYPE ADDIF
2517 015656 013746 015216 6$: MOV TEMP,-(SP) ;SAVE CURRENT STATE
2518 015662 104402 TYPOS
2519 015664 004 001 .BYTE 4,1
2520 015666 104400 016552 TYPE .TYANXX
2521 015672 042700 177400 BIC #177400,RO
2522 015676 010046 MOV RO,-(SP)
2523 015700 104402 TYPOS
2524 015702 003 001 .BYTE 3,1
2525 015704 104400 016536 TYPE .ACRLF
2526 015710 005237 015216 4$: INC TEMP
2527 015714 105723 TSTB (R3)+
2528 015716 005302 DEC %2
2529 015720 001276 BNE 2$
2530 015722 032777 010000 163206 BIT #BIT12,DSWR ;TEST BIT 12
2531 015730 001006 BNE SWDIST ;BR IF FORCED PRINTOUT
2532 015732 012605 MOV (SP)+,R5
2533 015734 000205 RTS R5
2534 015736 000000 20$: 0
2535 015740 000000 SKIPST: 0 ;SKIPPED STATE COUNT
2536 015742 000000 DIFERR: 0 ;OUTSIDE + OR - 1/2 LSB STATE COUNT
2537 015744 000000 EXCESS: 0 ;> 2 LSB STATE COUNT
2538
2539 015746 012703 024102 SWDIST: MOV #ABUFF4,%3
2540 015752 005037 015216 CLR TEMP
2541 015756 104400 016536 TYPE .ACRLF
2542 015762 013746 015740 MOV SKIPST,-(SP) ;SAVE SKIPPED STATES
2543 015766 104402 TYPOS
2544 015770 003 000 .BYTE 3,0
2545 015772 104400 014242 TYPE .SKPMSG

```

2546	015776	013746	015744			MOV	EXCESS,-(SP)		;SAVE EXCESSIVE WIDE STATES
2547	016002	104402				TYPOS			
2548	016004	003	000			.BYTE	3,0		
2549	016006	104400	014271			TYPE	,GRT2MG		
2550	016012	013746	015210			MOV	NARROW,-(SP)		
2551	016016	104402				TYPOS			
2552	016020	003	000			.BYTE	3,0		
2553	016022	104400	014320			TYPE	,NARMSG		
2554	016026	013746	015212			MOV	WIDE,-(SP)		;SAVE WIDE STATES
2555	016032	104402				TYPOS			
2556	016034	003	000			.BYTE	3,0		
2557	016036	104400	014360			TYPE	,WIDMSG		
2558	016042	012700	001776			MOV	#1022,R0		
2559	016046	163700	015212			SUB	WIDE,R0		
2560	016052	163700	015210			SUB	NARROW,R0		
2561	016056	004537	017044			JSR	R5,PRCNT		;CONVERT TO PERCENT
2562	016062	014420				PERHLF			
2563	016064	104400	014420			TYPE	,PERHLF		
2564	016070	013700	015206			MOV	QRTOK,R0		
2565	016074	004537	017044			JSR	R5,PRCNT		
2566	016100	014477				PERQRT			
2567	016102	104400	014477			TYPE	,PERQRT		
2568	016106	104400				TYPE			
2569	016110	016554				STDMSG			
2570	016112	013702	015216		6\$:	MOV	TEMP,R2		
2571	016116	006302				ASL	R2		
2572	016120	004737	011342			JSR	PC,DECPRT		;CONVERT R2 TO DEC.
2573	016124	113737	011463	016400		MOVB	DIGT1,RSV1		
2574	016132	113737	011464	016402		MOVB	DIGT2,RSV2		
2575	016140	113737	011465	016403		MOVB	DIGT3,RSV3		
2576									
2577	016146	111302				MOVB	(R3),R2		
2578	016150	004737	011342			JSR	PC,DECPRT		;CONVERT R2 TO DEC.
2579	016154	113737	011462	016405		MOVB	DIGT0,RSX1		
2580	016162	113737	011463	016406		MOVB	DIGT1,RSX2		
2581	016170	113737	011464	016407		MOVB	DIGT2,RSX3		
2582	016176	113737	011465	016410		MOVB	DIGT3,RSX4		
2583	016204	104400				TYPE			
2584	016206	016376				RSVMSG			
2585	016210	111305				MOVB	(%3),R5		
2586	016212	005305			2\$:	DEC	R5		
2587	016214	100403				BMI	1\$		
2588	016216	104400				TYPE			
2589	016220	016541				TYANX			
2590	016222	000773				BR	2\$		
2591	016224	023737	015176	015216	1\$:	CMP	LOLIM1,TEMP		
2592	016232	001413				BEQ	4\$		
2593	016234	023737	015202	015216		CMP	HILIM1,TEMP		
2594	016242	001412				BEQ	5\$		
2595	016244	023737	015214	015216		CMP	AMEAN,TEMP		
2596	016252	001010				BNE	3\$		
2597	016254	104400	016472			TYPE	,TYMEAN		
2598	016260	000405				BR	3\$		
2599	016262	104400	016414		4\$:	TYPE	,TYLOW		
2600	016266	000402				BR	3\$		
2601	016270	104400	016442		5\$:	TYPE	,TYHIGH		



2602	016274	005237	015216	3\$:	INC	TEMP		
2603	016300	105723			TSTB	(3)+		
2604	016302	022737	000145	015216	CMP	#145,TEMP		
2605	016310	001300			BNE	6\$		
2606	016212	104400			TYPE			
2607	016314	016536			ACRLF			
2608	016316	104400	016516		TYPE	OVERNG		
2609	016322	013746	015744		MOV	EXCESS,-(SP)		
2610	016326	104402			TYPOS			
2611	016330	004	001		.BYTE	4,1		
2612	016332	012605			MOV	(SP)+,R5		
2613	016334	000205			RTS	R5		
2614								
2615	016336	000000		12\$:	0			
2616								
2617	016340	005277	163142	CONVT:	INC	QVCSTAT		
2618	016344	105777	163136	2\$:	TSTB	QVCSTAT		
2619	016350	100375			BPL	2\$		
2620	016352	005005			CLR	%5		
2621	016354	013777	016644	163112	MOV	DIFCHN,QADCS		; CONVERT
2622	016362	105777	163106	1\$:	TSTB	QADCS		; WAIT FOR DONE
2623	016366	100375			BPL	1\$		
2624	016370	017705	163104		MOV	QADDBR,R5		; READ VALUE
2625	016374	000207			RTS	PC		
2626								
2627	016376	015	012	RSVMSG:	.BYTE	15,12		
2628	016400	060	056	RSV1:	.BYTE	60,56		
2629	016402	060		RSV2:	.BYTE	60		
2630	016403	060	055	RSV3:	.BYTE	60,55		
2631	016405	060		RSX1:	.BYTE	60		
2632	016406	060		RSX2:	.BYTE	60		
2633	016407	060		RSX3:	.BYTE	60		
2634	016410	060	040	111	RSX4:	.BYTE	60,40,111,0	
2635	016412	000						
2636	016414	026455	026455	026455	TYLOW:	.ASCIZ	"----- (1/2 LSB)"	
2637	016422	026455	026455	020040				
2638	016430	030450	031057	046040				
2639	016436	041123	000051					
2640	016442	026455	026455	026455	TYHIGH:	.ASCIZ	"----- (1 1/2 LSB)"	
2641	016450	026455	026455	020040				
2642	016456	030450	030440	031057				
2643	016464	046040	041123	000051				
2644	016472	026455	026455	026455	TYMEAN:	.ASCIZ	"----- (1 LSB)"	
2645	016500	026455	026455	020040				
2646	016506	030450	046040	041123				
2647	016514	000051						
2648	016516	052517	020124	043117	OVERNG:	.ASCIZ	/OUT OF RANGE - /	
2649	016524	051040	047101	042507				
2650	016532	026440	000040					
2651	016536	015	012	000	ACRLF:	.BYTE	15,12,0	
2652	016541	052	000		TYANX:	.BYTE	52,0	
2653	016543	040	020040	000	SP3MSG:	.ASCIZ	/ /	
2654	016547	000	000	000		.BYTE	0,0,0	
2655	016552	055	000		TYANXX:	.BYTE	55,0	
2656	016554	015	012		STDMSG:	.BYTE	15,12	
2657	016556	052123	052101	026505		.ASCII	/STATE-WIDTH DISTRIBUTION/	

```

2658 016564 044527 052104 020110
2659 016572 044504 052123 044522
2660 016600 052502 044524 047117
2661 016606 015 012
2662 016610 044527 052104 026510
2663 016616 052516 041115 051105
2664 016624 047440 020106 052123
2665 016632 052101 051505 000
2666 016640 000000
2667 016640 000000
2668 016642 000000
2669 016644 000000
2670
2671
2672 016646 012701 016764
2673 016652 005737 016762
2674 016656 001017
2675 016660 012702 017004
2676 016664 112737 000062 013150
2677 016672 112737 000060 013152
2678 016700 112737 000065 013175
2679 016706 112737 000060 013176
2680 016714 000416
2681 016716 012702 017022
2682 016722 112737 000061 013150
2683 016730 112737 000070 013152
2684 016736 112737 000064 013175
2685 016744 112737 000064 013176
2686 016752 012221
2687 016754 005711
2688 016756 100375
2689 016760 000207
2690
2691 016762 000000
2692
2693 016764 001754
2694 016766 000024
2695 016770 000050
2696 016772 000024
2697 016774 000062
2698 016776 000077
2699 017000 000001
2700 017002 100000
2701
2702 017004 001754
2703 017006 000024
2704 017010 000050
2705 017012 000024
2706 017014 000062
2707 017016 000077
2708 017020 000001
2709
2710 017022 001760
2711 017024 000020
2712 017026 000040
2713 017030 000022

```

```

.BYTE 15,12
.ASCIZ /WIDTH-NUMBER OF STATES/

```

```

.EVEN
VCREG1: 0
VCREG2: 0
DIFCHN: 0

```

```

.SBTTL PARAMETER ADJUSTMENT ROUTINE
WFADJ: MOV #V1754,R1 ;LOAD PARM. POINTER
TST WFTEST ;TEST IF OPTION TEST AREA
BNE 1$ ;BR IF IT WAS
MOV #VARLT1,R2 ;LOAD "STANDARD" PARM. VALUES
MOVB #'2,BASBT1 ;LOAD BIAS MESSAGE
MOVB #'0,BASBT2
MOVB #'5,BASBT3 ;LOAD BIAS LIMIT
MOVB #'0,BASBT3+1
BR 2$
1$: MOV #VARLT2,R2 ;LOAD "OPTION TEST AREA" PARM. VALUES
MOVB #'1,BASBT1 ;LOAD BIAS MESSAGE
MOVB #'8,BASBT2
MOVB #'4,BASBT3 ;LOAD BIAS LIMIT
MOVB #'4,BASBT3+1
2$: MOV (R2)+,(R1)+ ;LOAD INTO PARM. LIST
TST (R1) ;TEST FOR LAST
BPL 2$ ;BR IF NOT
RTS PC ;EXIT

```

```
WFTEST: 0
```

```

V1754: 1754
V24: 24
V50: 50
VA24: 24
V62: 62
V77: 77
V1: 1
BIT15

```

```

:1760 IF OPTION TEST AREA SELECTED
:20 " " " "
:40 " " " "
:224 " " " "
:452 " " " "
:457 " " " "
:15 " " " "
RM.

```

```

VARLT1: 1754
24
50
24
62
77
1
VARLT2: 1760
20
40
22

```



2714	017032	000045		45	
2715	017034	000045		45	
2716	017036	000015		15	
2717					
2718					.SBTTL SUBROUTINE TO CONVERT R0 TO DECIMAL PERCENTAGE OF 1022.
2719					
2720	017040	000000		MSGPNT:	0
2721	017042	000000		PCT:	0
2722	017044	012537	017040	PRCNT:	MOV (R5)+,MSGPNT ;GET MESSAGE POINTER
2723	017050	010037	017042		MOV R0,PCT ;
2724	017054	006200			ASR R0 ;
2725	017056	006200			ASR R0 ;
2726	017060	006200			ASR R0 ;
2727	017062	006200			ASR R0 ;
2728	017064	006200			ASR R0 ;
2729	017066	006200			ASR R0 ;
2730	017070	160037	017042		SUB R0,PCT ;
2731	017074	006200			ASR R0 ;
2732	017076	160037	017042		SUB R0,PCT ;PCT CONTAINS OCTAL %
2733	017102	013702	017042		MOV PCT,R2 ;LOAD R2 WITH PERCENTAGE
2734	017106	004737	011342		JSR PC,DECPRT ;CONVERT TO DEC.
2735	017112	013700	017040		MOV MSGPNT,R0 ;LOAD MSG. POINTER
2736	017116	022737	001750 017042		CMP #1000.,PCT ;TEST FOR 100 %
2737	017124	001411			BEQ 1\$ ;BR IF 100 %
2738	017126	113720	011463		MOVB DIGT1,(R0)+ ;LOAD A DIGIT
2739	017132	113720	011464		MOVB DIGT2,(R0)+ ;
2740	017136	112720	000056		MOVB #56,(R0)+ ;LOAD "."
2741	017142	113720	011465		MOVB DIGT3,(R0)+ ;LOAD 1/10 % DIGIT
2742	017146	000205			RTS ;EXIT
2743					
2744	017150	112720	000040	1\$:	MOVB #40,(R0)+ ;LOAD "SPACE"
2745	017154	112720	000061		MOVB #'1,(R0)+ ;LOAD '1' 100%
2746	017160	112720	000060		MOVB #'0,(R0)+ ;
2747	017164	112720	000060		MOVB #'0,(R0)+ ;
2748	017170	000205			RTS ;EXIT

```

2749
2750          .SBTTL  SCOPE HANDLER ROUTINE
2751
2752          ;*****
2753          ;*THIS ROUTINE CONTROLS THE LOOPING OF SUBTESTS. IT WILL INCREMENT
2754          ;*AND LOAD THE TEST NUMBER($STNM) INTO THE DISPLAY REG.(DISPLAY<7:0>)
2755          ;*AND LOAD THE ERROR FLAG ($ERFLG) INTO DISPLAY<15:08>
2756          ;*THE SWITCH OPTIONS PROVIDED BY THIS ROUTINE ARE:
2757          ;*SW14=1      LOOP ON TEST
2758          ;*SW11=1      INHIBIT ITERATIONS
2759          ;*SW09=1      LOOP ON ERROR
2760          ;*SW08=1      LOOP ON TEST IN SWR<7:0>
2761          ;*CALL
2762          ;*          SCOPE          ;;SCOPE=IOT
2763
2764          $SCOPE:
2765          017172      104405
2766          017172      032777      040000      161734      1$:      CKSWR
2767          017202      001114
2768          ;*****START OF CODE FOR THE XOR TESTER*****
2769          017204      000416      $XTSTR: BR      $S          ;;LOOP ON PRESENT TEST?
2770          ;*****END OF CODE FOR THE XOR TESTER*****
2771          017206      013746      000004          MOV      @#ERRVEC, -(SP)          ;;IF RUNNING ON THE "XOR" TESTER CHANGE
2772          017212      012737      017232      000004          MOV      #5$, @#ERRVEC          ;;THIS INSTRUCTION TO A "NOP" (NOP=240)
2773          017220      005737      177060          TST      @#177060          ;;SAVE THE CONTENTS OF THE ERROR VECTOR
2774          017224      012637      000004          MOV      (SP)+, @#ERRVEC          ;;SET FOR TIMEOUT
2775          017230      000463          BR      $SVLAD          ;;TIME OUT ON XOR?
2776          017232      022626      5$:      CMP      (SP)+, (SP)+          ;;RESTORE THE ERROR VECTOR
2777          017234      012637      000004          MOV      (SP)+, @#ERRVEC          ;;GO TO THE NEXT TEST
2778          017240      000423          BR      7$          ;;CLEAR THE STACK AFTER A TIME OUT
2779          017242          6$: ;*****END OF CODE FOR THE XOR TESTER*****          ;;RESTORE THE ERROR VECTOR
2780          017242      032777      000400      161666          BIT      #BIT08, @SWR          ;;LOOP ON THE PRESENT TEST
2781          017250      001404          BEQ      2$          ;;LOOP ON SPEC. TEST?
2782          017252      127737      161660      001102          CMPB     @SWR, $STNM          ;;BR IF NO
2783          017260      001465          BEQ      $OVER          ;;ON THE RIGHT TEST? SWR<7:0>
2784          017262      105737      001103      2$:      TSTB     $ERFLG          ;;BR IF YES
2785          017266      001421          BEQ      3$          ;;HAS AN ERROR OCCURRED?
2786          017270      123737      001115      001103          CMPB     $ERMAX, $ERFLG          ;;BR IF NO
2787          017276      101015          BHI      3$          ;;MAX. ERRORS FOR THIS TEST OCCURRED?
2788          017300      032777      001000      161630          BIT      #BIT09, @SWR          ;;LOOP ON ERROR?
2789          017306      001404          BEQ      4$          ;;BR IF NO
2790          017310      013737      001110      001106      7$:      MOV      $LPERR, $LPADR          ;;SET LOOP ADDRESS TO LAST SCOPE
2791          017316      000446          BR      $OVER
2792          017320      105037      001103      4$:      CLRB     $ERFLG          ;;ZERO THE ERROR FLAG
2793          017324      005037      001154          CLR      $TIMES          ;;CLEAR THE NUMBER OF ITERATIONS TO MAKE
2794          017330      000415          BR      1$          ;;ESCAPE TO THE NEXT TEST
2795          017332      032777      004000      161576      3$:      BIT      #BIT11, @SWR          ;;INHIBIT ITERATIONS?
2796          017340      001011          BNE      1$          ;;BR IF YES
2797          017342      005737      001206          TST      $PASS          ;;IF FIRST PASS OF PROGRAM
2798          017346      001406          BEQ      1$          ;;INHIBIT ITERATIONS
2799          017350      005237      001104          INC      $ICNT          ;;INCREMENT ITERATION COUNT
2800          017354      023737      001164      001104          CMP      $TIMES, $ICNT          ;;CHECK THE NUMBER OF ITERATIONS MADE
2801          017362      002024          BGE      $OVER          ;;BR IF MORE ITERATION REQUIRED
2802          017364      012737      000001      001104      1$:      MOV      #1, $ICNT          ;;REINITIALIZE THE ITERATION COUNTER
2803          017372      013737      017450      001164          MOV      $MXCNT, $TIMES          ;;SET NUMBER OF ITERATIONS TO DO
2804          017400      105237      001102          $SVLAD: INCB     $STNM          ;;COUNT TEST NUMBERS

```



K05

```

2805 017404 113737 001102 001204      MOVB   $TSTNM,$TESTN      ;; SET TEST NUMBER IN APT MAILBOX
2806 017412 011637 001106              MOV    (SP),$LPADR        ;; SAVE SCOPE LOOP ADDRESS
2807 017416 011637 001110              MOV    (SP),$LPERR       ;; SAVE ERROR LOOP ADDRESS
2808 017422 005037 001166              CLR    $ESCAPE           ;; CLEAR THE ESCAPE FROM ERROR ADDRESS
2809 017426 112737 000001 001115      MOVB   #1,$SERMAX        ;; ONLY ALLOW ONE(1) ERROR ON NEXT TEST
2810 017434 013777 001102 161476  $OVER:  MOV    $TSTNM,$DISPLAY  ;; DISPLAY TEST NUMBER
2811 017442 013716 001106              MOV    $LPADR,(SP)      ;; FUDGE RETURN ADDRESS
2812 017446 000002              RTI                      ;; FIXES PS
2813 017450 003720      $MXCNT: 2000.           ;; MAX. NUMBER OF ITERATIONS

.SBTTL  ERROR HANDLER ROUTINE

*****
*THIS ROUTINE WILL INCREMENT THE ERROR FLAG AND THE ERROR COUNT,
*SAVE THE ERROR ITEM NUMBER AND THE ADDRESS OF THE ERROR CALL
*AND GO TO $ERRTYP ON ERROR
*THE SWITCH OPTIONS PROVIDED BY THIS ROUTINE ARE:
*SW15=1      HALT ON ERROR
*SW13=1      INHIBIT ERROR TYPEOUTS
*SW10=1      BELL ON ERROR
*SW09=1      LOOP ON ERROR
*CALL
*      ERROR  N      ;;ERROR=EMT AND N=ERROR ITEM NUMBER

2829 017452      $ERROR:
2830 017452 105237 001103 7$:      INCB   $ERFLG           ;; SET THE ERROR FLAG
2831 017456 001775      BEQ    7$              ;; DON'T LET THE FLAG GO TO ZERO
2832 017460 013777 001102 161452      MOV    $TSTNM,$DISPLAY  ;; DISPLAY TEST NUMBER AND ERROR FLAG
2833 017466 032777 002000 161442      BIT    #BIT10,$SWR      ;; BELL ON ERROR?
2834 017474 001402      BEQ    1$              ;; NO - SKIP
2835 017476 104400 001170      TYPE   $SBELL          ;; RING BELL
2836 017502 005237 001112 1$:      INC    $ERTTL          ;; COUNT THE NUMBER OF ERRORS
2837 017506 011637 001116      MOV    (SP),$ERRPC     ;; GET ADDRESS OF ERROR INSTRUCTION
2838 017512 162737 000002 001116      SUB    #2,$ERRPC
2839 017520 117737 161372 001114      MOVB   $SERPC,$ITEMB   ;; STRIP AND SAVE THE ERROR ITEM CODE
2840 017526 032777 020000 161402      BIT    #BIT13,$SWR     ;; SKIP TYPEOUT IF SET
2841 017534 001004      BNE    20$            ;; SKIP TYPEOUTS
2842 017536 004737 020442      JSR    PC,$ERRTYP      ;; GO TO USER ERROR ROUTINE
2843 017542 104400 001175      TYPE   ,SCLF

2844 017546      20$:
2845 017546 122737 000001 001220      CMPB   #APTENV,$ENV    ;; RUNNING IN APT MODE
2846 017554 001007      BNE    2$              ;; NO SKIP APT ERROR REPORT
2847 017556 113737 001114 017570      MOVB   $ITEMB,21$     ;; SET ITEM NUMBER AS ERROR NUMBER
2848 017564 004737 021544      JSR    PC,$ATY4       ;; REPORT FATAL ERROR TO APT
2849 017570      000
2850 017571      000
2851 017572 000777      21$:      .BYTE 0
2852 017574 005777 161336      22$:      .BYTE 0
2853 017600 100001      2$:      BR     22$            ;; APT ERROR LOOP
2854 017602 000000      3$:      TST   $SWR           ;; HALT ON ERROR
2855 017604 032777 001000 161324      BPL   3$              ;; SKIP IF CONTINUE
2856 017612 001402      HALT   3$            ;; HALT ON ERROR!
2857 017614 013716 001110      4$:      BIT   #BIT09,$SWR    ;; LOOP ON ERROR SWITCH SET?
2858 017620 005737 001166      BEQ   4$              ;; BR IF NO
2859 017624 001402      MOV   $LPERR,(SP)    ;; FUDGE RETURN FOR LOOPING
2860 017626 013716 001166      TST   $ESCAPE        ;; CHECK FOR AN ESCAPE ADDRESS
                          BEQ   5$              ;; BR IF NONE
                          MOV   $ESCAPE,(SP)  ;; FUDGE RETURN ADDRESS FOR ESCAPE

```

```

2861 017632
2862 017632 022737 010436 000042 5$:      CMP      #SENDAD, @#42      ;;ACT-11 AUTO-ACCEPT?
2863 017640 001001      BNE      6$              ;;BRANCH IF NO
2864 017642 000000      HALT                    ;;YES
2865 017644
2866 017644 000002 6$:      RTI                    ;;RETURN
2867
2868 .SBTTL  TTY INPUT ROUTINE
2869
2870 ;*****
2871 ;*SOFTWARE SWITCH REGISTER CHANGE ROUTINE.
2872 ;*ROUTINE IS ENTERED FROM THE TRAP HANDLER, AND WILL
2873 ;*SERVICE THE TEST FOR CHANGE IN SOFTWARE SWITCH REGISTER TRAP CALL
2874 ;*WHEN OPERATING IN TTY FLAG MODE.
2875 017646 022737 000176 001136 $CKSWR:  CMP      #SWREG, SWR      ;; IS THE SOFT-SWR SELECTED?
2876 017654 001073      BNE      14$            ;; BRANCH IF NO
2877 017656 105777 161260      TSTB     @STKS          ;; CHAR THERE?
2878 017662 100070      BPL      14$            ;; IF NO, DON'T WAIT AROUND
2879 017664 117746 161254 2$:      MOV      @STKB, -(SP)     ;; SAVE THE CHAR
2880 017670 042716 177600      BIC      #1C177, (SP)   ;; STRIP-OFF THE ASCII
2881 017674 022726 000007      CMP      #7, (SP)+      ;; IS IT A CONTROL G?
2882 017700 001061      BNE      14$            ;; NO, RETURN TO USER
2883 017702 104400 020311      TYPE     ,SCNTLG        ;; YES, ECHO CONTROL G
2884
2885 017706 104400 020316 6$:      TYPE     $MSWR          ;; TYPE CURRENT CONTENTS
2886 017712 013746 000176      MOV      SWREG, -(SP)   ;; SAVE SWREG FOR TYPEOUT
2887 017716 104401      TYPOC   ,SCNTLU        ;; GO TYPE--OCTAL ASCII(ALL DIGITS)
2888 017720 104400 020327      TYPE     ,SMNEW         ;; PROMPT FOR NEW SWR
2889 017724 005046      CLR     -(SP)          ;; CLEAR COUNTER
2890 017726 005046      CLR     -(SP)          ;; THE NEW SWR
2891 017730 104406 7$:      RDCHR                    ;; GET NEXT CHAR
2892
2893 017732 022716 000025 8$:      CMP      #25, (SP)     ;; IS IT A CONTROL U?
2894 017736 001005      BNE      9$            ;; BRANCH IF NO
2895 017740 104400 020304      TYPE     ,SCNTLU        ;; YES, ECHO IT
2896 017744 062706 000006      ADD      #6, SP         ;; IGNORE PREVIOUS INPUT
2897 017750 000756      BR       6$            ;; LET'S TRY IT AGAIN
2898
2899 017752 022716 000015 9$:      CMP      #15, (SP)     ;; IS IT A <CR>?
2900 017756 001011      BNE      11$           ;; BRANCH IF NO
2901 017760 005766 000004      TST      4(SP)         ;; YES, IS IT THE FIRST CHAR?
2902 017764 001403      BEQ     10$           ;; BRANCH IF YES
2903 017766 016677 000002 161142      MOV      2(SP), @SWR    ;; SAVE NEW SWR
2904 017774 062706 000006 10$:      ADD      #6, SP         ;; CLEAR UP STACK
2905 020000 000417      BR       13$           ;; RETURN TO USER
2906 020002 022716 000012 11$:      CMP      #12, (SP)     ;; IS IT A <LF>?
2907 020006 001017      BNE      15$           ;; BRANCH IF NO
2908 020010 005766 000004      TST      4(SP)         ;; YES, IS IT THE FIRST CHAR?
2909 020014 001403      BEQ     12$           ;; YES
2910 020016 016677 000002 161112      MOV      2(SP), @SWR    ;; SAVE NEW SWR
2911 020024 062706 000006 12$:      ADD      #6, SP         ;; CLEAR UP STACK
2912 020030 013716 000046      MOV      @#46, (SP)     ;; GET RESTART
2913 020034 062716 000010      ADD      #10, (SP)      ;; ADDRESS
2914 020040 104400 001175 13$:      TYPE     ,SCRLF        ;; ECHO <CR> AND <LF>
2915 020044 000002 14$:      RTI                    ;; RETURN
2916 020046 004737 021456 15$:      JSR      PC, $TYPEPC   ;; ECHO CHAR

```



# M05

```

2917 020052 042726 177770          BIC      #177770,(SP)+  ;;RESTRICT TO 0-7
2918 020056 005766 000002          TST      2(SP)        ;;IS THIS THE FIRST CHAR
2919 020062 001403                   BEQ      16$          ;;BRANCH IF YES
2920 020064 006316                   ASL      (SP)        ;;NO, SHIFT PRESENT
2921 020066 006316                   ASL      (SP)        ;;CHAR OVER TO MAKE
2922 020070 006316                   ASL      (SP)        ;;ROOM FOR NEW ONE.
2923 020072 005266 000002          16$: INC      2(SP)        ;;KEEP COUNT OF CHAR
2924 020076 056616 177776          BIS      -2(SP),(SP) ;;SET IN NEW CHAR
2925 020102 000712                   BR       7$          ;;GET THE NEXT ONE
2926                                     ;;*****
2927                                     ;;THIS ROUTINE WILL INPUT A SINGLE CHARACTER FROM THE TTY
2928                                     ;;CALL:
2929                                     ;;
2930                                     ;;RDCHR                                ;;INPUT A SINGLE CHARACTER FROM THE TTY
2931                                     ;;RETURN HERE                          ;;CHARACTER IS ON THE STACK
2932                                     ;;WITH PARITY BIT STRIPPED OFF
2933
2934 020104 011646                   $RDCHR: MOV      (SP),-(SP) ;;PUSH DOWN THE PC
2935 020106 016666 000004 000002      MOV      4(SP),2(SP) ;;SAVE THE PS
2936 020114 105777 161022          1$: TSTB     @STKS      ;;WAIT FOR
2937 020120 100375                   BPL      1$          ;;A CHARACTER
2938 020122 117766 161016 000004      MOVB     @STKB,4(SP) ;;READ THE TTY
2939 020130 042766 177600 000004      BIC      #1<177>,4(SP) ;;GET RID OF JUNK IF ANY
2940 020136 026627 000004 000140      CMP      4(SP),#140  ;;IS IT UPPER CASE?
2941 020144 002407                   BLT      2$          ;;BRANCH IF YES
2942 020146 026627 000004 000175      CMP      4(SP),#175  ;;IS IT A SPECIAL CHAR?
2943 020154 003003                   BGT      2$          ;;BRANCH IF YES
2944 020156 042766 000040 000004      BIC      #40,4(SP)   ;;MAKE IT UPPER CASE
2945 020164 000002                   2$: RTI              ;;GO BACK TO USER
2946                                     ;;*****
2947                                     ;;THIS ROUTINE WILL INPUT A STRING FROM THE TTY
2948                                     ;;CALL:
2949                                     ;;
2950                                     ;;RDLIN                                ;;INPUT A STRING FROM THE TTY
2951                                     ;;RETURN HERE                          ;;ADDRESS OF FIRST CHARACTER WILL BE ON THE STACK
2952                                     ;;TERMINATOR WILL BE A BYTE OF ALL 0'S
2953
2954 020166 010346                   $RDLIN: MOV      R3, -(SP) ;;SAVE R3
2955 020170 012703 020274          1$: MOV      #STTYIN,R3  ;;GET ADDRESS
2956 020174 022703 020304          2$: CMP      #STTYIN+8.,R3 ;;BUFFER FULL?
2957 020200 101405                   BLOS     4$          ;;BR IF YES
2958 020202 104406                   RDCHR    ;;GO READ ONE CHARACTER FROM THE TTY
2959 020204 112613                   MOVB     (SP)+,(R3)  ;;GET CHARACTER
2960 020206 122713 000177          10$: CMPB    #177,(R3)  ;;IS IT A RUBOUT
2961 020212 001003                   BNE     3$          ;;SKIP IF NOT
2962 020214 104400 001174          4$: TYPE    $QUES     ;;TYPE A '?'
2963 020220 000763                   BR       1$          ;;CLEAR THE BUFFER AND LOOP
2964 020222 111337 020272          3$: MOVB     (R3),9$   ;;ECHO THE CHARACTER
2965 020226 104400 020272          TYPE    9$
2966 020232 122723 000015          CMPB    #15,(R3)+  ;;CHECK FOR RETURN
2967 020236 001356                   BNE     2$          ;;LOOP IF NOT RETURN
2968 020240 105063 177777          CLRB    -1(R3)     ;;CLEAR RETURN (THE 15)
2969 020244 104400 001176          TYPE    $LF        ;;TYPE A LINE FEED
2970 020250 012603                   MOV      (SP)+,R3   ;;RESTORE R3
2971 020252 011646                   MOV      (SP),-(SP) ;;ADJUST THE STACK AND PUT ADDRESS OF THE
2972 020254 016666 000004 000002      MOV      4(SP),2(SP) ;;FIRST ASCII CHARACTER ON IT
2973 020262 012766 020274 000004      MOV      #STTYIN,4(SP)

```

```

2973 020270 000002          RTI          ;; RETURN
2974 020272 000          9$: .BYTE 0      ;; STORAGE FOR ASCII CHAR. TO TYPE
2975 020273 000          .BYTE 0      ;; TERMINATOR
2976 020274 000010      $TTYIN: .BLKB 8.  ;; RESERVE 8 BYTES FOR TTY INPUT
2977 020304 052536 005015 000  $CNTLU: .ASCIZ /↑U<15><12>  ;; CONTROL "U"
2978 020311 136 006507 000012  $CNTLG: .ASCIZ /↑G<15><12>  ;; CONTROL "G"
2979 020316 005015 053523 020122  $MSWR: .ASCIZ <15><12>/SWR = /
2980 020324 020075 000
2981 020327 040 047040 053505  $MNEW: .ASCIZ / NEW = /
2982 020334 036440 000040
2983
2984 .SBTTL READ AN OCTAL NUMBER FROM THE TTY
2985
2986 *****
2987 *THIS ROUTINE WILL READ AN OCTAL (ASCII) NUMBER FROM THE TTY AND
2988 *CHANGE IT TO BINARY.
2989 *CALL:
2990 * RDOCT          ;; READ AN OCTAL NUMBER
2991 * RETURN HERE   ;; LOW ORDER BITS ARE ON TOP OF THE STACK
2992 *              ;; HIGH ORDER BITS ARE IN $HIOCT
2993
2994 020340 011646          $RDOCT: MOV (SP),-(SP)  ;; PROVIDE SPACE FOR THE
2995 020342 016666 000004 000002  MOV 4(SP),2(SP)  ;; INPUT NUMBER
2996 020350 010046          MOV R0,-(SP)    ;; PUSH R0 ON STACK
2997 020352 010146          MOV R1,-(SP)    ;; PUSH R1 ON STACK
2998 020354 010246          MOV R2,-(SP)    ;; PUSH R2 ON STACK
2999 020356 104407 1$: RDLIN          ;; READ AN ASCII LINE
3000 020360 012600          MOV (SP)+,R0    ;; GET ADDRESS OF 1ST CHARACTER
3001 020362 005001          CLR R1          ;; CLEAR DATA WORD
3002 020364 005002          CLR R2
3003 020366 112046 2$: MOV B (R0)+,-(SP)  ;; PICKUP THIS CHARACTER
3004 020370 001412          BEQ 3$          ;; IF ZERO GET OUT
3005 020372 006301          ASL R1          ;; *2
3006 020374 006102          ROL R2          ;; *4
3007 020376 006301          ASL R1          ;; *8
3008 020400 006102          ROL R2
3009 020402 006301          ASL R1
3010 020404 006102          ROL R2
3011 020406 042716 177770  BIC #1C7,(SP)  ;; STRIP THE ASCII JUNK
3012 020412 062601          ADD (SP)+,R1    ;; ADD IN THIS DIGIT
3013 020414 000764          BR 2$          ;; LOOP
3014 020416 005726 3$: TST (SP)+          ;; CLEAN TERMINATOR FROM STACK
3015 020420 010166 000012  MOV R1,12(SP)  ;; SAVE THE RESULT
3016 020424 010237 020440  MOV R2,$HIOCT
3017 020430 012602          MOV (SP)+,R2    ;; POP STACK INTO R2
3018 020432 012601          MOV (SP)+,R1    ;; POP STACK INTO R1
3019 020434 012600          MOV (SP)+,R0    ;; POP STACK INTO R0
3020 020436 000002          RTI          ;; RETURN
3021 020440 000000  $HIOCT: .WORD 0      ;; HIGH ORDER BITS GO HERE
3022
3023 .SBTTL ERROR MESSAGE TYPEOUT ROUTINE
3024
3025 *****
3026 *THIS ROUTINE USES THE "ITEM CONTROL BYTE" ($ITEMB) TO DETERMINE WHICH
3027 *ERROR IS TO BE REPORTED. IT THEN OBTAINS, FROM THE "ERROR TABLE" ($ERRTB),
3028

```



;\*AND REPORTS THE APPROPRIATE INFORMATION CONCERNING THE ERROR.

000000  
000001  
000002  
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000070

020442 104400 001175  
020443 010046  
020444 005000  
020445 153700 001114  
020446 001004  
  
020450 013746 001116  
  
020464 104401  
020465 000426  
020466 005300  
020467 006300  
020468 006300  
020469 006300  
020470 062700 001324  
020471 012037 020514  
020472 001404  
020473 104400  
020474 000000  
020475 104400 001175  
020476 012037 020532  
020477 001404  
020478 104400  
020479 000000  
020480 104400 001175  
020481 011000  
020482 001004  
020483 012600  
020484 104400 001175  
020485 000207  
  
020486 013046  
020487 104401  
020488 005710  
020489 001770  
020490 104400 020572  
020491 000771  
020492 020040 000  
020493 020576

\$ERRTYP:  
TYPE \$CRLF  
MOV RO,-(SP)  
CLR RO  
BISB 3(\$ITEMB,RO)  
BNE 1\$  
  
MOV \$ERRPC,-(SP)  
  
TYPOC  
BR 6\$  
1\$: DEC RO  
ASL RO  
ASL RO  
ASL RO  
ADD \$ERRTB,RO  
MOV (RO)+,2\$  
BEQ 3\$  
TYPE  
2\$: .WORD 0  
TYPE \$CRLF  
3\$: MOV (RO)+,4\$  
BEQ 5\$  
TYPE  
4\$: .WORD 0  
TYPE \$CRLF  
5\$: MOV (RO),RO  
BNE 7\$  
6\$: MOV (SP)+,RO  
TYPE \$CRLF  
7\$: RTS PC  
  
MOV 3(RO)+,-(SP)  
TYPOC  
TST (RO)  
BEQ 6\$  
TYPE 8\$  
BR 7\$  
8\$: .ASCIZ / /  
.EVEN

:: "CARRIAGE RETURN" & "LINE FEED"  
:: SAVE RO  
:: PICKUP THE ITEM INDEX  
  
:: IF ITEM NUMBER IS ZERO, JUST  
:: TYPE THE PC OF THE ERROR  
:: SAVE \$ERRPC FOR TYPEOUT  
:: ERROR ADDRESS  
:: GO TYPE--OCTAL ASCII(ALL DIGITS)  
:: GET OUT  
:: ADJUST THE INDEX SO THAT IT WILL  
:: WORK FOR THE ERROR TABLE  
  
:: FORM TABLE POINTER  
:: PICKUP "ERROR MESSAGE" POINTER  
:: SKIP TYPEOUT IF NO POINTER  
:: TYPE THE "ERROR MESSAGE"  
:: "ERROR MESSAGE" POINTER GOES HERE  
:: "CARRIAGE RETURN" & "LINE FEED"  
:: PICKUP "DATA HEADER" POINTER  
:: SKIP TYPEOUT IF 0  
:: TYPE THE "DATA HEADER"  
:: "DATA HEADER" POINTER GOES HERE  
:: "CARRIAGE RETURN" & "LINE FEED"  
:: PICKUP "DATA TABLE" POINTER  
:: GO TYPE THE DATA  
:: RESTORE RO  
:: "CARRIAGE RETURN" & "LINE FEED"  
:: RETURN  
  
:: SAVE 3(RO)+ FOR TYPEOUT  
:: GO TYPE--OCTAL ASCII(ALL DIGITS)  
:: IS THERE ANOTHER NUMBER?  
:: BR IF NO  
:: TYPE TWO(2) SPACES  
:: LOOP  
:: TWO(2) SPACES

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.SBTTL POWER DOWN AND UP ROUTINES

::\*\*\*\*\*

:POWER DOWN ROUTINE

```

$PWRDN: MOV $SILLUP, @PWRVEC ;; SET FOR FAST UP
        MOV #340, @PWRVEC+2 ;; PRIO:7
        MOV RO, -(SP) ;; PUSH RO ON STACK
        MOV R1, -(SP) ;; PUSH R1 ON STACK
        MOV R2, -(SP) ;; PUSH R2 ON STACK
        MOV R3, -(SP) ;; PUSH R3 ON STACK
        MOV R4, -(SP) ;; PUSH R4 ON STACK
        MOV R5, -(SP) ;; PUSH R5 ON STACK
        MOV @SWR, -(SP) ;; PUSH @SWR ON STACK
        MOV SP, @SAVR6 ;; SAVE SP
        MOV @PWRUP, @PWRVEC ;; SET UP VECTOR
        HALT
        BR -2 ;; HANG UP

```

::\*\*\*\*\*

:POWER UP ROUTINE

```

$PWRUP: MOV $SILLUP, @PWRVEC ;; SET FOR FAST DOWN
        MOV @SAVR6, SP ;; GET SP
        CLR @SAVR6 ;; WAIT LOOP FOR THE TTY
1$: INC @SAVR6 ;; WAIT FOR THE INC
    BNE 1$ ;; OF WORD
    MOV (SP)+, @SWR ;; POP STACK INTO @SWR
    MOV (SP)+, R5 ;; POP STACK INTO R5
    MOV (SP)+, R4 ;; POP STACK INTO R4
    MOV (SP)+, R3 ;; POP STACK INTO R3
    MOV (SP)+, R2 ;; POP STACK INTO R2
    MOV (SP)+, R1 ;; POP STACK INTO R1
    MOV (SP)+, RO ;; POP STACK INTO RO
    MOV @PWRDN, @PWRVEC ;; SET UP THE POWER DOWN VECTOR
    MOV #340, @PWRVEC+2 ;; PRIO:7
    TYPE PWRMSG ;; REPORT THE POWER FAILURE
    MOV (PC)+, (SP) ;; POWER FAIL MESSAGE POINTER
    SPWRAD: .WORD BEGIN ;; RESTART AT BEGIN
    RTI ;; RESTART ADDRESS
    HALT ;; THE POWER UP SEQUENCE WAS STARTED
    BR -2 ;; BEFORE THE POWER DOWN WAS COMPLETE
    $SAVR6: 0 ;; PUT THE SP HERE
    PWRMSG: .ASCIZ <15><12>/RESTARTING AFTER A POWER FAILURE/<15><12><12>

```

.EVEN



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.SBTTL BINARY TO OCTAL (ASCII) AND TYPE

```

*****
*THIS ROUTINE IS USED TO CHANGE A 16-BIT BINARY NUMBER TO A 6-DIGIT
*OCTAL (ASCII) NUMBER AND TYPE IT.
*$TYPOS---ENTER HERE TO SETUP SUPPRESS ZEROS AND NUMBER OF DIGITS TO TYPE
*CALL:
*      MOV      NUM,-(SP)      ;;NUMBER TO BE TYPED
*      TYPOS    ;;CALL FOR TYPEOUT
*      .BYTE   N              ;;N=1 TO 6 FOR NUMBER OF DIGITS TO TYPE
*      .BYTE   M              ;;M=1 OR 0
*                               ;;1=TYPE LEADING ZEROS
*                               ;;0=SUPPRESS LEADING ZEROS
*$TYPON----ENTER HERE TO TYPE OUT WITH THE SAME PARAMETERS AS THE LAST
*$TYPOS OR $TYPOC
*CALL:
*      MOV      NUM,-(SP)      ;;NUMBER TO BE TYPED
*      TYPON    ;;CALL FOR TYPEOUT
*$TYPOC---ENTER HERE FOR TYPEOUT OF A 16 BIT NUMBER
*CALL:
*      MOV      NUM,-(SP)      ;;NUMBER TO BE TYPED
*      TYPOC    ;;CALL FOR TYPEOUT
*$TYPOS: MOV      0(SP),-(SP)    ;;PICKUP THE MODE
        MOV      1(SP),SOFILL    ;;LOAD ZERO FILL SWITCH
        MOV      (SP)+,SOMODE+1  ;;NUMBER OF DIGITS TO TYPE
        ADD     #2,(SP)          ;;ADJUST RETURN ADDRESS
        BR      $TYPON
*$TYPOC: MOV      #1,SOFILL      ;;SET THE ZERO FILL SWITCH
        MOV      #6,SOMODE+1     ;;SET FOR SIX(6) DIGITS
*$TYPON: MOV      #5,SOCNT       ;;SET THE ITERATION COUNT
        MOV      R3,-(SP)        ;;SAVE R3
        MOV      R4,-(SP)        ;;SAVE R4
        MOV      R5,-(SP)        ;;SAVE R5
        MOV      SOMODE+1,R4     ;;GET THE NUMBER OF DIGITS TO TYPE
        NEG     R4
        ADD     #6,R4            ;;SUBTRACT IT FOR MAX. ALLOWED
        MOV      R4,SOMODE       ;;SAVE IT FOR USE
        MOV      SOFILL,R4       ;;GET THE ZERO FILL SWITCH
        MOV      12(SP),R5      ;;PICKUP THE INPUT NUMBER
        CLR     R3              ;;CLEAR THE OUTPUT WORD
        ROL    R5              ;;ROTATE MSB INTO "C"
        BR     2$              ;;GO DO MSB
        ROL    R5              ;;FORM THIS DIGIT
        BR     3$
        ROL    R5
        ROL    R5
        ROL    R5
        MOV     R5,R3
        ROL    R3              ;;GET LSB OF THIS DIGIT
        DECB   SOMODE          ;;TYPE THIS DIGIT?
        BPL    7$              ;;BR IF NO
        BIC   #177770,R3      ;;GET RID OF JUNK
        BNE   4$              ;;TEST FOR 0

```

021016	017646	000000	
021022	116637	000001	021241
021030	112637	021243	
021034	062716	000002	
021040	000406		
021042	112737	000001	021241
021050	112737	000006	021243
021056	112737	000005	021240
021064	010346		
021066	010446		
021070	010546		
021072	113704	021243	
021076	005404		
021100	062704	000006	
021104	110437	021242	
021110	113704	021241	
021114	016605	000012	
021120	005003		
021122	006105		1\$:
021124	000404		
021126	006105		2\$:
021130	006105		
021132	006105		
021134	010503		
021136	006103		3\$:
021140	105337	021242	
021144	100016		
021146	042703	177770	
021152	001002		

2179	021154	005704		TST	R4	:: SUPPRESS THIS 0?
2180	021156	001403		BEQ	5\$	:: BR IF YES
2181	021160	005204		4\$: INC	R4	:: DON'T SUPPRESS ANYMORE 0'S
2182	021162	052703	000060	BIS	#'0,R3	:: MAKE THIS DIGIT ASCII
2183	021166	052703	000040	5\$: BIS	#',R3	:: MAKE ASCII IF NOT ALREADY
2184	021172	110337	021236	MOVB	R3,8\$	:: SAVE FOR TYPING
2185	021176	104400	021236	TYPE	8\$	:: GO TYPE THIS DIGIT
2186	021202	105337	021240	7\$: DECB	\$OCNT	:: COUNT BY 1
2187	021206	003347		BGT	2\$	:: BR IF MORE TO DO
2188	021210	002402		BLT	6\$	:: BR IF DONE
2189	021212	005204		INC	R4	:: INSURE LAST DIGIT ISN'T A BLANK
2190	021214	000744		BR	2\$	:: GO DO THE LAST DIGIT
2191	021216	012605		6\$: MOV	(SP)+,R5	:: RESTORE R5
2192	021220	012604		MOV	(SP)+,R4	:: RESTORE R4
2193	021222	012603		MOV	(SP)+,R3	:: RESTORE R3
2194	021224	016666	000002 000004	MOV	2(SP),4(SP)	:: SET THE STACK FOR RETURNING
2195	021232	012616		MOV	(SP)+,(SP)	
2196	021234	000002		RTI		:: RETURN
2197	021236	000		8\$: .BYTE	0	:: STORAGE FOR ASCII DIGIT
2198	021237	000		.BYTE	00	:: TERMINATOR FOR TYPE ROUTINE
2199	021240	000		\$OCNT: .BYTE	00	:: OCTAL DIGIT COUNTER
2200	021241	000		\$OFILL: .BYTE	00	:: ZERO FILL SWITCH
2201	021242	000000		\$OMODE: .WORD	0	:: NUMBER OF DIGITS TO TYPE

.SBTTL TYPE ROUTINE

```

*****
*ROUTINE TO TYPE ASCIZ MESSAGE. MESSAGE MUST TERMINATE WITH A 0 BYTE.
*THE ROUTINE WILL INSERT A NUMBER OF NULL CHARACTERS AFTER A LINE FEED.
*NOTE1: $NULL CONTAINS THE CHARACTER TO BE USED AS THE FILLER CHARACTER.
*NOTE2: $FILLS CONTAINS THE NUMBER OF FILLER CHARACTERS REQUIRED.
*NOTE3: $FILLC CONTAINS THE CHARACTER TO FILL AFTER.
*

```

```

*CALL:
*1) USING A TRAP INSTRUCTION
*   TYPE      ,MESADR      ;;MESADR IS FIRST ADDRESS OF AN ASCIZ STRING
*OR
*   TYPE
*   MESADR
*

```

2220	021244	105737	001155	\$TYPE: TSTB	\$TPFLG	:: IS THERE A TERMINAL?
2221	021250	100002		BPL	1\$	:: BR IF YES
2222	021252	000000		HALT		:: HALT HERE IF NO TERMINAL
2223	021254	000430		BR	3\$	:: LEAVE
2224	021256	010046		1\$: MOV	RO,-(SP)	:: SAVE RO
2225	021260	017600	000002	MOV	32(SP),RO	:: GET ADDRESS OF ASCIZ STRING
2226	021264	122737	000001 001220	CMPB	#APTENV,\$ENV	:: RUNNING IN APT MODE
2227	021272	001011		BNE	62\$	:: NO GO CHECK FOR APT CONSOLE
2228	021274	132737	000100 001221	BITB	#APTSPool,\$ENVM	:: SPOOL MESSAGE TO APT
2229	021302	001405		BEQ	62\$	:: NO GO CHECK FOR CONSOLE
2230	021304	010037	021314	MOV	RO,61\$	:: SETUP MESSAGE ADDRESS FOR APT
2231	021310	004737	021534	JSR	PC,\$ATY3	:: SPOOL MESSAGE TO APT
2232	021314	000000		61\$: .WORD	0	:: MESSAGE ADDRESS
2233	021316	132737	000040 001221	62\$: BITB	#APTCSUP,\$ENVM	:: APT CONSOLE SUPPRESSED
2234	021324	001003		BNE	60\$	:: YES, SKIP TYPE OUT



```

3235 021326 112046      2$:   MOVB   (RO)+,-(SP)   ;; PUSH CHARACTER TO BE TYPED ONTO STACK
3236 021330 001005      BNE    4$              ;; BR IF IT ISN'T THE TERMINATOR
3237 021332 005726      TST    (SP)+          ;; IF TERMINATOR POP IT OFF THE STACK
3238 021334 012600      60$:   MOV    (SP)+,RO   ;; RESTORE RO
3239 021336 062716 000002 3$:   ADD    #2,(SP)       ;; ADJUST RETURN PC
3240 021342 000002      RTI                    ;; RETURN
3241 021344 122716 000011 4$:   CMPB   #HT,(SP)      ;; BRANCH IF <HT>
3242 021350 001430      BEQ    8$              ;;
3243 021352 122716 000200 8$:   CMPB   #CRLF,(SP)   ;; BRANCH IF NOT <CRLF>
3244 021356 001006      BNE    5$              ;;
3245 021360 005726      TST    (SP)+          ;; POP <CR><LF> EQUIV
3246 021362 104400      TYPE   $CRLF          ;; TYPE A CR AND LF
3247 021364 001175      $CRLF
3248 021366 105037 021522  CLRB   $CHARCNT       ;; CLEAR CHARACTER COUNT
3249 021372 000755      BR     2$              ;; GET NEXT CHARACTER
3250 021374 004737 021456 5$:   JSR    PC,$TYPEC      ;; GO TYPE THIS CHARACTER
3251 021400 123726 001154 6$:   CMPB   $FILLC,(SP)+  ;; IS IT TIME FOR FILLER CHARS.?
3252 021404 001350      BNE    2$              ;; IF NO GO GET NEXT CHAR.
3253 021406 013746 001152  MOV    $NULL,-(SP)    ;; GET # OF FILLER CHARS. NEEDED
3254                                ;; AND THE NULL CHAR.
3255 021412 105366 000001 7$:   DECB   1(SP)          ;; DOES A NULL NEED TO BE TYPED?
3256 021416 002770      BLT    6$              ;; BR IF NO--GO POP THE NULL OFF OF STACK
3257 021420 004737 021456  JSR    PC,$TYPEC      ;; GO TYPE A NULL
3258 021424 105337 021522  DECB   $CHARCNT       ;; DO NOT COUNT AS A COUNT
3259 021430 000770      BR     7$              ;; LOOP
    
```

; HORIZONTAL TAB PROCESSOR

```

3263 021432 112716 000040 8$:   MOVB   #' (SP)       ;; REPLACE TAB WITH SPACE
3264 021436 004737 021456 9$:   JSR    PC,$TYPEC      ;; TYPE A SPACE
3265 021442 132737 000007 021522 BITB   #7,$CHARCNT    ;; BRANCH IF NOT AT
3266 021450 001372      BNE    9$              ;; TAB STOP
3267 021452 005726      TST    (SP)+          ;; POP SPACE OFF STACK
3268 021454 000724      BR     2$              ;; GET NEXT CHARACTER
3269 021456 105777 157464 $TYPEC: TSTB   $STPB        ;; WAIT UNTIL PRINTER IS READY
3270 021462 100375      BPL    $TYPEC
3271 021464 116677 000002 157456 MOVB   2(SP),$STPB    ;; LOAD CHAR TO BE TYPED INTO DATA REG.
3272 021472 122766 000015 000002 CMPB   #CR,2(SP)      ;; IS CHARACTER A CARRIAGE RETURN?
3273 021500 001003      BNE    1$              ;; BRANCH IF NO
3274 021502 105037 021522  CLRB   $CHARCNT       ;; YES--CLEAR CHARACTER COUNT
3275 021506 000406      BR     $TYPEX          ;; EXIT
3276 021510 122766 000012 000002 1$:   CMPB   #LF,2(SP)     ;; IS CHARACTER A LINE FEED?
3277 021516 001402      BEQ    $TYPEX          ;; BRANCH IF YES
3278 021520 105227      INCB   (PC)+          ;; COUNT THE CHARACTER
3279 021522 000000  $CHARCNT: .WORD 0    ;; CHARACTER COUNT STORAGE
3280 021524 000207  $TYPEX: RTS    PC
    
```

.SBTTL APT COMMUNICATIONS ROUTINE

```

3286 021526 112737 000001 021772 $ATY1: MOVB   #1,$FFLG    ;; TO REPORT FATAL ERROR
3287 021534 112737 000001 021770 $ATY3: MOVB   #1,$MFLG    ;; TO TYPE A MESSAGE
3288 021542 000403      BR     $ATYC
3289 021544 112737 000001 021772 $ATY4: MOVB   #1,$FFLG    ;; TO ONLY REPORT FATAL ERROR
3290 021552  $ATYC:
    
```

```

3291 021552 010046          MOV      RO,-(SP)          ;; PUSH RO ON STACK
3292 021554 010146          MOV      R1,-(SP)          ;; PUSH R1 ON STACK
3293 021556 105737 021770      TSTB    $MFLG             ;; SHOULD TYPE A MESSAGE?
3294 021562 001450          BEQ     5$                ;; IF NOT: BR
3295 021564 122737 000001 001220  CMPB    #APTENV,$ENV      ;; OPERATING UNDER APT?
3296 021572 001031          BNE     3$                ;; IF NOT: BR
3297 021574 132737 000100 001221  BITB    #APTPOOL,$ENVM   ;; SHOULD SPOOL MESSAGES?
3298 021602 001425          BEQ     3$                ;; IF NOT: BR
3299 021604 017600 000004          MO'     24(SP),RO        ;; GET MESSAGE ADDR.
3300 021610 062766 000002 000004  ADD     #2,4(SP)          ;; BUMP RETURN ADDR.
3301 021616 005737 001200          TST     $MSGTYPE         ;; SEE IF DONE W/ LAST XMISSION?
3302 021622 001375          BNE     1$                ;; IF NOT: WAIT
3303 021624 010037 001214  MOV     RO,$MSGAD         ;; PUT ADDR IN MAILBOX
3304 021630 105720          TSTB    (RO)+            ;; FIND END OF MESSAGE
3305 021632 001376          BNE     2$                ;;
3306 021634 163700 001214  SUB     $MSGAD,RO         ;; SUB START OF MESSAGE
3307 021640 006200          ASR     RO                ;; GET MESSAGE LNGTH IN WORDS
3308 021642 010037 001216  MOV     RO,$MSGGLT        ;; PUT LENGTH IN MAILBOX
3309 021646 012737 000004 001200  MOV     #4,$MSGTYPE      ;; TELL APT TO TAKE MSG.
3310 021654 000413          BR      5$                ;;
3311 021656 017637 000004 021702 3$: MOV     24(SP),4$        ;; PUT MSG ADDR IN JSR LINKAGE
3312 021664 062766 000002 000004  ADD     #2,4(SP)          ;; BUMP RETURN ADDRESS
3313 021672 013746 177776          MOV     177776,-(SP)     ;; PUSH 177776 ON STACK
3314 021676 004737 021244  JSR     PC,$TYPE         ;; CALL TYPE MACRO
3315 021702 000000          4$: .WORD 0
3316 021704          5$:
3317 021704 105737 021772 10$: TSTB    $FFLG             ;; SHOULD REPORT FATAL ERROR?
3318 021710 001416          BEQ     12$              ;; IF NOT: BR
3319 021712 005737 001220  TST     $ENV              ;; RUNNING UNDER APT?
3320 021716 001413          BEQ     12$              ;; IF NOT: BR
3321 021720 005737 001200 11$: TST     $MSGTYPE         ;; FINISHED LAST MESSAGE?
3322 021724 001375          BNE     11$              ;; IF NOT: WAIT
3323 021726 017637 000004 001202  MOV     24(SP),$FATAL    ;; GET ERROR #
3324 021734 062766 000002 000004  ADD     #2,4(SP)          ;; BUMP RETURN ADDR.
3325 021742 005237 001200          INC     $MSGTYPE         ;; TELL APT TO TAKE ERROR
3326 021746 105037 021772 12$: CLRB   $FFLG             ;; CLEAR FATAL FLAG
3327 021752 105037 021771  CLRB   $LFLG             ;; CLEAR LOG FLAG
3328 021756 105037 021770  CLRB   $MFLG             ;; CLEAR MESSAGE FLAG
3329 021762 012601          MOV     (SP)+,R1         ;; POP STACK INTO R1
3330 021764 012600          MOV     (SP)+,RO        ;; POP STACK INTO RO
3331 021766 000207          RTS     PC                ;; RETURN
3332 021770          000          $MFLG: .BYTE 0          ;; MESSG. FLAG
3333 021771          000          $LFLG: .BYTE 0          ;; LOG FLAG
3334 021772          000          $FFLG: .BYTE 0          ;; FATAL FLAG
3335          021774          .EVEN
3336          000200          APTSIZE=200
3337          000001          APTENV=001
3338          000100          APTPOOL=100
3339          000040          APTCSUP=040
3340
3341          .SBTTL TRAP DECODER
3342
3343          ;;*****
3344          ;;*THIS ROUTINE WILL PICKUP THE LOWER BYTE OF THE "TRAP" INSTRUCTION
3345          ;;*AND USE IT TO INDEX THROUGH THE TRAP TABLE FOR THE STARTING ADDRESS
3346          ;;*OF THE DESIRED ROUTINE. THEN USING THE ADDRESS OBTAINED IT WILL

```



```

3347
3348
3349 021774 010046
3350 021776 016500 000002
3351 022002 005740
3352 022004 111000
3353 022006 006300
3354 022010 016000 022016
3355 022014 000200
3356
3357
3358
3359
3360
3361
3362
3363
3364
3365 022016
3366 022016 021244
3367 022020 021042
3368 022022 021016
3369 022024 021056
3370 022026 010472
3371 022030 017646
3372 022032 020104
3373 022034 020166
3374 022036 020340
3375
3376 022040 000000
3377 022042 001020
3378 024102 000000
3379 024104 000200
3380 024504 000000
3381 000001
  
```

;\*GO TO THAT ROUTINE.

```

$TRAP:  MOV    RO, -(SP)      ;; SAVE RO
        MOV    2(SP), RO    ;; GET TRAP ADDRESS
        TST   -(RO)        ;; BACKUP BY 2
        MOVB  (RO), RO     ;; GET RIGHT BYTE OF TRAP
        ASL   RO           ;; POSITION FOR INDEXING
        MOV   $TRPAD(RO), RO ;; INDEX TO TABLE
        RTS   RO           ;; GO TO ROUTINE
  
```

.SBTTL TRAP TABLE

;\*THIS TABLE CONTAINS THE STARTING ADDRESSES OF THE ROUTINES CALLED  
 ;\*BY THE "TRAP" INSTRUCTION.

ROUTINE  
 -----

```

$TRPAD: $TYPE    ;;CALL=TYPE    TRAP+0(104400) TTY TYPEOUT ROUTINE
        $TYPOC   ;;CALL=TYPOC   TRAP+1(104401) TYPE OCTAL NUMBER (WITH LEADING ZEROS)
        $TYPOS   ;;CALL=TYPOS   TRAP+2(104402) TYPE OCTAL NUMBER (NO LEADING ZEROS)
        $TYPON   ;;CALL=TYPON   TRAP+3(104403) TYPE OCTAL NUMBER (AS PER LAST CALL)
        $TYPDS   ;;CALL=TYPDS   TRAP+4(104404) TYPE DECIMAL NUMBER (WITH SIGN)
        $CKSWR   ;;CALL=CKSWR   TRAP+5(104405) TEST FOR CHANGE IN SOFT-SWR
        $RDCHR   ;;CALL=RDCHR   TRAP+6(104406) TTY TYPEIN CHARACTER ROUTINE
        $RDLIN   ;;CALL=RDLIN   TRAP+7(104407) TTY TYPEIN STRING ROUTINE
        $RDOCT   ;;CALL=RDOCT   TRAP+10(104410) READ AN OCTAL NUMBER FROM TTY
  
```

```

ADBUFF: 0
        .BLKW 1020
ABUFF4: 0
        .BLKW 200
LAST: 0
        .END
  
```

















		1700	1715	1716	1722*	1723*	1724*	1725*	1729*	1730*	1731*	1732*	1733*	1735*
RSLT1	014764	1700	1715	1716	1722*	1723*	1724*	1725*	1729*	1730*	1731*	1732*	1733*	1735*
RSLT2	015022	1736	1762	2385*										
RSLT3	015060	1122	1202	2390*										
RSVMSG	016376	1165	1206	2395*										
RSV1	016400	2584	2627*											
RSV2	016402	2573*	2628*											
RSV3	016403	2574*	2629*											
RSX1	016405	2575*	2630*											
RSX2	016406	2579*	2631*											
RSX3	016407	2580*	2632*											
RSX4	016410	2581*	2633*											
RD	=%000000	2582*	2634*											
		221*	603*	608*	685	712*	713*	714	717*	719*	1637*	1640	1641	1649
		1653*	1654*	1655*	1656*	1657*	1658*	1661	1699*	1702	1703	1711	1715*	1716*
		1717*	1718*	1719*	1720*	1723*	1782*	1812*	1815	1842	1852*	1856	1872*	1873*
		1886*	1914	1936*	1960*	1988*	2034*	2036*	2072*	2074*	2076*	2078*	2079*	2082*
		2087*	2452*	2476*	2487*	2488*	2490*	2491*	2493*	2495	2497	2499*	2507	2521*
		2522	2550*	2559*	2560*	2564*	2572*	2574*	2575*	2576*	2577*	2578*	2579*	2580*
		2731*	2732*	2733*	2734*	2735*	2736*	2737*	2738*	2739*	2740*	2741*	2742*	2743*
		2003	2019*	2023*	2024*	2025*	2026*	2027*	2028*	2029*	2030*	2031*	2032*	2033*
		2059*	2063*	2064*	2065*	2066*	2067*	2068*	2069*	2070*	2071*	2072*	2073*	2074*
		2304	2306*	2307*	2308*	2309*	2310*	2311*	2312*	2313*	2314*	2315*	2316*	2317*
R1	=%000001	222*	677*	678*	679*	680*	681*	682*	683*	684*	685*	686*	687*	688*
		111*	1153	1164*	1167*	1168*	1169*	1170*	1171*	1172*	1173*	1174*	1175*	1176*
		1177*	1178*	1179*	1180*	1181*	1182*	1183*	1184*	1185*	1186*	1187*	1188*	1189*
		1957*	1958*	1959*	1960*	1961*	1962*	1963*	1964*	1965*	1966*	1967*	1968*	1969*
		2103*	2104*	2105*	2106*	2107*	2108*	2109*	2110*	2111*	2112*	2113*	2114*	2115*
R2	=%000002	223*	611*	612*	613*	614*	615*	616*	617*	618*	619*	620*	621*	622*
		1163*	1164*	1165*	1166*	1167*	1168*	1169*	1170*	1171*	1172*	1173*	1174*	1175*
		1700*	1710*	1711*	1712*	1713*	1714*	1715*	1716*	1717*	1718*	1719*	1720*	1721*
		1974*	1975*	1976*	1977*	1978*	1979*	1980*	1981*	1982*	1983*	1984*	1985*	1986*
		2000*	2001*	2002*	2003*	2004*	2005*	2006*	2007*	2008*	2009*	2010*	2011*	2012*
R3	=%000003	224*	614*	617*	620*	624*	628*	630*	631*	632*	633*	634*	635*	636*
		1180*	1181*	1182*	1183*	1184*	1185*	1186*	1187*	1188*	1189*	1190*	1191*	1192*
		1974*	1975*	1976*	1977*	1978*	1979*	1980*	1981*	1982*	1983*	1984*	1985*	1986*
		2000*	2001*	2002*	2003*	2004*	2005*	2006*	2007*	2008*	2009*	2010*	2011*	2012*
R4	=%000004	225*	617*	621*	625*	629*	633*	635*	636*	637*	638*	639*	640*	641*
		1193*	1194*	1195*	1196*	1197*	1198*	1199*	1200*	1201*	1202*	1203*	1204*	1205*
		1974*	1975*	1976*	1977*	1978*	1979*	1980*	1981*	1982*	1983*	1984*	1985*	1986*
		2000*	2001*	2002*	2003*	2004*	2005*	2006*	2007*	2008*	2009*	2010*	2011*	2012*
R5	=%000005	226*	618*	622*	626*	630*	634*	636*	637*	638*	639*	640*	641*	642*
		1206*	1207*	1208*	1209*	1210*	1211*	1212*	1213*	1214*	1215*	1216*	1217*	1218*
		1974*	1975*	1976*	1977*	1978*	1979*	1980*	1981*	1982*	1983*	1984*	1985*	1986*
		2000*	2001*	2002*	2003*	2004*	2005*	2006*	2007*	2008*	2009*	2010*	2011*	2012*
R6	=%000006	227*	619*	623*	627*	631*	635*	637*	638*	639*	640*	641*	642*	643*
R7	=%000007	228*	620*	624*	628*	632*	636*	638*	639*	640*	641*	642*	643*	644*
SAR	010716	1101	1137	1170	1180	1218	1225	1248	1255	1290	1310	1330	1350	1371
SARCHN	015166	1101	1137	1170	1180	1218	1225	1248	1255	1290	1310	1330	1350	1371
SKIPST	015740	1101	1137	1170	1180	1218	1225	1248	1255	1290	1310	1330	1350	1371







		2039	2475	2516	2520	2525	2541	2545	2549	2553	2557	2563	2567	2569
		2583	2588	2597	2599	2601	2606	2608	2635	2643	2683	2685	2688	2695
		2914	2961	2964	2968	3032	3049	3051	3054	3056	3060	3067	3107	3185
		3246	3366*											
TYP0C =	104401	962	2887	3040	3064	3367*								
TYP0N =	104403	3369*												
TYP0S =	104402	701	966	1273	1277	2026	2030	2037	2518	2523	2543	2547	2551	2555
TYPSTG	011510	2610	3368*											
VARLT1	017004	1201	1205	1757	1761	2034*								
VARLT2	017022	2675	2702*											
VAR24	016772	2681	2710*											
VCREG1	016640	941	942	2696*										
VCREG2	016642	2435*	2440*	2442*	2460	2462*	2667*							
VSTAT	001506	2436*	2441*	2443*	2457*	2458	2463*	2668*						
		597*	770*	772*	773	779*	780	787*	788	798*	803*	804	814*	975*
V0XREG	001510	989*	990*	1004*	1018*	1033*	1048*	1063*	1078*	1087*	2439*	2617*	2618	
		596*	1096	1124*	1134*	1171	1181	1219	1226	1249	1256	1369*	1392*	1416
V0YREG	001512	1438	1565*	1586	1602*	1611*	1643	1703*						
		599*	1097	1128	1138	1167*	1177*	1217*	1247*	1291	1311	1331	1351	1372
		1394	1414*	1436*	1478	1497	1515	1541	1567	1584*	1620*	1629*	1641*	1705
V1	017000	2493	2699*											
V1754	016764	881	2672	2693*										
V24	016766	885	911	915	2694*									
V50	016770	900	2695*											
V62	016774	1148	1191	1486	1505	1523	1549	2697*						
V77	016776	1574	1593	1652	1714	2698*								
WFADJ	016646	684	2672*											
WFTEST	016762	604*	607*	2673	2691*									
WIDE	015212	2421*	2483*	2504*	2554	2559								
WIDMSG	014360	2326*	2557											
XDACPK	015142	1401*	1465	2411*										
XDACRM	015140	1379*	1464	2410*										
YDACP	015146	1445*	1469	2413*										
YDACRM	015144	1423*	1468	2412*										
SAPTHD	001000	366	372*											
SARG1	002110	669*												
SASTAT =	*****	3317	3332											
SATYC	021552	3288	3290*											
SATY1	021526	3286*												
SATY3	021534	3231	3287*											
SATY4	021544	2848	3289*											
SBASE	001254	474*	613	628	1777									
SBDADR	001122	399*												
SBDAT	001126	401*	635	773*	774	780*	781	788*	789	809*	810	824*	839*	854*
		869*	884*	899*	914*	928*	949*	950	979*	980	994*	995	1008*	1009
		1022*	1023	1037*	1038	1052*	1053	1067*	1068	1082*	1083	1101*	1105*	1109*
		1111	1144*	1145*	1146	1187*	1188*	1189	1231*	1232*	1233	1261*	1262*	1263*
		1296*	1299	1316*	1319	1336*	1339	1356*	1359	1377*	1378*	1379	1381	1399*
		1400*	1401	1403	1421*	1422*	1423	1425	1443*	1444*	1445	1447	1484*	1503*
		1521*	1528*	1547*	1554*	1571*	1572*	1590*	1591*	1606*	1624*	1675*	1736*	2075
		2362	2365	2367	2369	2371	2374							
S2ELL	001170	421*	2835	2867										
S2DW1	001260	476*												
S2DW2	001262	477*												
S2HARC	021522	3248*	3258*	3265	3274*	3279*								
S2KSWR	017646	2875*	3371											

U

















.HEADE	190#	
.SETUP	190#	632
.SWRHI	190#	315
.SWRLO	328#	
.SACTI	190#	344
.SAPT8	190#	426#
.SAPTH	190#	356
.SAPTY	190#	3282
.SCATC	190#	328
.SCMTA	190#	379
.SEOP	190#	1786
.SERRO	190#	2814
.SERRT	190#	3023
.SPARM	190#	
.SPOWE	190#	3072
.SROOC	190#	2983
.SREAD	190#	2867
.SSAVE	190#	
.SSCOP	190#	2749
.SSPAC	190#	
.SSWDO	190#	
.STRAP	190#	3340
.STYPD	190#	1828
.STYPE	190#	3202
.STYPO	190#	3124





INC	617	716	803	946	1800	1859	1921	1950	1955	1973	1999	2005	2051	2501	2503
INCB	2504	2506	2509	2510	2515	2526	2602	2617	2799	2836	2923	3096	3181	3189	3325
IOT	2454	2490	2804	2830	3278										
JMP	206														
JSR	340	341	342	687	690	1781	1783	1820							
	684	822	826	837	841	852	856	867	871	882	886	897	901	912	916
	926	930	951	977	992	1006	1020	1035	1050	1065	1080	1095	1127	1137	1149
	1170	1180	1192	1201	1205	1218	1225	1236	1248	1255	1266	1290	1310	1330	1350
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	1540	1550	1557	1566	1575	1585	1594	1604	1608	1622	1626	1642	1676	1704	1737
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	3257	3264	3314												
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	667	673	676	677	678	680	700	712	713	717	718	769	771	772	773
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	2847	2879	2938	2958	2963	3003	3151	3152	3155	3156	3157	3161	3164	3165	3184
	3235	3263	3271	3286	3287	3289	3352								
NEG	1850	1983	2078	3162											
NOP	1816	1817	1818												
RESET	609	631	675	1780	1814										
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RTS	1989	2008	2032	2042	2063	2084	2088	2533	2613	2625	2689	2742	2748	3061	3280



SUB	3331 1145 1630 2732 2047	3355 1189 1654 2838	1232 1662 3306	1262 1673	1378 1716	1400 1724	1422 1735	1444 1857	1528 1964	1554 1982	1572 2000	1591 2076	1611 2559	1612 2560	1629 2730
SWAB	3357 615 1872 3014	3367 620 1925 3065	3368 662 1970 3179	3369 681 1974 3237	3370 685 2458 3245	3371 688 2513 3267	3372 720 2673 3301	3373 1649 2687 3319	3374 1664 2773 3321	1690 2797 3351	1711 2852	1726 2858	1751 2901	1771 2908	1862 2918
TRAP	804 2877 1946	947 2936 1956	1864 3220	1878 3269	1930 3293	1941 3304	1951 3317	1979	2052	2507	2527	2603	2618	2622	2784
TSTB	1946 422 422 422	1956 423 424	2148 695	2211 699	2212 706	2213 710	2219 960	2224 1823	2232 2091	2248 2095	2261 2102	2288 2107	2293 2112	2657 2118	2123
WAIT	422 422 422	423 424	2148 695	2211 699	2212 706	2213 710	2219 960	2224 1823	2232 2091	2248 2095	2261 2102	2288 2107	2293 2112	2657 2118	2123
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1930 1935 1940 1945 1950 1955 1960 1965 1970 1975 1980 1985 1990 1995 2000 2005 2010 2015 2020 2025 2030 2035 2040 2045 2050 2055 2060 2065 2070 2075 2080 2085 2090 2095 2100 2105 2110 2115 2120 2125 2130 2135 2140 2145 2150 2155 2160 2165 2170 2175 2180 2185 2190 2195 2200 2205 2210 2215 2220 2225 2230 2235 2240 2245 2250 2255 2260 2265 2270 2275 2280 2285 2290 2295 2300 2305 2310 2315 2320 2325 2330 2335 2340 2345 2350 2355 2360 2365 2370 2375 2380 2385 2390 2395 2400 2405 2410 2415 2420 2425 2430 2435 2440 2445 2450 2455 2460 2465 2470 2475 2480 2485 2490 2495 2500 2505 2510 2515 2520 2525 2530 2535 2540 2545 2550 2555 2560 2565 2570 2575 2580 2585 2590 2595 2600 2605 2610 2615 2620 2625 2630 2635 2640 2645 2650 2655 2660 2665 2670 2675 2680 2685 2690 2695 2700 2705 2710 2715 2720 2725 2730 2735 2740 2745 2750 2755 2760 2765 2770 2775 2780 2785 2790 2795 2800 2805 2810 2815 2820 2825 2830 2835 2840 2845 2850 2855 2860 2865 2870 2875 2880 2885 2890 2895 2900 2905 2910 2915 2920 2925 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310		000240	...G1	2611	016330	004	...G5
323			...H1	2667	016640	000000	...H5
352			...I1	2723	017050	010037	...I5
388		000052	...J1	2758			...J5
434	001100	000000	...K1	2914	017450	003720	...K5
490	001204	000000	...L1	2870			...L5
546	001314	000000	...M1	2926			...M5
	001374	012161	...N1	2982	020334	036440	...N5
560	001420	014650	...B2	3038	020460	013746	...B6
612	001554	012737	...C2	3080	020614	010146	...C6
668	002104	012637	...D2	3132			...D6
724			...E2	3188	021210	002402	...E6
774	002600	023737	...F2	3244	021356	001006	...F6
825	003062	012737	...G2	3300	021610	062766	...G6
870	003252	012737	...H2	3356	022014	000200	...H6
915	003442	013737	...I2				...I6
971			...J2	ARBYCT	001466		...J6
1022	004172	013737	...K2	CH16 =	000016		...K6
1067	004366	013737	...L2	DIFERR	015742		...L6
1098	004504	001	...M2	NBEXT	001472		...M6
1125	004610	010137	...N2	RSX2	016406		...N6
1168	004776	010137	...B3				...B7
1221	005232	001771	...C3	TST13	003530		...C7
1251	005360	000010	...D3	VARLT1	017004		...D7
1293	005544	001000	...E3	\$DDWO	001264		...E7
1333	005674	000003	...F3	\$ITEMB	001114		...F7
1373	006030	000	...G3				...G7
1417	006214	000	...H3	\$40CAT=	***** U		...H7
1473			...I3	NEWTST	311#	765	...I7
1518	006642	000201	...J3	.SCMTA	190#	379#RE	...J7
1569	007076	001001	...K3	BCC	1867	2455	...K7
1606	007262	013737	...L3		1540	1550	...L7
1641	007444	012077	...M3	TSTB	804	947	...M7
1697	007712	000004	...N3		645	647	...N7
1753	010202	001341	...B4		891	895	...B8
1794			...C4	**END**	USER DAVIES, TOM		...C8
1836			...D4				
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1906			...F4				
1962	011220	020103	...G4				
2001	011402	100373	...H4				
2052	011576	105777	...I4				
2099	012012	050124	...J4				
2155	012451	105	...K4				
2211	013146	024040	...L4				
2267	013606	020051	...M4				
2323	014250	050111	...N4				