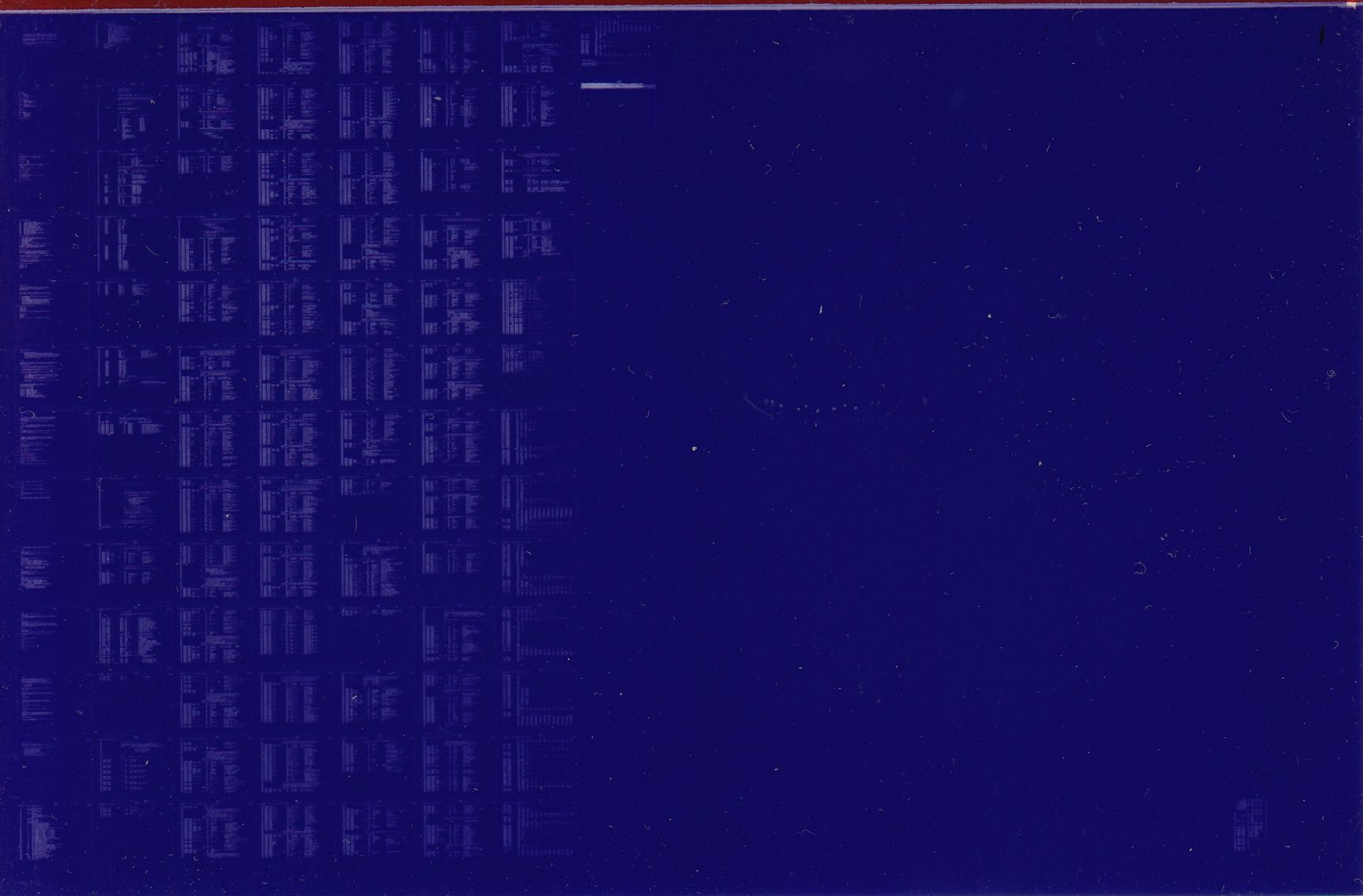


TA11

LOGIC TEST 2
MD-11-DZTAB-C

EP-DZTAB-C-DL-A NOV 1976
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FICHE 1 OF 1 MADE IN USA



IDENTIFICATION

000000

PRODUCT CODE: MAINTEN-11-00743-11
PRODUCT NAME: TALL LOGIC TEST PART 2
DATE REVISED: 21 JUNE 76
MAINTAINER: DIAGNOSTIC ENGINEERING
AUTHOR: JIM LACEY

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1. ABSTRACT

THIS PROGRAM CONTAINS A SERIES OF BASIC LOGIC TESTS THAT CHECK THE TAIL FOR PROPER OPERATION.

2. REQUIREMENTS

2.1 EQUIPMENT

2.2 PDP-11 COMPUTER WITH OR WITHOUT HARDWARE SWITCH REGISTER WITH CONSOLE TELETYPE, AND A TAIL CASSETTE STORAGE

THIS PROGRAM REQUIRES APPROX. 4K STORAGE.

2.3 PRELIMINARY PROGRAMS

MAINDEC-11-DZTAA

3. LOADING PROCEDURE

USE STANDARD PROCEDURE FOR LOADING .ABS TAPES OR A CASSETTE TAPE.

4. STARTING PROCEDURE

4.1 CONTROL SWITCH SETTINGS

SEE 5.1.

4.2 STARTING ADDRESSES

```

NORMAL STARTING ADDRESS
SELECT DRIVE(S) BEFORE STARTING TEST
SELECT DRIVE(S) AND ADDRESSES BEFORE STARTING TEST
SETUP FOR MANUAL LOOPING
WRITE FILE GAP FROM BOT TO EOT
WRITE CONTINUOUS BLOCKS OF DATA
READ CONTINUOUS BLOCKS OF DATA
WRITE FILE GAP AND A BLOCK OF DATA
READ BLOCK OF DATA AND INTO A FILE GAP
SPACE FWD FILE GAP FROM BOT TO EOT
BACK SPACE FILE GAPS
LOAD SWITCH REGISTER INTO THE TACS
WRITE SWITCH REGISTER ON TAPE FROM BOT TO EOT
READ FROM BOT TO EOT

```

4.3 PROGRAM & OPERATOR ACTION

1. LOAD PROGRAM INTO MEMORY (SEE SECTION 3.)
2. LOAD A WRITE ENABLED CASSETTE IN BOTH DRIVES
3. REWIND BOTH DRIVES
4. LOAD ADDRESS 200.
5. SET SWITCHES (SEE SECTION 5.1)
6. PRESS START.
7. THE PROGRAM WILL LOOP & TTY BELL WILL RING ONCE EVERY PASS, IF SW<10>=0.

*** NOTE: IF USING THE SOFTWARE SWITCH REGISTER PROGRAM WILL TYPE "SWR=XXXXXX NEW=" TO ALLOW SETTING OF REGISTER BEFORE BEGINNING TEST.

4.3.1 DRIVE SELECTION

STARTING THE PROGRAM AT 200 WILL RESULT IN AUTOMATIC SELECTION OF DRIVES "A" AND "B" TO BE TESTED.
NOTE: IF LOAD MEDIUM IS CASSETTE WITH STANDARD VECTOR PROGRAM WILL RESPOND AS IF STARTED AT 210.

STARTING THE PROGRAM AT 204, 210, OR 214 ALLOWS THE OPERATOR TO SELECT THE DRIVE(S) TO BE TESTED.

THE PROGRAM WILL TYPE "DRIVE(S)?".

EITHER OR BOTH DRIVES CAN BE SELECTED BY TYPING "A" AND/OR "B" FOLLOWED BY A CARRIAGE RETURN.

4.3.1.1 DRIVE SELECTION EXAMPLES

```

DRIVE(S)? A,B
DRIVE(S)? AB
DRIVE(S)? B,A
DRIVE(S)? B

```

4.3.2 ADDRESS SELECTION

STARTING THE PROGRAM AT 210 OR 214 ALLOWS THE OPERATOR TO CHANGE THE "CONTROL AND STATUS" AND "DATA BUFFER" REGISTER ADDRESSES, THE VECTOR ADDRESS AND THE PRIORITY LEVEL.

THE PROGRAM WILL ASK FOR THE DRIVES TO BE TESTED AS PER 4.3.1. AFTER THE DRIVES HAVE BEEN SELECTED IT WILL ASK FOR:

1. BUS ADDRESS OF THE CONTROL AND STATUS REGISTER (TACS)
 2. VECTOR ADDRESS
 3. PRIORITY LEVEL
- AND THE OPERATOR MUST RESPOND WITH THE DESIRED PARAMETER OR A CARRIAGE RETURN (WHICH IMPLIES LEAVE AS IS). WHEN ALL PARAMETERS HAVE BEEN DEFINED THE PROGRAM WILL TYPE THEM BACK OUT AND ASK IF THEY ARE OK AT WHICH TIME THE OPERATOR RESPONDES WITH A "Y" OR A "CARRIAGE RETURN" FOR "YES" ANYTHING ELSE IS A "NO".

4.3.2.1 ADDRESS SELECTION EXAMPLES

```
DRIVES(S) A
TACS? 177500
VECTOR? 260
PRIORITY? 6
TACS=177500 TADB=177502 VECTOR=000260 PRIORITY=000300
OK?
```

```
DRIVES(S) A,B
TACS? 470
VECTOR?
PRIORITY?
TACS=177470 TADB=177472 VECTOR=000260 PRIORITY=000300
OK?
```

5. OPERATING PROCEDURE

5.1 OPERATIONAL SWITCH SETTINGS

IF THE DIAGNOSTIC IS RUN ON A CPU WITHOUT A SWITCH REGISTER THEN A SOFTWARE SWITCH REGISTER IS USED WHICH ALLOWS THE USER THE SAME SWITCH OPTIONS AS THE HARDWARE SWITCH REGISTER. IF THE HARDWARE SWITCH REGISTER DOES NOT EXIST OR IF ONE DOES AND IT CONTAINS ALL ONES (177777) THEN THE SOFTWARE SWITCH REGISTER (LOC. 176) IS USED.

CONTROL:

THIS PROGRAM ALSO SUPPORTS THE DYNAMIC LOADING OF THE SOFTWARE SWITCH REGISTER (LOC. 176) FROM THE TTY. THIS CAN BE ACCOMPLISHED BY DOING THE FOLLOWING:

- 1) TYPE CONTROL G (<↑G>): THIS WILL ALLOW THE TTY TO ENTER DATA INTO LOC. 176 AT SELECTED POINTS WITHIN THE PROGRAM.
- 2) THE MACHINE WILL THEN TYPE: SWR=XXXXXXNEW= (XXXXXX IS THE COTAL CONTENTS OF THE SOFTWARE SWITCH REGISTER.)
- 3) AFTER THE '*NEW='* HAS BEEN TYPED THEN THE OPERATOR CAN DO ONE OF THE FOLLOWING AT THE TTY:
 - A) TYPE A NUMBER TO BE LOADED INTO LOC. 176 FOLLOWED BY A <CR>. (ONLY NUMBERS BETWEEN 0-7 WILL BE ACCEPTED AND ONLY 6 NUMBERS WILL BE ALLOWED)
IF A <CR> IS THE FIRST KEY DEPRESSED THE SOFTWARE SWITCH REGISTER CONTENTS WILL NOT BE CHANGED.
 - B) IF A CONTROL U (<↑U>) IS DEPRESSED THEN THE PROGRAM WILL SEND YOU BACK TO STEP 2.

WITH SW<15:09>=0 THE PROGRAM WILL PRINT OUT ON ERRORS AND CONTINUE IN TEST. BELL WILL RING AT COMPLETION OF A PASS.
THE SWITCH SETTINGS ARE:

SW<15>=1...HALT ON ERROR
 SW<14>=1...LOOP ON TEST
 SW<13>=1...INHIBIT ERROR TYPEOUTS
 SW<11>=1...INHIBIT ITERATIONS
 SW<10>=1...RING BELL ON ERROR
 SW<10>=0...RING BELL ON PASS COMPLETE
 SW<09>=1...LOOP ON ERROR
 SW<08>=1...LOOP ON TEST AS PER SW<07:00>
 SW<07>=1...LOCK ON CURRENT DRIVE (ONLY VALID FOR STARTING ADDRESSES 220 THRU 250).

5.2 SUBROUTINE ABSTRACTS

5.2.1 SCOPE

THIS SUBROUTINE CALL (VIA AN IST INSTRUCTION) IS PLACED BETWEEN EACH TEST IN THE INSTRUCTION SECTION. IT RECORDS THE STARTING ADDRESS OF EACH TEST IN LOCATION "SLPADR" AND "SLPERR" AS IT IS BEING ENTERED.

: THIS ROUTINE SUPPORTS THE S/W SWITCH REG FUNCTIONS

5.2.2 TRAPCATCHER

A "+2" - "HALT" SEQUENCE IS REPEATED FROM LOC. 0 TO LOC. 776 TO CATCH ANY UNEXPECTED TRAPS. THUS, ANY UNEXPECTED TRAPS WILL HALT AT THE DEVICE TRAP VECTOR +2.

5.2.3 ERROR

THIS SUBROUTINE CALL (VIA A EMT INSTRUCTION) IS USED TO REPORT ALL ERRORS. (REFER TO 6.)

: THIS ROUTINE SUPPORTS THE S/W SWITCH REG FUNCTIONS

5.2.4 TRAP

A NUMBER OF SUBROUTINES ARE CALLED BY THE TRAP INSTRUCTION. FOLLOWING IS THE CALLS USED AND THE LABEL OF THE STARTING ADDRESS OF THE SUBROUTINES.

5.2.4.1 TYPE (STYPE)

ROUTINE TO TYPE AN ASCIZ STRING ON THE TTY

THE ROUTINE WILL INSERT A NUMBER OF NULL CHARACTERS AFTER A LINE FEED.

5.2.4.2 RDCHR (SRDCHR)

READ A SINGLE ASCII CHARACTER FROM THE TTY

5.2.4.3 RDLIN (SRDLIN)

READ AN ASCII STRING FROM THE TTY

5.2.4.4 WAITREADY (WAIT.ON.READY)

WAIT ON THE "TALI READY" BIT TO SET

5.2.4.5 WAITXFER (WAIT.FOR.XFER.REQ)

WAIT ON THE "TALI TRANSFER REQUEST" BIT TO SET

5.2.4.6 STYPCO

CHANGE A BINARY NUMBER TO OCTAL ASCII AND TYPE IT.

5.2.5 THE FOLLOWING SUBROUTINES ARE CALLED BY A OFF

5.2.5.1 \$TYPEC

ROUTINE TO TYPE A SINGLE ASCII CHARACTER

5.2.5.2 TYPERR

THIS ROUTINE WILL TYPE THE ERROR MESSAGES

5.2.5.3 SELDRV

THIS ROUTINE IS USED TO ASK THE OPERATOR WHAT DRIVERS,
ARE TO BE TESTED

5.2.5.4 SELADR

THIS ROUTINE WILL ASK THE OPERATOR FOR THE ADDRESSES OF
THE "TACS", "TADS" AND VECTOR AND THE PRIORITY TO USE.

5.2.6 THE FOLLOW ROUTINES ARE USED TO MAKE ADJUSTMENTS TO THE TUGS. BEFORE USING ANY OF THEM LOAD AND START 214.

5.2.6.1 WFGSUB

WRITE FILE GAPS FROM "BOT" TO "EOT"
START AT 220
THIS ROUTINE CAN BE USED TO ADJUST THE "WRITE GAP MONO" AND THE "WRITE DELAY MONO".

5.2.6.2 WRTSUB

WRITE CONTINUOUS BLOCKS OF DATA
START AT 224
THE PROGRAM WILL HALT THREE(3) TIMES
AFTER EACH HALT SET THE SWR AND PRESS CONTINUE
HALT 1 --- SWR<7:0> = NUMBER OF BYTES PER BLOCK
HALT 2 --- SWR<7:0> = PATTERN DESIRED
HALT 3 --- SWR<15:0> = OPERATIONAL SWITCH SETTINGS
THIS ROUTINE CAN BE USED TO ADJUST THE "GAP TIME MONO"

** IF USING SOFTWARE SWITCH REGISTER, AFTER EACH HALT OPERATOR WILL BE PROMPTED FOR THE VALUE WITH "SWR=XXXXXX NEW="

5.2.6.3 RDSUB

READ CONTINUOUS BLOCKS OF DATA
START AT 230
THIS ROUTINE CAN BE USED TO ADJUST THE "SIGNAL MONO" AND THE "THRESHOLD POT"

5.2.6.4 WGPBLK

WRITE A FILE GAP AND A BLOCK OF DATA FROM BOT TO ECT
START AT 234
THE PROGRAM WILL HALT THREE (3) TIMES
AFTER EACH HALT SET THE SWR AND PRESS CONTINUE
HALT 1 --- SWR<7:0> = NUMBER OF BYTES PER BLOCK
HALT 2 --- SWR<7:0> = PATTERN DESIRED
HALT 3 --- SWR<15:0> = OPERATIONAL SWITCH SETTINGS
THIS ROUTINE CAN BE USED TO ADJUST THE "WRITE GAP MONO" AND THE "GAP TIME MONO".

** IF USING SOFTWARE SWITCH REGISTER, AFTER EACH HALT OPERATOR WILL BE PROMPTED FOR THE VALUE WITH "SWR=XXXXXX NEW="

5.2.6.5 ROBLK

READ A BLOCK OF DATA AND A FILE GAP

START AT 240

THIS ROUTINE IS USED AFTER "WRITE A BLOCK AND A FILE GAP" ROUTINE
IT CAN BE USED TO ADJUST THE "SIGNAL MON". THE THRESHOLD PCT"
AND THE "TAPE BLANK MONC".

5.2.6.6 SFFGSB

SPACE FORWARD FILE GAP FROM "BOT" TO "EOT"

START AT 244

THIS ROUTINE CAN BE USED AFTER "WRITE FILE GAP" FOR LOW SPEED
SPACE FORWARD (TAPE BLANK MONC CAN BE ADJUSTED). OR AFTER READ OR
WRITE A FILE GAP AND A BLOCK OF DATA FOR HIGH SPEED SPACE FORWARD
(SIGNAL MONC CAN BE CHECKED).

5.2.6.7 BSFGSB

BACK SPACE FILE GAP

START AT 250

THIS ROUTINE CAN BE USED TO ADJUST OR CHECK THE "SIGNAL MONC".

5.2.7 THE FOLLOWING SUBROUTINES ARE USED BY THE ADJUSTMENT
ROUTINES

5.2.7.1 SETBUF

SETUP BLOCK SIZE AND PATTERN

5.2.7.2 WRTBLK

WRITES A BLOCK OF DATA

5.2.7.3 ROBLK

READS A BLOCK OF DATA

5.2.7.4 NXTDRV

CHANGE DRIVE

6. ERRORS

THERE ARE A NUMBER OF ERRORS THAT CAN OCCUR IN THIS PROGRAM. WHEN AN ERROR IS ENCOUNTERED THE CALL TO THE ERROR ROUTINE IS MADE AND IF SW<13> IS NOT SET AN ERROR MESSAGE PERTAINING TO THE ERROR WILL BE TYPED. EACH ERROR TYPE OUT WILL CONTAIN THE FOLLOWING:

1. AN ERROR MESSAGE
2. A DATA HEADER
3. A DATA STRING

REFER TO THE LISTING UNDER SEARTEB FOR THE DIFFERENT ERRORS THAT CAN OCCUR.

7. RESTRICTIONS

BEFORE STARTING THE PROGRAM THE OPERATOR MUST INSURE THAT A CASSETTE IS LOADED IN THE DRIVE(S) TO BE TESTED AND IS WRITE ENABLED.

8. MISCELLANEOUS

8.1 EXECUTION TIME

THE FIRST PASS TAKES APPROXIMATELY 100 SECONDS ALL SUBSEQUENT PASSES TAKE APPROXIMATELY 475 SECONDS.

8.2 STACK POINTER

STACK IS INITIALLY SET TO 1100.

8.3 PASS COUNT

A PROGRAM PASS THRU COUNT IS KEPT IN "\$PASS".

8.4 ITERATIONS

THE FIRST PASS OF THE PROGRAM WILL AUTOMATICALLY INHIBIT ITERATIONS. ALL SUBSEQUENT PASSES WILL PERFORM FULL (2000 DECIMAL UNLESS OTHERWISE SPECIFIED WITHIN A TEST), ITERATIONS.

8.5 SPECIAL REGISTERS

R3, R4 AND R5 ARE RESERVED FOR "DRIVE", "TACS" AND "TADS THROUGH OUT THE PROGRAM.

3. PROGRAM DESCRIPTION

THIS PROGRAM IS A SEQUENCE OF SMALL INDEPENDENT TESTS THAT CHECK THE TALI FOR PROPER OPERATION.

THE TESTS CAN BE GROUPED INTO THE FOLLOWING GENERAL GROUPS.

1. TEST THE TIMING ERROR CIRCUITRY
2. TEST THE INTERRUPT CIRCUITRY
3. TEST THE SPACING FUNCTIONS
4. TEST THE FILE GAP CIRCUITRY
5. TEST THAT DATA CAN BE WRITTEN AND READ
6. TEST CRC CIRCUITRY
7. INSURE THAT DATA CAN BE WRITTEN AND READ WHILE THE OTHER DRIVE IS REWINDING

MACY11 BASIC LOGIC TEST (PART 2) MAINDEC-11-DTAB-C

SCOPE HANDLER ROUTINE
 ERROR HANDLER ROUTINE
 ERROR TIMEOUT ROUTINE
 ROUTINE TO WAIT ON THE READY BIT TO SET
 ROUTINE TO WAIT ON TRANSFER REQUEST
 ROUTINE TO ASK THE OPERATOR WHAT DRIVE(S) TO TEST
 ROUTINE TO INPUT CSA, DBR, AND VECTOR ADDRESS AND PRIORITY
 ROUTINE TO CALCULATE THE CRC
 ***** MANUAL ADJUSTMENT ROUTINES *****
 WRITE FILE GAPS FROM "BOT" TO "EOT"
 WRITE CONTINUOUS BLOCKS OF DATA
 READ CONTINUOUS BLOCKS OF DATA
 WRITE A FILE GAP AND A BLOCK OF DATA FROM BOT TO ECT
 READ A BLOCK OF DATA AND A FILE GAP
 SPACE FORWARD FILE GAP FROM "BOT" TO "EOT"
 BACK SPACE FILE GAP
 SETUP BLOCK SIZE AND PATTERN FOR SUBROUTINES
 WRITE ROUTINE FOR THE MANUAL OPERATIONS
 READ ROUTINE FOR THE MANUAL OPERATIONS
 ROUTINE TO CHANGE DRIVES
 ROUTINE TO EXAMINE DRIVE(S) FOR AVAILABILITY
 TYPE ROUTINE
 READ AN OCTAL NUMBER FROM THE TTY
 TTY INPUT ROUTINE
 BINARY TO OCTAL (ASCII) AND TYPE
 TRAP DECODER
 TRAP TABLE
 POWER DOWN AND UP ROUTINES

TITLE TH11 BASIC LOGIC TEST (PART 2) MAINDEC-11-DZTAB-C
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 *MAYNARD, MASS. 01754
 *PROGRAM BY JAMES LACEY
 *THIS PROGRAM WAS ASSEMBLED USING THE PDP-11 MAINDEC SYSMAC
 *PACKAGE (MAINDEC-11-DZQAC-C1), MAR 24, 1976.

 .REM!

GENERAL INFORMATION ABOUT THE TA11/TU6C CASSETTE

ADDRESS MNEMONIC DESCRIPTION

777500 TACS CONTROL AND STATUS REGISTER
 777502 TADB DATA BUFFER REGISTER
 260 TAVEC INTERRUPT VECTOR

TACS REGISTER DESCRIPTION

BIT	NAME	INIT STATE	READ AND/OR WRITE
15	ERROR	0	READ ONLY
14	BLOCK CHECK ERROR	0	READ ONLY
13	CLEAR LEADER	0	READ ONLY
12	WRITE LOCK	0	READ ONLY
11	FILE GAP	0	READ ONLY
10	TIMING ERROR	0	READ ONLY
09	OFF LINE	0	READ ONLY
08	UNIT SELECT	0	READ/WRITE
07	TRANSFER REQUEST	0	READ ONLY
06	INTERRUPT ENABLE	0	READ/WRITE
05	READY	0	READ ONLY
04	ILBS	0	READ/WRITE
03	FUNCTION BIT 02	0	READ/WRITE
02	FUNCTION BIT 01	0	READ/WRITE
01	FUNCTION BIT 00	0	READ/WRITE
	0=WRITE-FILE-GAP		
	1=WRITE		
	2=READ		
	3=BACK SPACE FILE GAP		
	4=BACK SPACE BLOCK GAP		
	5=SPACE FORWARD FILE GAP		
	6=SPACE FORWARD BLOCK GAP		
	7=REWIND		
00	GO BIT	0	WRITE ONLY!


```

*****
.SBTTL OPERATIONAL SWITCH SETTINGS
*
* SWITCH USE
* -----
* 15 HALT ON ERROR
* 14 LOOP ON TEST
* 13 INHIBIT ERROR TIMEOUTS
* 12 INHIBIT ITERATIONS
* 11 BELL ON ERROR
* 10 LOOP ON ERROR
* 9 LOOP ON TEST IN SWR(7:0)
* 7 LOCK ON CURRENT DRIVE (ONLY VALID WITH MANUAL LOOPING.)
*****

```

.SBTTL BASIC DEFINITIONS

;*INITIAL ADDRESS OF THE STACK POINTER *** 1100 ***

001100

```

STACK= 1100
.EQUIV EMT.ERROR ;;BASIC DEFINITION OF ERROR CALL
.EQUIV IOT,SCOPE ;;BASIC DEFINITION OF SCOPE CALL

```

;*MISCELLANEOUS DEFINITIONS

000011
000012
000015
000200
177776

```

HT= 11 ;;CODE FOR HORIZONTAL TAB
LF= 12 ;;CODE FOR LINE FEED
CR= 15 ;;CODE FOR CARRIAGE RETURN
CRLF= 200 ;;CODE FOR CARRIAGE RETURN-LINE FEED
PS= 177776 ;;PROCESSOR STATUS WORD
.EQUIV PS,PSW

```

177774
177772
177570
177570

```

STKLMT= 177774 ;;STACK LIMIT REGISTER
FIQ= 177772 ;;PROGRAM INTERRUPT REQUEST REGISTER
DSWR= 177570 ;;HARDWARE SWITCH REGISTER
DISP= 177570 ;;HARDWARE DISPLAY REGISTER

```

;*GENERAL PURPOSE REGISTER DEFINITIONS

000000
000001
000002
000003
000004
000005
000006
000007

```

R0= %0 ;;GENERAL REGISTER
R1= %1 ;;GENERAL REGISTER
R2= %2 ;;GENERAL REGISTER
R3= %3 ;;GENERAL REGISTER
R4= %4 ;;GENERAL REGISTER
R5= %5 ;;GENERAL REGISTER
R6= %6 ;;GENERAL REGISTER
R7= %7 ;;GENERAL REGISTER
.EQUIV R6,SP ;;STACK POINTER
.EQUIV R7,PC ;;PROGRAM COUNTER

```

;*PRIORITY LEVEL DEFINITIONS

000000
000040
000100
000140
000200
000240
000300
000340

```

PR0= 0 ;;PRIORITY LEVEL 0
PR1= 40 ;;PRIORITY LEVEL 1
PR2= 100 ;;PRIORITY LEVEL 2
PR3= 140 ;;PRIORITY LEVEL 3
PR4= 200 ;;PRIORITY LEVEL 4
PR5= 240 ;;PRIORITY LEVEL 5
PR6= 300 ;;PRIORITY LEVEL 6
PR7= 340 ;;PRIORITY LEVEL 7

```

;* "SWITCH REGISTER" SWITCH DEFINITIONS

000000
000001
000002
000003
000004
000005
000006
000007
000008
000009
000010
000011
000012
000013
000014
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000099
000100

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TAIL FUNCTIONS

```

000000 WFG= 0 ;WRITE FILE GAP FUNCTION
000001 WRITE= 2 ;WRITE FUNCTION
000002 READ= 4 ;READ FUNCTION
000003 BSFG= 6 ;BACK SPACE FILE GAP FUNCTION
000004 BSBG= 10 ;BACK SPACE BLOCK GAP FUNCTION
000005 STFG= 12 ;SPACE FWD FILE GAP FUNCTION
000006 STBG= 14 ;SPACE FWD BLOCK GAP FUNCTION
000007 REWIND= 16 ;REWIND FUNCTION
000008
:;*****
    
```

TAIL BIT ASSIGNMENT

```

000000 ERROR= BIT15
000001 CRCERR= BIT14
000002 LEADER= BIT13
000003 WRTLOCK= BIT12
000004 FGAP= BIT11
000005 TIMERR= BIT10
000006 OFFLINE= BIT09
000007 UNIT= BIT08
000008 TR. REQ= BIT07
000009 INT. EN= BIT06
000010 READY= BIT05
000011 ILBS= BIT04
000012 FUNC2= BIT03
000013 FUNC1= BIT02
000014 FUNC0= BIT01
000015 GO= BIT00
    
```

FUNCTION= FUNC2+FUNC1+FUNC0

SPECIAL REGISTERS

```

000000 DRIVE= %3 ;R3 CONTAINS THE DRIVE UNDER TEST
000001 TACS= %4 ;R4 IS USED AS A POINTER TO THE TACS REGISTER
000002 TACB= %5 ;R5 IS USED AS A POINTER TO THE TADB REGISTER.
    
```

```

:;*****
    
```

MAIN: DEFE: LOGIC TEST PART 2) MAINDEC-11-DATAB-0 DATAB-0.YE: TAIL DEFINITIONS

.SBTTL TRAP CATCHER

000000

.*=0
:*ALL UNUSED LOCATIONS FROM 4 - 776 CONTAIN A ".+2,HALT"
:*SEQUENCE TO CATCH ILLEGAL TRAPS AND INTERRUPTS
:*LOCATION 0 CONTAINS 3 TO CATCH IMPROPERLY LOADED VECTORS

000174

.*=174
DISPREG: .WORD 0 ;;SOFTWARE DISPLAY REGISTER
SWREG: .WORD 0 ;;SOFTWARE SWITCH REGISTER

000174
000176

000000
000000

.SBTTL STARTING ADDRESS(ES)

;;JLMP TO STARTING ADDRESS OF PROGRAM
;;SELECT DRIVE(S) BEFORE STARTING TEST
;;SELECT DRIVE(S) AND ADDRESSES BEFORE TESTING
;;SETUP FOR MANUAL LOOPING
;;WRITE FILE GAP FROM BOT TO EOT
;;WRITE CONTINUOUS BLOCKS OF DATA
;;READ CONTINUOUS BLOCKS OF DATA
;;WRITE FILE GAP AND A BLOCK OF DATA
;;READ BLOCK OF DATA AND INTO A FILE GAP
;;SPACE FWD FILE GAP FROM BOT TO EOT
;;BACK SPACE FILE GAPS

000200 000137 001336
000204 000137 001370
000210 000137 001376
000214 000137 001404
000220 000137 013776
000224 000137 014062
000230 000137 014150
000234 000137 014230
000240 000137 014332
000244 000137 014426
000250 000137 014512

JMP @#BEGIN1
JMP @#BEGIN2
JMP @#BEGIN3
JMP @#BEGIN4
JMP @#WFGSUB
JMP @#WRTSUB
JMP @#RDSUB
JMP @#WGPBLK
JMP @#RGPBLK
JMP @#SFFGUB
JMP @#SFGUB

MAINDEC-11-D2TAB-C
TOGGLE IN ROUTINES

: //
: //

: THE FOLLOWING ROUTINES CAN BE TOGGLED IN.

: //
: //

.REM :

THE FOLLOWING ROUTINES (LOOP1, LOOP2, & LOOP3) CAN BE TOGGLED
IN WHEN IT IS IMPOSSIBLE TO LOAD THE DIAGNOSTICS

NOTE: BEFORE USING THESE ROUTINES INSURE THAT R3, R4, & R5
ARE SETUP PROPERLY.

** NOTE: IF USING SOFTWARE SWITCH REGISTER
LOCATION SWR (=1140) MUST CONTAIN
ADDRESS "SWREG" (=176).
*** PUT VALUE INTO 176 ***
** REGISTERS 3, 4, & 5 MUST BE SETUP**
** VIA MOVE INSTRUCTIONS **

R3= 0 IF USING DRIVE A
400 IF USING DRIVE B

R4= TA11 STATUS REG ADDRESS (TACS 177503)

R5= TA11 DATA BUFFER ADDRESS (TADB 177502)

LOOP1 WILL LOAD THE SWITCH REGISTER INTO THE TACS.

LOOP2 WILL WRITE THE CONTENTS OF THE SWITCH REGISTER
ALL THE WAY TO END-OF-TAPE(EOT).

LOOP3 WILL READ TO EOT. DATA WILL GO TO R0.

NOTE: LOOP2 AND LOOP3 WILL REWIND WHEN EOT IS REACHED AND
THEN START OVER.

:
: ; //
: ; //

: LOAD SWITCH REGISTER INTO THE TACS

: ; //
: ; //

. =500

000500 017714 000434
000504 000775

LOOP1: MOV @SWR, @TACS :LOAD TACS
BR LOOP1 :LOOP

000500 017714 000434
000504 000775
000508 017714 000434
000512 000775
000516 000000 000000
000520 000000 000000
000524 000000 000000
000528 000000 000000
000532 000000 000000
000536 000000 000000
000540 000000 000000
000544 000000 000000
000548 000000 000000
000552 000000 000000
000556 000000 000000
000560 000000 000000
000564 000000 000000
000568 000000 000000
000572 000000 000000
000576 000000 000000
000580 000000 000000
000584 000000 000000
000588 000000 000000
000592 000000 000000
000596 000000 000000
000600 000000 000000
000604 000000 000000
000608 000000 000000
000612 000000 000000
000616 000000 000000
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000624 000000 000000
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000632 000000 000000
000636 000000 000000
000640 000000 000000
000644 000000 000000
000648 000000 000000
000652 000000 000000
000656 000000 000000
000660 000000 000000
000664 000000 000000
000668 000000 000000
000672 000000 000000
000676 000000 000000
000680 000000 000000
000684 000000 000000
000688 000000 000000
000692 000000 000000
000696 000000 000000
000700 000000 000000
000704 000000 000000
000708 000000 000000
000712 000000 000000
000716 000000 000000
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000732 000000 000000
000736 000000 000000
000740 000000 000000
000744 000000 000000
000748 000000 000000
000752 000000 000000
000756 000000 000000
000760 000000 000000
000764 000000 000000
000768 000000 000000
000772 000000 000000
000776 000000 000000
000780 000000 000000
000784 000000 000000
000788 000000 000000
000792 000000 000000
000796 000000 000000
000800 000000 000000
000804 000000 000000
000808 000000 000000
000812 000000 000000
000816 000000 000000
000820 000000 000000
000824 000000 000000
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000832 000000 000000
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000852 000000 000000
000856 000000 000000
000860 000000 000000
000864 000000 000000
000868 000000 000000
000872 000000 000000
000876 000000 000000
000880 000000 000000
000884 000000 000000
000888 000000 000000
000892 000000 000000
000896 000000 000000
000900 000000 000000
000904 000000 000000
000908 000000 000000
000912 000000 000000
000916 000000 000000
000920 000000 000000
000924 000000 000000
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000932 000000 000000
000936 000000 000000
000940 000000 000000
000944 000000 000000
000948 000000 000000
000952 000000 000000
000956 000000 000000
000960 000000 000000
000964 000000 000000
000968 000000 000000
000972 000000 000000
000976 000000 000000
000980 000000 000000
000984 000000 000000
000988 000000 000000
000992 000000 000000
000996 000000 000000
001000 000000 000000

::*****

:WRITE SWITCH REGISTER ON TAPE FROM BOT TO EOT

::*****

. =600

```

LOOP2: RESET ;CLEAR ALL FLAGS
        MOV  DRIVE,@TACS ;SELECT DRIVE
        MOV  #REWIND!GO,@TACS ;GO TO BOT
        BIT  #READY,@TACS ;WAIT TILL READY COMES UP
1$:     BEQ  1$
        MOV  #WRITE!GO,@TACS ;START A WRITE
2$:     TSTB @TACS ;CHECK FOR TRANSFER REQUEST
        BPL  3$ ;BR IF NOT SET
        MOV  @SWR,@TABD ;SEND DATA TO TAPI
        BR   2$ ;LOOP
3$:     BIT  #READY,@TACS ;DID READY SET?
        BNE  LOOP2 ;START OVER IF YES
        BR   2$ ;LOOP

```

::*****

:READ FROM BOT TO EOT

::*****

. =700

```

LOOP3: RESET ;CLEAR ALL FLAGS
        MOV  DRIVE,@TACS ;SELECT DRIVE
        MOV  #REWIND!GO,@TACS ;START A REWIND
1$:     BIT  #READY,@TACS ;WAIT ON REWIND TO FINISH
        BEQ  1$
        MOV  #READ!GO,@TACS ;START A READ
2$:     TSTB @TACS ;CHECK TRANSFER REQ
        BPL  3$ ;BR IF NOT SET
        MOV  @TABD,@R0 ;PICKUP THE DATA
        BR   2$ ;LOOP
3$:     BIT  #READY,@TACS ;CHECK READY
        BNE  LOOP3 ;START OVER
        BR   2$ ;LOOP

```

```

000600 000005
000602 010314
000604 112714 000017
000610 032714 000040
000614 001775
000616 112714 000003
000622 105714
000624 100003
000626 017715 000306
000632 000773
000634 032714 000040
000640 001357
000642 000767

000700 000005
000702 010314
000704 112714 000017
000710 032714 000040
000714 001775
000716 112714 000003
000722 105714
000724 100002
000726 011500
000730 000774
000732 032714 000040
000736 001350
000742 000770

```

.SBTTL COMMON TAGS

 *THIS TABLE CONTAINS VARIOUS COMMON STORAGE LOCATIONS
 *USED IN THE PROGRAM.

Address	Value	Label	Format	Description
352	001100	001100		START OF COMMON TAGS
353	001100	000000		CONTAINS PASS COUNT
354	001102	000		CONTAINS THE TEST NUMBER
355	001103	000		CONTAINS ERROR FLAG
356	001104	000000		CONTAINS SUBTEST ITERATION COUNT
357	001106	000000		CONTAINS SCOPE LOOP ADDRESS
358	001110	000000		CONTAINS SCOPE RETURN FOR ERRORS
359	001112	000000		CONTAINS TOTAL ERRORS DETECTED
360	001114	000		CONTAINS ITEM CONTROL BYTE
361	001115	001		CONTAINS MAX. ERRORS PER TEST
362	001116	000000		CONTAINS PC OF LAST ERROR INSTRUCTION
363	001120	000000		CONTAINS ADDRESS OF 'GOOD' DATA
364	001122	000000		CONTAINS ADDRESS OF 'BAD' DATA
365	001124	000000		CONTAINS 'GOOD' DATA
366	001126	000000		CONTAINS 'BAD' DATA
367	001130	000000		RESERVED--NOT TO BE USED
368	001132	000000		
369	001134	000		AUTOMATIC MODE INDICATOR
370	001135	000		INTERRUPT MODE INDICATOR
371	001136	000000		
372	001140	177570		ADDRESS OF SWITCH REGISTER
373	001142	177570		ADDRESS OF DISPLAY REGISTER
374	001144	177560		TTY KBD STATUS
375	001146	177562		TTY KBD BUFFER
376	001150	177564		TTY PRINTER STATUS REG. ADDRESS
377	001152	177566		TTY PRINTER BUFFER REG. ADDRESS
378	001154	000		CONTAINS NULL CHARACTER FOR FILLS
379	001155	002		CONTAINS # OF FILLER CHARACTERS REQUIRED
380	001156	012		INSERT FILL CHARS. AFTER A "LINE FEED"
381	001157	000		"TERMINAL AVAILABLE" FLAG (BIT<07>=0=YES)
382	001160	000000		CONTAINS THE ADDRESS FROM WHICH (\$REG0) WAS OBTAINED
383	001162	000000		CONTAINS ((\$REGAD)+0)
384	001164	000000		CONTAINS ((\$REGAD)+2)
385	001166	000000		MAX. NUMBER OF ITERATIONS
386	001170	000000		ESCAPE ON ERROR ADDRESS
387	001172	177607	000377	CODE FOR BELL
388	001176	077		QUESTION MARK
389	001177	015		CARRIAGE RETURN
390	001200	000012		LINE FEED
391	001202	000000		STORAGE FOR THE PC
392	001204	000000		STORAGE FOR THE PS
393	001206	177500		LOW BYTE ADDRESS OF TACS
394	001210	177501		HIGH BYTE ADDRESS OF TACS
395	001212	177502		LOW BYTE ADDRESS OF TADB
396	001214	177503		HIGH BYTE ADDRESS OF TADB
397	001216	000260	000262	TAIL VECTOR ADDRESS

L02

TAB1 BASIC LOGIC TEST PART 2) MAINDEC-11-DZTAB-C
DZTAB.C.NEW COMMON TAGS

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SEC 0024
SEC 0024

393	001222	000300	
394	001224	000000	000000
395	001230	001224	
396	001232	000000	
397	001234	000000	

TAPRIO: 300
 DRVKEY: 0 0
 DRVPNT: DRVKEY
 ASKKEY: 0
 CURDRV: 0

:TAB1 BR LEVEL 6
 :DRIVE SELEC. KEY:

:CURRENT DRIVE BEING TESTED

.SBTTL ERROR POINTER TABLE

::*THIS TABLE CONTAINS THE INFORMATION FOR EACH ERROR THAT CAN OCCUR.
::*THE INFORMATION IS OBTAINED BY USING THE INDEX NUMBER FOUND IN
::*LOCATION \$ITEMB. THIS NUMBER INDICATES WHICH ITEM IN THE TABLE IS PERTINENT.
::*NOTE1: IF \$ITEMB IS 0 THE ONLY PERTINENT DATA IS (\$ERRPC).
::*NOTE2: EACH ITEM IN THE TABLE CONTAINS 4 POINTERS EXPLAINED AS FOLLOWS:

::* EM ::POINTS TO THE ERROR MESSAGE
::* DH ::POINTS TO THE DATA HEADER
::* JT ::POINTS TO THE DATA
::* DF ::POINTS TO THE DATA FORMAT

001235

\$ERRTB:

:NOTE: ALL NUMBERS ARE TYPED AS 6-DIGIT OCTAL NUMBERS

:ITEM

1
EM1 :STATUS PROBLEM
DH1 :PC TACS
DT1 :\$ERRPC \$REGO
0

001236 017066
001240 017256
001242 017420
001244 000000

:ITEM

2
EM2 :READY FAILED TO SET
DH2 :PC TACS WAIT ADDRESS
DT2 :\$ERRPC \$REGO SAVPC
0

001246 017105
001250 017273
001252 017426
001254 000000

:ITEM

3
EM3 :TRANSFER REQUEST FAILED TO SET
DH2 :PC TACS WAIT ADDRESS
DT2 :\$ERRPC \$REGO SAVPC
0

001256 017133
001260 017273
001262 017426
001264 000000

:ITEM

4
EM4 :THE WRONG FLAG SET
DH2 :PC TACS WAIT ADDRESS
DT2 :\$ERRPC \$REGO SAVPC
0

001266 017174
001270 017273
001272 017426
001274 000000

:ITEM

5
EM5 :DATA PROBLEM
DH5 :PC TACS EXPECT RCV'D
DT5 :\$ERRPC \$REGO \$GDDAT \$BDDAT
0

001276 017217
001300 017330
001302 017436
001304 000000

:ITEM

6
EM6 :INTERRUPT PROBLEM
DH6 :PC TACS BR LEVEL
DT6 :\$ERRPC \$REGO \$GDDAT
0

001306 017234
001310 017366
001312 017450
001314 000000

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TA11 BASIC LOGIC TEST (PART 2) MAINDEC-11-DZTAB-C
DZTAB.C.NEW ERROR POINTER TABLE

453	001316	
454		
455	001316	017472
456	001320	017544
457	001322	017460
458	001324	000000
459		
460	001326	017521
461	001330	017561
462	001332	017460
463	001334	000000
464		

ITEMS2:

; ITEMS 201-202

EM201	: TA11 FAILED TO RESPOND
CH201	: PC TACS
DT201	: SERRPC TACS
0	: BOTH NUMBERS ARE TYPED AS OCTAL NUMBERS
EM202	: NO DRIVES AVAILABLE
CH202	: PC
DT202	: SERRPC
0	:

:*****

:BEGIN1 IS FOR NORMAL START
:BEGIN2 IS FOR DRIVE SELECTION
:BEGIN3 IS FOR DRIVE & ADDRESS SELECTION
:BEGIN4 IS FOR MANUAL OPERATION

:*****

001336 005005
001340 012737 041101 001224
001346 :22737 000005 000041
001354 001015
001356 022737 000260 001216
001364 001011
001366 000403
001370 012705 000001
001374 000405
001376 012705 000002
001402 000402
001404 012705 000003
001410

BEGIN1: CLR R5 ;NORMAL START
MOV #AB, @DRVKEY
CMPB #5, @41 ;CASSETTE DDP?
BNE BGNCMN ;GO BEGIN COMMON CODE IF NO
CMP #260, @TAVEC ;STANDARD VECTOR?
BNE BGNCMN ;GO BEGIN COMMON CODE IF NO
ER BEGIN3 ;GET DRIVES AND ADDRESSES
BEGIN2: MOV #1, R5 ;ASK FOR DRIVES FLAG
BR BGNCMN ;BEGIN COMMON CODE
BEGIN3: MOV #2, R5 ;ASK FOR DRIVES AND ADDRESSES
BR BGNCMN
BEGIN4: MOV #3, R5
BGNCMN:

001410 012706 001100
001414 005026
001416 022706 001140
001422 001374
001424 012706 001100
001430 012737 012264 000020
001436 012737 000340 000022
001444 012737 012536 000030
001452 012737 000340 000032
001460 012737 016476 000034
001466 012737 000340 000036
001474 012737 016562 000024
001502 012737 000340 000026
001510 016767 010476 01046E
001516 005067 177444
001522 005067 177442
001526 112767 000001 177361
001534 012767 001534 177344
001542 012767 001542 177340

:SBTTL INITIALIZE THE COMMON TAGS
::CLEAR THE COMMON TAGS (%CMTAG) AREA
MOV #%CMTAG, R6 ;FIRST LOCATION TO BE CLEARED
CLR (R6)+ ;CLEAR MEMORY LOCATION
CMP #SWR, R6 ;:DONE?
BNE -6 ;:LOOP BACK IF NO
MOV #STACK, SP ;:SETUP THE STACK POINTER
::INITIALIZE A FEW VECTORS
MOV #SCOPE, @IOTVEC ;:IOT VECTOR FOR SCOPE ROUTINE
MOV #340, @IOTVEC+2 ;:LEVEL 7
MOV #ERROR, @EMTVEC ;:EMT VECTOR FOR ERROR ROUTINE
MOV #340, @EMTVEC+2 ;:LEVEL 7
MOV #TRAP, @TRAPVEC ;:TRAP VECTOR FOR TRAP CALLS
MOV #340, @TRAPVEC+2 ;:LEVEL 7
MOV #SPWRDN, @PWRVEC ;:POWER FAILURE VECTOR
MOV #340, @PWRVEC+2 ;:LEVEL 7
MOV #ENDCT, @EOPCT ;:SETUP END-OF-PROGRAM COUNTER
CLR \$TIMES ;:INITIALIZE NUMBER OF ITERATIONS
CLR \$ESCAPE ;:CLEAR THE ESCAPE ON ERROR ADDRESS
MOVB #1, \$ERMAX ;:ALLOW ONE ERROR PER TEST
MOV #, \$LPADR ;:INITIALIZE THE LOOP ADDRESS FOR SCOPE
MOV #, \$LPERR ;:SETUP THE ERROR LOOP ADDRESS
::SIZE FOR A HARDWARE SWITCH REGISTER. IF NOT FOUND OR IT IS
::EQUAL TO A "-1" SETUP FOR A SOFTWARE SWITCH REGISTER.

001550 013746 000004
001554 012737 001610 000004
001562 012767 177570 177350
001570 012767 177570 177344
001576 022777 177777 177334
001604 001012
001606 000403

MOV @ERRVEC, -(SP) ;:SAVE ERROR VECTOR
MOV #645, @ERRVEC ;:SET UP ERROR VECTOR
MOV #DSWR, SWR ;:SETUP FOR A HARDWARE SWICH REGISTER
MOV #DDISP, DISPLAY ;:AND A HARDWARE DISPLAY REGISTER
CMP #-1, @SWR ;:TRY TO REFERENCE HARDWARE SWR
BNE 66\$;:BRANCH IF NO TIMEOUT TRAP OCCURRED
AND THE HARDWARE SWR IS NOT = -1
BR 65\$;:BRANCH IF NO TIMEOUT

LOGIC TEST (PART 2) MAINDEC-11-DZTAB-C
INITIALIZE THE COMMON TAGS

```

001610 012716 001616 645: MOV #655,(SP) ;;SET UP FOR TRAP RETURN
001614 000000 RTI
001616 012716 000176 177314 655: MOV #SWREG,SWR ;;POINT TO SOFTWARE SWP
001624 012716 000174 177310 655: MOV #DISPREG,DISPLAY
001632 012637 000004 655: MOV (SP)+,2#ERRVEC ;;RESTORE ERROR VECTOR

.SBTTL TYPE PROGRAM NAME
;;TYPE THE NAME OF THE PROGRAM IF FIRST PASS
001636 005227 177777 INC #1 ;;FIRST TIME?
001642 001036 BNE HERE ;;BRANCH IF NO
001644 022737 012232 000042 CMP #SENDAD,2#42 ;;ACT-11?
001652 001432 BEQ HERE ;;BRANCH IF YES
001654 104401 001712 TYPE MSGID ;;TYPE ASCIZ STRING

.SBTTL GET VALUE FOR SOFTWARE SWITCH REGISTER
001660 005737 000042 TST 2#42 ;;ARE WE RUNNING UNDER XYDP/ACT?
001664 001006 BNE 675 ;;BRANCH IF YES
001666 026727 177246 000176 CMP SWR,#SWREG ;;SOFTWARE SWITCH REG SELECTED?
001674 001005 BNE 685 ;;BRANCH IF NO
001676 104405 STSWR ;;GET SOFT-SWR SETTINGS
001700 000400 BR 685
001702 112767 000001 177224 675: MOVB #1,$AUTOB ;;SET AUTO-MODE INDICATOR
001710 685: BR HERE ;;GET OVER THE ASCIZ
001740 000413 .:MSGID: .ASCIZ <CRLF>/MAINDEC-11-DZTAB-C/<CRLF>
HERE:
*****
*****
;THE CONTENTS OF R5 DETERMINES WHAT WILL BE DONE
;
; R5=3 MANUAL OPERATIONS
; R5=2 ASK FOR DRIVE(S) AND ADDRESSES (TACS AND VECTOR)
; R5=1 ASK FOR DRIVE(S)
; R5=0 DON'T ASK FOR ANYTHING
*****
001740 010504 BEGINX: MOV R5,R4 ;;COPY R5
001742 005305 DEC R5 ;;ASK FOR DRIVES?
001744 002406 BLT CHKADR ;;BR IF NO
001746 004737 013254 JSR PC,2#ASKDRV ;;GO GET DRIVES TO BE TESTED
001752 005305 DEC R5 ;;ASK FOR ADDRESSES?
001754 002406 BLT CHKADR ;;BR IF NO
001756 004737 013354 JSR PC,2#ASKADR ;;GO GET TAIL ADDRESSES
*****
*****
;CHECK THAT "TACS" WILL RESPOND TO ADDRESSING
;
; I. TIMEOUT OCCURRED
; A. TYPE ERROR MESSAGE
; B. EXAMINE R4
; 1. R4>0 GOTO BEGINX
; 2. R4=0 EXAMINE (42)
; A. (42)=0 GOTO BEGINX
; 3. (42)>0 GOTO SENDAD

```


MAINDEC-11-DZTAB-C
SOFTWARE SWITCH REGISTER

MAKE SURE THE DRIVES IN THE DRIVE TABLE CAN BE TESTED

- I. DESIRED DRIVES CAN NOT BE TESTED
 - A. TYPE ERROR MESSAGE
 - B. EXAMINE R4
 - 1. R4=0 GOTO BEGINX
 - 2. R4=0 EXAMINE (42)
 - A. (42)=0 GOTO BEGINX
 - B. (42)>0 GOTO SENDAD
- II. BOTH DRIVES IN THE TABLE BUT ONLY ONE OF THEM CAN BE TESTED
 - A. CLEAR BAD DRIVE FROM THE DRIVE TABLE
 - B. CONTINUE IN PROGRAM
- III. DESIRED DRIVE(S) CAN BE TESTED
 - A. CONTINUE IN PROGRAM

```

*****
CHKDRV: MOV      #DRVKEY,R0      ; PICKUP ADDRESS OF ASCII DRIVE KEY
        JSR      PC,2#EXAM      ; GO EXAMINE FIRST DRIVE
        BR       1$            ; OK TO TEST---GO CHECK NEXT
        MOVB     1(R0),(R0)     ; REPLACE 1ST WITH 2ND
        BEQ      2$            ; BR IF NO 2ND DRIVE SELECTED
        JSR      PC,2#EXAM      ; GO EXAMINE DRIVE
        BR       2$            ; OK TO TEST
        CLR      (R0)          ; CLEAR DRIVE CODES
        BR       2$
1$: INC     R0                  ; POINT TO 2ND
        JSR      PC,2#EXAM      ; GO EXAMINE DRIVE
        BR       2$            ; OK TO TEST
        CLRB     (R0)          ; CLEAR 2ND
        BR       2$            ; RESET ADDRESS POINTERS
2$: MOV      #DRVKEY,R0
        MOV      R0,2#DRVPNT
        CMPB     (R0),1(R0)     ; 1ST = 2ND?
        BNE     3$            ; BR IF NO
        CLRB     1(R0)         ; YES---CLEAR 2ND
        BR       3$            ; ANY DRIVES?
3$: TST      (R0)              ; BR IF NO
        BEQ      5$
        BR      MANUAL
5$: ERROR    202                ; NO DRIVES AVAILABLE
        MOV      #2,R5          ; DRIVES 3 ADDRESS
        TST      R4            ; OPERATOR INPUTS?
        BNE     BEGINX         ; BR IF YES
        MOV      2#42,R0       ; GET MONITOR RETURN ADDRESS
        BEQ      BEGINX        ; NO MONITOR
        JMP      2#SENDAD       ; GO TO END
MANUAL: CMP     R4,#3
        BNE     OK
        MOV      -1,R4
        MOV      R4,2#ASKKEY
        BR      START
PWRST: TYPE    MSGIO           ; POWER FAIL RESTART
        MOV      #DRVKEY,2#DRVPNT

```

002046	012730	001224	
002052	004737	015046	
002056	000410		
002060	116010	000001	
002064	001412		
002066	004737	015046	
002072	000407		
002074	005010		
002076	000405		
002100	005200		
002102	004737	015046	
002106	000401		
002110	105010		
002112	012700	001224	
002116	010037	001230	
002120	121060	000001	
002126	001002		
002130	105060	000001	
002134	005710		
002136	001401		
002140	000412		
002142	104202		
002144	012705	000002	
002150	005704		
002152	001272		
002154	013700	000042	
002160	001667		
002162	000137	012232	
002166	020427	000003	
002172	001002		
002174	016704	175577	
002200	010437	001232	
002204	000405		
002206	104401	001712	
002212	012737	001224	001230

F03

TAL: BASIC LOGIC TEST (PART 2) MAINDEC-11-DZTAB-C
 DZTABO.NEW GET VALUE FOR SOFTWARE SWITCH REGISTER

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SEQ 0031
 SEC 0031

```

655 002220 013777 001220 176770 START: MOV      @TAVEC+2,@TAVEC      ;SETUP TAIL TRAP VECTOR
656 002226 005077 176766          CLR      @TAVEC+2
657 002232 012737 000340 177776          MOV      @340,@#PS      ;LOCKOUT ALL I/O INT
658 002240 013704 001206          MOV      @#TACSL,TACS   ;SETUP TACS
659 002244 013705 001212          MOV      @#TADBL,TADB   ;SETUP TADB
660 002250 005737 001100          TST     @#SPASS         ;IF FIRST PASS SETUP FOR EXTRA LONG WAIT LOOPS
661 002254 001003          BNE     IS              ;OTHERWISE USE OLD VALUES
662 002256 012737 077777 013150          MOV      @#CBIT15,@#MAXCNT
663 002264 005037 001102          CLR     @#STSTNM       ;ZERO THE TEST NUMBER
664 002270 005003          CLR     DRIVE          ;SET DRIVE TO "A"
665 002272 013701 001230          MOV      @#DRVPNT,R1    ;GET DRIVE POINTER
666 002276 121127 000101          CMPB    (R1),#'A        ;IS IT DRIVE "A"?
667 002302 001402          BEQ     TDRV           ;BR IF YES
668 002304 012703 000400          MOV      @UNIT,DRIVE    ;SET DRIVE TO "B"
669 002310          TDRV:
670 002310 104401 002316          TYPE    655             ;;TYPE ASCIZ STRING
671 002314 000411          BR      645             ;;GET OVER THE ASCIZ
672          ;;655: .ASCIZ <15><12>*TESTING DRIVE *
673          645:
674 002340          MOVVB   (R1)+,CURDRV    ;SETUP TO TYPE CURRENT DRIVE
675 002344 112167 176670          TYPE    .CURDRV
676 002344 104401 001234          TYPE    .SCRFL
677 002350 104401 001177          TYPE    (R1)           ;TYPE A CR & LF
678 002354 105711          TSTB    (R1)           ;LAST DRIVE BEEN SELECTED
679 002356 001002          BNE     IS              ;BR IF NO
680 002360 012701 001224          MOV      @#DRVKEY,R1    ;RESET DRIVE POINTER
681 002364 010137 001230          MOV      R1,@#DRVPNT    ;SAVE DRIVE POINTER FOR NEXT TIME
682 002370 005737 001232          TST     @#ASKKEY        ;GO START TESTING IF NO MANUAL
683 002374 002007          BGE     25              ;OPERATIONS REQUESTED
684 002376 005000          CLR     RD
685 002402 022767 000176 175530          HALT
686 002410 001001          CMP     @SWREG,SWR      ;GIVE CONTROL TO THE OPERATOR
687 002412 104405          BNE     205             ;USING S/W SWITCH REG.?
688 002414          GTSWR                    ;NO- GET OUT
689          ;LET HIM CHANGE IT
690          ;CONTINUE
691 002414 005737 000042          ;THIS CODE IS FOR ACT11 & DDP
692 002420 001406          25:  TST     @#42         ;IS THERE A MONITOR?
693 002422 010314          BEQ     TST1            ;GO START TESTING IF NO
694 002424 112714 000017          MOV      DRIVE,@TACS    ;IF YES SELECT DRIVE
695 002430 032714 000040          MOVVB   @REWIND!GO,@TACS ;SEND TAPE TO BOT
696 002434 001775          BIT     @READY,@TACS    ;WAIT ON READY
697          BEQ     35          ;FALL THRU IF READY=1
  
```


MAINDEC-11-D2TAB-C
SOFTWARE SWITCH REGISTER

002436	000004		
002440	012767	000001	176520
002446	005737	001100	
002452	001021		
002454	000005		
002456	010314		
002460	112714	000017	
002464	104412		
002466	112714	000001	
002472	104412		
002474	163737	013124	013150
002502	005237	013150	
002506	006137	013150	
002512	006137	013150	

////////////////////
////////////////////////////////////
////////////////////////////////////
////////////////////////////////////
////////////////////////////////////

THIS ISN'T A REAL TEST BUT A SMALL ROUTINE TO DETERMINE THE MAX.
TIME FOR THE WAIT LOOPS (WAIT FOR "READY" AND "TRANSFER REQUEST")

*TEST 1 ROUTINE TO DETERMINE TIME OF WAIT LOOPS

```

*ST1:  SCOPE
        MOV     #1,$TIMES           ;;DO 1 ITERATION
        TST     @#$PASS             ;;IS THIS THE FIRST PASS?
        BNE     TST2               ;;BR IF NO
        RESET
        MOV     DRIVE,@TACS         ;;SELECT THE DRIVE
        MOVB    #REWIND!GO,@TACS    ;;START A REWIND
        WAITREADY                   ;;WAIT FOR READY
        MOVB    #WFG!GO,@TACS       ;;WRITE A FILE GAP
        WAITREADY                   ;;WAIT ON READY
        SUB     @#HIGHTIM,@#MAXCNT  ;;GET THE TIME IT TOOK
        INC     @#MAXCNT             ;;MAKE IT BIGGER
        ROL     @#MAXCNT
        ROL     @#MAXCNT
    
```

////////////////////////////////////
////////////////////////////////////
THE FOLLOWING TEST WILL CHECK TIMING ERRORS FOR BOTH "WRITE" AND "READ".
////////////////////////////////////
////////////////////////////////////

*TEST 2 TEST "TIMING ERROR" FOR "WRITE"

002516	000004		
002520	012767	000012	176410
002526	012767	002554	176352
002534	012767	002656	176426
002542	000005		
002544	010314		
002546	112714	000017	
002552	104412		
002554	112714	000003	
002560	104413		
002562	105714		
002564	100401		
002566	104001		
002570	005015		
002572	005000		
002574	005001		
002576	105714		
002600	100405		
002602	062700	000010	
002606	005501		
002610	100372		
002612	104001		
002614	105714		
002616	100005		
002620	162700	000001	
002624	005501		
002626	100372		

```

*ST2:  SCOPE
        MOV     #10,$TIMES          ;;DO 10 ITERATIONS
        MOV     #75,$LPADR          ;;SET SCOPE LOOP ADDRESS
        MOV     #TST3,$ESCAPE      ;;ESCAPE TO TEST 3 ON ERROR
        RESET
        MOV     DRIVE,@TACS         ;;SELECT DRIVE
        MOVB    #REWIND!GO,@TACS    ;;GO TO "CLEAR LEADER"
        WAITREADY                   ;;WAIT ON "READY"
        MOVB    #WRITE!GO,@TACS     ;;WRITE FUNCTION
        WAITXFER                    ;;WAIT ON "XFER REQ"
        TSTB    @TACS               ;;IS "XFER REQ" = 1
        BMI     1$                  ;;BR IF YES
        ERROR   1$                  ;;NO "XFER REQ" OCCURRED
        CLR     @TADB                ;;KNOCK DOWN "XFER REQ"
        ROL     R0                   ;;CLEAR COUNTER
        CLR     R1
        TSTB    @TACS               ;;WAIT FOR "XFER REQ"
        BMI     3$                  ;;KEEP TRACK OF HOW LONG
        ADD     #10,R0              ;;IT TAKES "XFER REQ" TO SET
        ADC     R1
        BPL     2$                  ;;"XFER REQ" FAILED TO SET
        ERROR   1$                  ;;SAMPLE "XFER REQ"
        TSTB    @TACS               ;;IF "XFER REQ" = 0 SOMETHING IS WRONG
        BPL     4$                  ;;DOWN COUNT THE COUNTER
        SUB     #1,R0                ;;SO A "TIMING ERROR" WILL
        SEC     R1                   ;;OCCUR
        BPL     3$
    
```

```

750 002630 104001
751 002632 104001
752 002634 105715
753 002636 104412
754 002640 005714
755 002642 100401
756 002644 104001
757 002646 032714 002000
758 002652 001001
759 002654 104001
760
761
762
763
764
765 002656 000004
766 002660 012767 000012 176000
767 002666 012767 002714 176212
768 002674 012767 003062 176266
769 002702 000005
770 002704 010314
771 002706 112714 000017
772 002712 104412
773 002714 112714 000003
774 002720 012700 000006
775 002724 104413
776 002726 112715 000377
777 002732 104413
778 002734 005300
779 002736 003373
780 002740 052714 000020
781 002744 104412
782 002746 112714 000017
783 002752 104412
784 002754 112714 000005
785 002760 104413
786 002762 105714
787 002764 100401
788 002766 104001
789 002770 105715
790 002772 005000
791 002774 005001
792 002776 105714
793 003000 100405
794 003002 062700 000012
795 003006 005501
796 003010 100372
797 003012 104001
798 003014 105714
799 003016 100005
800 003020 162700 000001
801 003024 005601
802 003026 100372
803 003030 000401
804 003032 104001
805 003034 105715
806 003036 052714 000020
807 003042 104412

```

```

48: BR 5$
ERROR 1
58: TSTB @TADB ;"XFER REQ" SHOULDN'T HAVE CLEARED
WAITREADY ;CLEAR "XFER REQ"
TST @TACS ;WAIT FOR "READY"
BMI 6$ ;IS "ERROR" BIT SET?
ERROR 1 ;BR IF YES
68: BIT #TIMERR,@TACS ;"ERROR" BIT NOT SET
BNE TST3 ;IS "TIMING ERROR" = 1?
ERROR 1 ;:BR IF YES
;"TIMING ERROR" NOT SET
*****
*TEST 3 TEST "TIMING ERROR" FOR "READ"
*****
TST3: SCOPE
MOV #10,@TIMES ;DO 10 ITERATIONS
MOV #7,$LPADR ;SET SCOPE LOOP ADDRESS
MOV #TST4,$ESCAPE ;ESCAPE TO TEST 4 ON ERPCR
RESET
MOV DRIVE,@TACS ;SELECT DRIVE
MOVB #REWIND!GO,@TACS ;GO TO "CLEAR LEADER"
WAITREADY ;WAIT FOR "READY"
78: MOVB #WRITE!GO,@TACS ;WRITE
MOV #6,R0 ;WRITE THIS MANY BYTES ON TAPE
WAITXFER ;WAIT FOR "XFER REQ"
88: MOVB #377,@TADB ;WRITE ONE BYTE
WAITXFER ;WAIT FOR "XFER REQ"
DEC R0 ;LAST BYTE OUT?
BGT 8$ ;BR IF NO
BIS #ILBS,@TACS ;WRITE "CRC"
WAITREADY ;WAIT FOR "READY"
MOVB #REWIND!GO,@TACS ;BACK OVER THE BLOCK
WAITREADY ;WAIT FOR "READY"
MOVB #READ!GO,@TACS ;START A "READ"
WAITXFER ;WAIT FOR "XFER REQ"
TSTB @TACS ;IS "XFER REQ" SET
BMI 1$ ;BR IF YES
ERROR 1 ;"XFER REQ" DIDN'T SET
18: TSTB @TADB ;KNOCK DOWN "XFER REQ"
CLR R0 ;CLEAR COUNTERS
CLR R1
28: TSTB @TACS ;FIND OUT HOW LONG IT
BMI 3$ ;TAKES FOR "XFER REQ"
ADD #10,R0 ;TO SET
ADC R1
BPL 2$
ERROR 1 ;"XFER REQ" FAILED TO SET
38: TSTB @TACS ;NOW WASTE MORE THAN THE ABOVE
BPL 4$ ;AMOUNT OF TIME SO THAT
SUB #1,R0 ;A "TE" WILL OCCUR
SEC R1
BPL 3$
BR 5$
48: ERROR 1 ;"XFER REQ" CLEARED SOME HOW
58: TSTB @TADB ;KNOCK DOWN "XFER REQ"
BIS #ILBS,@TACS ;STOP THE READ
WAITREADY ;WAIT ON "READY"

```

TEST "TIMING ERROR" FOR "READ"

```

00000000 003044 005714      TST      @TACS      ; IS "ERROR" SET?
00000001 003046 100401      BMI      5$         ; BR IF YES
00000002 003050 104001      ERROR   1          ; "ERROR" NOT = 1
00000003 003052 032714 002000 6$: BIT      @TIMERR,@TACS ; IS THERE A "TIMING ERROR"
00000004 003056 001001      BNE     1          ;:GO TO NEXT TEST IF YES
00000005 003060 104001      ERROR   1          ; "TIMING ERROR" NOT SET
;*****
;+TEST 4      TEST "ERROR" OUTPUT OF STATUS ROM USING A "TIMING ERROR"
;*****
;T4:  SCOPE
MOV      @10,@STIMES ;:DO 10. ITERATIONS
MOV      @1$,@$LPADR ;:SET SCOPE LOOP ADDRESS
MOV      @TST5,$$ESCAPE ;:ESCAPE TO TEST 5 ON ERROR
RESET
MOV      @DRIVE,@TACS ;:SELECT DRIVE
MOV      @REWIND!GO,@TACS ;:GO TO BEGINNING OF TAPE
WAITREADY ;:WAIT ON "READY" TO SET
MOV      @WFG!GO,@TACS ;:GET ON THE OXIDE
WAITREADY ;:WAIT ON "READY" TO SET
MOV      @WRITE!GO,@TACS ;:START A WRITE
WAITXFER ;:WAIT FOR "TRANSFER REQ"
MOV      @377,@TADB ;:WRITE ONE BYTE
CLR      @R0 ;:CLEAR THE DOUBLE LENGTH COUNTER
CLR      @R1
TSTB    @TACS ;:CHECK FOR "XFER REQ"
BMI      3$         ;:BR IF SET
ADD      @10.,@R0 ;:COUNT UP UNTIL "XFER REQ"
ADC      @R1 ;:SETS OR OVERFLOW OCCURS
BPL      2$         ;:"XFER REQ" FAILED TO SET
ERROR   1          ;:CHECK "XFER REQ"
TSTB    @TACS ;:BR IF SET
BMI      4$         ;:"XFER REQ" SHOULDN'T HAVE CLEARED
SUB      @1,@R0 ;:COUNT DOWN TO CAUSE A "TE"
SBC      @R1
BPL      3$         ;:CLEAR "XFER REQ"
TSTB    @TADB ;:WAIT FOR READY
WAITREADY ;:MAKE SURE "ERROR" IS SET
TST      @TACS
BMI      5$         ;:"ERROR" DIDN'T SET
ERROR   1          ;:MAKE SURE "TE" IS SET
BIT      @TIMERR,@TACS
BNE      6$         ;:"TE" FAILED TO SET
ERROR   1          ;:CHECK "ERROR" WITH "WFG"
MOV      @WFG,@TACS ;:SAMPLE THE "ERROR" BIT
TST      @TACS ;:BR IF "ERROR" = 0
BPL      7$         ;:"ERROR" NOT = 0
ERROR   1          ;:CHECK "ERROR" WITH "WRITE"
MOV      @WRITE,@TACS ;:SAMPLE THE "ERROR" BIT
TST      @TACS ;:BR IF "ERROR" = 1
BMI      8$         ;:"ERROR" NOT = 1
ERROR   1          ;:CHECK "ERROR" WITH "READ"
MOV      @READ,@TACS ;:SAMPLE THE "ERROR" BIT
TST      @TACS ;:BR IF "ERROR" = 1
BMI      9$         ;:"ERROR" NOT = 1
ERROR   1

```

```

0033260 112714 000005 95: MOV B #BSEFG, @TACS ; CHECK "ERROR" WITH "BSEFG"
0033264 005714 TST @TACS ; SAMPLE THE "ERROR" BIT
0033266 100001 BPL 105 ; BR IF "ERROR" = 0
0033270 104001 ERROR 1 ; "ERROR" NOT = 0
0033272 112714 000010 105: MOV B #BSEB, @TACS ; CHECK "ERROR" WITH "BSEB"
0033276 005714 TST @TACS ; SAMPLE THE "ERROR" BIT
0033300 100001 BPL 115 ; BR IF "ERROR" = 0
0033302 104001 ERROR 1 ; "ERROR" NOT = 0
0033304 112714 000012 115: MOV B #SFFG, @TACS ; CHECK "ERROR" WITH "SFFG"
0033310 005714 TST @TACS ; SAMPLE THE "ERROR" BIT
0033312 100001 BPL 125 ; BR IF "ERROR" = 0
0033314 104001 ERROR 1 ; "ERROR" NOT = 0
0033316 112714 000014 125: MOV B #SFBG, @TACS ; CHECK "ERROR" WITH "SFBG"
0033322 005714 TST @TACS ; SAMPLE THE "ERROR" BIT
0033324 100001 BPL 135 ; BR IF "ERROR" = 0
0033326 104001 ERROR 1 ; "ERROR" NOT = 0
0033330 112714 000016 135: MOV B #REWIND, @TACS ; CHECK "ERROR" WITH "REWIND"
0033334 005714 TST @TACS ; SAMPLE THE "ERROR" BIT
0033336 100001 BPL TST5 ; BR IF "ERROR" = 0
0033340 104001 ERROR 1 ; "ERROR" NOT = 0

```

```

////////////////////////////////////
THE FOLLOWING TESTS CHECK THE INTERRUPT CIRCUITRY FOR:
1) INTERRUPT OCCURS AT THE PROPER "BR" LEVEL
2) "READY" WILL INTERRUPT
3) "TRANSFER REQUEST" WILL INTERRUPT
4) IMPROPER INTERRUPTS DO NOT OCCUR
5) WITH AN ERROR PENDING AND "XFER REQ." SET THE FOLLOWING SEQUENCE OCCURS
A) "XFER REQ" CAUSES AN INTERRUPT
B) CLEARING "XFER REQ" ALLOWS "READY" TO SET
C) "READY" CAUSES AN INTERRUPT
////////////////////////////////////

```

```

;*****
; THIS TEST WILL SETUP THE TAIL'S VECTOR ADDRESS AND AT ADDRESS 0
; A JMP @ (PC)+ IS STORED IN CASE THE VECTOR IS READ AS 0.
; IT WILL THEN SET THE PRIORITY LEVEL TO 0 AND WASTE ENOUGH TIME
; FOR AN INTERRUPT TO OCCUR.
; AFTER THE TIME IS UP IT WILL RESTORE THE PS TO LEVEL 7
; AND CHECK IF AN INTERRUPT OCCURRED.
;*****

```

```

;*****
; TEST 5 TEST INTERRUPT WITH READY = "1" AT LEVEL 0
;*****

```

```

0033342 000004 TST5: SCOPE
0033344 012767 003362 175534 MOV #15, $LPADR ; SET SCOPE LOOP ADDRESS
0033352 012767 003520 175610 MOV #65, $ESCAPE ; ESCAPE TO 65 ON ERROR
0033360 000005 RESET
0033362 010314 15: MOV DRIVE, @TACS
0033364 012737 000340 177776 MOV #340, @PS ; LOCK OUT ALL INTERRUPTS
0033372 012700 000000 MOV #0, R0 ; TEST AT PRIORITY LEVEL 0
0033376 005001 CLR R1 ; CLEAR INTERRUPT KEY
003400 012702 000001 MOV #1, R2 ; SETUP TO WASTE A LITTLE TIME
003404 012777 003510 175604 MOV #45, @TAVEC ; INTERRUPT RETURN
003412 012777 000340 175600 MOV #340, @TAVEC+2 ; LOCK OUT THE WORLD IF INTERRUPT

```

```

0000 003420 012737 000137 000000 MOV #137,2#0 ;SETUP TO CATCH INTERRUPT IF
0001 003426 012737 000514 000002 MOV #55,2#2 ;IT GOES TO LOCATION 0
0002 003434 112714 000100 MOV# #INT.EN,2TACS ;SET INTERRUPT ENABLE
0003 003440 012737 000000 177776 MOV #0*40,2#PS ;SET TO PRIORITY LEVEL 0
0004 003446 006302 25: ASL R2 ;KILL SOME TIME
0005 003450 001376 BNE 25
0006 003452 012737 000340 177776 MOV #340,2#PS ;LOCK OUT THE WORLD
0007 003460 005701 TST R1 ;DID AN INTERRUPT OCCUR?
0008 003462 001005 BNE 25 ;BR IF YES
0009 003464 022737 000000 001222 CMP #0*40,2#TAPR10 ;WAS AN INTERRUPT EXPECTED?
0010 003472 002012 BNE 25 ;BR IF NO
0011 003474 104006 ERROR 6 ;INTERRUPT FAILED TO OCCUR AT PRIORITY LEVEL 0
0012 003476 022737 000000 001222 35: CMP #0*40,2#TAPR10 ;INTERRUPT OCCURRED--SHOULD IT HAVE?
0013 003504 002405 BLT 65 ;BR IF YES
0014 003506 104006 ERROR 6 ;INTERRUPT OCCURRED AT PRIORITY LEVEL 0
0015 003510 005201 45: INC R1 ;SET INTERRUPT KEY
0016 003512 000002 RTI ;RETURN AFTER INTERRUPT
0017 003514 022626 55: CMP (SP)+,(SP)+ ;POP THE STACK
0018 003516 104006 ERROR 6 ;INTERRUPT WENT TO LOCATION 0
0019 003520 005014 65: CLR 2TACS ;CLEAR INTERRUPT ENABLE
0020 003522 013777 001220 175466 MOV 2#TAVEC+2,2TAVEC ;SET TRAP CATCHER
0021 003530 005077 CLR 2TAVEC+2
0022 003534 005037 CLR 2#0
0023 003540 005037 CLR 2#2
::*****
;THIS TEST WILL SETUP THE TAIL'S VECTOR ADDRESS AND AT ADDRESS 0
;A JMP 2(PC)+ IS STORED IN CASE THE VECTOR IS READ AS 0.
;IT WILL THEN SET THE PRIORITY LEVEL TO 1 AND WASTE ENOUGH TIME
;FOR AN INTERRUPT TO OCCUR.
;AFTER THE TIME IS UP IT WILL RESTORE THE PS TO LEVEL 7
;AND CHECK IF AN INTERRUPT OCCURRED.
::*****
;*TEST 6 TEST INTERRUPT WITH READY = "1" AT LEVEL 1
::*****
*ST6: SCOPE
MOV #15,$LPADR ;;SET SCOPE LOOP ADDRESS
MOV #65,$ESCAPE ;;ESCAPE TO 65 ON ERROR
RESET
15: MOV DRIVE,2TACS
MOV #340,2#PS ;LOCK OUT ALL INTERRUPTS
MOV #1,R0 ;TEST AT PRIORITY LEVEL 1
CLR R1 ;CLEAR INTERRUPT KEY
MOV #1,R2 ;SETUP TO WASTE A LITTLE TIME
MOV #45,2TAVEC ;INTERRUPT RETURN
MOV #340,2TAVEC+2 ;LOCK OUT THE WORLD IF INTERRUPT
MOV #137,2#0 ;SETUP TO CATCH INTERRUPT IF
MOV #55,2#2 ;IT GOES TO LOCATION 0
MOV# #INT.EN,2TACS ;SET INTERRUPT ENABLE
MOV #1*40,2#PS ;SET TO PRIORITY LEVEL 1
25: ASL R2 ;KILL SOME TIME
BNE 25
MOV #340,2#PS ;LOCK OUT THE WORLD
TST R1 ;DID AN INTERRUPT OCCUR?

```

L03

```

976 003664 001005 BNE 3$ ;BR IF YES
977 003666 022737 000040 001222 CMP #1*40, @TAPRIO ;WAS AN INTERRUPT EXPECTED?
978 003674 002012 BGE 6$ ;BR IF NO
979 003676 104006 ERROR 6 ;INTERRUPT FAILED TO OCCUR AT PRIORITY LEVEL 1
980
981 003700 022737 000040 001222 3$: CMP #1*40, @TAPRIO ;INTERRUPT OCCURRED--SHOULD IT HAVE?
982 003706 002405 BLT 6$ ;BR IF YES
983 003710 104006 ERROR 6 ;INTERRUPT OCCURRED AT PRIORITY LEVEL 1
984
985 003712 005201 4$: INC R1 ;SET INTERRUPT KEY
986 003714 000002 RTI ;RETURN AFTER INTERRUPT
987 003716 022626 5$: CMP (SP)+, (SP)+ ;POP THE STACK
988 003720 104006 ERROR 6 ;INTERRUPT WENT TO LOCATION 0
989
990 003722 005014 6$: CLR @TACS ;CLEAR INTERRUPT ENABLE
991 003724 013777 001220 175264 MOV @TAVEC+2, @TAVEC ;SET TRAP CATCHER
992 003732 005077 175262 CLR @TAVEC+2
993 003736 005037 000000 CLR @#0
994 003742 005037 000002 CLR @#2
995
996 ::*****
997 ;THIS TEST WILL SETUP THE TA11'S VECTOR ADDRESS AND AT ADDRESS 0
998 ;A JMP @PC+ IS STORED IN CASE THE VECTOR IS READ AS 0.
999 ;IT WILL THEN SET THE PRIORITY LEVEL TO 2 AND WASTE ENOUGH TIME
1000 ;FOR AN INTERRUPT TO OCCUR.
1001 ;AFTER THE TIME IS UP IT WILL RESTORE THE PS TO LEVEL 7
1002 ;AND CHECK IF AN INTERRUPT OCCURRED.
1003
1004 ::*****
1005 *TEST 7 TEST INTERRUPT WITH READY = "1" AT LEVEL 2
1006 ::*****
1007 $T7: SCOPE
1008 MOV #1$, $LPADR ;:SET SCOPE LOOP ADDRESS
1009 MOV #6$, $ESCAPE ;:ESCAPE TO 6$ ON ERROR
1010
1011 1$: MOV DRIVE, @TACS
1012 MOV #340, @#PS ;LOCK OUT ALL INTERRUPTS
1013 CLR R0 ;TEST AT PRIORITY LEVEL 2
1014 MOV #1, R2 ;CLEAR INTERRUPT KEY
1015 MOV #4$, @TAVEC ;SETUP TO WASTE A LITTLE TIME
1016 MOV #340, @TAVEC+2 ;INTERRUPT RETURN
1017 MOV #137, @#0 ;LOCK OUT THE WORLD IF INTERRUPT
1018 MOV #5$, @#2 ;SETUP TO CATCH INTERRUPT IF
1019 MOV #INT.EN, @TACS ;IT GOES TO LOCATION 0
1020 MOV #2*40, @#PS ;SET INTERRUPT ENABLE
1021 2$: ASL R2 ;SET TO PRIORITY LEVEL 2
1022 BNE 2$ ;KILL SOME TIME
1023 MOV #340, @#PS ;LOCK OUT THE WORLD
1024 TST R1 ;DID AN INTERRUPT OCCUR?
1025 BNE 3$ ;BR IF YES
1026 CMP #2*40, @TAPRIO ;WAS AN INTERRUPT EXPECTED?
1027 BGE 6$ ;BR IF NO
1028 ERROR 6 ;INTERRUPT FAILED TO OCCUR AT PRIORITY LEVEL 2
1029
1030 3$: CMP #2*40, @TAPRIO ;INTERRUPT OCCURRED--SHOULD IT HAVE?
1031 BLT 6$ ;BR IF YES
1032 ERROR 6 ;INTERRUPT OCCURRED AT PRIORITY LEVEL 2

```

```

1032
1033 004114 005201 4$: INC R1 ;SET INTERRUPT KEY
1034 004116 000002 RTI ;RETURN AFTER INTERRUPT
1035 004120 022626 5$: CMP (SP)+,(SP)+ ;POP THE STACK
1036 004122 104006 ERROR 6 ;INTERRUPT WENT TO LOCATION 0
1037
1038 004124 005014 6$: CLR @TACS ;CLEAR INTERRUPT ENABLE
1039 004126 013777 001220 175062 MOV @TAVEC+2,@TAVEC ;SET TRAP CATCHER
1040 004134 005077 175060 CLR @TAVEC+2
1041 004140 005037 000000 CLR @#0
1042 004144 005037 000002 CLR @#2
1043
1044 ::*****
1045 :THIS TEST WILL SETUP THE TAI'S VECTOR ADDRESS AND AT ADDRESS 0
1046 :A JMP @PC+ IS STORED IN CASE THE VECTOR IS READ AS 0.
1047 :IT WILL THEN SET THE PRIORITY LEVEL TO 3 AND WASTE ENOUGH TIME
1048 :FOR AN INTERRUPT TO OCCUR.
1049 :AFTER THE TIME IS UP IT WILL RESTORE THE PS TO LEVEL 7
1050 :AND CHECK IF AN INTERRUPT OCCURRED.
1051 ::*****
1052 :*TEST 10 TEST INTERRUPT WITH READY = "1" AT LEVEL 3
1053 :*****
1054 TST10: SCOPE
1055 004150 000004 MOV #1$, $LPADR ;;SET SCOPE LOOP ADDRESS
1056 004152 012767 004170 174726 MOV #6$, $ESCAPE ;;ESCAPE TO 6$ ON ERROR
1057 004160 012767 004326 175002 RESET
1058 004166 000005 1$: MOV DRIVE,@TACS
1059 004170 010314 MOV #340,@#PS ;LOCK OUT ALL INTERRUPTS
1060 004172 012737 000340 177776 MOV #3,@R0 ;TEST AT PRIORITY LEVEL 3
1061 004200 012700 000003 CLR R1 ;CLEAR INTERRUPT KEY
1062 004204 005001 MOV #1,R2 ;SETUP TO WASTE A LITTLE TIME
1063 004206 012702 000001 MOV #4$,@TAVEC ;INTERRUPT RETURN
1064 004212 012777 004316 174776 MOV #340,@TAVEC+2 ;LOCK OUT THE WORLD IF INTERRUPT
1065 004220 012777 000340 174772 MOV #137,@#0 ;SETUP TO CATCH INTERRUPT IF
1066 004226 012737 000137 000000 MOV #5$,@#2 ; IT GOES TO LOCATION 0
1067 004234 012737 004322 000002 MOVB #INT.EN,@TACS ;SET INTERRUPT ENABLE
1068 004242 112714 000100 MOV #3*40,@#PS ;SET TO PRIORITY LEVEL 3
1069 004246 012737 000140 177776 2$: ASL R2 ;KILL SOME TIME
1070 004254 006302 BNE 2$
1071 004256 001376 MOV #340,@#PS ;LOCK OUT THE WORLD
1072 004260 012737 000340 177776 TST R1 ;DID AN INTERRUPT OCCUR?
1073 004266 005701 BNE 3$ ;BR IF YES
1074 004270 001005 CMP #3*40,@#TAPRIO ;WAS AN INTERRUPT EXPECTED?
1075 004272 022737 000140 001222 BGE 6$ ;BR IF NO
1076 004300 002012 ERROR 6 ;INTERRUPT FAILED TO OCCUR AT PRIORITY LEVEL 3
1077 004302 104006
1078 004304 022737 000140 001222 3$: CMP #3*40,@#TAPRIO ;INTERRUPT OCCURRED--SHOULD IT HAVE?
1079 004312 002405 BLT 6$ ;BR IF YES
1080 004314 104006 ERROR 6 ;INTERRUPT OCCURRED AT PRIORITY LEVEL 3
1081
1082 004316 005201 4$: INC R1 ;SET INTERRUPT KEY
1083 004320 000002 RTI ;RETURN AFTER INTERRUPT
1084 004322 022626 5$: CMP (SP)+,(SP)+ ;POP THE STACK
1085 004324 104006 ERROR 6 ;INTERRUPT WENT TO LOCATION 0
1086
1087 004326 005014 6$: CLR @TACS ;CLEAR INTERRUPT ENABLE
1088 004330 013777 001220 174660 MOV @TAVEC+2,@TAVEC ;SET TRAP CATCHER

```

```

1088 004336 005077 174656
1089 004342 005037 000000
1090 004346 005037 000002
1091
1092
1093
1094
1095
1096
1097
1098
1099
1100
1101 004352 000004
1102 004354 012767 004372 174524
1103 004362 012767 004530 174600
1104 004370 000005
1105 004372 010314
1106 004374 012737 000340 177776
1107 004402 012700 000004
1108 004406 005001
1109 004410 012702 000001
1110 004414 012777 004520 174574
1111 004422 012777 000340 174570
1112 004430 012737 000137 000000
1113 004436 012737 004524 000002
1114 004444 112714 000100
1115 004450 012737 000200 177776
1116 004456 006302
1117 004460 001376
1118 004462 012737 000340 177776
1119 004470 005701
1120 004472 001005
1121 004474 022737 000200 001222
1122 004502 002012
1123 004504 104006
1124 004506 022737 000200 001222
1125 004514 002405
1126 004516 104006
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1129 004520 005201
1130 004522 000002
1131 004524 022626
1132 004526 104006
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1134 004530 005014
1135 004532 013777 001220 174456
1136 004540 005077 174454
1137 004544 005037 000000
1138 004550 005037 000002

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CLR @TAVEC+2
CLR @#0
CLR @#2
::*****
:THIS TEST WILL SETUP THE TAIL'S VECTOR ADDRESS AND AT ADDRESS 0
:A JMP @PC)+ IS STORED IN CASE THE VECTOR IS READ AS 0.
:IT WILL THEN SET THE PRIORITY LEVEL TO 4 AND WASTE ENOUGH TIME
:FOR AN INTERRUPT TO OCCUR.
:AFTER THE TIME IS UP IT WILL RESTORE THE PS TO LEVEL 7
:AND CHECK IF AN INTERRUPT OCCURRED.
::*****
*TEST 11 TEST INTERRUPT WITH READY = "1" AT LEVEL 4
::*****
TEST11: SCOPE
MOV #1$, $LPADR ::SET SCOPE LOOP ADDRESS
MOV #6$, $ESCAPE ::ESCAPE TO 6$ ON ERROR
RESET
1$: MOV DRIVE, @TACS
MOV #340, @#PS ;LOCK OUT ALL INTERRUPTS
MOV #4, R0 ;TEST AT PRIORITY LEVEL 4
CLR R1 ;CLEAR INTERRUPT KEY
MOV #1, R2 ;SETUP TO WASTE A LITTLE TIME
MOV #4$, @TAVEC ;INTERRUPT RETURN
MOV #340, @TAVEC+2 ;LOCK OUT THE WORLD IF INTERRUPT
MOV #137, @#0 ;SETUP TO CATCH INTERRUPT IF
MOV #5$, @#2 ;IT GOES TO LOCATION 0
MOVB #INT.EN, @TACS ;SET INTERRUPT ENABLE
MOV #4*40, @#PS ;SET TO PRIORITY LEVEL 4
2$: ASL R2 ;KILL SOME TIME
BNE 2$
MOV #340, @#PS ;LOCK OUT THE WORLD
TST R1 ;DID AN INTERRUPT OCCUR?
BNE 3$ ;BR IF YES
CMP #4*40, @#TAPRIO ;WAS AN INTERRUPT EXPECTED?
BGE 6$ ;BR IF NO
ERROR 6$ ;INTERRUPT FAILED TO OCCUR AT PRIORITY LEVEL 4
3$: CMP #4*40, @#TAPRIO ;INTERRUPT OCCURRED--SHOULD IT HAVE?
BLT 6$ ;BR IF YES
ERROR 6$ ;INTERRUPT OCCURRED AT PRIORITY LEVEL 4
4$: INC R1 ;SET INTERRUPT KEY
RTI ;RETURN AFTER INTERRUPT
5$: CMP (SP)+, (SP)+ ;POP THE STACK
ERROR 6$ ;INTERRUPT WENT TO LOCATION 0
6$: CLR @TACS ;CLEAR INTERRUPT ENABLE
MOV @TAVEC+2, @TAVEC ;SET TRAP CATCHER
CLR @TAVEC+2
CLR @#0
CLR @#2
::*****
:THIS TEST WILL SETUP THE TAIL'S VECTOR ADDRESS AND AT ADDRESS 0
:A JMP @PC)+ IS STORED IN CASE THE VECTOR IS READ AS 0.
:IT WILL THEN SET THE PRIORITY LEVEL TO 5 AND WASTE ENOUGH TIME
:FOR AN INTERRUPT TO OCCUR.

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004554 000004
004556 012767 004574 174322
004564 012767 004732 174376
004572 000005
004574 010314
004576 012737 000340 177776
004604 012700 000005
004610 005001
004612 012702 000001
004616 012777 004722 174372
004624 012777 000340 174356
004632 012737 000137 000000
004640 012737 004726 000002
004646 112714 000100
004652 012737 000240 177776
004660 006302
004662 001376
004664 012737 000340 177776
004672 005701
004674 001005
004676 022737 000240 001222
004704 022012
004706 104006
004710 022737 000240 001222
004716 002405
004720 104006
004722 005201
004724 000002
004726 022626
004730 104006
004732 005014
004734 013777 001220 174254
004742 005077 174232
004746 005037 000000
004752 005037 000002

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;AFTER THE TIME IS UP IT WILL RESTORE THE PS TO LEVEL 7
;AND CHECK IF AN INTERRUPT OCCURRED.
;*****
;TEST 12 TEST INTERRUPT WITH READY = "1" AT LEVEL 5
;*****
TEST12: SCOPE
MOV #15,$LPADR ;;SET SCOPE LOOP ADDRESS
MOV #65,$ESCAPE ;;ESCAPE TO 65 ON ERROR
RESET
15: MOV DRIVE,$TACS
MOV #340,$#PS ;LOCK OUT ALL INTERRUPTS
MOV #5,$R0 ;TEST AT PRIORITY LEVEL 5
CLR R1 ;CLEAR INTERRUPT KEY
MOV #1,$R2 ;SETUP TO WASTE A LITTLE TIME
MOV #4,$@TAVEC ;INTERRUPT RETURN
MOV #340,$@TAVEC+2 ;LOCK OUT THE WORLD IF INTERRUPT
MOV #137,$#0 ;SETUP TO CATCH INTERRUPT IF
MOV #55,$#2 ;IT GOES TO LOCATION 0
MOV#B #INT.EN,$TACS ;SET INTERRUPT ENABLE
MOV #5*40,$#PS ;SET TO PRIORITY LEVEL 5
25: ASL R2 ;KILL SOME TIME
BNE 25
MOV #340,$#PS ;LOCK OUT THE WORLD
TST R1 ;DID AN INTERRUPT OCCUR?
BNE 35 ;BR IF YES
CMP #5*40,$@TAPRIO ;WAS AN INTERRUPT EXPECTED?
BGE 65 ;BR IF NO
ERROR 6 ;INTERRUPT FAILED TO OCCUR AT PRIORITY LEVEL 5
35: CMP #5*40,$@TAPRIO ;INTERRUPT OCCURRED--SHOULD IT HAVE?
BLT 65 ;BR IF YES
ERROR 6 ;INTERRUPT OCCURRED AT PRIORITY LEVEL 5
45: INC R1 ;SET INTERRUPT KEY
RTI ;RETURN AFTER INTERRUPT
55: CMP ($P)+,($P)+ ;POP THE STACK
ERROR 6 ;INTERRUPT WENT TO LOCATION 0
65: CLR @TACS ;CLEAR INTERRUPT ENABLE
MOV @TAVEC+2,@TAVEC ;SET TRAP CATCHER
CLR @TAVEC+2
CLR #0
CLR #2
;*****
;THIS TEST WILL SETUP THE TAIL'S VECTOR ADDRESS AND AT ADDRESS C
;A JMP @($P)+ IS STORED IN CASE THE VECTOR IS READ AS 0.
;IT WILL THEN SET THE PRIORITY LEVEL TO 6 AND WASTE ENOUGH TIME
;FOR AN INTERRUPT TO OCCUR.
;AFTER THE TIME IS UP IT WILL RESTORE THE PS TO LEVEL 7
;AND CHECK IF AN INTERRUPT OCCURRED.
;*****
;TEST 13 TEST INTERRUPT WITH READY = "1" AT LEVEL 6
;*****
TEST13: SCOPE
MOV #15,$LPADR ;;SET SCOPE LOOP ADDRESS
MOV #65,$ESCAPE ;;ESCAPE TO 65 ON ERROR

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00000000 0050774 000005 RESET
00000001 0050776 010314 15: MOV DRIVE, @TACS
00000002 0050000 012737 000340 177776 MOV #340, @PS
00000003 0050006 012700 000006 MOV #6, R0 ; LOCK OUT ALL INTERRUPTS
00000004 0050112 005001 CLR R1 ; TEST AT PRIORITY LEVEL 6
00000005 0050114 012702 000001 CLR R1 ; CLEAR INTERRUPT KEY
00000006 0050220 012777 005124 174170 MOV #1, R2 ; SETUP TO WASTE A LITTLE TIME
00000007 0050226 012777 000340 174164 MOV #45, @TAVEC ; INTERRUPT RETURN
00000008 0050334 012737 000137 000000 MOV #340, @TAVEC+2 ; LOCK OUT THE WORLD IF INTERRUPT
00000009 0050442 012737 005130 000002 MOV #137, @0 ; SETUP TO CATCH INTERRUPT IF
00000010 0050550 112714 000100 MOV #55, @2 ; IT GOES TO LOCATION C
00000011 0050554 012737 000300 177776 MOV #INT.EN, @TACS ; SET INTERRUPT ENABLE
00000012 0050562 005302 25: MOV #6*40, @PS ; SET TO PRIORITY LEVEL 6
00000013 0050564 001376 BNE R2 ; KILL SOME TIME
00000014 0050566 012737 000340 177776 MOV #340, @PS ; LOCK OUT THE WORLD
00000015 0050574 005701 TEST R1 ; DID AN INTERRUPT OCCUR?
00000016 0050576 001005 BNE R3 ; BR IF YES
00000017 005100 022737 000300 001222 CMP #6*40, @TAPRIC ; WAS AN INTERRUPT EXPECTED?
00000018 005106 002012 BGE R5 ; BR IF NO
00000019 005110 104006 EPROR E ; INTERRUPT FAILED TO OCCUR AT PRIORITY LEVEL 6
00000020 005112 022737 000300 001222 35: CMP #6*40, @TAPRIC ; INTERRUPT OCCURRED--SHOULD IT HAVE?
00000021 005120 002405 BLT R5 ; BR IF YES
00000022 005122 104006 EPROR E ; INTERRUPT OCCURRED AT PRIORITY LEVEL 6
00000023 005124 005201 45: INC R1 ; SET INTERRUPT KEY
00000024 005126 000002 RTI ; RETURN AFTER INTERRUPT
00000025 005130 022626 55: CMP (SP)+, (SP)+ ; POP THE STACK
00000026 005132 104006 EPROR E ; INTERRUPT WENT TO LOCATION C
00000027 005134 005014 65: CLR @TACS ; CLEAR INTERRUPT ENABLE
00000028 005136 013777 001220 174052 MOV @TAVEC+2, @TAVEC ; SET TRAP CATCHER
00000029 005144 005077 174050 CLR @TAVEC+2
00000030 005150 005037 000000 CLR @0
00000031 005154 005037 000002 CLR @2
00000032 *****
00000033 ; THIS TEST WILL SETUP THE TAIL'S VECTOR ADDRESS AND AT ADDRESS C
00000034 ; A JMP @PC+ IS STORED IN CASE THE VECTOR IS READ AS 0.
00000035 ; IT WILL THEN SET THE PRIORITY LEVEL TO 7 AND WASTE ENOUGH TIME
00000036 ; FOR AN INTERRUPT TO OCCUR.
00000037 ; AFTER THE TIME IS UP IT WILL RESTORE THE PS TO LEVEL 7
00000038 ; AND CHECK IF AN INTERRUPT OCCURRED.
00000039 *****
00000040 ; TEST 14 TEST INTERRUPT WITH READY = "1" AT LEVEL 7
00000041 *****
00000042 TEST:4: SCOPE
00000043 MOV #15, $LADR ; SET SCOPE LOOP ADDRESS
00000044 MOV #65, $ESCAPE ; ESCAPE TO 65 ON ERROR
00000045 RESET
00000046 15: MOV DRIVE, @TACS
00000047 MOV #340, @PS ; LOCK OUT ALL INTERRUPTS
00000048 MOV #7, R0 ; TEST AT PRIORITY LEVEL 7
00000049 CLR R1 ; CLEAR INTERRUPT KEY
00000050 MOV #1, R2 ; SETUP TO WASTE A LITTLE TIME
00000051 MOV #45, @TAVEC ; INTERRUPT RETURN
00000052 MOV #340, @TAVEC+2 ; LOCK OUT THE WORLD IF INTERRUPT

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1256 005236 012737 000137 000000      MOV      #137, @R0      ; SETUP TO CATCH INTERRUPT IF
1257 005244 012737 005332 000002      MOV      #55, @R2      ; IT GOES TO LOCATION 0
1258 005252 112714 000100      MOV      @INT.EN, @TACS ; SET INTERRUPT ENABLE
1259 005256 012737 000340 177776      MOV      #7*40, @RPS   ; SET TO PRIORITY LEVEL 7
1260 005264 005302 2S:      ASL      R2            ; KILL SOME TIME
1261 005266 001376      BNE      2S
1262 005270 012737 000340 177776      MOV      #340, @RPS    ; LOCK OUT THE WORLD
1263 005276 005701      TST      R1            ; DID AN INTERRUPT OCCUR?
1264 005300 001005      BNE      3S            ; BR IF YES
1265 005302 022737 000340 001222      CMP      #7*40, @TAPRIO ; WAS AN INTERRUPT EXPECTED?
1266 005310 002012      BGE      6S            ; BR IF NO
1267 005312 104006      ERROR    6            ; INTERRUPT FAILED TO OCCUR AT PRIORITY LEVEL 7
1268 005314 022737 000340 001222 3S:      CMP      #7*40, @TAPRIO ; INTERRUPT OCCURRED--SHOULD IT HAVE?
1269 005322 002405      BLT      6S            ; BR IF YES
1270 005324 104006      ERROR    6            ; INTERRUPT OCCURRED AT PRIORITY LEVEL 7
1271 005326 005201 4S:      INC      R1            ; SET INTERRUPT KEY
1272 005330 000002      RTI                          ; RETURN AFTER INTERRUPT
1273 005332 022626 5S:      CMP      (SP)+, (SP)+   ; POP THE STACK
1274 005334 104006      ERROR    6            ; INTERRUPT WENT TO LOCATION 0
1275 005336 005014 6S:      CLR      @TACS         ; CLEAR INTERRUPT ENABLE
1276 005340 013777 001220 173650      MOV      @TAVEC+2, @TAVEC ; SET TRAP CATCHER
1277 005346 005077      CLR      @TAVEC+2
1278 005352 005037 000000      CLR      @R0
1279 005356 005037 000002      CLR      @R2
1280 *****
1281 *TEST 15      TEST INTERRUPT WITH "TRANSFER REQUEST" = 1
1282 *****
1283 *ST:5:  SCOPE
1284      MOV      #10, @TIMES      ;; DO 10 ITERATIONS
1285      MOV      #45, @LADR      ;; SET SCOPE LOOP ADDRESS
1286      MOV      #35, @ESCAPE   ;; ESCAPE TO 3S ON ERROR
1287      RESET
1288      MOV      @DRIVE, @TACS   ; SELECT DRIVE
1289      MOV      @REWIND+GO, @TACS ; POSITION THE TAPE
1290      WAITREADY              ; GO WAIT FOR "READY" TO SET
1291      MOV      @DRIVE, @TACS   ; SELECT DRIVE
1292      MOV      @WFG+GO, @TACS  ; START A "WRITE FILE GAP"
1293      WAITREADY              ; GO WAIT FOR "READY" TO SET
1294      MOV      #340, @RPS     ; LOCK OUT ALL INTERRUPTS
1295      MOV      #1, @R1        ; SETUP TO WASTE SOME TIME
1296      MOV      #25, @TAVEC    ; SETUP INTERRUPT RETURN
1297      MOV      #340, @TAVEC+2 ; PRIORITY "7" ON INTERRUPT
1298      MOV      #137, @R0     ; CATCH INTERRUPT IF IT GOES
1299      MOV      #55, @R2      ; TO LOCATION 0
1300      MOV      @WRITE!INT.EN!GO, @TACS ; START A "WRITE" WITH "INT. EN."
1301      WAITXFER              ; GO WAIT ON "TRANSFER REQUEST" TO SET
1302      CLR      @RPS          ; LEVEL 0
1303      CLR      @R0
1304      ASL      R1            ; KILL A LITTLE TIME
1305      BNE      1S
1306      MOV      #340, @RPS    ; LOCK OUT ALL INTERRUPTS
1307      ERROR    6            ; INTERRUPT FOR XFER REQ. FAILED
1308      CMP      (SP)+, (SP)+ ; POP THE STACK
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E04

TEST PART 21 MAINDEC-11-D2TAB-C
TEST INTERRUPT WITH "TRANSFER REQUEST" = 1

1340 005524 104006
1341 005526 022626
1342 005530 000005
1343 005532 013777 001220 173456
1344 005540 005077 173454
1345 005544 005037 000000
1346 005550 005037 000002

1351 005554 000004
1352 005556 012767 000001 173402
1353 005564 012767 005724 173376
1354 005572 012767 005724 000064
1355 005600 012701 000001
1356 005604 012737 000340 177776
1357 005612 000005
1358 005614 010314
1359 005616 104412
1360 005620 012777 005714 173370
1361 005626 012777 000340 173364
1362 005634 012737 000137 000000
1363 005642 012737 005720 000002
1364 005650 112714 000117
1365 005654 032714 000240
1366 005660 001404
1367 005662 005327
1368 005664 000000
1369 005666 001372
1370 005670 104001
1371 005672 005037 177776
1372 005676 005000
1373 005700 0063J1
1374 005702 001376
1375 005704 012737 000340 177776
1376 005712 000404
1377 005714 022626
1378 005716 104006
1379 005720 022626
1380 005722 104006
1381 005724 000005
1382 005726 013777 001220 173262
1383 005734 005077 173260
1384 005740 005037 000000
1385 005744 005037 000002

1390 005750 000004
1391 005752 012767 000012 173206
1392 005760 012767 006014 173120
1393 005766 012767 006222 173174
1394 005774 000005
1395 005776 010314
1396 006000 112714 000017
1397 006004 104412

ERROR 6 ; INTERRUPT WENT TO LOCATION 0
25: CMP (SP)+,(SP)+ ; POP STACK
35: RESET ; CLEAR ALL
MOV @TAVEC+2,@TAVEC ; SET TRAP CATCHER
CLR @TAVEC+2
CLR @#0
CLR @#2
:*****
:TEST 16 TEST INTERRUPT WITH "READY" = 0 AND "XFER REQ" = 0
:*****
15T16: SCOPE
MOV #1,\$TIMES ; DO 1 ITERATION
MOV #5,\$\$ESCAPE ; ESCAPE TO \$\$ ON ERROR
MOV #5,\$\$35
MOV #1,\$R1
MOV #340,\$#PS ; LOCK OUT THE WORLD
RESET ; CLEAR THE WORLD
MOV DRIVE,\$TACS ; SELECT DRIVE
WAITREADY ; GO WAIT FOR "READY" TO SET
MOV #45,\$TAVEC ; RETURN IF INTERRUPT OCCURS
MOV #340,\$TAVEC+2 ; LEVEL "7" IF INTERRUPT
MOV #137,\$#0 ; CATCH INTERRUPT IF IT GOES TO
MOV #75,\$#2 ; LOCATION 0
MOVB #REWIND!INT.EN!GO,\$TACS
RPT #TR.REG!READY,\$TACS ; GIVE READY TIME TO CLEAR
BEQ 25
DEC (PC)+
35: 0
BNE 15
ERROR 1 ; READY OR XFER REQ = "1"
25: CLR #PS ; ALLOW INTERRUPT
CLR R0 ; AT LEVEL 0
ASL R1 ; GIVE INTERRUPT TIME TO COME IN
BNE 65
MOV #340,\$#PS ; LOCK OUT INTERRUPT
BR 55 ; EXIT WITH NO ERRORS
45: CMP (SP)+,(SP)+ ; POP STACK
ERROR 6 ; INTERRUPT OCCURRED
75: CMP (SP)+,(SP)+ ; POP THE STACK
ERROR 6 ; INTERRUPTED TO LOCATION 0
55: RESET ; CLEAR THE WORLD
MOV @TAVEC+2,@TAVEC ; SET TRAP CATCHER
CLR @TAVEC+2
CLR @#0
CLR @#2
:*****
:TEST 17 TEST INTERRUPT WITH "XFER REQ" = 1 & "TIMING ERROR" = 0
:*****
15T17: SCOPE
MOV #10,\$TIMES ; DO 10 ITERATIONS
MOV #15,\$LPADR ; SET SCOPE LOOP ADDRESS
MOV #115,\$\$ESCAPE ; ESCAPE TO 115 ON ERROR
RESET ; CLEAR ALL
MOV DRIVE,\$TACS ; SELECT DRIVE
MOVB #REWIND!GO,\$TACS ; GO TO "BOT"
WAITREADY ; WAIT ON READY

F04

MAIN: BAS, LOGIC TEST PART 2) MAINDEC-11-DZTAB-C MACY11 27(732) 19-AUG-76 11:51 PAGE 31
 DZTAB0.NEW TEST INTERRUPT WITH "XFER REQ" = 1 & "TIMING ERROR" = 1

SEQ 0044
 SEQ 0044

139	006006	112714	000001		MOV B	#WFG!GO, @TACS		; SET ON OXIDE
140	006012	104412			WAITREADY			
141	006014	010314		15:	MOV	DRIVE, @TACS		; SELECT DRIVE
142	006016	112714	000003		MOV B	#WRITE!GO, @TACS		; START A WRITE
143	006022	104413			WAITXFER			; WAIT ON "TRANSFER REQUEST"
144	006024	112715	000377		MOV B	#377, @TADB		; WRITE A BYTE
145	006030	005000			CLR	R0		; ZERO THE COUNTER
146	006032	005001			CLR	R1		
147	006034	105714		25:	TSTB	@TACS		; CHECK "XFER REQ"
148	006036	100405			BMI	35		; BR IF SET
149	006040	062700	000010		ADD	#10, R0		; FIND OUT HOW LONG IT TAKES
150	006044	005501			ADC	R1		; FOR "XFER REQ" TO SET
151	006046	100372			SPL	25		
152	006050	104001			ERROR	1		; "XFER REQ" DIDN'T SET
153	006052	105714		35:	TSTB	@TACS		; CHECK "XFER REQ" IF IT IS = 0
154	006054	100005			SPL	45		; SOMETHING IS WRONG
155	006056	162700	000001		SUB	#1, R0		; DOWN COUNT THE COUNTER
156	006062	005601			SBC	R1		; SO A "TIMING ERROR" WILL OCCUR
157	006064	100372			SPL	35		
158	006066	000401			BR	55		
159	006070	104001		45:	ERROR	1		; "XFER REQ" CLEARED
160	006072	012777	006160	173116	55:	MOV	#85, @TAVEC	; SETUP INTERRUPT RETURN
161	006100	012777	000340	173112		MOV	#340, @TAVEC+2	; PRIORITY "7" ON INTERRUPT
162	006106	012737	000137	000000		MOV	#137, @#0	; CATCH INTERRUPT IF IT GOES
163	006114	012737	006154	000002		MOV	#75, @#2	; TO LOCATION 0
164	006122	012701	000001			MOV	#1, R1	; SETUP TO WASTE SOME TIME
165	006126	005037	177776			CLR	@#PS	; ALLOW INTERRUPTS
166	006132	005000				CLR	R0	; AT LEVEL "0"
167	006134	052714	000100			BIS	#INT.EN, @TACS	; TURN ON "INTERRUPT ENABLE"
168	006140	006301		65:	ASL	R1		; GIVE INTERRUPT TIME TO OCCUR
169	006142	001376			BNE	65		
170	006144	012737	000340	177776		MOV	#340, @#PS	; LOCK OUT INTERRUPTS
171	006152	104006			ERROR	6		; FAILED TO INTERRUPT WITH "XFER REQ" = 1
172	006154	022626		75:	CMP	(SP)+, (SP)+		; POP THE STACK
173	006156	104006			ERROR	6		; INTERRUPT WENT TO LOCATION 0
174	006160	022626		85:	CMP	(SP)+, (SP)+		; POP THE STACK
175	006162	105715			TSTB	@TADB		; KNOCK DOWN "XFER REQ"
176	006164	104412			WAITREADY			; WAIT ON "READY" CAUSED BY "TIMING ERROR"
177	006166	012777	006220	173022		MOV	#105, @TAVEC	; SETUP INTERRUPT RETURN
178	006174	012701	000001			MOV	#1, R1	; SETUP TO WASTE TIME
179	006200	005037	177776			CLR	@#PS	; ALLOW INTERRUPTS
180	006204	006301		95:	ASL	R1		; GIVE INTERRUPTS TIME TO OCCUR
181	006206	001376			BNE	95		
182	006210	012737	000340	177776		MOV	#340, @#PS	; LOCK OUT INTERRUPTS
183	006215	104006			ERROR	6		; INTERRUPT FAILED TO OCCUR FOR READY
184	006220	022626		105:	CMP	(SP)+, (SP)+		; POP THE STACK
185	006222	000005		115:	RESET			; CLEAR ALL
186	006224	012777	001220	172764		MOV	@TAVEC+2, @TAVEC	; RESTORE THE TRAP CATCHER
187	006232	005077	172762			CLR	@TAVEC+2	
188	006236	005037	000000			CLR	@#0	
189	006242	005037	000002			CLR	@#2	

THE FOLLOWING TEST ARE USED TO CHECK THE BASIC OPERATION OF THE SPACING FUNCTIONS

*TEST 20 TEST "BACK SPACE FILE GAP"

```

TST20:  SCOPE
        MOV      #10, $TIMES      ;; DO 10 ITERATIONS
        MOV      #35, $LPADR     ;; SET SCOPE LOOP ADDRESS
        MOV      #TST21, $ESCAPE ;; ESCAPE TO TEST 21 ON ERROR
        RESET
35:     MOV      DRIVE, @TACS      ; SELECT DRIVE
        MOVB     #REWIND!GO, @TACS ; POSITION TAPE ON CLEAR LEADER
        WAITREADY ; GO WAIT FOR "READY" TO SET
        MOVB     #WFG!GO, @TACS   ; WRITE A FILE GAP OFF OF CLEAR LEADER
        WAITREADY ; GO WAIT FOR "READY" TO SET
        MOVB     #WRITE!GO, @TACS ; PUT A DATA BLOCK ON THE TAPE
        WAITXFER ; GO WAIT ON "TRANSFER REQUEST" TO SET
        MOVB     #377, @TADB      ; KNOCK DOWN XFER REQ
        WAITXFER ; GO WAIT ON "TRANSFER REQUEST" TO SET
        BIS      #ILBS, @TACS     ; WRITE "CRC"
        WAITREADY ; GO WAIT FOR "READY" TO SET
        MOVB     #BSFG!GO, @TACS  ; BACK UP OVER THE DATA
        WAITREADY ; GO WAIT FOR "READY" TO SET
        BIT      #FGAP, @TACS     ; CHECK FOR FILE GAP
        BNE      1$              ; BR IF "FILE GAP" =1
        ERROR    1                ; ERROR--NO FILE GAP INDICATION
13:     TST      @TACS             ; IS "ERROR" =1?
        BPL      2$              ; BR IF NO
        ERROR    1                ; "ERROR" =1
28:     BIT      #LEADER, @TACS   ; IS "CLEAR LEADER" =1?
        BEQ      TST21           ;; BR IF NO
        ERROR    1                ; "WFG" OR "BSFG" FAILED

```

*TEST 21 TEST "SPACE FORWARD FILE GAP" FUNCTION

```

TST21:  SCOPE
        MOV      #5, $TIMES      ;; DO 5 ITERATIONS
        MOV      #15, $LPADR     ;; SET SCOPE LOOP ADDRESS
        MOV      #TST22, $ESCAPE ;; ESCAPE TO TEST 22 ON ERROR
        RESET
        MOV      DRIVE, @TACS     ; SELECT A DRIVE
        MOVB     #REWIND!GO, @TACS ; POSITION THE TAPE
        WAITREADY ; GO WAIT FOR "READY" TO SET
        MOVB     #WFG!GO, @TACS   ; WRITE FILE GAP OFF OF CLEAR LEADER
        WAITREADY ; GO WAIT FOR "READY" TO SET
        MOVB     #WRITE!GO, @TACS ; PUT SOME DATA ON THE TAPE
        WAITXFER ; GO WAIT ON "TRANSFER REQUEST" TO SET
        MOVB     #377, @TADB      ; KNOCK DOWN XFER REQ
        WAITXFER ; GO WAIT ON "TRANSFER REQUEST" TO SET
        BIS      #ILBS, @TACS     ; WRITE CRC AND SHUT DOWN
        WAITREADY ; GO WAIT FOR "READY" TO SET
        MOVB     #WFG!GO, @TACS  ; WRITE FILE GAP AFTER THE RECORD
        WAITREADY ; GO WAIT FOR "READY" TO SET
15:     MOVB     #REWIND!GO, @TACS ; REPOSITION THE TAPE

```

1451	006246	000004		
1452	006250	012767	000012	172710
1453	006256	012767	006274	172622
1454	006264	012767	006370	172676
1455	006272	000005		
1456	006274	010314		
1457	006276	112714	000017	
1458	006302	104412		
1459	006304	112714	000001	
1460	006310	104412		
1461	006312	112714	000003	
1462	006316	104413		
1463	006320	112715	000377	
1464	006324	104413		
1465	006326	052714	000020	
1466	006332	104412		
1467	006334	112714	000007	
1468	006340	104412		
1469	006342	032714	004000	
1470	006346	001001		
1471	006350	104001		
1472	006352	005714		
1473	006354	100001		
1474	006356	104001		
1475	006360	032714	020000	
1476	006364	001401		
1477	006366	104001		
1478	006370	000004		
1479	006372	012767	000005	172566
1480	006400	012767	006464	172500
1481	006406	012767	006510	172554
1482	006414	000005		
1483	006416	010314		
1484	006420	112714	000017	
1485	006424	104412		
1486	006426	112714	000001	
1487	006432	104412		
1488	006434	112714	000003	
1489	006440	104413		
1490	006442	112715	000377	
1491	006446	104413		
1492	006450	052714	000020	
1493	006454	104412		
1494	006456	112714	000001	
1495	006462	104412		
1496	006464	112714	000017	

```

1480 006470 104412 WAITREADY ;GO WAIT FOR "READY" TO SET
1481 006472 112714 000013 MOVB #SFFG!GO,@TACS ;SPACE OVER THE RECORD
1482 006476 104412 WAITREADY ;GO WAIT FOR "READY" TO SET
1483 006503 032714 004000 BIT #FGAP,@TACS ;SETTING IN A FILE GAP?
1484 006504 001001 BNE TST22 ;;BR IF YES
1485 006506 104001 ERROR 1 ;NO FILE GAP INDICATION
*****
;*TEST 22 TEST "BACK SPACE BLOCK GAP"
*****
TST22: SCOPE
MOV #5,$TIMES ;;DO 5 ITERATIONS
MOV #1,$SLPADR ;;SET SCOPE LOOP ADDRESS
MOV #TST23,$ESCAPE ;;ESCAPE TO TEST 23 ON ERROR
RESET
1S: MOV DRIVE,@TACS ;SELECT DRIVE
MOVB #REWIND!GO,@TACS ;GO TO CLEAR LEADER
WAITREADY ;GO WAIT FOR "READY" TO SET
MOVB #WFG!GO,@TACS ;WRITE A FILE GAP
WAITREADY ;GO WAIT FOR "READY" TO SET
MOVB #WRITE!GO,@TACS ;WRITE A BLOCK OF DATA
WAITXFER ;GO WAIT ON "TRANSFER REQUEST" TO SET
MOVB #377,@TADB ;WRITE ONE BYTE
WAITXFER ;GO WAIT ON "TRANSFER REQUEST" TO SET
BIS #ILBS,@TACS ;WRITE "CRC"
WAITREADY ;GO WAIT FOR "READY" TO SET
MOVB #BSBG!GO,@TACS ;BACK OVER THE DATA
WAITREADY ;GO WAIT FOR "READY" TO SET
TST @TACS ;CHECK FOR ERRORS
BPL 2S ;BR IF NONE
ERROR 1 ;ERROR OCCURRED
1S: BIT #LEADER,@TACS ;IS CLEAR LEADER=1?
BEQ TST23 ;;BR IF NO
ERROR 1 ;ON CLEAR LEADER
*****
;*TEST 23 TEST "SPACE FWD BLOCK GAP"
*****
TST23: SCOPE
MOV #5,$TIMES ;;DO 5 ITERATIONS
MOV #1,$SLPADR ;;SET SCOPE LOOP ADDRESS
MOV #TST24,$ESCAPE ;;ESCAPE TO TEST 24 ON ERROR
RESET
MOV DRIVE,@TACS
MOVB #REWIND!GO,@TACS ;GO TO CLEAR LEADER
WAITREADY ;GO WAIT FOR "READY" TO SET
MOVB #WFG!GO,@TACS ;PUT FILE GAP ON TAPE
WAITREADY ;GO WAIT FOR "READY" TO SET
MOVB #WRITE!GO,@TACS ;WRITE ONE BYTE BLOCK
WAITXFER ;GO WAIT ON "TRANSFER REQUEST" TO SET
MOVB #377,@TADB ;GO WAIT ON "TRANSFER REQUEST" TO SET
WAITXFER ;GO WAIT ON "TRANSFER REQUEST" TO SET
BIS #ILBS,@TACS ;WRITE "CRC"
WAITREADY ;GO WAIT FOR "READY" TO SET
MOVB #WFG!GO,@TACS ;WRITE A FILE GAP AFTER THE DATA
WAITREADY ;GO WAIT FOR "READY" TO SET
1S: MOVB #REWIND!GO,@TACS ;GO TO "CLEAR LEADER"
WAITREADY ;GO WAIT FOR "READY" TO SET

```

TAB: BASIC LOGIC TEST (PART 2) MAINDEC-11-DZTAB-C
DZTAB-C.NEW TEST "SPACE FWD BLOCK GAP"

006724 112714 000015
006730 104412
006732 032714 124000
006736 001401
006740 104001

```
MOV #SFBG!GO,@TACS ;SEE IF SFBG WILL SPACE OVER THE BLOCK OF DATA
WAITREADY ;GO WAIT FOR "READY" TO SET
BIT #ERROR!LEADER!FGAP,@TACS ;"ERROR" SHOULD BE CLEAR
BEG TST24 ;:50 SHOULD "CLEAR LEADER" AND "FILE GAP"
ERROR 1 ;TAPE IS IN THE WRONG PLACE AFTER A SFBG
```

THE FOLLOWING TESTS ARE USED TO CHECK THE "FILE GAP" CIRCUITS

*TEST 24 TEST AUTOMATIC "WFG" WHEN WRITING OFF OF "CLEAR LEADER"

006742 000004
006744 012767 000012 172214
006752 012767 006770 172126
006760 012767 007050 172202
006766 000005
006770 010314
006772 112714 000017
006776 104412
007000 112714 000003
007004 104413
007006 012715 000377
007012 104413
007014 052714 000020
007020 104412
007022 112714 000007
007026 104412
007030 032714 004000
007034 001001
007036 104001
007040 032714 120000
007044 001401
007046 104001

```
ST24: SCOPE
MOV #10,@STIMES ;:DO 10. ITERATIONS
MOV #19,$LPADR ;:SET SCOPE LOOP ADDRESS
MOV #TST25,$ESCAPE ;:ESCAPE TO TEST 25 ON ERROR
RESET
18: MOV DRIVE,@TACS ;SELECT DRIVE
MOV #REWIND!GO,@TACS ;GO TO CLEAR LEADER
WAITREADY ;GO WAIT FOR "READY" TO SET
MOV #WRITE!GO,@TACS ;WRITE "AUTO. FILE GAP"
WAITXFER ;GO WAIT ON "TRANSFER REQUEST" TO SET
MOV #377,@TADB ;WRITE "DATA"
WAITXFER ;GO WAIT ON "TRANSFER REQUEST" TO SET
BIS #ILBS,@TACS ;WRITE "CRC"
WAITREADY ;GO WAIT FOR "READY" TO SET
MOV #SFBG!GO,@TACS ;BACK OVER THE DATA BLOCK
WAITREADY ;GO WAIT FOR "READY" TO SET
BIT #FGAP,@TACS ;IN FILE GAP?
BNE 25 ;BR IF YES
ERROR 1 ;NO FILE GAP INDICATION
25: BIT #ERROR!LEADER,@TACS ;ERROR SHOULD BE CLEAR
BEG TST25 ;:50 SHOULD "CLEAR LEADER"
ERROR 1 ;ERROR OR CLEAR LEADER ON A 01
```

*TEST 25 TEST "READ" INTO FILE GAP CAUSES AN ERROR

007050 000004
007052 012767 000012 172106
007060 012767 007106 172020
007066 012767 007176 172074
007074 000005
007076 010314
007100 112714 000017
007104 104412
007106 112714 000003
007112 104413
007114 112715 000377
007120 104413
007122 052714 000020
007126 104412
007130 112714 000001
007134 104412
007136 112714 000011
007142 104412
007144 112714 000015

```
ST25: SCOPE
MOV #10,@STIMES ;:DO 10. ITERATIONS
MOV #19,$LPADR ;:SET SCOPE LOOP ADDRESS
MOV #TST26,$ESCAPE ;:ESCAPE TO TEST 26 ON ERROR
RESET
18: MOV DRIVE,@TACS ;SELECT DRIVE
MOV #REWIND!GO,@TACS ;GO TO BEGINNING OF TAPE
WAITREADY ;WAIT ON "READY"
MOV #WRITE!GO,@TACS ;START A WRITE
WAITXFER ;WAIT ON "XFER REQ"
MOV #377,@TADB ;WRITE ONE BYTE
WAITXFER ;WAIT ON "XFER REQ"
BIS #ILBS,@TACS ;WRITE "CRC"
WAITREADY ;WAIT ON "READY"
MOV #WFG!GO,@TACS ;WRITE A "FILE GAP"
WAITREADY ;WAIT ON "READY"
MOV #DSBG!GO,@TACS ;BACK OVER THE DATA
WAITREADY ;WAIT ON "READY"
MOV #SFBG!GO,@TACS ;SPACE OVER THE DATA BLOCK
```


"YES" "READ" INTO FILE GAP CAUSES AN ERROR

```

1593 007150 104412          WAITREADY          ;WAIT ON "READY"
1594 007152 112714 000005  MOVB #READ!GO,@TACS ;START A "READ"
1595 007156 104412          WAITREADY          ;WAIT ON "READY"
1596 007160 005714          TST @TACS          ;DID AN "ERROR" OCCUR?
1597 007162 100401          BMI 25            ;BR IF YES
1598 007164 104001          ERROR 1           ;ERROR FAILED TO SET
1599 007166 032714 004000 25: BIT #FGAP,@TACS    ;IS FILE GAP=1?
1600 007172 001001          BNE TST26         ;;BR IF YES
1601 007174 104001          ERROR 1           ;FILE GAP NOT=1
1602 *****
1603 *TEST 26 TEST "SFBG" INTO FILE GAP CAUSES AN ERROR
1604 *****
1605 †TST26: SCOPE
1606 007176 000004          MOV #10,@STIMES    ;;DO 10. ITERATIONS
1607 007200 012767 000012 171760 MOV #15,$LPADR     ;;SET SCOPE LOOP ADDRESS
1608 007206 012767 007234 171672 MOV #TST27,$ESCAPE ;;ESCAPE TO TEST 27 ON ERROR
1609 007214 012767 007324 171746 RESET
1610 007222 000005          MOV DRIVE,@TACS   ;SELECT DRIVE
1611 007224 010314          MOVB #REWIND!GO,@TACS ;GO TO BEGINNING OF TAPE
1612 007226 112714 000017          WAITREADY        ;WAIT ON "READY"
1613 007232 104412          MOVB #WRITE!GO,@TACS ;START A WRITE
1614 007234 112714 000003 15: WAITXFER        ;WAIT ON "XFER REQ"
1615 007240 104413          MOVB #377,@TACB   ;WRITE ONE BYTE
1616 007242 112715 000377          WAITXFER        ;WAIT ON "XFER REQ"
1617 007246 104413          BIS #ILBS,@TACS  ;WRITE "CRC"
1618 007250 052714 000020          WAITREADY        ;WAIT ON "READY"
1619 007254 104412          MOVB #WFG!GO,@TACS ;WRITE A "FILE GAP"
1620 007256 112714 000001          WAITREADY        ;WAIT ON "READY"
1621 007262 104412          MOVB #BSBG!GO,@TACS ;BACK OVER THE DATA
1622 007264 112714 000011          WAITREADY        ;WAIT ON "READY"
1623 007270 104412          MOVB #SFBG!GO,@TACS ;SPACE OVER THE DATA BLOCK
1624 007272 112714 000015          WAITREADY        ;WAIT ON "READY"
1625 007276 104412          MOVB #SFBG!GO,@TACS ;START A "S" "SG"
1626 007300 112714 000015          WAITREADY        ;WAIT ON "READY"
1627 007304 104412          TST @TACS        ;DID AN "ERROR" OCCUR?
1628 007306 005714          BMI 25            ;BR IF YES
1629 007310 100401          ERROR 1           ;ERROR FAILED TO SET
1630 007312 104001          BIT #FGAP,@TACS  ;IS FILE GAP=1?
1631 007314 032714 004000 25: BNE TST27         ;;BR IF YES
1632 007320 001001          ERROR 1           ;FILE GAP NOT=1
1633 *****
1634 *TEST 27 TEST "ERROR" OUTPUT OF THE STATUS ROM WITH "FILE GAP=1"
1635 *****
1636 †TST27: SCOPE
1637 007324 000004          MOV #10,@STIMES    ;;DO 10. ITERATIONS
1638 007326 012767 000012 171632 MOV #15,$LPADR     ;;SET SCOPE LOOP ADDRESS
1639 007334 012767 007430 171544 MOV #TST30,$ESCAPE ;;ESCAPE TO TEST 30 ON ERROR
1640 007342 012767 007574 171620 RESET
1641 007350 000005          MOV DRIVE,@TACS   ;SELECT DRIVE
1642 007352 010314          MOVB #REWIND!GO,@TACS ;MAKE SURE READY IS SET
1643 007354 104412          WAITREADY        ;GO TO BEGINNING OF TAPE
1644 007356 112714 000017          WAITREADY        ;WAIT ON "READY"
1645 007362 104412          MOVB #WFG!GO,@TACS ;GET OFF OF CLEAR LEADER
1646 007364 112714 000001          WAITREADY        ;WAIT FOR READY
1647 007370 104412          MOVB #WRITE!GO,@TACS ;START A WRITE
1648 007372 112714 000003          WAITXFER        ;WAIT ON "XFER REQ"
1649 007376 104413

```

```

1648 007400 112715 000377      MOVB      #377,@TACB      ;WRITE ONE BYTE
1649 007404 104413      WAITXFER      ;WAIT ON "XFER REG"
1650 007406 112715 000377      MOVB      #377,@TACB      ;WRITE
1651 007412 104413      WAITXFER      ;WAIT ON "XFER REG"
1652 007414 052714 000020      BIS       #ILBS,@TACS     ;WRITE "CRC"
1653 007420 104412      WAITREADY     ;WAIT ON "READY"
1654 007422 112714 000001      MOVB      #WFG!GO,@TACS   ;WRITE A "FILE GAP"
1655 007426 104412      WAITREADY     ;WAIT ON "READY"
1656 007430 112714 000017      MOVB      #REWIND!GO,@TACS ;BACK OVER THE DATA
1657 007434 104412      WAITREADY     ;WAIT ON "READY"
1658 007436 112714 000013      MOVB      #SFFG!GO,@TACS  ;DO A SPACE FWD FILE GAP
1659 007442 104412      WAITREADY
1660 007444 032714 004900      BIT       #FGAP,@TACS     ;IS FILE GAP=1?
1661 007450 001001      BNE        2$           ;BR IF YES
1662 007452 104001      ERROR      1           ;FILE GAP NOT=1
1663
1664 007454 112714 000000      ;NOW CHECK "ERROR" BIT FOR ALL FUNCTIONS
2$:  MOVB      #WFG,@TACS     ;CHECK "ERROR" WITH "WFG"
    TST      @TACS         ;SAMPLE THE "ERROR" BIT
    BPL      3$           ;BR IF "ERROR" = 0
1666 007462 100001      ERROR      1           ;"ERROR" NOT = 0
1667 007464 104001      MOVB      #WRITE,@TACS   ;CHECK "ERROR" WITH "WRITE"
1668 007466 112714 000002      TST      @TACS         ;SAMPLE THE "ERROR" BIT
1669 007472 005714      BPL      4$           ;BR IF "ERROR" = 0
1670 007474 100001      ERROR      1           ;"ERROR" NOT = 0
1671 007476 104001      MOVB      #READ,@TACS    ;CHECK "ERROR" WITH "READ"
1672 007500 112714 000004      TST      @TACS         ;SAMPLE THE "ERROR" BIT
1673 007504 005714      BMI      5$           ;BR IF "ERROR" = 1
1674 007506 100401      ERROR      1           ;"ERROR" NOT = 1
1675 007510 104001      MOVB      #BSFG,@TACS    ;CHECK "ERROR" WITH "BSFG"
1676 007512 112714 000006      TST      @TACS         ;SAMPLE THE "ERROR" BIT
1677 007516 005714      BPL      6$           ;BR IF "ERROR" = 0
1678 007520 100001      ERROR      1           ;"ERROR" NOT = 0
1679 007522 104001      MOVB      #BSBG,@TACS    ;CHECK "ERROR" WITH "BSBG"
1680 007524 112714 000010      TST      @TACS         ;SAMPLE THE "ERROR" BIT
1681 007530 005714      BPL      7$           ;BR IF "ERROR" = 0
1682 007532 100001      ERROR      1           ;"ERROR" NOT = 0
1683 007534 104001      MOVB      #SFFG,@TACS    ;CHECK "ERROR" WITH "SFFG"
1684 007536 112714 000012      TST      @TACS         ;SAMPLE THE "ERROR" BIT
1685 007542 005714      BPL      8$           ;BR IF "ERROR" = 0
1686 007544 100001      ERROR      1           ;"ERROR" NOT = 0
1687 007546 104001      MOVB      #SFBG,@TACS    ;CHECK "ERROR" WITH "SFBG"
1688 007550 112714 000014      TST      @TACS         ;SAMPLE THE "ERROR" BIT
1689 007554 005714      BMI      9$           ;BR IF "ERROR" = 1
1690 007556 100401      ERROR      1           ;"ERROR" NOT = 1
1691 007560 104001      MOVB      #REWIND,@TACS  ;CHECK "ERROR" WITH "REWIND"
1692 007562 112714 000016      TST      @TACS         ;SAMPLE THE "ERROR" BIT
1693 007566 005714      BPL      TST30         ;BR IF "ERROR" = 0
1694 007570 100001      ERROR      1           ;"ERROR" NOT = 0
1695 007572 104001

```

```

1696
1697
1698
1699
1700
1701
1702
1703 007574 000004
1704 007576 012767 000012 171362
1705 007604 012767 007700 171274
1706 007612 012767 010000 171350
1707 007620 000005
1708 007622 010314
1709 007624 104412
1710 007626 112714 000017
1711 007632 104412
1712 007634 112714 000001
1713 007640 104412
1714 007642 112714 000003
1715 007646 104413
1716 007650 112715 000377
1717 007654 104413
1718 007656 112715 000377
1719 007662 104413
1720 007664 052714 000020
1721 007670 104412
1722 007672 112714 000001
1723 007676 104412
1724 007700 112714 000017
1725 007704 104412
1726 007706 112714 000013
1727 007712 104412
1728 007714 032714 004000
1729 007720 001001
1730 007722 104001
1731 007724 112714 000007
1732 007730 104412
1733 007732 032714 004000
1734 007736 001001
1735 007740 104001
1736 007742 032714 100000
1737 007746 001401
1738 007750 104001
1739 007752 112714 000007
1740 007756 104412
1741 007760 032714 100000
1742 007764 001001
1743 007766 104001
1744 007770 032714 020000
1745 007774 001001
1746 007776 104001

```

```

: ////////////////////////////////////////////////////////////////////
: ////////////////////////////////////////////////////////////////////
: THE FOLLOWING TESTS INSURE THAT BACKING INTO "CLEAR LEADER" CAUSES AN ERROR
: ////////////////////////////////////////////////////////////////////
: ////////////////////////////////////////////////////////////////////
: *****
: *TEST 30 TEST BACK-SPACE-FILE-GAP INTO CLEAR LEADER
: *****
TST30: SCOPE
MOV #10,$TIMES ;;DO 10. ITERATIONS
MOV #1,$LPADR ;;SET SCOPE LOOP ADDRESS
MOV #TST31,$ESCAPE ;;ESCAPE TO TEST 31 ON ERROR
RESET
MOV DRIVE,@TACS ;SELECT DRIVE
WAITREADY ;MAKE SURE READY IS SET
MOVB #REWIND!GO,@TACS ;GO TO BEGINNING OF TAPE
WAITREADY ;WAIT ON "READY"
MOVB #WFG!GO,@TACS ;GET OFF OF CLEAR LEADER
WAITREADY ;WAIT FOR READY
MOVB #WRITE!GO,@TACS ;START A WRITE
WAITXFER ;WAIT ON "XFER REQ"
MOVB #377,@TADB ;WRITE ONE BYTE
WAITXFER ;WAIT ON "XFER REQ"
MOVB #377,@TADB ;WRITE
WAITXFER ;WAIT ON "XFER REQ"
BIS #ILBS,@TACS ;WRITE "CRC"
WAITREADY ;WAIT ON "READY"
MOVB #WFG!GO,@TACS ;WRITE A "FILE GAP"
WAITREADY ;WAIT ON "READY"
1$: MOVB #REWIND!GO,@TACS ;BACK OVER THE DATA
WAITREADY ;WAIT ON "READY"
MOVB #SFFG!GO,@TACS ;DO A SPACE FWD FILE GAP
WAITREADY
BIT #FGAP,@TACS ;IS FILE GAP=1?
BNE 2$ ;BR IF YES
ERROR 1 ;FILE GAP NOT=1
2$: MOVB #BSFG!GO,@TACS ;GO TO FIRST FILE GAP ON TAPE
WAITREADY ;GO WAIT FOR "READY" TO SET
BIT #FGAP,@TACS ;IN A FILE GAP?
BNE 3$ ;BR IF YES
ERROR 1 ;DIDN'T STOP IN A GAP
3$: BIT #ERROR,@TACS ;DID WE GET AN ERROR?
BEQ 4$ ;BR IF NO
ERROR 1 ;AN ERROR OCCURRED
4$: MOVB #BSFG!GO,@TACS ;BACK ONTO THE CLEAR LEADER
WAITREADY ;GO WAIT FOR "READY" TO SET
BIT #ERROR,@TACS ;CHECK FOR AN ERROR
BNE 5$ ;BR IF "ERROR" BIT IS SET
ERROR 1 ;"ERROR" BIT FAILED TO SET
5$: BIT #LEADER,@TACS ;CHECK FOR CLEAR LEADER
BNE TST31 ;;BR IF "CLEAR LEADER" = 1
ERROR 1 ;"ERROR" WASN'T DUE TO "CLEAR LEADER"

```

```

1747
1748
1749
1750 010003 000004
1751 010002 012767 000012 171156
1752 010010 012767 010104 171070
1753 010016 012767 010172 171144
1754 010024 000005
1755 010026 010314
1756 010030 104412
1757 010032 112714 000017
1758 010036 104412
1759 010040 112714 000001
1760 010044 104412
1761 010046 112714 000003
1762 010052 104413
1763 010054 112715 000377
1764 010060 104413
1765 010062 112715 000377
1766 010066 104413
1767 010070 052714 000020
1768 010074 104412
1769 010076 112714 000001
1770 010102 104412
1771 010104 112714 000017
1772 010110 104412
1773 010112 112714 000013
1774 010116 104412
1775 010120 032714 004000
1776 010124 001001
1777 010126 104001
1778 010130 112714 000011
1779 010134 104412
1780 010136 032714 124000
1781 010142 001401
1782 010144 104001
1783 010146 112714 000011
1784 010152 104412
1785 010154 005714
1786 010156 100401
1787 010160 104001
1788 010162 032714 020000
1789 010166 001001
1790 010170 104001
1791
1792
1793
1794
1795
1796
1797
1798 010172 000004
1799 010174 012767 000012 170764
1800 010202 012767 010230 170676
1801 010210 012767 010330 170752
1802 010216 000005

```

```

*****
*TEST 31 TEST BACK-SPACE-BLOCK-GAP INTO CLEAR LEADER
*****
TST31: SCOPE
MOV #10,$TIMES ;;DO 10. ITERATIONS
MOV #15,$LPADR ;;SET SCOPE LOOP ADDRESS
MOV #TST32,$ESCAPE ;;ESCAPE TO TEST 32 ON ERROR
RESET
MOV DRIVE,@TACS ;SELECT DRIVE
WAITREADY ;MAKE SURE READY IS SET
MOVB #REWIND!GO,@TACS ;GO TO BEGINNING OF TAPE
WAITREADY ;WAIT ON "READY"
MOVB #WFG!GO,@TACS ;GET OFF OF CLEAR LEADER
WAITREADY ;WAIT FOR READY
MOVB #WRITE!GO,@TACS ;START A WRITE
WAITXFER ;WAIT ON "XFER REQ"
MOVB #377,@TADB ;WRITE ONE BYTE
WAITXFER ;WAIT ON "XFER REQ"
MOVB #377,@TADB ;WRITE
WAITXFER ;WAIT ON "XFER REQ"
BIS #ILBS,@TACS ;WRITE "CRC"
WAI"READY ;WAIT ON "READY"
MOVB #WFG!GO,@TACS ;WRITE A "FILE GAP"
WAI"READY ;WAIT ON "READY"
1$: MOVB #REWIND!GO,@TACS ;BACK OVER THE DATA
WAI"READY ;WAIT ON "READY"
MOVB #SFFG!GO,@TACS ;DO A SPACE FWD FILE GAP
WAI"READY
BIT #FGAP,@TACS ;IS FILE GAP=1?
BNE 2$ ;BR IF YES
ERROR 1 ;FILE GAP NOT=1
2$: MOVB #BSBG!GO,@TACS ;GO TO FIRST FILE GAP ON TAPE
WAITREADY ;GO WAIT FOR "READY" TO SET
BIT #ERROR!FGAP!LEADER,@TACS ;DID WE GET ANY ERROR?
BEG 3$ ;BR IF NO
ERROR 1 ;AN ERROR OCCURRED
3$: MOVB #BSBG!GO,@TACS ;BSBG WHILE IN A FILE GAP
WAITREADY ;GO WAIT FOR "READY" TO SET
TST @TACS ;"ERROR" BIT SHOULD BE SET
BMI 4$ ;BR IF IT IS
ERROR 1 ;"ERROR" BIT WASN'T SET
4$: BIT #LEADER,@TACS ;"CLEAR LEADER" SHOULD BE SET
BNE TST32 ;;BR IF "CLEAR LEADER" = 1
ERROR 1 ;"CLEAR LEADER" NOT SET

```

```

//
//
// THE FOLLOWING TESTS INSURE THAT DATA CAN BE WRITING ONTO AND READ FROM THE TAPE
//
//

```

```

*****
*TEST 32 TEST "WRITE" 377 & 0 "READ" 377 & 0
*****
TST32: SCOPE
MOV #10,$TIMES ;;DO 10. ITERATIONS
MOV #35,$LPADR ;;SET SCOPE LOOP ADDRESS
MOV #TST33,$ESCAPE ;;ESCAPE TO TEST 33 ON ERROR
RESET

```

```

1803 010220 110314      MOV      DRIVE,@TACS      ;SELECT DRIVE
1804 010222 112714 000017  MOVB     #REWIND!GO,@TACS ;GO TO "CLEAR LEADER"
1805 010226 104412      WAITREADY                ;WAIT ON "READY"
1806 010230 112714 000003 3$:      MOVB     #WRITE!GO,@TACS ;WRITE DATA
1807 010234 104413      WAITXFER                ;WAIT ON "XFER REQ"
1808 010236 112715 000377  MOVB     #377,@TABD      ;1ST BYTE
1809 010242 104413      WAITXFER                ;WAIT ON "XFER REQ"
1810 010244 112715 000000  MOVB     #0,@TABD        ;2ND BYTE
1811 010250 104413      WAITXFER                ;WAIT ON "XFER REQ"
1812 010252 052714 000020  BIS      #ILBS,@TACS     ;WRITE "CRC"
1813 010256 104412      WAITREADY                ;WAIT ON "READY"
1814 010260 112714 000011  MOVB     #BSBG!GO,@TACS  ;BACK OVER THE DATA
1815 010264 104412      WAITREADY                ;WAIT ON "READY"
1816 010266 112714 000005  MOVB     #READ!GO,@TACS  ;START A "READ"
1817 010272 104413      WAITXFER                ;WAIT ON "XFER REQ"
1818 010274 0115C1      MOV      @TABD,R1        ;PUT THIS BYTE IN R1
1819 010276 012700 000377  MOV      #377,R0         ;PUT WHAT IT SHOULD BE IN R0
1820 010302 020100      CMP      R1,R0          ;DID DATA READ GOOD?
1821 010304 001401      BEQ      1$            ;BR IF YES
1822 010306 104005      ERROR   5              ;DATA WASN'T = 377
1823 010310 104413 1$:      WAITXFER                ;WAIT ON "XFER"
1824 010312 005000      CLR      R0              ;READ
1825 010314 011501      MOV      @TABD,R1        ;BR IF DATA = 000
1826 010316 001401      BEQ      2$            ;DATA WASN'T = 000
1827 010320 104005      ERROR   5              ;SHUT DOWN
1828 010322 052714 000020 2$:      BIS      #ILBS,@TACS     ;WAIT ON "READY"
1829 010326 104412      WAITREADY
*****
; *TEST 33      TEST "WRITE & READ" A COUNT PATTERN
*****
1833: SCOPE
1833 010330 000004      MOV      #10,$TIMES      ;;DO 10. ITERATIONS
1834 010332 012767 000012 170626  MOV      #3$, $LPADR     ;;SET SCOPE LOOP ADDRESS
1835 010340 012767 010442 170540  MOV      #TST34,$ESCAPE  ;;ESCAPE TO TEST 34 ON ERROR
1836 010346 012767 010516 170614  RESET
1837 010354 000005      MOV      DRIVE,@TACS    ;CLEAR A_L
1838 010356 010314      MOVB     #REWIND!GO,@TACS ;SELECT DRIVE
1839 010360 112714 000017  WAITREADY                ;GO TO "BOT"
1840 010364 104412      WAITREADY                ;WAIT ON "READY" TO SET
1841 010366 112714 000001  MOVB     #WFG!GO,@TACS   ;GET ON OXIDE
1842 010372 104412      WAITREADY                ;WAIT ON "READY" TO SET
1843 010374 012700 000377  MOV      #377,R0         ;FIRST DATA PATTERN AND COUNTER
1844 010400 112714 000003  MOVB     #WRITE!GO,@TACS ;START A WRITE
1845 010404 104413      WAITXFER                ;WAIT ON "XFER REQ" TO SET
1846 010406 010015 1$:      MOV      R0,@TABD        ;WRITE A BYTE ON TAPE
1847 010410 104413      WAITXFER                ;WAIT ON "XFER REQ" TO SET
1848 010412 011501      MOV      @TABD,R1        ;GET BACK THE LAST BYTE WRITTEN
1849 010414 020001      CMP      R0,R1          ;AND CHECK IT
1850 010416 001401      BEQ      2$            ;BR IF IT LOOKS GOOD
1851 010420 104005      ERROR   5              ;THE DATA IN TABD WAS BAD
1852 010422 005300 2$:      DEC      R0              ;NEXT PATTERN
1853 010424 002370      BGE     1$            ;BR IF MORE TO DO
1854 010426 052714 000020  BIS      #ILBS,@TACS     ;WRITE THE "CRC"
1855 010432 104412      WAITREADY                ;WAIT ON "READY" TO SET
1856 010434 005714      TST     @TACS           ;ANY ERRORS OCCUR?
1857 010436 100001      BPL     3$            ;BR IF NO
1858 010440 104001      ERROR   1              ;ERROR OCCURRED DURING WRITE

```

TEST "WRITE & READ" A COUNT PATTERN

```

000011 35:  MOVB  #BSBG!GO,@TACS      ;GO TO BEGINNING OF BLOCK.
      WAITREADY                ;WAIT ON "READY" TO SET
000025 45:  MOVB  #READ!GO,@TACS       ;START A "READ"
      MOV   #377,RO            ;FIRST DATA PATTERN AND COUNTER
      WAITXFER                 ;WAIT ON "XFER REQ" TO SET
      MOV   @TACB,R1           ;READ A BYTE FROM TAPE
      CMP   RO,R1              ;IS IT VALID?
      BEQ   55                 ;BR IF YES
      ERROR 5                   ;BAD DATA READ FROM TAPE
000030 55:  DEC   RO                    ;NEXT PATTERN
      BGE   45                 ;BR IF MORE TO READ
      WAITXFER                 ;WAIT ON "XFER REQ" TO SET
000020  BIS   #ILBS,@TACS          ;SHUT DOWN THE "READ" OPERATION
      WAITREADY                ;WAIT ON "READY" TO SET
140000  BIT   #ERROR!CRCERR,@TACS  ;ERROR AND CRCERR SHOULD BE = 0
      BEQ   TST34              ;;BR IF THEY ARE
      ERROR 1                   ;(ERROR ! CRCERR) = 1

```

////////////////////////////////////
 THE FOLLOWING TESTS ARE USED TO INSURE THAT THE "CRC" CIRCUITRY FUNCTIONS PROPERLY
 //////////////////////////////////////

 *TEST 34 TEST "ERROR" WITH "CRCERR" = 1

```

000034 TST34: SCOPE
012767 000012 170440  MOV   #10,STIMES      ;;DO 10. ITERATIONS
012767 010562 170352  MOV   #205,$LPADR    ;;SET SCOPE LOOP ADDRESS
012767 010776 170426  MOV   #TST35,$ESCAPE ;;ESCAPE TO TEST 35 ON ERROR
010314  RESET
010314  MOV   DRIVE,@TACS    ;SELECT DRIVE
010546 112714 000017  MOVB  #REWIND!GO,@TACS ;GO TO CLEAR LEADER
010552 104412  WAITREADY           ;WAIT ON "READY"
010554 112714 000001  MOVB  #WFG!GO,@TACS  ;GET OFF OF CLEAR LEADER
010560 104412  WAITREADY           ;WAIT FOR READY
010562 112714 000003  MOVB  #WRITE!GO,@TACS ;START A WRITE
010566 012700 000006  MOV   #6,RO          ;SETUP FOR 6 BYTES
010572 104413 15:  WAITXFER            ;WAIT ON "XFER REQ"
010574 005300  DEC   RO             ;DOWN COUNT
010576 002403  BLT   25            ;DONE?
010600 112715 000377  MOVB  #377,@TACB    ;NO--WRITE ON TAPE
010604 000772  BR    15            ;LOOP
010606 052714 000020 25:  BIS   #ILBS,@TACS  ;WRITE "CRC"
      WAITREADY           ;WAIT ON "READY"
010612 104412 000011  MOVB  #BSBG!GO,@TACS ;BACK OVER THE DATA BLOCK
      WAITREADY           ;WAIT ON "READY"
010620 104412 000005  MOVB  #READ!GO,@TACS ;START A "READ"
010626 012700 000003  MOV   #3,RO         ;DO 3 BYTES
010632 104413 35:  WAITXFER            ;WAIT FOR "XFER REQ"
010634 005300  DEC   RO             ;COUNT # OF BYTES
010636 002402  BLT   45            ;CLEAR "XFER REQ"
010640 105715 45:  TSTB  @TACB
      BR    35
010642 000773  BIS   #ILBS,@TACS  ;DO "ILBS"
010644 052714 000020  WAITREADY           ;WAIT ON "READY"
010650 104412  TST   @TACS         ;CHECK FOR "ERROR"
010652 005714  BMI   55            ;BR IF "ERROR"
010654 100401

```

TA: BASIC LOGIC TEST PART 21 MAINDEC-11-DZTAB-C
TAB:ABC.NEW 734 TEST "ERROR" WITH "CRCERR" = 1

```

1915 010656 104001          ERROR 1          ;"ERROR" BIT NOT SET
1916 010660 032714 040000 58: BIT #CRCERR,@TACS ;CHECK FOR "CRC" ERROR
1917 010654 001001          BNE 65          ;BR IF "CRC" ERROR
1918 010666 104001          ERROR 1          ;NO "CRC" ERROR
1919
1920 010670 112714 000000 65: MOVB #WFG,@TACS ;CHECK "ERROR" WITH "WFG"
1921 010674 005714          TST @TACS      ;SAMPLE THE "ERROR" BIT
1922 010676 100001          BPL 75          ;BR IF "ERROR" = 0
1923 010700 104001          ERROR 1          ;"ERROR" NOT = 0
1924 010702 112714 000002 75: MOVB #WRITE,@TACS ;CHECK "ERROR" WITH "WRITE"
1925 010706 005714          TST @TACS      ;SAMPLE THE "ERROR" BIT
1926 010710 100401          BMI 85          ;BR IF "ERROR" = 1
1927 010712 104001          ERROR 1          ;"ERROR" NOT = 1
1928 010714 112714 000006 85: MOVB #BSFG,@TACS ;CHECK "ERROR" WITH "BSFG"
1929 010720 005714          TST @TACS      ;SAMPLE THE "ERROR" BIT
1930 010722 100001          BPL 95          ;BR IF "ERROR" = 0
1931 010724 104001          ERROR 1          ;"ERROR" NOT = 0
1932 010726 112714 000010 95: MOVB #BSBG,@TACS ;CHECK "ERROR" WITH "BSBG"
1933 010732 005714          TST @TACS      ;SAMPLE THE "ERROR" BIT
1934 010734 100001          BPL 105         ;BR IF "ERROR" = 0
1935 010736 104001          ERROR 1          ;"ERROR" NOT = 0
1936 010740 112714 000012 105: MOVB #SFFG,@TACS ;CHECK "ERROR" WITH "SFFG"
1937 010744 005714          TST @TACS      ;SAMPLE THE "ERROR" BIT
1938 010746 100001          BPL 115         ;BR IF "ERROR" = 0
1939 010750 104001          ERROR 1          ;"ERROR" NOT = 0
1940 010752 112714 000014 115: MOVB #SFBG,@TACS ;CHECK "ERROR" WITH "SFBG"
1941 010756 005714          TST @TACS      ;SAMPLE THE "ERROR" BIT
1942 010760 100001          BPL 125         ;BR IF "ERROR" = 0
1943 010762 104001          ERROR 1          ;"ERROR" NOT = 0
1944 010764 112714 000016 125: MOVB #REWIND,@TACS ;CHECK "ERROR" WITH "REWIND"
1945 010770 005714          TST @TACS      ;SAMPLE THE "ERROR" BIT
1946 010772 100001          BPL TST35      ;BR IF "ERROR" = 0
1947 010774 104001          ERROR 1          ;"ERROR" NOT = 0
1948
1949
1950
1951 010776 000004          ;*****
1952 011000 012767 000005 170160 ;*TEST 35 TEST "DATA OF 0 GIVES CRC OF 0"
1953 011006 012767 011034 170072 ;*****
1954 011014 012767 011160 170146 ;*****
1955 011022 000005          TST35: SCOPE
1956 011024 010314          MOV #5,STIMES ;DO 5 ITERATIONS
1957 011026 112714 000017          MOV #E$,SLPADR ;SET SCOPE LOOP ADDRESS
1958 011032 104412          MOV #TST36,$ESCAPE ;ESCAPE TO TEST 36 ON ERROR
1959 011034 112714 000003 58: RESET
1960 011040 104413          MOV DRIVE,@TACS ;SELECT DRIVE
1961 011042 105015          MOVB #REWIND!GO,@TACS ;GO TO "CLEAR LEADER"
1962 011044 104413          WAITREADY ;WAIT ON "READY"
1963 011046 105015          MOVB #WRITE!GO,@TACS ;WRITE THE DATA
1964 011050 104413          WAITXFER ;WAIT ON "XFER REQ"
1965 011052 052714 000020          CLRB @TADB ;1ST BYTE = 0
1966 011056 104412          WAITXFER ;WAIT ON "XFER REQ"
1967 011060 112714 000011          CLRB @TADB ;2ND BYTE = 0
1968 011064 104412          WAITXFER ;WAIT ON "XFER REQ"
1969 011066 112714 000005          BIS #ILBS,@TACS ;WRITE "CRC"
1970 011072 104413          WAITREADY ;WAIT ON "READY"
          MOVB #BSBG!GO,@TACS ;BACK OVER THE DATA
          WAITREADY ;WAIT ON "READY"
          MOVB #READ!GO,@TACS ;START A "READ"
          WAITXFER ;WAIT ON "XFER REQ" FIRST

```

TEST "CRC" CIRCUIT USING A COUNT PATTERN

011100 000000
011101 000000
011102 000000
011103 000000
011104 000000
011105 000000
011106 000000
011107 000000
011108 000000
011109 000000
011110 000000
011111 000000
011112 000000
011113 000000
011114 000000
011115 000000
011116 000004
011117 012767
011118 012767
011119 012767
011120 000005
011121 010314
011122 112714
011123 104412
011124 112714
011125 104412
011126 012700
011127 112714
011128 104413
011129 010015
011130 104413
011131 011501
011132 020001
011133 001401
011134 104005
011135 005300
011136 002370
011137 052714
011138 104412
011139 005714
011140 100001
011141 104001
011142 112714
011143 104412
011144 104001
011145 000001
011146 104001
011147 032714
011148 000001
011149 104001

15: CLR R0
MOV @TAB,R1
BEQ 15
ERROR 5
WAITXFER
MOV @TAB,R1
BEQ 25
ERROR 5
WAITXFER
MOV @TAB,R1
BEQ 35
ERROR 5
WAITXFER
MOV @TAB,R1
BEQ 45
ERROR 5
BIS #ILBS,@TACS
WAITREADY
TST @TACS
BMI 55
ERROR 1
BIT #CRCERR,@TACS
BNE TST36
ERROR 1
25: DEC R0
BGE 15
BIS #ILBS,@TACS
WAITREADY
TST @TACS
BPL 35
ERROR 1
MOV @BSBG!GO,@TACS
WAITREADY
MOVB #READ!GO,@TACS

SET R0 TO WHAT THE DATA SHOULD BE
READ DATA BYTE
BR IF DATA = 0
1ST BYTE NOT = 0
WAIT ON "XFER REQ"
READ SECOND DATA BYTE
BR IF 2ND BYTE = 0
DATA WASN'T = 0
WAIT ON "XFER REQ"
READ FIRST CRC BYTE
BR IF 1ST CRC BYTE = 0
1ST BYTE OF CRC BAD
WAIT ON "XFER REQ"
READ SECOND CRC BYTE
BR IF 2ND CRC BYTE = 0
2ND BYTE OF CRC BAD
WAIT FOR "READY"
"ERROR" SHOULD BE = 1
BR IF "ERROR" IS = 1
IS THE ERROR A "CRC" ERROR?
GO TO NEXT TEST IF YES
THE ERROR WASN'T A "CRC" ERROR

TEST 36 TEST "CRC" CIRCUIT USING A COUNT PATTERN

TST36: SCOPE
MOV #10,@TIMES ;;DO 10 ITERATIONS
MOV #35,@LPADR ;;SET SCOPE LOOP ADDRESS
MOV #TST37,@ESCAPE ;;ESCAPE TO TEST 37 ON ERROR
RESET
MOV DRIVE,@TACS ;;SELECT DRIVE
MOVB #REWIND!GO,@TACS ;;GO TO "BOT"
WAITREADY ;;WAIT ON "READY" TO SET
MOVB #WFG!GO,@TACS ;;GET ON OXIDE
WAITREADY ;;WAIT ON "READY" TO SET
MOV #377,R0 ;;FIRST DATA PATTERN AND COUNTER
MOVB #WRITE!GO,@TACS ;;START A WRITE
WAITXFER ;;WAIT ON "XFER REQ" TO SET
MOV R0,@TAB ;;WRITE A BYTE ON TAPE
WAITXFER ;;WAIT ON "XFER REQ" TO SET
MOV @TAB,R1 ;;GET BACK THE LAST BYTE WRITTEN
CMP R0,R1 ;;AND CHECK IT
BEQ 25 ;;BR IF IT LOOKS GOOD
ERROR 5 ;;THE DATA IN TAB WAS BAD
NEXT PATTERN
BR I MORE TO DO
WRITE THE "CRC"
WAIT ON "READY" TO SET
ANY ERRORS OCCUR?
BR IF NO
ERROR OCCURRED DURING WRITE
BACK OVER THE DATA BLOCK
WAIT ON "READY" TO SET
START A "READ"


```

20027 011304 012700 000000
20028 011310 005003 013774
20029 011314 004737 013654
20030 011320 104412
20031 011322 011501
20032 011324 020001
20033 011326 001401
20034 011330 104005
20035 011332 005000
20036 011334 002714 000020
20037 011336 005000
20038 011340 153700 013774
20039 011344 104412
20040 011346 052714 000020
20041 011352 011501
20042 011354 020001
20043 011356 001401
20044 011360 104005
20045 011362 005000
20046 011364 153700 013775
20047 011370 104412
20048 011372 011501
20049 011374 020001
20050 011376 001401
20051 011400 104005
20052 011402 002714 140000
20053 011406 001401
20054 011410 104001
20055
20056
20057
20058
20059
20060
20061
20062
20063
20064
20065
20066
20067
20068
20069
20070
20071 011412 000004
20072 011414 012767 000005 167544
20073 011422 012767 011436 167456
20074 011430 012767 011552 167532
20075 011436 105737 001225
20076 011442 001443
20077 011444 012701 000001
20078 011450 010302
20079 011452 062702 000400
20080 011456 042702 177377
20081 011462 000005
20082 011464 010314

```

```

MOV #37, RC ; FIRST DATA PATTERN AND COUNTER
CLR #CRC.WD ; INITIALIZES THE "CRC WORD"
JSR PC, @CRC.CRC ; COMBINE THIS BYTE (RD) WITH THE "CRC WORD"
WAITXFER ; WAIT ON "XFER REG" TO SET
MOV @TADB, R1 ; READ A BYTE FROM TAPE
CMP RC, R1 ; IS IT VALID?
BEQ #5 ; BR IF YES
ERROR #5 ; BAD DATA READ FROM TAPE
DEC RC ; NEXT PATTERN
BGE #4 ; BR IF MORE TO READ
CLR RC ; GET LOW BYTE OF "CRC WORD"
BISB @CRC.WD, RC
WAITXFER ; WAIT ON "XFER REG" TO SET
BIS #1LBS, @TACS ; SHUT DOWN THE "READ" OPERATION
MOV @TADB, R1 ; PICK FIRST BYTE OF TUBO "CRC"
CMP RC, R1 ; IS IT GOOD?
BEQ #5 ; BR IF YES
ERROR #5 ; FIRST BYTE OF TUBO "CRC" IS BAD
CLR RC ; GET HIGH BYTE OF "CRC WORD"
BISB @CRC.WD+1, RC
WAITREADY ; WAIT ON "READY" TO SET
MOV @TADB, R1 ; READ THE 2ND BYTE OF TUBO "CRC"
CMP RC, R1 ; IS IT GOOD?
BEQ #5 ; BR IF YES
ERROR #5 ; HIGH BYTE OF "CRC" IS BAD
BIT #ERROR!CRCERR, @TACS ; ERROR AND CRCERR SHOULD BE = 0
BEQ TST37 ; BR IF THEY ARE
ERROR #1 ; (ERROR ! CRCERR) = 1

```

```

*****
THIS TEST REQUIRES BOTH DRIVES
FOR THE FOLLOWING DESCRIPTION DRIVE "1" IS THE DRIVE UNDER TEST
AND DRIVE "2" IS THE OTHER DRIVE

```

- ```

TEST DESCRIPTION
1) REWIND DRIVE 1
2) WAIT FOR READY TO SET
3) START DRIVE 2 REWINDING
4) WAIT FOR READY TO CLEAR
5) SELECT DRIVE 1 AND CHECK THAT READY IS STILL SET
6) SELECT DRIVE 2 AND CHECK THAT READY IS STILL CLEAR

```

```

*TEST 37 TRY TO HANG "READY" ON "REWIND"

```

```

TST37: SCOPE
MOV #5, $TIMES ; DO 5 ITERATIONS
MOV #1, $LPADR ; SET SCOPE LOOP ADDRESS
MOV #TST40, $ESCAPE ; ESCAPE TO TEST 40 ON ERROR
TSTB @DRVKEY+1 ; TESTING TWO DRIVES?
BEQ TST40 ; BR IF NO
MOV #1, R1 ; SET TIMER
MOV DRIVE, R2 ; SET R2 TO THE OTHER DRIVE
ADD #UNIT, R2
BIC #CUNIT, R2
RESET ; CLEAR ALL
MOV DRIVE, @TACS ; SELECT 1ST DRIVE

```

F05

|        |        |        |     |                       |  |                                     |
|--------|--------|--------|-----|-----------------------|--|-------------------------------------|
| 011466 | 104412 |        |     | WAITREADY             |  | :WAIT ON "READY"                    |
| 011470 | 010214 |        |     | MOV R2,@TACS          |  | :SELECT 2ND DRIVE                   |
| 011472 | 104412 |        |     | WAITREADY             |  | :WAIT ON "READY"                    |
| 011474 | 112714 | 000017 |     | MOVB #REWIND!GO,@TACS |  | :SEND 2ND DRIVE TO BOT              |
| 011500 | 006301 |        | 25: | ASL R1                |  | :GIVE "READY" TIME TO CLEAR         |
| 011502 | 001001 |        |     | BNE Z5                |  |                                     |
| 011504 | 104001 |        |     | ERROR 1               |  | : "READY" FAILED TO CLEAR           |
|        |        |        |     |                       |  | :NOTE: THIS ERROR OCCURRED ON THE   |
|        |        |        |     |                       |  | :2ND DRIVE. I.E. IF TESTING DRIVE A |
|        |        |        |     |                       |  | :THEN DRIVE B FAILED                |
|        |        |        |     |                       |  | :WAIT FOR "READY" TO CLEAR          |
| 011506 | 032714 | 000040 | 33: | BIT #READY,@TACS      |  |                                     |
| 011512 | 001272 |        |     | BNE Z5                |  |                                     |
| 011514 | 010214 |        |     | MOV DRIVE,@TACS       |  | :SELECT 1ST DRIVE                   |
| 011516 | 112714 | 000016 |     | MOVB #REWIND,@TACS    |  | :LOAD A "REWIND"                    |
| 011522 | 032714 | 000040 |     | BIT #READY,@TACS      |  | :CHECK "READY"                      |
| 011528 | 001001 |        |     | BNE Z5                |  | :BR IF STILL SET                    |
| 011530 | 104001 |        |     | ERROR 1               |  | : "READY" WAS CLEAR                 |
| 011532 | 010214 |        | 45: | MOV R2,@TACS          |  | :SELECT 2ND DRIVE                   |
| 011534 | 112714 | 000016 |     | MOVB #REWIND,@TACS    |  | :LOAD A REWIND                      |
| 011540 | 032714 | 000040 |     | BIT #READY,@TACS      |  | :CHECK "READY"                      |
| 011544 | 001401 |        |     | BEG Z5                |  | :BR IF STILL CLEAR                  |
| 011546 | 104001 |        |     | ERROR 1               |  | : "READY" WAS SET                   |
| 011550 | 104412 |        | 55: | WAITREADY             |  | :WAIT ON "READY"                    |

\*\*\*\*\*  
 :THIS TEST REQUIRES BOTH DRIVES  
 :FOR THE FOLLOWING DESCRIPTION DRIVE "1" IS THE DRIVE UNDER TEST  
 :AND DRIVE "2" IS THE OTHER DRIVE  
 :  
 :TEST DESCRIPTION  
 :1) REWIND DRIVE 1  
 :2) WFG ON DRIVE 1  
 :3) START A REWIND ON DRIVE 2  
 :4) WHILE DRIVE 2 IS REWINDING WRITE DATA ON DRIVE 1  
 :5) CHECK FOR ERRORS  
 :6) START A REWIND ON DRIVE 2  
 :7) WHILE DRIVE 2 IS REWINDING READ DATA FROM DRIVE 1  
 :8) CHECK FOR ERRORS  
 :  
 :\*\*\*\*\*

\*\*\*\*\*  
 :\*TEST 40 TRY TO GLITCH THE "POWER SUPPLY"  
 :\*\*\*\*\*

|        |        |        |        |                       |  |                              |
|--------|--------|--------|--------|-----------------------|--|------------------------------|
| 011552 | 000004 |        |        | TST40: SCOPE          |  |                              |
| 011554 | 012767 | 000005 | 167404 | MOV #5,@TIMES         |  | ::DO 5 ITERATIONS            |
| 011562 | 012767 | 011576 | 167316 | MOV #15,@SLPADR       |  | ::SET SCOPE LOOP ADDRESS     |
| 011570 | 012767 | 012134 | 167372 | MOV #TST41,@\$ESCAPE  |  | ::ESCAPE TO TEST 41 ON ERROR |
| 011576 | 105737 | 001225 |        | TSTB @DRVKEY+1        |  | :TESTING TWO DRIVES"         |
| 011602 | 001554 |        |        | BEG TST41             |  | ::BR IF NO                   |
| 011604 | 012701 | 000001 |        | MOV #1,R1             |  | :SET TIMER                   |
| 011610 | 010302 |        |        | MOV DRIVE,R2          |  | :SET R2 TO THE OTHER DRIVE   |
| 011612 | 062702 | 000400 |        | ADD #UNIT,R2          |  |                              |
| 011616 | 042702 | 177377 |        | BIC #1CUNIT,R2        |  |                              |
| 011622 | 000005 |        |        | RESET                 |  | :CLEAR ALL                   |
| 011624 | 010214 |        |        | MOV DRIVE,@TACS       |  | :SELECT 1ST DRIVE            |
| 011626 | 112714 | 000017 |        | MOVB #REWIND!GO,@TACS |  | :REWIND TO BOT               |
| 011632 | 104412 |        |        | WAITREADY             |  | :WAIT ON "READY"             |
| 011634 | 112714 | 000001 |        | MOVB #WFG!GO,@TACS    |  | :GET ON OXIDE                |

11:51:54.450  
11:51:54.451  
11:51:54.452  
11:51:54.453  
11:51:54.454  
11:51:54.455  
11:51:54.456  
11:51:54.457  
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11:52:00.000

011640 104412  
011642 104414  
011644 104412  
011646 112714  
011652 006601  
011654 001001  
011656 104001  
  
011660 032714  
011664 001372  
011666 010314  
011670 112714  
011674 104413  
011676 112715  
011702 104413  
011704 105015  
011706 104413  
011710 052714  
011714 104412  
011716 005714  
011720 100001  
011722 104001  
011724 112714  
011730 104412  
011732 005714  
011734 100001  
011736 104001  
011740 112714  
011744 104413  
011746 012700  
011752 011501  
011754 020001  
011756 001401  
011760 104005  
011762 104413  
011764 005000  
011766 011501  
011770 001401  
011772 104005  
011774 104413  
011776 052714  
012002 104412  
012004 005714  
012006 100001  
012010 104001  
012012 112714  
012016 104412  
012020 005714  
012022 100001  
012024 104001  
012026 010214  
012030 104412  
012032 112714  
012036 012701

WAITREADY  
MOV R2, @TACS  
WAITREADY  
MOVB #REWIND!GO, @TACS  
28: ASL R1  
BNE 35  
ERROR 1  
  
33: BIT #READY, @TACS  
BNE 25  
MOV DRIVE, @TACS  
MOVB #WRITE!GO, @TACS  
WAITXFER  
MOVB #377, @TADB  
WAITXFER  
CLRB @TADB  
WAITXFER  
BIS #ILBS, @TACS  
WAITREADY  
TST @TACS  
BPL 45  
ERROR 1  
48: MOVB #BSBG!GO, @TACS  
WAITREADY  
TST @TACS  
BPL 55  
ERROR 1  
53: MOVB #READ!GO, @TACS  
WAITXFER  
MOV #377, R0  
MOV @TADB, R1  
CMP R0, R1  
BEQ 65  
ERROR 5  
63: WAITXFER  
CLR R0  
MOV @TADB, R1  
BEQ 75  
ERROR 5  
73: WAITXFER  
BIS #ILBS, @TACS  
WAITREADY  
TST @TACS  
BPL 85  
ERROR 1  
83: MOVB #BSBG!GO, @TACS  
WAITREADY  
TST @TACS  
BPL 95  
ERROR 1  
93: MOV R2, @TACS  
WAITREADY  
MOVB #REWIND!GO, @TACS  
MOV #1, R1

:WAIT ON "READY"  
:SELECT 2ND DRIVE  
:WAIT ON "READY"  
:START A "REWIND"  
:GIVE "READY" TIME TO CLEAR  
  
:"READY" FAILED TO CLEAR  
:NOTE: THIS ERROR OCCURRED ON THE  
:2ND DRIVE. I.E. IF TESTING DRIVE A  
:THEN DRIVE B FAILED  
:WAIT FOR "READY" TO CLEAR  
  
:SELECT 1ST DRIVE  
:WRITE WHILE THE OTHER DRIVE IS "REWINDING"  
:WAIT ON "XFER REQ"  
:WRITE 1ST BYTE  
:WAIT ON "XFER REQ"  
:WRITE 2ND BYTE  
:WAIT ON "XFER REQ"  
:WRITE "CRC" & SHUT DOWN  
:WAIT ON "READY"  
:ANY ERROR?  
:BR IF NO  
:ERROR OCCURRED DURING "WRITE"  
:POSITION TAPE FOR "READ"  
:WAIT ON "READY"  
:ANY ERROR?  
:BR IF NO  
:ERROR OCCURRED DURING "BSBG"  
:START A "READ"  
:WAIT ON "XFER REQ"  
:FIRST DATA PATTERN  
:PICKUP FIST BYTE  
:IS IT GOOD?  
:BR IF NO  
:1ST DATA BYTE IS BAD  
:WAIT ON "XFER REQ"  
:2ND DATA PATTERN  
:PICKUP 2ND BYTE  
:BR IF IT IS GOOD  
:2ND BYTE IS BAD  
:WAIT ON "XFER REQ"  
:SHUT DOWN  
:WAIT ON "READY"  
:ERROR?  
:BR IF NO  
:ERROR OCCURRED  
:POSITION FOR NEXT "READ"  
:WAIT ON "READY"  
:ERROR?  
:BR IF NO  
:ERROR OCCURRED DURING "BSBG"  
:SELECT 2ND DRIVE  
:WAIT ON "READY"  
:START A "REWIND"  
:SET THE TIMER

H05

```

012042 005201 :CS: INC R1 ;GIVE "READY" TIME TO CLEAR
012044 001001 BNE 11$;"READY" FAILED TO CLEAR
012046 104001 ERROR 11$;NOTE: THIS ERROR OCCURRED ON THE
 ;2ND DRIVE. I.E. IF TESTING DRIVE A
 ;WHEN DRIVE B FAILED
 ;WAIT FOR "READY" TO CLEAR
012050 032714 000040 :11$: BIT #READY,@TACS
012054 001372 BNE 10$
012056 010314 MOV DRIVE,@TACS ;SELECT 1ST DRIVE
012060 104412 WAITREADY ;WAIT ON "READY"
012062 112714 000005 MOV #READ!GO,@TACS ;"READ" WHILE OTHER DRIVE IS "REWINDING"
012066 104413 WAITXFER ;WAIT ON "XFER REQ"
012070 012700 000037 MOV #377,R0 ;1ST DATA PATTERN
012074 011501 MOV @TADB,R1 ;READ 1ST BYTE
012076 020001 CMP R0,R1 ;IS IT GOOD?
012100 001401 BEQ 12$
012102 104005 ERROR 5 ;BR IF NO
 ;1ST BYTE FAILED
012104 104413 WAITXFER ;WAIT ON "XFER REQ"
012106 005000 CLR R0 ;2ND DATA PATTERN
012110 011501 MOV @TADB,R1 ;READ 2ND BYTE
012112 001401 BEQ 13$
012114 104005 ERROR 5 ;BR IF IT IS GOOD
 ;2ND BYTE FAILED
012116 104413 WAITXFER ;WAIT ON "XFER REQ"
012120 052714 000020 BIS #ILBS,@TACS ;SHUT DOWN
012124 104412 WAITREADY ;WAIT ON "READY"
012126 005714 TST @TACS ;ERROR?
012130 100001 BPL TST41 ;;BR IF NO
012132 104001 ERROR 1 ;ERROR OCCURRED
;*****
;*TEST 41 END OF TEST CODE
;*****
*ST41: SCOPE
 MOV #1,$TIMES ;;DO 1 ITERATION
 RESET
 MOV DRIVE,@TACS ;SELECT DRIVE
 MOV #REWIND!GO,@TACS
 WAITREADY
.SETTL END OF PASS ROUTINE
;*****
;*INCREMENT THE PASS NUMBER ($PASS)
;*TYPE "END PASS"
;*IF THERES A MONITOR GO TO IT
;*IF THERE ISN'T JUMP TO START
;*IF IT IS DESIRED TO HAVE A BELL INDICATE THE "END OF PASS" LOCATION
;*$ENDMG CAN BE CHANGED TO 7.
$EOP: SCOPE
 CLR $STNM ;;ZERO THE TEST NUMBER
 CLR $TIMES ;;ZERO THE NUMBER OF ITERATIONS
 INC $PASS ;;INCREMENT THE PASS NUMBER
 BIC #100000,$PASS ;DON'T ALLOW A NEG. NUMBER
 DEC (PC)+ ;;LOOP?
$EOPCT: .WORD 1
 BGT $DOAGN ;;YES
012156 000004
012156 005067 166716 CLR $STNM
012160 005067 166776 CLR $TIMES
012164 005267 166704 INC $PASS
012170 042767 100000 166676 BIC #100000,$PASS
012202 005327 DEC (PC)+
012204 000001 .WORD 1
012206 000015 BGT $DOAGN

```



.SSTL SCOPE HANDLER ROUTINE

```

*THIS ROUTINE CONTROLS THE LOOPING OF SUBTESTS. IT WILL INCREMENT
*AND LOAD THE TEST NUMBER($TSTNM) INTO THE DISPLAY REG.(DISPLAY'7:0')
*AND LOAD THE ERROR FLAG ($ERFLG) INTO DISPLAY'15:08'
*THE SWITCH OPTIONS PROVIDED BY THIS ROUTINE ARE:
*SW14=1 LOOP ON TEST
*SW11=1 INHIBIT ITERATIONS
*SW09=1 LOOP ON ERROR
*SW08=1 LOOP ON TEST IN SWR<7:0>
*CALL
* SCOPE ::SCOPE=IOT

```

```

$SCOPE:
CKSWR ;;TEST FOR CHANGE IN SOFT-SWR
 ;;LOOP ON PRESENT TEST?
 ;;YES IF SW14=1
 ;;TESTER*****
 ;;IF RUNNING ON THE "XOR" TESTER CHANGE
 ;;THIS INSTRUCTION TO A "NOP" (NOP=240)
 ;;SAVE THE CONTENTS OF THE ERROR VECTOR
 ;;SET FOR TIMEOUT
 ;;TIME OUT ON XOR?
 ;;RESTORE THE ERROR VECTOR
 ;;GO TO THE NEXT TEST
 ;;CLEAR THE STACK AFTER A TIME OUT
 ;;RESTORE THE ERROR VECTOR
 ;;LOOP ON THE PRESENT TEST
 ;;TESTER*****
 ;;LOOP ON SPEC. TEST?
 ;;BR IF NO
 ;;ON THE RIGHT TEST? SWR:7:0\
 ;;BR IF YES
 ;;HAS AN ERROR OCCURRED?
 ;;BR IF NO
 ;;MAX. ERRORS FOR THIS TEST OCCURRED?
 ;;BR IF NO
 ;;LOOP ON ERROR?
 ;;BR IF NO
 ;;SET LOOP ADDRESS TO LAST SCOPE
 ;;ZERO THE ERROR FLAG
 ;;CLEAR THE NUMBER OF ITERATIONS TO MAKE
 ;;ESCAPE TO THE NEXT TEST
 ;;INHIBIT ITERATIONS?
 ;;BR IF YES
 ;;IF FIRST PASS OF PROGRAM
 ;;INHIBIT ITERATIONS
 ;;INCREMENT ITERATION COUNT
 ;;CHECK THE NUMBER OF ITERATIONS MADE
 ;;BR IF MORE ITERATION REQUIRED
 ;;REINITIALIZE THE ITERATION COUNTER
 ;;SET NUMBER OF ITERATIONS TO DO
 ;;COUNT TEST NUMBERS
 ;;SAVE SCOPE LOOP ADDRESS

```

```

012264
012264 104406
012266 032777 040000 166544
012274 001111
012276 000416
012300 013746 000004
012304 012737 012324 000004
012312 005737 177060
012316 012637 000004
012322 000463
012324 022626
012326 012637 000004
012332 000423
012334
012334 032777 000400 166576
012342 001404
012344 127767 166570 166530
012352 001462
012354 105767 166523
012360 001421
012362 126767 166527 166513
012370 101015
012372 032777 001000 166540
012400 001404
012402 016767 166502 166476
012410 000443
012412 105067 166465
012416 005067 166544
012422 000415
012424 032777 004000 166506
012432 001011
012434 005767 166440
012440 001406
012442 005267 166436
012446 026767 166514 166430
012454 002021
012456 012767 000001 166420
012464 016767 000044 166474
012472 105267 166404
012476 011667 166404

```

|        |        |        |        |        |                |      |
|--------|--------|--------|--------|--------|----------------|------|
| 000004 | 012502 | 011567 | 166402 |        |                | MOV  |
| 000005 | 012506 | 005067 | 166456 |        |                | CLR  |
| 000006 | 012512 | 112767 | 000001 | 166375 |                | MOVB |
| 000007 | 012520 | 016777 | 166356 | 166414 | \$OVER:        | MOV  |
| 000008 | 012526 | 016716 | 166354 |        |                | MOV  |
| 000009 | 012532 | 000002 |        |        |                | RTI  |
| 000010 | 012534 | 003720 |        |        | \$MXCNT: 2000. |      |

```

(SP), $LPERR :: SAVE ERROR LOOP ADDRESS
$ESCAPE :: CLEAR THE ESCAPE FROM ERROR ADDRESS
#1, $SERMAX :: ONLY ALLOW ONE(1) ERROR ON NEXT TEST
$STNM, @DISPLAY :: DISPLAY TEST NUMBER
$LPADR, (SP) :: FUDGE RETURN ADDRESS
 :: FIXES PS
 :: MAX. NUMBER OF ITERATIONS

```

.SBTTL ERROR HANDLER ROUTINE

```

*THIS ROUTINE WILL INCREMENT THE ERROR FLAG AND THE ERROR COUNT,
*SAVE THE ERROR ITEM NUMBER AND THE ADDRESS OF THE ERROR CALL
*AND GO TO TYPERR ON ERROR
*THE SWITCH OPTIONS PROVIDED BY THIS ROUTINE ARE:
*SW15=1 HALT ON ERROR
*SW13=1 INHIBIT ERROR TYPEOUTS
*SW10=1 BELL ON ERROR
*SW09=1 LOOP ON ERROR
*CALL ERROR N ;;ERROR=EMT AND N=ERROR ITEM NUMBER

```

```

$ERROR: CKSWR ;;TEST FOR CHANGE IN SOFT-SWR
 MOV @TACS,@#$REG0 ;SAVE THE STATUS REG.
 MOV @TADB,@#$REG1 ;SAVE THE DATA BUFFER
 MOV @R0,@#$GDDAT ;R0 WILL CONTAIN THE GOOD DATA
 MOV @R1,@#$BDDAT ;R1 WILL CONTAIN THE BAD DATA
7$: INCB $ERFLG ;;SET THE ERROR FLAG
 BEQ 7$;;DON'T LET THE FLAG GO TO ZERO
 MOV $STNM,@DISPLAY ;DISPLAY TEST NUMBER AND ERROR FLAG
 BIT #BIT10,@SWR ;BELL ON ERROR?
 BEQ 1$;NO - SKIP
 TYPE $BELL ;RING BELL
1$: INC $ERTTL ;COUNT THE NUMBER OF ERRORS
 MOV (SP),$ERRPC ;GET ADDRESS OF ERROR INSTRUCTION
 SUB #2,$ERRPC
 MOV8 @ERRPC,$ITEMB ;;STRIP AND SAVE THE ERROR ITEM CODE
 BIT #BIT13,@SWR ;;SKIP TYPEOUT IF SET
 BNE 20$;;SKIP TYPEOUTS
 JSR PC,TYPERR ;;GO TO USER ERROR ROUTINE
 TYPE ,SCLF
20$: 23$: TST @SWR ;;HALT ON ERROR
 BPL 3$;;SKIP IF CONTINUE
 HALT ;HALT ON ERROR!
 CKSWR ;TEST FOR CHANGE IN SOFT-SWR
3$: BIT #BIT09,@SWR ;LOOP ON ERROR SWITCH SET?
 BEQ 4$;BR IF NO
 MOV $LPERR,(SP) ;FUDGE RETURN FOR LOOPING
 TST $ESCAPE ;CHECK FOR AN ESCAPE ADDRESS
 BEQ 5$;BR IF NONE
 MOV $ESCAPE,(SP) ;FUDGE RETURN ADDRESS FOR ESCAPE
5$: CMP #$ENDAD,@#42 ;;ACT-11 AUTO-ACCEPT?
 BNE 6$;;BRANCH IF NO
 HALT ;YES
6$: RTI ;;RETURN

```

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012536 104406
012536 011437 001162
012540 011537 001164
012544 010037 001124
012550 010137 001126
012560 105267 166317
012564 001775
012566 016777 166310 166346
012574 032777 002000 166336
012602 001402
012604 104401 001172
012610 005267 166276
012614 011667 166276
012620 162767 000002 166270
012626 117767 166264 166260
012634 032777 020000 166276
012642 001004
012644 004767 000060
012650 104401 001177
012654
012654 005777 166260
012660 100002
012662 000000
012664 104406
012666 032777 001000 166244
012674 001402
012676 016716 166206
012702 005767 166262
012706 001402
012710 016716 166254
012714
012714 022737 012232 000042
012722 001001
012724 000000
012726
012726 000002

```



```

2383 ;:*****
2384 ;:THIS ROUTINE WILL TYPEOUT THE ERROR MESSAGES
2385
2386 012733 104401 001177 TYPERR: TYPE $CRLF ;TYPE A CARRIAGE RETURN & LINE FEED
2387 012734 010046 MOV RD, -(SP) ;SAVE RD
2388 012736 113700 001114 MOVB @#$ITEMB, RD ;PICKUP THE ITEM INDEX
2389 012742 005300 DEC RD ;ADJUST THE INDEX
2390 012744 006300 ASL RD ;SO IT WILL WORK FOR
2391 012746 006300 ASL RD ;THE ERROR TABLE
2392 012750 005300 ASL RD
2393 012752 062700 001236 ADD #$ERRTB, RD ;FORM THE TABLE POINTER
2394 012756 012067 000002 MOV (RD)+, 1$;PICKUP "ERROR MESSAGE" POINTER
2395 012762 104401 TYPE "ERROR MESSAGE"
2396 012764 000000 1$: 0 ;"ERROR MESSAGE POINTER" GOES HERE
2397 012766 104401 001177 TYPE $CRLF
2398 012772 012067 000004 MOV (RD)+, 2$;PICKUP "DATA HEADER" POINTER
2399 012776 001404 BEQ 3$;IF "0" DON'T TYPE
2400 013000 104401 TYPE "DATA HEADER"
2401 013002 000000 2$: 0 ;"DATA HEADER" POINTER GOES HERE
2402 013004 104401 001177 TYPE $CRLF
2403 013010 012000 3$: MOV (RD)+, RD ;PICKUP "DATA POINTER"
2404 013012 001004 BNE 5$;IF THERE IS DATA TO TYPE GO DO IT
2405 013014 012600 4$: MOV (SP)+, RD ;RESTORE RD
2406 013016 104401 001177 TYPE $CRLF ;TYPE A CARRIAGE RETURN&LINE FEED
2407 013022 000207 RTS PC ;RETURN
2408 013024 5$:
2409 013024 013046 MOV @ (RD)+, -(SP) ;:SAVE @ (RD)+ FOR TYPEOUT
2410
2411 013026 104402 TYPDC ;:TYPE DATA
2412 013030 005710 TST (RD) ;:GO TYPE--OCTAL ASCII(ALL DIGITS)
2413 013032 001770 BEQ 4$;TERMINATOR?
2414 013034 104401 013042 TYPE 6$;BR IF YES
2415 013040 000771 BR 5$;TYPE 2 SPACES
2416 013042 020040 000 6$: .ASCIZ / / ;LOOP
2417 013046 .EVEN ;ASCII STRING OF 2 SPACES

```

```

2418 ;:*****
2419 ;ROUTINE TO WAIT ON THE READY BIT
2420
2421 013046 WAIT.ON.READY:
2422 013046 005067 000044 CLR WAIT2 ;SETUP MAX. TIME TO WAIT ON "READY"
2423 013052 015767 000072 000044 MOV MAXCNT,HGHTIM
2424 013060 012637 001202 MOV (SP)+, @#SAVPC ;GET THE PC OF THE WAITREADY INSTRUCTION
2425 013064 162737 000002 001202 SUB #2, @#SAVPC
2426 013072 012637 001204 MOV (SP)+, @#SAVPS ;SAVE THE PS
2427 01307E 052714 000040 WAIT1: BIT #READY, @TACS ;READY=1?
2428 013102 001013 BNE WAIT3 ;GO ON IF YES
2429 013104 105714 TSTB @TACS ;CHECK TRANSFER REQUEST
2430 013106 100002 SPL WAIT4
2431 013110 104004 ERROR 4 ;"TRANSFER REQUEST" SET WHILE WAITING ON "READY"
2432 013112 000407 BR WAIT3
2433 013114 005227 WAIT4: INC (PC)+ ;COUNT FAST COUNTER
2434 013116 000000 WAIT2: 0
2435 013120 001366 BNE WAIT1 ;GO CHECK "READY" AGAIN
2436 013122 005327 WAIT2: DEC (PC)+ ;COUNT LOOP COUNTER
2437 013124 000000 HGHTIM: 0
2438 013126 003363 BGT WAIT1 ;GO LOOP AGAIN
2439 013130 104002 ERROR 2 ;"READY" FAILED TO SET
2440 013132 013746 001204 WAIT3: MOV @#SAVPS, -(SP) ;GET THE STATUS BACK
2441 013136 013746 001202 MOV @#SAVPC, -(SP) ;GET THE PC
2442 013142 062716 000002 ADD #2, (SP)
2443 013146 000002 RTI
2444 013150 000000 MAXCNT: 0
2445
2446 ;:*****
2447 ;ROUTINE TO WAIT ON "RANSFER REQUEST"
2448
2449 013152 WAIT.FOR.XFER.REQ:
2450 013152 005067 000044 CLR 2$;SETUP WASTE TIME LOOP
2451 013156 013767 013150 000044 MOV @#MAXCNT, 3$
2452 013164 012637 001202 MOV (SP)+, @#SAVPC ;GET THE PC OF THE WAITXFER INSTRUCTION
2453 013170 162737 000002 001202 SUB #2, @#SAVPC
2454 013176 012637 001204 MOV (SP)+, @#SAVPS ;SAVE THE PS
2455 013202 105714 1$: TSTB @TACS ;CHECK XFER REQ
2456 013204 100414 BMI 4$;EXIT IF SET
2457 013206 032714 000040 BIT #READY, @TACS ;LOOK AT READY
2458 013212 001402 BEQ 5$;BR IF "READY" ISN'T SET
2459 013214 104004 ERROR 4 ;"READY" SET WHILE WAITING FOR "XFER REQ"
2460 013216 000407 BR 4$
2461 013220 005227 5$: INC (PC)+ ;COUNT
2462 013222 000000 2$: 0
2463 013224 001366 BNE 1$;BR IF MORE TO DO
2464 013226 005327 DEC (PC)+
2465 013230 000000 3$: 0
2466 013232 003363 BGT 1$
2467 013234 104003 ERROR 3 ;"TRANSFER REQUEST" FAILED TO SET
2468 013236 013746 001204 4$: MOV @#SAVPS, -(SP) ;GET THE STATUS BACK
2469 013242 013746 001202 MOV @#SAVPC, -(SP) ;GET THE PC
2470 013246 062716 000002 ADD #2, (SP)
2471 013252 000002 RTI ;GO BACK

```

\*\*\*\*\*

.SBTTL ROUTINE TO ASK THE OPERATOR WHAT DRIVE(S) TO TEST

:CALL  
:JSR PC,@ASKDRV  
:RETURN ;NOTE: R0 AND R1 ARE DESTROYED

```

ASKDRV: TYPE MSGDRV ;<CR LF>"DRIVE(S)"
CLR DRVKEY ;GO GET A DRIVE
RDLIN ;SETUP TO CHECK FOR VALID DRIVE(S)
MOV (SP)+,R0 ;WAS A DRIVE SELECTED?
TSTB JRO ;BR IF NO
BEQ NOTLGL ;WAS DRIVE "A" SELECTED?
MOV #DRVKEY,R1 ;BR IF NO
LOOP: CMPB #'A,JRO ;SET KEY FOR DRIVE "A"
BNE NOTA ;WAS DRIVE "B" SELECTED?
MOVB (R0)+,(R1)+ ;BR IF NO
BR NEXT ;SET KEY FOR DRIVE "B"
NOTA: CMPB #'B,JRO ;WAS A COMMA TYPED?
BNE NOTB ;BR IF NO
MOVB (R0)+,(R1)+ ;DUMP THE COMMA
BR NEXT ;TERMINATOR?
NOTB: CMPB #54,JRO ;BR IF YES
BNE NOTLGL ;TWO DRIVES SELECTED?
TSTB (R0)+ ;BR IF NO
JRO LOOP ;ILLEGAL INPUT DETECTED
BEQ EXIT ;GO TRY AGAIN
CMP #DRVKEY+2,R1 ;ANY DRIVE SELECTED?
BHI LOOP ;BR IF NO
NOTLGL: TYPE ,SQUES ;ILLEGAL INPUT DETECTED
BR ASKDRV ;GO TRY AGAIN
EXIT: TST DRVKEY ;ANY DRIVE SELECTED?
BEQ NOTLGL ;BR IF NO
RTS PC

```

\*\*\*\*\*

:CALL  
:JSR PC,@ASKADR

```

ASKADR: MOV R0,-(SP) ;SAVE R0
IS: TYPE ,MSGASK ;"TACS?"
RDOCT ;GET VALUE
MOV (SP)+,R0 ;PICK UP THE OCTAL NUMBER
BEQ 35 ;IF "0" USE OLD VALUES
CMP R0,#160000 ;MAKE SURE IT IS A BUS ADDRESS
BLO IS
MOV R0,@TACSL ;SAVE TCE TACS
ADD #2,R0 ;STEP TO TADB ADDRESS
MOV R0,@TADBL ;AND SAVE IT
MOV @TACSL,@TACSH ;SETUP TACS UPPER
INC @TACSH ;BYTE POINTER

013434 013737 001212 001214 MOV @TADBL,@TADBH ;SETUP TADB UPPER
013442 005237 001214 ;BYTE POINTER
013446 104401 016770 35: TYPE ,MSGVEC ;"VECTOR?"

```

```

013325 104401 016744
013326 105357 165740
013327 104410
013328 012600
013329 105710
013330 001425
013331 012701 001224
013332 122710 000101
013333 001002
013334 112021
013335 000411
013336 122710 000102
013337 001002
013338 112021
013339 000404
013340 122710 000354
013341 001006
013342 105720
013343 105710
013344 001406
013345 022701 001226
013346 101355
013347 104401 001176
013348 000740
013349 005767 165644
013350 001772
013351 000207
013364 010046
013366 104401 016760
013372 104411
013374 012600
013376 001423
013400 020027 160000
013404 103770
013406 010037 001206
013412 062700 000002
013416 010037 001212
013422 013737 001206 001210
013430 005237 001210
013434 013737 001212 001214
013442 005237 001214
013446 104401 016770

```

```

013452 104411 RDOCT
013454 012600 MOV (SP)+,RO
013456 001411 BEQ SS
013460 020027 001000 CMP RO,#1000 ;MAKE SURE ADDRESS IS IN VECTOR AREA
013464 103370 BHS JS
013466 010037 001216 MOV RO,08TAVEC ;SAVE AS VECTOR ADDRESS
013470 062700 000002 ADD #2,RO
013474 010037 001220 MOV RO,08TAVEC+2
013502 104401 017000 ES: TYPE ,MSGPRI ;ASK FOR PRIORITY
013506 104411 RDOCT
013510 012600 MOV (SP)+,RO
013512 001413 BEQ SS ;IF "0" USE OLD VALUE
013514 020027 000007 CMP RO,#7 ;MAKE SURE ITS VALID
013520 101370 BHI SHAB
013524 006200 ASR RO ;PUT INTO HIGH BYTE
013526 006200 ASR RO ;AND SHIFT
013530 006200 ASR RO ;INTO PROPER
013532 042700 177437 BIC #1C(340),RO ;POSITION
013536 010037 001222 MOV RO,08TAPRIO ;SAVE ONLY PRIORITY BITS
013542 104401 017012 ES: TYPE ,TACS ;STORE IT AWAY
013546 016716 165434 MOV TACSL,-(SP) ;TACS="
013552 104402 TYPCC ;::SAVE TACSL FOR TYPEOUT
013554 174401 017020 TYPE ,TADB ;::GO TYPE--OCTAL ASCII(ALL DIGITS)
013560 016746 165426 MOV TADBL,-(SP) ;TADB="
013564 104402 TYPCC ;::SAVE TADBL FOR TYPEOUT
013566 104401 017027 TYPE ,MTAVEC ;::GO TYPE--OCTAL ASCII(ALL DIGITS)
013572 016746 165420 MOV TAVEC,-(SP) ;TAVEC="
013576 104402 TYPCC ;::SAVE TAVEC FOR TYPEOUT
013600 104401 017040 TYPE ,MTAPRI ;::GO TYPE--OCTAL ASCII(ALL DIGITS)
013604 016746 165412 MOV TAPRIO,-(SP) ;TAPRIO="
013610 104402 TYPCC ;::SAVE TAPRIO FOR TYPEOUT
013612 104401 017053 TYPE ,MSGOK ;::GO TYPE--OCTAL ASCII(ALL DIGITS)
013616 104407 RDCHR ;"OK?"
013620 012600 MOV (SP)+,RO ;GO READ ONE CHARACTER
013622 022700 000015 CMP #15,RO ;GET IT
013626 001406 BEQ 7$;IS IT "CR"?
013630 022700 00013: BCC 7$;BRANCH IF YES
013634 001403 CMP #Y,RO ;IS IT "Y"?
013636 104401 001176 BEQ 7$;IT WAS
013642 000651 BR 1$;TYPE "?"
013644 104401 017061 7$: TYPE ,MYES ;AND LET HIM CORRECT THEM
013650 012600 MOV (SP)+,RO ;TYPE OUT "YES"
013652 000207 RTS PC ;RESTORE RO
;AND RETURN

```

ROUTINE TO CALCULATE THE CRC  
MAINDEC-11-DITAB-C

```

013654 000000
013655 000000
013656 000000
013657 000000
013658 000000
013659 000000
013660 000000
013661 000000
013662 000000
013663 000000
013664 000000
013665 000000
013666 000000
013667 000000
013668 000000
013669 000000
013670 000000
013671 000000
013672 000000
013673 000000
013674 000000
013675 000000
013676 000000
013677 000000
013678 000000
013679 000000
013680 000000
013681 000000
013682 000000
013683 000000
013684 000000
013685 000000
013686 000000
013687 000000
013688 000000
013689 000000
013690 000000
013691 000000
013692 000000
013693 000000
013694 000000
013695 000000
013696 000000
013697 000000
013698 000000
013699 000000
013700 000000
013701 000000
013702 000000
013703 000000
013704 000000
013705 000000
013706 000000
013707 000000
013708 000000
013709 000000
013710 000000
013711 000000
013712 000000
013713 000000
013714 000000
013715 000000
013716 000000
013717 000000
013718 000000
013719 000000
013720 000000
013721 000000
013722 000000
013723 000000
013724 000000
013725 000000
013726 000000
013727 000000
013728 000000
013729 000000
013730 000000
013731 000000
013732 000000
013733 000000
013734 000000
013735 000000
013736 000000
013737 000000
013738 000000
013739 000000
013740 000000
013741 000000
013742 000000
013743 000000
013744 000000
013745 000000
013746 000000
013747 000000
013748 000000
013749 000000
013750 000000
013751 000000
013752 000000
013753 000000
013754 000000
013755 000000
013756 000000
013757 000000
013758 000000
013759 000000
013760 000000
013761 000000
013762 000000
013763 000000
013764 000000
013765 000000
013766 000000
013767 000000
013768 000000
013769 000000
013770 000000
013771 000000
013772 000000
013773 000000
013774 000000

```

```

: THIS ROUTINE WILL CALCULATE THE CRC
: CALL:
: JSR PC, DC, CRC ; RD=1 BYTE OF DATA

DC, CRC:
MOV R0, -(SP) ; PUSH R0 ON STACK
MOV R1, -(SP) ; PUSH R1 ON STACK
MOV R2, -(SP) ; PUSH R2 ON STACK
MOV R3, -(SP) ; PUSH R3 ON STACK
MOV R4, -(SP) ; PUSH R4 ON STACK
MOV #8, R5 ; MAKE EIGHT ITERATIONS
MOV CRC, W, R3 ; PICKUP THE CRC WORD

15:
CLR R1
MOV R3, R2 ; GET THE PARTIAL CRC WORD
BIC #1, BIT0, R2 ; STRIP AWAY EVERYTHING BUT BIT00
ROR R0 ; PULL OFF THIS BIT
ROL R1 ; AND SETUP TO XOR IT
MOV R1, R4 ; FORM THE XOR OF "R1" AND "R2"
BIC R2, R4
BIC R1, R2
BIS R4, R2 ; RESULTS TO "R2"
ROR R2
BCC #3, R3
MOV #BIT14!BIT01, R1
MOV R1, R4 ; FORM THE XOR OF "R1" AND "R3"
BIC R2, R4
BIC R1, R3
BIS R4, R3 ; RESULTS TO "R3"

25:
POR R3
DEC (PC)+

35:
O
BGT #8, R5 ; BR IF MORE BITS TO DO
MOV R3, CRC, W
MOV (SP)+, R4
MOV (SP)+, R3
MOV (SP)+, R2
MOV (SP)+, R1
MOV (SP)+, R0
RTS PC

CRC, W: 0 ; CRC WORD

```

```

//
// THE FOLLOWING ROUTINES CAN BE USED TO MAKE ADJUSTMENTS TO THE TUEO
// NOTE: *** BEFORE USING ANY OF THE ROUTINES LOAD AND START AT 214 ***
//

```

```

:*****

```

```

: WRITE FILE GAPS FROM "BOT" TO "EOT"
: START AT 220
: THIS ROUTINE CAN BE USED TO ADJUST THE "WRITE GAP MONO" AND
: THE "WRITE DELAY MONO".
:*****

```

```

2600 013776 012706 001100
2601 014002 013704 001206
2602 014006 013705 001212
2603 014012 000005
2604 014014 012737 013776 001100
2605 014022 004737 015002
2606 014026 010314
2607 014030 112714 000017
2608 014034 032714 000040
2609 014040 001775
2610 014042 112714 000001
2611 014046 104412
2612 014050 032714 020000
2613 014054 001772
2614 014056 000000
2615 014060 000746

```

```

WFGSUB: MOV #STACK, SP ;KEEP THE STACK OUT OF THE WAY
MOV #TACSL, TACS ;SETUP THE TAIL STATUS AND
MOV #TADBL, TADB ;DATA BUFFER REGISTERS
RESET ;RESET THE WORLD
MOV #WFGSUB, #SLPERR ;SETUP THE LOOP ON ERROR ADDRESS
JSR PC, #NXTDRV ;GO SETUP FOR NEXT DRIVE
MOV DRIVE, #TACS ;SELECT DRIVE
MOV #REWIND!GO, #TACS ;SEND TAPE TO "BOT"
100$: BIT #READY, #TACS ;WAIT ON READY
BEQ 100$
1$: MOV #WFG!GO, #TACS ;WRITE A FILE GAP
WAITREADY ;WAIT ON READY
BIT #LEADER, #TACS ;AT "CLEAR LEADER"?
BEQ 1$;BR IF NO
HALT ;STOP IF YES
BR WFGSUB ;LOOP ON CONT.

```

```

:*****

```

```

: WRITE CONTINUOUS BLOCKS OF DATA
: START AT 224
: THE PROGRAM WILL HALT THREE(3) TIMES
: AFTER EACH HALT SET THE SWR AND PRESS CONTINUE
: HALT 1 --- SWR<7:0> = NUMBER OF BYTES PER BLOCK
: HALT 2 --- SWR<7:0> = PATTERN DESIRED
: HALT 3 --- SWR<15:0> = OPERATIONAL SWITCH SETTINGS
: THIS ROUTINE CAN BE USED TO ADJUST THE "GAP TIME MONO"
: ** IF USING SOFTWARE SWITCH REGISTER, AFTER
: EACH HALT OPERATOR WILL BE PROMPTED
: FOR THE VALUE WITH "SWR=XXXXXX NEW="
:*****

```

```

2660 014062 004737 014544
2661 014066 012706 001100
2662 014072 013704 001206
2663 014076 013705 001212
2664 014102 000005
2665 014104 012737 014066 001110
2666 014112 004737 015002
2667 014116 010314
2668 014120 112714 000017
2669 014124 032714 000040
2670 014130 001775

```

```

WRTSUB: JSR PC, #SETBUF ;GET BLOCK SIZE AND PATTERN
WLOOP: MOV #STACK, SP ;KEEP THE STACK OUT OF THE WAY
MOV #TACSL, TACS ;SETUP THE TAIL STATUS AND
MOV #TADBL, TADB ;DATA BUFFER REGISTERS
RESET ;RESET THE WORLD
MOV #WLOOP, #SLPERR ;SETUP THE LOOP ON ERROR ADDRESS
JSR PC, #NXTDRV ;GO SETUP FOR NEXT DRIVE
MOV DRIVE, #TACS ;SELECT DRIVE
MOV #REWIND!GO, #TACS ;SEND TAPE TO "BOT"
100$: BIT #READY, #TACS ;WAIT ON READY
BEQ 100$

```

```

014133 004737 014550 :S: JSR PC, @WRTBLK ;WRITE A BLOCK
014134 032714 020000 BIT @LEADER, @TACS ;AT "CLEAR LEADER"
014143 001775 BEQ IS ;BR IF NO
014144 000000 HALT ;STOP IF "EOT"
014146 000747 BR WLOOP ;LOOP IF CONT.

:*****
: READ CONTINUOUS BLOCKS OF DATA
: START AT 230
: THIS ROUTINE CAN BE USED TO ADJUST THE "SIGNAL MONO"
: AND THE "THRESHOLD POT".
:*****
RDSUB: MOV @STACK, SP ;KEEP THE STACK OUT OF THE WAY
MOV @TACSL, TACS ;SETUP THE TAIL STATUS AND
MOV @TADBL, TADB ;DATA BUFFER REGISTERS
RESET ;RESET THE WORLD
MOV @RDSUB, @SLPERR ;SETUP THE LOOP ON ERROR ADDRESS
JSR PC, @NXTDRV ;GO SETUP FOR NEXT DRIVE
MOV DRIVE, @TACS ;SELECT DRIVE
MOVB @REWIND!GO, @TACS ;SEND TAPE TO "BOT"
100$: BIT @READY, @TACS ;WAIT ON READY
BEQ 100$
IS: JSR PC, @RDBLK ;READ A BLOCK
BIT @LEADER, @TACS ;AT "CLEAR LEADER"?
BNE RDSUB ;BR IF YES---LOOP
BR IS

:*****
: WRITE A FILE GAP AND A BLOCK OF DATA FROM BOT TO EOT
: START AT 234
: THE PROGRAM WILL HALT THREE(3) TIMES
: AFTER EACH HALT SET THE SWR AND PRESS CONTINUE
: HALT 1 --- SWR<7:0> = NUMBER OF BYTES PER BLOCK
: HALT 2 --- SWR<7:0> = PATTERN DESIRED
: HALT 3 --- SWR<15:0> = OPERATIONAL SWITCH SETTINGS
: ** IF USING SOFTWARE SWITCH REGISTER, AFTER
: EACH HALT OPERATOR WILL BE PROMPTED
: FOR THE VALUE WITH "SWR=XXXXXX NEW="
: AND THE "GAP TIME MONO".
: THIS ROUTINE SUPPORTS THE S/W SWITCH REG FUNCTIONS
:*****
WGPBLK: JSR PC, @SETBUF ;GET BLOCK SIZE AND PATTERN
WGBL0P: MOV @STACK, SP ;KEEP THE STACK OUT OF THE WAY
MOV @TACSL, TACS ;SETUP THE TAIL STATUS AND
MOV @TADBL, TADB ;DATA BUFFER REGISTERS
RESET ;RESET THE WORLD
MOV @WGBL0P, @SLPERR ;SETUP THE LOOP ON ERROR ADDRESS
JSR PC, @NXTDRV ;GO SETUP FOR NEXT DRIVE
MOV DRIVE, @TACS ;SELECT DRIVE
MOVB @REWIND!GO, @TACS ;SEND TAPE TO "BOT"
100$: BIT @READY, @TACS ;WAIT ON READY
BEQ 100$
IS: MOVB @WFG!GO, @TACS ;WRITE A FILE GAP
WAITREADY ;WAIT ON READY

```

|        |        |        |
|--------|--------|--------|
| 014306 | 032714 | 020000 |
| 014308 | 001005 |        |
| 014310 | 004737 | 014560 |
| 014312 | 032714 | 020000 |
| 014314 | 001775 |        |
| 014316 | 000000 |        |
| 014318 | 000000 |        |
| 014320 | 000000 |        |
| 014322 | 000000 |        |
| 014324 | 000000 |        |
| 014326 | 000000 |        |
| 014328 | 000000 |        |
| 014330 | 000000 |        |

25:

```

BIT #LEADER,@TACS ;AT "CLEAR LEADER"?
BNE @S ;BR IF YES
JSR PC,@WRTBLK ;WRITE A BLOCK
BIT #LEADER,@TACS ;AT "CLEAR LEADER"?
BEQ @S ;BR IF NO
HALT ;STOP AT "EOT"
BR WGBLOP ;START OVER ON CONT.

```

```

: READ A BLOCK OF DATA AND A FILE GAP
: START AT 240
: THIS ROUTINE IS USED AFTER "WRITE A BLOCK AND A FILE GAP" ROUTINE
: IT CAN BE USED TO ADJUST THE "SIGNAL MONO", THE THRESHOLD POT"
: AND THE "TAPE BLANK MONO".

```

|        |        |        |
|--------|--------|--------|
| 014332 | 012706 | 001100 |
| 014336 | 013704 | 001206 |
| 014342 | 013705 | 001212 |
| 014346 | 000005 |        |
| 014350 | 012737 | 014332 |
| 014356 | 004737 | 015002 |
| 014362 | 010314 |        |
| 014364 | 112714 | 000017 |
| 014370 | 032714 | 000040 |
| 014374 | 001775 |        |
| 014376 | 004737 | 014722 |
| 014402 | 032714 | 020000 |
| 014406 | 001351 |        |
| 014410 | 112714 | 000015 |
| 014414 | 104412 |        |
| 014416 | 032714 | 020000 |
| 014422 | 001343 |        |
| 014424 | 000764 |        |

```

RGPBLK: MOV #STACK, SP ;KEEP THE STACK OUT OF THE WAY
MOV @TACSL,TACS ;SETUP THE TAIL STATUS AND
MOV @TADBL,TADB ;DATA BUFFER REGISTERS
RESET ;RESET THE WORLD
MOV #RGPBLK,@SLPERR ;SETUP THE LOOP ON ERROR ADDRESS
JSR PC,@NXTDRV ;GO SETUP FOR NEXT DRIVE
MOV DRIVE,@TACS ;SELECT DRIVE
MOVB #REWIND!GO,@TACS ;SEND TAPE TO "BOT"
100$: BIT #READY,@TACS ;WAIT ON READY
BEQ 100$
1$: JSR PC,@RDBLK ;READ A BLOCK OF DATA
BIT #LEADER,@TACS ;AT "CLEAR LEADER"?
BNE RGPBLK ;BR IF YES
MOVB #SFBG!GO,@TACS ;GET INTO A FILE GAP
WAITREADY
BIT #LEADER,@TACS ;AT "CLEAR LEADER"?
BNE RGPBLK ;BR IF YES
BR @S ;LOOP

```

```

: SPACE FORWARD FILE GAP FROM "BOT" TO "EOT"
: START AT 244
: THIS ROUTINE CAN BE USED AFTER "WRITE FILE GAP" FOR LOW SPEED
: SPACE FOWARD (TAPE BLANK MONO CAN BE ADJUSTED), OR AFTER READ OR
: WRITE A FILE GAP AND A BLOCK OF DATA FOR HIGH SPEED SPACE FORWARD
: (SIGNAL MONO CAN BE CHECKED).

```

|        |        |        |
|--------|--------|--------|
| 014426 | 012706 | 001100 |
| 014432 | 013704 | 001206 |
| 014436 | 013705 | 001212 |
| 014442 | 000005 |        |
| 014444 | 012737 | 014426 |
| 014452 | 004737 | 015002 |
| 014456 | 010314 |        |
| 014460 | 112714 | 000017 |
| 014464 | 032714 | 000040 |
| 014470 | 001775 |        |
| 014472 | 112714 | 000013 |
| 014476 | 104412 |        |

```

SFFGSB: MOV #STACK, SP ;KEEP THE STACK OUT OF THE WAY
MOV @TACSL,TACS ;SETUP THE TAIL STATUS AND
MOV @TADBL,TADB ;DATA BUFFER REGISTERS
RESET ;RESET THE WORLD
MOV #SFFGSB,@SLPERR ;SETUP THE LOOP ON ERROR ADDRESS
JSR PC,@NXTDRV ;GO SETUP FOR NEXT DRIVE
MOV DRIVE,@TACS ;SELECT DRIVE
MOVB #REWIND!GO,@TACS ;SEND TAPE TO "BOT"
100$: BIT #READY,@TACS ;WAIT ON READY
BEQ 100$
1$: MOVB #SFFG!GO,@TACS ;SPACE INTO A FILE GAP
WAITREADY ;WAIT ON READY

```





```

014650 013701 014654
014654 112711 000003
014670 104413
014672 032714 000040
014676 001010
014700 005302
014702 002405
014704 113714 014656
014710 000767
014712 052714 000020
014716 104412
014720 000207

014722 013702 014654
014726 013700 014656
014732 112714 000005
014736 104413
014740 032714 000040
014744 001012
014746 005302
014750 002405
014752 011501
014754 120001
014756 001767
014760 104005
014762 000406
014764 052714 000020
014770 104412
014772 005714
014774 100001
014776 104001
015000 000207

015002 105777 164132
015006 100416
015010 005003
015012 013701 001230
015016 122127 000101
015022 001402
015024 012703 000400
015030 105711
015032 001002
015034 012701 001224
015040 010137 001230
015044 000207

```

```

: WRITE ROUTINE FOR THE MANUAL OPERATIONS
:*****
:ATBLK: MOV @BLKLIM,R1 ;PICKUP THE BLOCK SIZE
MOV @WRITE!GO,@TACS ;START A WRITE
1$: WAITXFER ;WAIT ON TRANSFER REQUEST
BIT #READY,@TACS ;DID READY SET?
BNE 3$;BR IF YES
DEC R1 ;COUNT THIS REQUEST
BLT 2$;BR IF TIME FOR ILBS
MOVB @PATTRN,@TADB ;PUT DATA ON TAPE
BR 1$;LOOP
2$: BIS #ILBS,@TACS ;WRITE CRC AND SHUT DOWN
WAITREADY ;WAIT ON THE READY FLAG
3$: RTS PC

```

```

: READ ROUTINE FOR THE MANUAL OPERATIONS
:*****
:ROBLK: MOV @BLKLIM,R2 ;PICKUP THE BLOCK SIZE
MOV @PATTRN,R0 ;USE THIS DATA PATTERN TO COMPARE TO
MOVB #READ!GO,@TACS ;START A READ
1$: WAITXFER ;WAIT ON TRANSFER REQUEST
BIT #READY,@TACS ;IS READY SET?
BNE 3$;BR IF YES
DEC R2 ;COUNT THIS REQUEST
BLT 2$;BR IF TIME FOR ILBS
MOV @TADB,R1 ;READ THE DATA BUFFER
CMPB R0,R1 ;CHECK THE DATA
BEQ 1$;BR IF OK
ERROR 5 ;BAD DATA
BR 4$;GET OUT
2$: BIS #ILBS,@TACS ;READ ILBS
WAITREADY ;WAIT ON READY
3$: TST @TACS ;CHECK FOR ERROR
BPL 4$;BR IF NONE
ERROR 1 ;ERROR OCCURRED
4$: RTS PC ;RETURN

```

```

: ROUTINE TO CHANGE DRIVES
:*****
:NXTDRV: TSTB @SWR ;IS SW07 ON A (1)?
BMI 3$;BR IF YES
CLR DRIVE ;SET DRIVE TO "A"
MOV @DRVPT,R1 ;GET DRIVE POINTER
CMPB (R1)+,#'A ;IS IT DRIVE "A"?
BEQ 1$;BR IF YES
MOV #UNIT,DRIVE ;SET DRIVE TO "B"
1$: TSTB (R1) ;LAST DRIVE BEEN SELECTED
BNE 2$;BR IF NO
MOV #DRVKEY,R1 ;RESET DRIVE POINTER
2$: MOV R1,@DRVPT ;SAVE DRIVE POINTER FOR NEXT TIME
3$: RTS PC ;GO BACK

```

\*\*\*\*\*  
ROUTINE TO CHANGE DRIVES

\*\*\*\*\*

SBTTL ROUTINE TO EXAMINE DRIVE(S) FOR AVAILABILITY

CALL:

MOV #DRVKEY,RO  
JSR PC,@EXAM ;R1 IS DESTROYED  
NORMAL RETURN  
ERROR RETURN

015046  
015052  
015054  
015060  
015062  
015066  
015072  
015074  
015076  
015100  
015104  
015106  
015112  
015114  
015122  
015124  
015132  
015134  
015136  
015142  
015144  
015150

013701 001206  
005011  
122710 000101  
001402  
052711 000400  
032711 000040  
001775  
005711  
100024  
032711 001000  
001017  
032711 010000  
001411  
122777 000201 164016  
001412  
122777 000203 164006  
001406  
000402  
032711 020000  
001002  
062716 000002  
000207

EXAM: MOV @TACSL,R1 ;PICKUP THE "CONTROL & STATUS" REG. ADDR.  
CLR (R1) ;DRIVE="A" FUNCTION="WFG"  
CMPB #'A,(R0) ;EXAMINE DRIVE "A"?  
BEQ 1\$ ;BR IF YES  
BIS #UNIT,(R1) ;SELECT DRIVE "B"  
1\$: BIT #READY,(R1) ;WAIT ON READY  
BEQ 1\$  
TST (R1) ;ANY ERROR?  
BPL 4\$ ;BR IF NO  
BIT #OFFLINE,(R1) ;ERROR DUE TO "OFF LINE"?  
BNE 3\$ ;BR IF YES  
BIT #WRTLOCK,(R1) ;ERROR DUE TO "WRITE LOCK"?  
BEQ 2\$ ;BR IF NO  
CMPB #BIT07!BIT00,@SWR ;"READONLY" SELECTED? (RDIPAS.  
BEQ 4\$ ;BR IF YES  
CMPB #BIT07!BIT01!BIT00,@SWR ;(RD2PAS)?  
BEQ 4\$ ;BR IF YES  
BR 3\$ ;TAKE THE ERROR EXIT  
2\$: BIT #LEADER,(R1) ;ERROR DUE TO "CLEAR LEADER"?  
BNE 4\$ ;BR IF YES  
3\$: ADD #2,(SP) ;TAKE ERROR RETURN  
4\$: RTS PC ;RETURN

\*.SBTTL TYPE ROUTINE

```

*ROUTINE TO TYPE ASCIZ MESSAGE. MESSAGE MUST TERMINATE WITH A 0 BYTE.
*THE ROUTINE WILL INSERT A NUMBER OF NULL CHARACTERS AFTER A LINE FEED.
*NOTE1: $NULL CONTAINS THE CHARACTER TO BE USED AS THE FILLER CHARACTER.
*NOTE2: $FILLS CONTAINS THE NUMBER OF FILLER CHARACTERS REQUIRED.
*NOTE3: $FILLC CONTAINS THE CHARACTER TO FILL AFTER.

```

```

*CALL:
*1) USING A TRAP INSTRUCTION
* TYPE ,MESADR ;;MESADR IS FIRST ADDRESS OF AN ASCIZ STRING
*OR
* TYPE
* MESADR

```

2967  
2968  
2969  
2970  
2971  
2972  
2973  
2974  
2975  
2976  
2977  
2978  
2979  
2980

```

015152 105767 164001 $TYPE: TSTB $TFPLG ;; IS THERE A TERMINAL?
015156 100002 BPL 1$;; BR IF YES
015160 000000 HALT ;; HALT HERE IF NO TERMINAL
015162 000407 BR 3$;; LEAVE
015164 010046 1$: MOV RO, -(SP) ;; SAVE RO
015166 017600 000002 MOV 02(SP), RO ;; GET ADDRESS OF ASCIZ STRING
015172 112046 2$: MOVB (RO)+, -(SP) ;; PUSH CHARACTER TO BE TYPED ONTO STACK
015174 001005 BNE 4$;; BR IF IT ISN'T THE TERMINATOR
015176 005726 TST (SP)+ ;; IF TERMINATOR POP IT OFF THE STACK
015200 012600 60$: MOV (SP)+, RO ;; RESTORE RO
015202 062716 3$: ADD #2, (SP) ;; ADJUST RETURN PC
015206 000002 RTI ;; RETURN
015210 122716 4$: CMPB #HT, (SP) ;; BRANCH IF <HT>
015214 001430 BEQ 8$
015216 122716 000200 CMPB #CRLF, (SP) ;; BRANCH IF NOT <CRLF>
015222 001006 BNE 5$
015224 005726 TST (SP)+ ;; POP <CR><LF> EQUIV
015226 104401 TYPE ;; TYPE A CR AND LF
015230 001177 $CRLF
015232 105067 000130 CLRB $CHARCNT ;; CLEAR CHARACTER COUNT
015236 000755 BR 2$;; GET NEXT CHARACTER
015240 004767 000056 5$: JSR PC, $TYPEC ;; GO TYPE THIS CHARACTER
015244 126726 6$: CMPB $FILLC, (SP)+ ;; IS IT TIME FOR FILLER CHARS.?
015250 001350 BNE 2$;; IF NO GO GET NEXT CHAR.
015252 016746 163E76 MOV $NULL, -(SP) ;; GET # OF FILLER CHARS. NEEDED
;; AND THE NULL CHAR.
015256 105366 000001 7$: DECB 1(SP) ;; DOES A NULL NEED TO BE TYPED?
015262 002770 BLT 6$;; BR IF NO--GO POP THE NULL OFF OF STACK
015264 004767 000032 JSR PC, $TYPEC ;; GO TYPE A NULL
015270 105367 000072 DECB $CHARCNT ;; DO NOT COUNT AS A COUNT
015274 000770 BR 7$;; LOOP

```

;HORIZONTAL TAB PROCESSOR

```

015276 112716 000040 8$: MOVB #' (SP) ;; REPLACE TAB WITH SPACE
015302 004767 000014 9$: JSR PC, $TYPEC ;; TYPE A SPACE
015306 132767 000007 000052 BITB #7, $CHARCNT ;; BRANCH IF NOT AT
015314 001372 BNE 9$;; TAB STOP
015316 005726 TST (SP)+ ;; POP SPACE OFF STACK

```

TR: BASIC LOGIC TEST (PART 2) MAINDEC-11-DZTAB-C  
DZTABC.NEW TYPE ROUTINE

```

2981 015320 000724 BR 2$;;GET NEXT CHARACTER
2982 015322 105777 163622 $TYPEC: TSTB 3$TPS ;;WAIT UNTIL PRINTER IS READY
2983 015326 100375 BPL $TYPEC
2984 015330 116677 000002 163614 MOVB 2(SP),2$TPB ;;LOAD CHAR TO BE TYPED INTO DATA REG.
2985 015336 122766 000015 000002 CMPB *CR,2(SP) ;;IS CHARACTER A CARRIAGE RETURN?
2986 015344 001003 BNE 1$;;BRANCH IF NO
2987 015346 105067 000014 CLRB $CHARCNT ;;YES--CLEAR CHARACTER COUNT
2988 015352 000406 BR $TYPEX ;;EXIT
2989 015354 122766 000012 000002 1$: CMPB *LF,2(SP) ;;IS CHARACTER A LINE FEED?
2990 015362 001402 BEQ $TYPEX ;;BRANCH IF YES
2991 015364 105227 INCB (PC)+ ;;COUNT THE CHARACTER
2992 015366 000000 $CHARCNT: .WORD 0 ;;CHARACTER COUNT STORAGE
2993 015370 000207 $TYPEX: RTS PC

.SBTTL READ AN OCTAL NUMBER FROM THE TTY

;;*****
;;*THIS ROUTINE WILL READ AN OCTAL (ASCII) NUMBER FROM THE TTY AND
;;*CHANGE IT TO BINARY.
;;*CALL:
;;* RDOCT ;;READ AN OCTAL NUMBER
;;* RETURN HERE ;;LOW ORDER BITS ARE ON TOP OF THE STACK
;;* ;;HIGH ORDER BITS ARE IN $HIOCT

3005 015372 011646 000004 000002 $RDOCT: MOV (SP),-(SP) ;;PROVIDE SPACE FOR THE
3006 015374 016666 MOV 4(SP),2(SP) ;;INPUT NUMBER
3007 015402 010046 MOV R0,-(SP) ;;PUSH R0 ON STACK
3008 015404 010146 MOV R1,-(SP) ;;PUSH R1 ON STACK
3009 015406 010246 MOV R2,-(SP) ;;PUSH R2 ON STACK
3010 015410 104410 1$: RDLIN ;;READ AN ASCII LINE
3011 015412 012600 MOV (SP)+,R0 ;;GET ADDRESS OF 1ST CHARACTER
3012 015414 005001 CLR R1 ;;CLEAR DATA WORD
3013 015416 005002 CLR R2
3014 015420 112046 2$: MOVB (R0)+,-(SP) ;;PICKUP THIS CHARACTER
3015 015422 001412 BEQ 3$;;IF ZERO GET OUT
3016 015424 006301 ASL R1 ;;*2
3017 015426 006102 ROL R2 ;;*4
3018 015430 006301 ASL R1 ;;*8
3019 015432 006102 ROL R2
3020 015434 006301 ASL R1
3021 015436 006102 ROL R2
3022 015440 042716 177770 BIC *1C7,(SP) ;;STRIP THE ASCII JUNK
3023 015444 062601 ADD (SP)+,R1 ;;ADD IN THIS DIGIT
3024 015446 000764 BR 2$;;LOOP
3025 015450 005726 3$: TST (SP)+ ;;CLEAN TERMINATOR FROM STACK
3026 015452 010166 000012 MOV R1,12(SP) ;;SAVE THE RESULT
3027 015456 010267 000010 MOV R2,$HIOCT
3028 015462 012602 MOV (SP)+,R2 ;;POP STACK INTO R2
3029 015464 012601 MOV (SP)+,R1 ;;POP STACK INTO R1
3030 015466 012600 MOV (SP)+,R0 ;;POP STACK INTO R0
3031 015470 000002 RTI
3032 015472 000000 $HIOCT: .WORD 0 ;;HIGH ORDER BITS GO HERE
3033 .SBTTL TTY INPUT ROUTINE
3034
3035 ;;*****
3036 .ENABL LSB

```

```

3037
3038
3039
3040
3041
3042
3043 015474 022767 000176 163436 $CKSWR: CMP #SWREG,SWR ;; IS THE SOFT-SWR SELECTED?
3044 015502 001074 BNE 15$;; BRANCH IF NO
3045 015504 105777 163434 TSTB @STKS ;; CHAR THERE?
3046 015510 100071 BPL 15$;; IF NO, DON'T WAIT AROUND
3047 015512 117746 163430 MOVB @STKB,-(SP) ;; SAVE THE CHAR
3048 015516 042716 177600 BIC #1C177,(SP) ;; STRIP-OFF THE ASCII
3049 015522 022726 000007 CMP #7,(SP)+ ;; IS IT A CONTROL G?
3050 015526 001062 BNE 15$;; NO, RETURN TO USER
3051 015530 126727 163400 000001 CMPB $AUTOB,#1 ;; ARE WE RUNNING IN AUTO-MODE?
3052 015536 001456 BEQ 15$;; BRANCH IF YES
3053
3054 015540 104401 016221 $GTSWR: TYPE , $CNTLG ;; ECHO THE CONTROL-G (↑G)
3055 015544 104401 016226 TYPE $MSWR ;; TYPE CURRENT CONTENTS
3056 015550 016746 162422 MOV SWREG,-(SP) ;; SAVE SWREG FOR TYPEOUT
3057 015554 104402 TYPOC ;; GO TYPE--OCTAL ASCII(ALL DIGITS)
3058 015556 104401 016237 TYPE , $MNEW ;; PROMPT FOR NEW SWR
3059 015562 005046 19$: CLR -(SP) ;; CLEAR COUNTER
3060 015564 005046 7$: CLR -(SP) ;; THE NEW SWR
3061 015566 105777 163352 TSTB @STKS ;; CHAR THERE?
3062 015572 100375 BPL 7$;; IF NOT TRY AGAIN
3063
3064 015574 117746 163346 MOVB @STKB,-(SP) ;; PICK UP CHAR
3065 015600 042716 177600 BIC #1C177,(SP) ;; MAKE IT 7-BIT ASCII
3066
3067
3068
3069 015604 021627 000025 9$: CMP (SP),#25 ;; IS IT A CONTROL-U?
3070 015610 001005 BNE 10$;; BRANCH IF NOT
3071 015612 104401 016214 TYPE , $CNTLU ;; YES, ECHO CONTROL-U (↑U)
3072 015616 062706 000006 20$: ADD #6,SP ;; IGNORE PREVIOUS INPUT
3073 015622 000757 BR 19$;; LET'S TRY IT AGAIN
3074
3075
3076 015624 021627 000015 10$: CMP (SP),#15 ;; IS IT A <CR>?
3077 015630 001022 BNE 16$;; BRANCH IF NO
3078 015632 005766 000004 TST 4(SP) ;; YES, IS IT THE FIRST CHAR?
3079 015636 001403 BEQ 11$;; BRANCH IF YES
3080 015640 016677 000002 163272 MOV 2(SP),@SWR ;; SAVE NEW SWR
3081 015646 062706 000006 11$: ADD #6,SP ;; CLEAR UP STACK
3082 015652 104401 001177 14$: TYPE , $CRLF ;; ECHO <CR> AND <LF>
3083 015656 126727 163253 000001 CMPB $INTAG,#1 ;; RE-ENABLE TTY KBD INTERRUPTS?
3084 015664 001003 BNE 15$;; BRANCH IF NOT
3085 015666 012777 000100 163250 MOV #100,@STKS ;; RE-ENABLE TTY KBD INTERRUPTS
3086 015674 000002 15$: RTI ;; RETURN
3087 015676 004767 177420 16$: JSR PC,$TYPEC ;; ECHO CHAR
3088 015702 021627 000060 CMP (SP),#60 ;; CHAR < 0?
3089 015706 002420 BLT 18$;; BRANCH IF YES
3090 015710 021627 000067 CMP (SP),#67 ;; CHAR > 7?
3091 015714 003015 BGT 18$;; BRANCH IF YES
3092 015716 042726 000060 BIC #60,(SP)+ ;; STRIP-OFF ASCII

```

```

3093 015722 005766 000002 TST 2(SP) ;; IS THIS THE FIRST CHAP
3094 015726 001403 BEQ 17$;; BRANCH IF YES
3095 015730 006316 ASL (SP) ;; NO, SHIFT PRESENT
3096 015732 006316 ASL (SP) ;; CHAR OVER TO MAKE
3097 015734 006316 ASL (SP) ;; ROOM FOR NEW ONE.
3098 015736 005266 000002 17$: INC 2(SP) ;; KEEP COUNT OF CHAR
3099 015742 056616 177776 SIS -2(SP),(SP) ;; SET IN NEW CHAR
3100 015746 000707 BR 7$;; GET THE NEXT ONE
3101 015750 104401 001176 18$: TYPE $QUES ;; TYPE ?<CR><LF>
3102 015754 050720 BR 20$;; SIMULATE CONTROL-U
3103 .DSABL LSB
3104
3105
3106 ;;*****
3107 ;;*THIS ROUTINE WILL INPUT A SINGLE CHARACTER FROM THE TTY
3108 ;;*CALL:
3109 ;;*
3110 ;;* RDCHR ;; INPUT A SINGLE CHARACTER FROM THE TTY
3111 ;;* RETURN HERE ;; CHARACTER IS ON THE STACK
3112 ;;*
3113 ;;*
3114 015755 011646 $RDCHR: MOV (SP),-(SP) ;; PUSH DOWN THE PC
3115 015760 016666 000004 000002 MOV 4(SP),2(SP) ;; SAVE THE PS
3116 015766 105777 163152 1$: TSTB 2$TKS ;; WAIT FOR
3117 015772 100375 BPL 1$;; A CHARACTER
3118 015774 117766 163146 000004 MOVB 2$TKB,4(SP) ;; READ THE TTY
3119 016002 042766 177600 000004 BIC #1C<177>,4(SP) ;; GET RID OF JUNK IF ANY
3120 016010 026627 000004 000023 CMP 4(SP),#23 ;; IS IT A CONTROL-S?
3121 016016 001013 BNE 3$;; BRANCH IF NO
3122 016020 105777 163120 2$: TSTB 2$TKS ;; WAIT FOR A CHARACTER
3123 016024 100375 BPL 2$;; LOOP UNTIL ITS THERE
3124 016026 117746 163114 MOVB 2$TKB,-(SP) ;; GET CHARACTER
3125 016032 042716 177600 BIC #1C177,(SP) ;; MAKE IT 7-BIT ASCII
3126 016036 022627 000021 CMP (SP)+,#21 ;; IS IT A CONTROL-Q?
3127 016042 001366 BNE 2$;; IF NOT DISCARD IT
3128 016044 000750 BR 1$;; YES, RESUME
3129 016046 026627 000004 000140 3$: CMP 4(SP),#140 ;; IS IT UPPER CASE?
3130 016054 002407 BLT 4$;; BRANCH IF YES
3131 016056 026627 000004 000175 CMP 4(SP),#175 ;; IS IT A SPECIAL CHAR?
3132 016064 003003 BGT 4$;; BRANCH IF YES
3133 016066 042766 000040 000004 BIC #40,4(SP) ;; MAKE IT UPPER CASE
3134 016074 000002 4$: RTI ;; GO BACK TO USER
3135 ;;*****
3136 ;;*THIS ROUTINE WILL INPUT A STRING FROM THE TTY
3137 ;;*CALL:
3138 ;;*
3139 ;;* RDLIN ;; INPUT A STRING FROM THE TTY
3140 ;;* RETURN HERE ;; ADDRESS OF FIRST CHARACTER WILL BE ON THE STACK
3141 ;;*
3142 ;;*
3142 016076 010346 $RDLIN: MOV R3,-(SP) ;; SAVE R3
3143 016100 012703 016204 1$: MOV #$TTYIN,R3 ;; GET ADDRESS
3144 016104 022703 016214 2$: CMP #$TTYIN+8.,R3 ;; BUFFER FULL?
3145 016110 101405 BLOS 4$;; BR IF YES
3146 016112 104407 RDCHR ;; GO READ ONE CHARACTER FROM THE TTY
3147 016114 112613 MOVB (SP)+,(R3) ;; GET CHARACTER
3148 016116 122713 000177 10$: CMPB #177,(R3) ;; IS IT A RUBOUT

```

```

016250 017646 000000
016254 116667 000001 000211
016262 112667 000207
016266 062716 000002
016272 000406
016274 112767 000001 000171
016302 112767 000006 000165
016310 112767 000005 000154

```

```

00: 000000
01: 000004
02: 000004
03: 000004
04: 000004
05: 000004
06: 000004
07: 000004
08: 000004
09: 000004
10: 000004
11: 000004
12: 000004
13: 000004
14: 000004
15: 000004
16: 000004
17: 000004
18: 000004
19: 000004
20: 000004
21: 000004
22: 000004
23: 000004
24: 000004
25: 000004
26: 000004
27: 000004
28: 000004
29: 000004
30: 000004
31: 000004
32: 000004
33: 000004
34: 000004
35: 000004
36: 000004
37: 000004
38: 000004
39: 000004
40: 000004
41: 000004
42: 000004
43: 000004
44: 000004
45: 000004
46: 000004
47: 000004
48: 000004
49: 000004
50: 000004
51: 000004
52: 000004
53: 000004
54: 000004
55: 000004
56: 000004
57: 000004
58: 000004
59: 000004
60: 000004
61: 000004
62: 000004
63: 000004
64: 000004
65: 000004
66: 000004
67: 000004
68: 000004
69: 000004
70: 000004
71: 000004
72: 000004
73: 000004
74: 000004
75: 000004
76: 000004
77: 000004
78: 000004
79: 000004
80: 000004
81: 000004
82: 000004
83: 000004
84: 000004
85: 000004
86: 000004
87: 000004
88: 000004
89: 000004
90: 000004
91: 000004
92: 000004
93: 000004
94: 000004
95: 000004
96: 000004
97: 000004
98: 000004
99: 000004

```

```

::SKIP IF NOT
::TYPE A '?'
::CLEAR THE BUFFER AND LOOP
::ECHO THE CHARACTER
::CHECK FOR RETURN
::LOOP IF NOT RETURN
::CLEAR RETURN (THE 15)
::TYPE A LINE FEED
::RESTORE R3
::ADJUST THE STACK AND PUT ADDRESS OF THE
::FIRST ASCII CHARACTER ON IT
::RETURN
::STORAGE FOR ASCII CHAR. TO TYPE
::TERMINATOR
::RESERVE 8 BYTES FOR TTY INPUT
::CONTROL "U"
::CONTROL "G"

```

```

*THIS ROUTINE IS USED TO CHANGE A 16-BIT BINARY NUMBER TO A 6-DIGIT
*OCTAL (ASCII) NUMBER AND TYPE IT.
*STYPOS---ENTER HERE TO SETUP SUPPRESS ZEROS AND NUMBER OF DIGITS TO TYPE
*CALL:
* MOV NUM,-(SP) ;;NUMBER TO BE TYPED
* TYPOS N ;;CALL FOR TYPEOUT
* .BYTE M ;;N=1 TO 6 FOR NUMBER OF DIGITS TO TYPE
* .BYTE M ;;M=1 OR 0
* ;;I=TYPE LEADING ZEROS
* ;;O=SUPPRESS LEADING ZEROS
*STYPCN---ENTER HERE TO TYPE OUT WITH THE SAME PARAMETERS AS THE LAST
*STYPOS OR $TYPCN
*CALL:
* MOV NUM,-(SP) ;;NUMBER TO BE TYPED
* TYPON N ;;CALL FOR TYPEOUT
*STYPOC---ENTER HERE FOR TYPEOUT OF A 16 BIT NUMBER
*CALL:
* MOV NUM,-(SP) ;;NUMBER TO BE TYPED
* TYPOC N ;;CALL FOR TYPEOUT
*STYPOS: MOV 2(SP),-(SP) ;;PICKUP THE MODE
* MOVB 1(SP),%OFILL ;;LOAD ZERO FILL SWITCH
* MOVB (SP)+,%CMODE+1 ;;NUMBER OF DIGITS TO TYPE
* ADD #2,(SP) ;;ADJUST RETURN ADDRESS
* BR $TYPCN
*STYPOC: MOVB #1,%OFILL ;;SET THE ZERO FILL SWITCH
* MOVB #6,%CMODE+1 ;;SET FOR SIX(6) DIGITS
*STYPCN: MOVB #5,%SCNT ;;SET THE ITERATION COUNT

```



BASEIC LOGIC TEST DATA  
TO CONTROL SYSTEMS AND PERIPHERALS

```

000001 016431 010346 MOV R4, (SP)
000002 016432 010446 MOV R4, (SP)
000003 016433 010546 MOV R4, (SP)
000004 016434 116704 MOV R4, #MODE+1,R4
000005 016435 005404 MOV R4, #6,R4
000006 016436 062704 ADD R4, #MODE
000007 016437 110467 MOV R4, #0+ILL,R4
000008 016438 116704 MOV R4, (SP),R4
000009 016439 016606 MOV R4, (SP),R4
000010 016440 007104 CLR R4
000011 016441 000404 ROL R4
000012 016442 006104 ROL R4
000013 016443 006104 ROL R4
000014 016444 006104 ROL R4
000015 016445 010504 MOV R3,R3
000016 016446 006104 ROL R3
000017 016447 105367 DECB #MODE,R3
000018 016448 100016 BPL #177770,R3
000019 016449 042704 BIC #177770,R3
000020 016450 001004 BNE #45
000021 016451 005704 TST R4
000022 016452 001404 BEG #55
000023 016453 005204 INC R4
000024 016454 052704 BIS #0,R3
000025 016455 052704 BIS #0,R3
000026 016456 110367 MOV R3,R3
000027 016457 104401 TYPE #8
000028 016458 105367 DECB #CNT,R3
000029 016459 003347 BGT #65
000030 016460 002404 BPL #65
000031 016461 005204 INC R4
000032 016462 000744 BR #25
000033 016463 012605 MOV (SP)+,R5
000034 016464 012604 MOV (SP)+,R4
000035 016465 012603 MOV (SP)+,R3
000036 016466 016656 MOV 2(SP),4(SP)
000037 016467 012616 MOV (SP)+,(SP)
000038 016468 000002 RTI
000039 016469 000 85: .BYTE 0
000040 016470 000 .BYTE 0
000041 016471 000 .BYTE 0
000042 016472 000 .BYTE 0
000043 016473 000 .BYTE 0
000044 016474 000000 .WORD 0

```

```

: :SAVE R3
: :SAVE R4
: :SAVE R5
: :GET THE NUMBER OF DIGITS TO TYPE

: :SUBTRACT IT FOR MAX. ALLOWED
: :SAVE IT FOR USE
: :GET THE ZERO FILL SWITCH
: :PICKUP THE INPUT NUMBER
: :CLEAR THE OUTPUT WORD
: :ROTATE MSB INTO "C"
: :GO DO MSB
: :FORM THIS DIGIT

: :GET LSB OF THIS DIGIT
: :TYPE THIS DIGIT?
: :BR IF NO
: :GET RID OF JUNK
: :TEST FOR 0
: :SUPPRESS THIS 0?
: :BR IF YES
: :DON'T SUPPRESS ANYMORE 0'S
: :MAKE THIS DIGIT ASCII
: :MAKE ASCII IF NOT ALREADY
: :SAVE FOR TYPING
: :GO TYPE THIS DIGIT
: :COUNT BY 1
: :BR IF MORE TO DO
: :BR IF DONE
: :INSURE LAST DIGIT ISN'T A BLANK
: :GO DO THE LAST DIGIT
: :RESTORE R5
: :RESTORE R4
: :RESTORE R3
: :SET THE STACK FOR RETURNING

: :RETURN
: :STORAGE FOR ASCII DIGIT
: :TERMINATOR FOR TYPE ROUTINE
: :LOCAL DIGIT COUNTER
: :ZERO FILL SWITCH
: :NUMBER OF DIGITS TO TYPE

```

.SBTTL TRAP DECODER

\*\*\*\*\*
\*THIS ROUTINE WILL PICKUP THE LOWER BYTE OF THE "TRAP" INSTRUCTION
\*AND USE IT TO INDEX THROUGH THE TRAP TABLE FOR THE STARTING ADDRESS
\*OF THE DESIRED ROUTINE. THEN USING THE ADDRESS OBTAINED IT WILL
\*GO TO THAT ROUTINE.

016476 016046
016478 016046 000002
016504 016046
016506 016000
016510 016300
016512 016300 016532
016516 016300

\$TRAP: MOV RO, -(SP) ;; SAVE RO
MOV 2(SP), RO ;; GET TRAP ADDRESS
TST -(RO) ;; BACKUP BY 2
MOVB (RO), RO ;; GET RIGHT BYTE OF TRAP
ASL RO ;; POSITION FOR INDEXING
MOV \$TRAPAD(RO), RO ;; INDEX TO TABLE
RTS RO ;; GO TO ROUTINE

:: THIS IS USE TO HANDLE THE "GETPRI" MACRO

016520 011646
016522 016566 000004 000002
016532 000002

\$TRAP2: MOV (SP), -(SP) ;; MOVE THE PC DOWN
MOV 4(SP), 2(SP) ;; MOVE THE PSW DOWN
RTI ;; RESTORE THE PSW

.SBTTL TRAP TABLE

\*THIS TABLE CONTAINS THE STARTING ADDRESSES OF THE ROUTINES CALLED
\*BY THE "TRAP" INSTRUCTION.

ROUTINE

016532 016520
016534 015152
016536 015274
016540 016250
016542 016310
016544 015544
016546 015474
016550 015756
016552 016076
016554 015372
016556 013046
016560 013152

\$TRAPAD: .WORD \$TRAP2
\$TYPE ;; CALL=TYPE TRAP+1(104401) TTY TYPEOUT ROUTINE
\$TYPOC ;; CALL=TYPOC TRAP+2(104402) TYPE OCTAL NUMBER (WITH LEADING ZEROS)
\$TYPOS ;; CALL=TYPOS TRAP+3(104403) TYPE OCTAL NUMBER (NO LEADING ZEROS)
\$TYPON ;; CALL=TYPON TRAP+4(104404) TYPE OCTAL NUMBER (AS PER LAST CALL)
\$GTSWR ;; CALL=GTSWR TRAP+5(104405) GET SOFT-SWR SETTING
\$CKSWR ;; CALL=CKSWR TRAP+6(104406) TEST FOR CHANGE IN SOFT-SWR
\$RDCHR ;; CALL=RDCHR TRAP+7(104407) TTY TYPEIN CHARACTER ROUTINE
\$RDLIN ;; CALL=RDLIN TRAP+10(104410) TTY TYPEIN STRING ROUTINE
\$RDOCT ;; CALL=RDOCT TRAP+11(104411) READ AN OCTAL NUMBER FROM TTY
WAIT.ON.READY ;; CALL=WAITREADY TRAP+12(104412) WAIT ON THE READY BIT TO
WAIT.FOR.XFER ;; CALL=WAITXFER TRAP+13(104413) WAIT ON XFER REQ.

.SBTTL POWER DOWN AND UP ROUTINES

\*\*\*\*\*  
POWER DOWN ROUTINE  
\*\*\*\*\*

|        |        |        |        |              |                  |                      |
|--------|--------|--------|--------|--------------|------------------|----------------------|
| 016562 | 012737 | 016726 | 000024 | \$PWRDN: MOV | ;\$ILLUP,@PWRVEC | ::SET FOR FAST UP    |
| 016570 | 012737 | 000340 | 000026 | MOV          | \$340,@PWRVEC+2  | ::PRIO:7             |
| 016576 | 010046 |        |        | MOV          | R0,-(SP)         | ::PUSH R0 ON STACK   |
| 016600 | 010146 |        |        | MOV          | R1,-(SP)         | ::PUSH R1 ON STACK   |
| 016602 | 010246 |        |        | MOV          | R2,-(SP)         | ::PUSH R2 ON STACK   |
| 016604 | 010346 |        |        | MOV          | R3,-(SP)         | ::PUSH R3 ON STACK   |
| 016506 | 010446 |        |        | MOV          | R4,-(SP)         | ::PUSH R4 ON STACK   |
| 016610 | 010546 |        |        | MOV          | R5,-(SP)         | ::PUSH R5 ON STACK   |
| 016512 | 011746 | 162322 |        | MOV          | QSWR,-(SP)       | ::PUSH QSWR ON STACK |
| 016616 | 010667 | 000110 |        | MOV          | SP,\$SAVR6       | ::SAVE SP            |
| 016622 | 012737 | 016634 | 000024 | MOV          | ;\$PWRUP,@PWRVEC | ::SET UP VECTOR      |
| 016630 | 000000 |        |        | HALT         |                  |                      |
| 016632 | 000776 |        |        | BR           | .-2              | ::HANG UP            |

\*\*\*\*\*  
POWER UP ROUTINE  
\*\*\*\*\*

|        |        |        |        |                 |                  |                                      |
|--------|--------|--------|--------|-----------------|------------------|--------------------------------------|
| 016634 | 012737 | 016726 | 000024 | \$PWRUP: MOV    | ;\$ILLUP,@PWRVEC | ::SET FOR FAST DOWN                  |
| 016642 | 016706 | 000064 |        | MOV             | \$\$SAVR6,SP     | ::GET SP                             |
| 016646 | 005067 | 000060 |        | CLR             | \$\$SAVR6        | ::WAIT LOOP FOR THE TTY              |
| 016652 | 005267 | 000054 |        | 1\$: INC        | \$\$SAVR6        | ::WAIT FOR THE INC                   |
| 016656 | 001375 |        |        | BNE             | 1\$              | ::OF WORD                            |
| 016660 | 012677 | 162254 |        | MOV             | (SP)+,QSWR       | ::POP STACK INTO QSWR                |
| 016664 | 012605 |        |        | MOV             | (SP)+,R5         | ::POP STACK INTO R5                  |
| 016666 | 012604 |        |        | MOV             | (SP)+,R4         | ::POP STACK INTO R4                  |
| 016670 | 012503 |        |        | MOV             | (SP)+,R3         | ::POP STACK INTO R3                  |
| 016672 | 012602 |        |        | MOV             | (SP)+,R2         | ::POP STACK INTO R2                  |
| 016674 | 012601 |        |        | MOV             | (SP)+,R1         | ::POP STACK INTO R1                  |
| 016676 | 012600 |        |        | MOV             | (SP)+,R0         | ::POP STACK INTO R0                  |
| 016700 | 012737 | 016562 | 000024 | MOV             | ;\$PWRDN,@PWRVEC | ::SET UP THE POWER DOWN VECTOR       |
| 016706 | 012737 | 000340 | 000026 | MOV             | \$340,@PWRVEC+2  | ::PRIO:7                             |
| 016714 | 104401 |        |        | TYPE            |                  | ::REPORT THE POWER FAILURE           |
| 016716 | 016734 |        |        | \$PWRMG: .WORD  | \$POWER          | ::POWER FAIL MESSAGE POINTER         |
| 016720 | 012716 |        |        | MOV             | (PC)+,(SP)       | ::RESTART AT PWRST                   |
| 016722 | 002206 |        |        | \$PWRAD: .WORD  | PWRST            | ::RESTART ADDRESS                    |
| 016724 | 000002 |        |        | RTI             |                  |                                      |
| 016726 | 000000 |        |        | \$ILLUP: HALT   |                  | ::THE POWER UP SEQUENCE WAS STARTED  |
| 016730 | 000776 |        |        | BR              | .-2              | ::BEFORE THE POWER DOWN WAS COMPLETE |
| 016732 | 000000 |        |        | \$\$SAVR6: 0    |                  | ::PUT THE SP HERE                    |
| 016734 | 005015 | 047520 | 042527 | \$POWER: .ASCIZ | <15><12>"POWER"  |                                      |
| 016742 | 000122 |        |        | .EVEN           |                  |                                      |

TEST PART 2 MAINDEF-11-DZTAB-C  
 POWER DOWN AND UP ROUTINES

|        |        |        |        |                |                                    |
|--------|--------|--------|--------|----------------|------------------------------------|
| 016744 | 042200 | 044522 | 042526 | MSGDRV: .ASCIZ | <CRLF>"DRIVE(S) "                  |
| 016750 | 051450 | 047451 | 000040 |                |                                    |
| 016760 | 005015 | 040524 | 051503 | MSGASK: .ASCIZ | <15><12>/TACS/                     |
| 016766 | 000077 |        |        |                |                                    |
| 016770 | 042526 | 052103 | 051117 | MSGVEC: .ASCIZ | /VECTOR/                           |
| 016776 | 000077 |        |        |                |                                    |
| 017000 | 051120 | 047511 | 044522 | MSGPRI: .ASCIZ | /PRIORITY/                         |
| 017006 | 054524 | 000077 |        |                |                                    |
| 017012 | 040524 | 051503 | 000075 | MTACS: .ASCIZ  | /TACS=/                            |
| 017020 | 052040 | 042101 | 036502 | MTADB: .ASCIZ  | /TADB=/                            |
| 017026 | 000    |        |        |                |                                    |
| 017027 | 040    | 042526 | 052103 | MTAVEC: .ASCIZ | /VECTOR=/                          |
| 017034 | 051117 | 000075 |        |                |                                    |
| 017040 | 050040 | 044522 | 051117 | MTAPRI: .ASCIZ | /PRIORITY=/                        |
| 017046 | 052111 | 036531 | 000    |                |                                    |
| 017053 | 015    | 047412 | 037513 | MSGOK: .ASCIZ  | <15><12>/OK/                       |
| 017060 | 000    |        |        |                |                                    |
| 017061 | 131    | 051505 | 000200 | MYES: .ASCIZ   | /YES/<CRLF>                        |
| 017066 | 052123 | 052101 | 051525 | EM1: .ASCIZ    | /STATUS PROBLEM/                   |
| 017074 | 050040 | 047522 | 046102 |                |                                    |
| 017102 | 046505 | 000    |        |                |                                    |
| 017105 | 042    | 042522 | 042101 | EM2: .ASCIZ    | /"READY" FAILED TO SET/            |
| 017112 | 021131 | 043040 | 044501 |                |                                    |
| 017120 | 042514 | 020104 | 047524 |                |                                    |
| 017126 | 051440 | 052105 | 000    |                |                                    |
| 017133 | 042    | 051124 | 047101 | EM3: .ASCIZ    | /"TRANSFER REQUEST" FAILED TO SET/ |
| 017140 | 043123 | 051105 | 051040 |                |                                    |
| 017146 | 050505 | 042525 | 052123 |                |                                    |
| 017154 | 020042 | 040506 | 046111 |                |                                    |
| 017162 | 042105 | 052040 | 020117 |                |                                    |
| 017170 | 042523 | 000124 |        |                |                                    |
| 017174 | 044124 | 020105 | 051127 | EM4: .ASCIZ    | /THE WRONG FLAG SET/               |
| 017202 | 047117 | 020107 | 046106 |                |                                    |
| 017210 | 043501 | 051440 | 052105 |                |                                    |
| 017216 | 000    |        |        |                |                                    |
| 017217 | 154    | 052101 | 020101 | EM5: .ASCIZ    | /DATA PROBLEM/                     |
| 017224 | 051120 | 041117 | 042514 |                |                                    |
| 017232 | 000115 |        |        |                |                                    |
| 017234 | 047111 | 042524 | 051122 | EM6: .ASCIZ    | /INTERRUPT PROBLEM/                |
| 017242 | 050125 | 020124 | 051120 |                |                                    |
| 017250 | 041117 | 042514 | 000115 |                |                                    |
| 017256 | 041520 | 020040 | 020040 | DH1: .ASCIZ    | /PC TACS/                          |
| 017264 | 020040 | 040524 | 051503 |                |                                    |
| 017272 | 000    |        |        |                |                                    |
| 017273 | 120    | 020103 | 020040 | DH2: .ASCIZ    | /PC TACS WAIT ADDRESS/             |
| 017300 | 020040 | 052040 | 041501 |                |                                    |
| 017306 | 020123 | 020040 | 053440 |                |                                    |
| 017314 | 044501 | 020124 | 042101 |                |                                    |
| 017322 | 051104 | 051505 | 000123 |                |                                    |
| 017330 | 041520 | 020040 | 020040 | DH5: .ASCIZ    | /PC TACS EXPECT RCV'D/             |
| 017336 | 020040 | 040524 | 051503 |                |                                    |
| 017344 | 020040 | 020040 | 054105 |                |                                    |
| 017352 | 042520 | 052103 | 020040 |                |                                    |
| 017360 | 041522 | 023526 | 000104 |                |                                    |
| 017366 | 041520 | 020040 | 020040 | DH6: .ASCIZ    | /PC TACS BR LEVEL/                 |
| 017374 | 020040 | 040524 | 051503 |                |                                    |

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|        |        |        |        |        |        |        |                              |
|--------|--------|--------|--------|--------|--------|--------|------------------------------|
| 017466 | 001116 | 000000 | 001162 | 000000 | DT1:   | .WORD  | SERRPC,SREGO,0               |
| 017466 | 001116 | 000000 | 001162 | 001202 | DT2:   | .WORD  | SERRPC,SREGO,SAVPC,0         |
| 017466 | 001116 | 000000 | 001162 | 001124 | DT5:   | .WORD  | SERRPC,SREGO,SGDDAT,SBDDAT,0 |
| 017466 | 001116 | 000000 | 001162 | 001124 | DT6:   | .WORD  | SERRPC,SREGO,SGDDAT,0        |
| 017466 | 001116 | 000000 | 001206 | 000000 | DT201: | .WORD  | SERRPC,TACSL,0               |
| 017466 | 001116 | 000000 |        |        | DT202: | .WORD  | SERRPC,0                     |
| 017472 | 040524 | 030461 | 043040 |        | EM201: | .ASCIZ | "TAIL FAILED TO RESPOND"     |
| 017500 | 044501 | 042514 | 020104 |        |        |        |                              |
| 017506 | 047524 | 051040 | 051505 |        |        |        |                              |
| 017514 | 047520 | 042116 | 000    |        |        |        |                              |
| 017521 | 116    | 020117 | 051104 |        | EM202: | .ASCIZ | "NO DRIVE AVAILABLE"         |
| 017526 | 053111 | 020105 | 053101 |        |        |        |                              |
| 017532 | 044501 | 040514 | 046102 |        |        |        |                              |
| 017537 | 000100 |        |        |        |        |        |                              |
| 017543 | 041520 | 020040 | 020040 |        | DH201: | .ASCIZ | /PC TACS/                    |
| 017549 | 020040 | 040524 | 051503 |        |        |        |                              |
| 017555 | 000    |        |        |        |        |        |                              |
| 017561 | 000    |        |        |        |        |        |                              |
| 017567 | 000    |        |        |        |        |        |                              |
| 017573 | 000    |        |        |        |        |        |                              |
| 017579 | 000    |        |        |        |        |        |                              |
| 017585 | 000    |        |        |        |        |        |                              |
| 017591 | 000    |        |        |        |        |        |                              |
| 017597 | 000    |        |        |        |        |        |                              |
| 017603 | 000    |        |        |        |        |        |                              |
| 017609 | 000    |        |        |        |        |        |                              |
| 017615 | 000    |        |        |        |        |        |                              |
| 017621 | 000    |        |        |        |        |        |                              |
| 017627 | 000    |        |        |        |        |        |                              |
| 017633 | 000    |        |        |        |        |        |                              |
| 017639 | 000    |        |        |        |        |        |                              |
| 017645 | 000    |        |        |        |        |        |                              |
| 017651 | 000    |        |        |        |        |        |                              |
| 017657 | 000    |        |        |        |        |        |                              |
| 017663 | 000    |        |        |        |        |        |                              |
| 017669 | 000    |        |        |        |        |        |                              |
| 017675 | 000    |        |        |        |        |        |                              |
| 017681 | 000    |        |        |        |        |        |                              |
| 017687 | 000    |        |        |        |        |        |                              |
| 017693 | 000    |        |        |        |        |        |                              |
| 017699 | 000    |        |        |        |        |        |                              |
| 017705 | 000    |        |        |        |        |        |                              |
| 017711 | 000    |        |        |        |        |        |                              |
| 017717 | 000    |        |        |        |        |        |                              |
| 017723 | 000    |        |        |        |        |        |                              |
| 017729 | 000    |        |        |        |        |        |                              |
| 017735 | 000    |        |        |        |        |        |                              |
| 017741 | 000    |        |        |        |        |        |                              |
| 017747 | 000    |        |        |        |        |        |                              |
| 017753 | 000    |        |        |        |        |        |                              |
| 017759 | 000    |        |        |        |        |        |                              |
| 017765 | 000    |        |        |        |        |        |                              |
| 017771 | 000    |        |        |        |        |        |                              |
| 017777 | 000    |        |        |        |        |        |                              |
| 017783 | 000    |        |        |        |        |        |                              |
| 017789 | 000    |        |        |        |        |        |                              |
| 017795 | 000    |        |        |        |        |        |                              |
| 017801 | 000    |        |        |        |        |        |                              |
| 017807 | 000    |        |        |        |        |        |                              |
| 017813 | 000    |        |        |        |        |        |                              |
| 017819 | 000    |        |        |        |        |        |                              |
| 017825 | 000    |        |        |        |        |        |                              |
| 017831 | 000    |        |        |        |        |        |                              |
| 017837 | 000    |        |        |        |        |        |                              |
| 017843 | 000    |        |        |        |        |        |                              |
| 017849 | 000    |        |        |        |        |        |                              |
| 017855 | 000    |        |        |        |        |        |                              |
| 017861 | 000    |        |        |        |        |        |                              |
| 017867 | 000    |        |        |        |        |        |                              |
| 017873 | 000    |        |        |        |        |        |                              |
| 017879 | 000    |        |        |        |        |        |                              |
| 017885 | 000    |        |        |        |        |        |                              |
| 017891 | 000    |        |        |        |        |        |                              |
| 017897 | 000    |        |        |        |        |        |                              |
| 017903 | 000    |        |        |        |        |        |                              |
| 017909 | 000    |        |        |        |        |        |                              |
| 017915 | 000    |        |        |        |        |        |                              |
| 017921 | 000    |        |        |        |        |        |                              |
| 017927 | 000    |        |        |        |        |        |                              |
| 017933 | 000    |        |        |        |        |        |                              |
| 017939 | 000    |        |        |        |        |        |                              |
| 017945 | 000    |        |        |        |        |        |                              |
| 017951 | 000    |        |        |        |        |        |                              |
| 017957 | 000    |        |        |        |        |        |                              |
| 017963 | 000    |        |        |        |        |        |                              |
| 017969 | 000    |        |        |        |        |        |                              |
| 017975 | 000    |        |        |        |        |        |                              |
| 017981 | 000    |        |        |        |        |        |                              |
| 017987 | 000    |        |        |        |        |        |                              |
| 017993 | 000    |        |        |        |        |        |                              |
| 017999 | 000    |        |        |        |        |        |                              |



... ..

|     |     |     |      |       |       |       |       |      |      |       |       |       |       |      |      |      |      |
|-----|-----|-----|------|-------|-------|-------|-------|------|------|-------|-------|-------|-------|------|------|------|------|
| ... | ... | ... | 524* | 2327* | 2353* |       |       |      |      |       |       |       |       |      |      |      |      |
| ... | ... | ... | 477* | 620   | 633   | 654   | 679   | 2075 | 2128 | 2461* | 2496  | 2500  | 2504  |      |      |      |      |
| ... | ... | ... | 654* | 665   | 680*  | 2884  | 2991* |      |      |       |       |       |       |      |      |      |      |
| ... | ... | ... | 5:5  |       |       |       |       |      |      |       |       |       |       |      |      |      |      |
| ... | ... | ... | 437  | 3399* |       |       |       |      |      |       |       |       |       |      |      |      |      |
| ... | ... | ... |      |       |       |       |       |      |      |       |       |       |       |      |      |      |      |
| ... | ... | ... | 176* |       |       |       |       |      |      |       |       |       |       |      |      |      |      |
| ... | ... | ... | 417  |       |       |       |       |      |      |       |       |       |       |      |      |      |      |
| ... | ... | ... | 422  |       |       |       |       |      |      |       |       |       |       |      |      |      |      |
| ... | ... | ... | 429  |       |       |       |       |      |      |       |       |       |       |      |      |      |      |
| ... | ... | ... | 425  |       |       |       |       |      |      |       |       |       |       |      |      |      |      |
| ... | ... | ... | 441  |       |       |       |       |      |      |       |       |       |       |      |      |      |      |
| ... | ... | ... | 447  |       |       |       |       |      |      |       |       |       |       |      |      |      |      |
| ... | ... | ... | 193* |       |       | 1567  | 1736  | 1741 | 1780 | 1873  | 2052  |       |       |      |      |      |      |
| ... | ... | ... | 169* |       |       | 514*  | 525*  | 591* | 587* | 2299  | 2290* | 2292* | 2295* |      |      |      |      |
| ... | ... | ... | 621  |       |       | 630   | 2903* |      |      |       |       |       |       |      |      |      |      |
| ... | ... | ... | 2499 |       |       | 2504* |       |      |      |       |       |       |       |      |      |      |      |
| ... | ... | ... | 197* |       |       | 1449  | 1483  | 1538 | 1564 | 1598  | 1629  | 1660  | 1729  | 1733 | 1775 | 1780 |      |
| ... | ... | ... | 209* |       |       |       |       |      |      |       |       |       |       |      |      |      |      |
| ... | ... | ... | 207* |       |       | 209   |       |      |      |       |       |       |       |      |      |      |      |
| ... | ... | ... | 206* |       |       | 209   |       |      |      |       |       |       |       |      |      |      |      |
| ... | ... | ... | 205* |       |       | 209   |       |      |      |       |       |       |       |      |      |      |      |
| ... | ... | ... | 227* |       |       | 544   | 672   | 3280 | 3281 | 3282  | 3283  | 3295  | 3297  | 3288 | 3289 | 3290 | 3291 |
| ... | ... | ... | 208* |       |       | 303   | 306   | 325  | 328  | 693   | 710   | 712   | 731   | 733  | 771  | 773  | 782  |
| ... | ... | ... | 784  |       |       | 823   | 825   | 827  | 1292 | 1295  | 1303  | 1335  | 1366  | 1368 | 1371 | 1437 | 1439 |
| ... | ... | ... | 1441 |       |       | 1447  | 1467  | 1469 | 1471 | 1477  | 1479  | 1481  | 1495  | 1497 | 1499 | 1505 | 1522 |
| ... | ... | ... | 1524 |       |       | 1526  | 1532  | 1534 | 1536 | 1554  | 1556  | 1562  | 1579  | 1581 | 1587 | 1589 | 1591 |
| ... | ... | ... | 1593 |       |       | 1610  | 1612  | 1618 | 1620 | 1622  | 1624  | 1642  | 1644  | 1646 | 1654 | 1656 | 1658 |
| ... | ... | ... | 1710 |       |       | 1712  | 1714  | 1722 | 1724 | 1726  | 1731  | 1739  | 1757  | 1759 | 1761 | 1769 | 1771 |
| ... | ... | ... | 1773 |       |       | 1778  | 1783  | 1804 | 1806 | 1814  | 1816  | 1839  | 1841  | 1844 | 1859 | 1861 | 1899 |
| ... | ... | ... | 1891 |       |       | 1893  | 1902  | 1904 | 1957 | 1959  | 1967  | 1969  | 2004  | 2006 | 2009 | 2024 | 2026 |
| ... | ... | ... | 2086 |       |       | 2136  | 2138  | 2142 | 2152 | 2163  | 2168  | 2186  | 2193  | 2205 | 2230 | 2636 | 2639 |
| ... | ... | ... | 2668 |       |       | 2691  | 2722  | 2725 | 2750 | 2756  | 2778  | 2781  | 2797  | 2842 | 2860 |      |      |
| ... | ... | ... | 539  |       |       | 687   | 2812  | 2825 | 2831 | 3285* |       |       |       |      |      |      |      |
| ... | ... | ... | 530  |       |       | 532   | 543   | 545* |      |       |       |       |       |      |      |      |      |
| ... | ... | ... | 714  |       |       | 2423* | 2437* |      |      |       |       |       |       |      |      |      |      |
| ... | ... | ... | 79*  |       |       | 2954  | 2995  |      |      |       |       |       |       |      |      |      |      |
| ... | ... | ... | 204* |       |       | 780   | 806   | 1445 | 1475 | 1503  | 1530  | 1560  | 1595  | 1616 | 1652 | 1720 | 1767 |
| ... | ... | ... | 1812 |       |       | 1828  | 1854  | 1871 | 1900 | 1911  | 1965  | 1987  | 2019  | 2040 | 2158 | 2181 | 2219 |
| ... | ... | ... | 2850 |       |       | 2871  |       |      |      |       |       |       |       |      |      |      |      |
| ... | ... | ... | 202* |       |       | 922   | 970   | 1018 | 1066 | 1114  | 1162  | 1210  | 1258  | 1303 | 1335 | 1398 |      |
| ... | ... | ... | 174* |       |       | 497*  | 498*  |      |      |       |       |       |       |      |      |      |      |
| ... | ... | ... | 452* |       |       |       |       |      |      |       |       |       |       |      |      |      |      |
| ... | ... | ... | 195* |       |       | 1455  | 1510  | 1538 | 1567 | 1744  | 1780  | 1788  | 2641  | 2672 | 2695 | 2727 | 2730 |
| ... | ... | ... | 2754 |       |       | 2758  | 2783  | 2799 | 2921 |       |       |       |       |      |      |      |      |
| ... | ... | ... | 80*  |       |       | 2989  | 2995  |      |      |       |       |       |       |      |      |      |      |

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PROGRAM NAME: ... REFERENCE ...

Table with columns for program names (e.g., LOOP, NEXT, NOTE) and numerical values. Includes a large block of asterisks representing missing or zeroed-out data.





TA11 BASIC LOGIC TEST (PART 2) MAINDEC-11-DZTAB-C  
DZTABC.NEW CROSS REFERENCE TABLE -- USER SYMBOLS

|         |        |       |       |       |       |       |      |       |      |      |      |      |      |       |
|---------|--------|-------|-------|-------|-------|-------|------|-------|------|------|------|------|------|-------|
| TRAPVE= | 000034 | 177*  | 501*  | 502*  |       |       |      |       |      |      |      |      |      |       |
| TRTVEC= | 000014 | 172*  |       |       |       |       |      |       |      |      |      |      |      |       |
| TR.REQ= | 000200 | 201*  | 1336  |       |       |       |      |       |      |      |      |      |      |       |
| TST1    | 002436 | 691   | 704*  |       |       |       |      |       |      |      |      |      |      |       |
| TST10   | 004150 | 1053* |       |       |       |       |      |       |      |      |      |      |      |       |
| TST11   | 004352 | 1101* |       |       |       |       |      |       |      |      |      |      |      |       |
| TST12   | 004554 | 1149* |       |       |       |       |      |       |      |      |      |      |      |       |
| TST13   | 004756 | 1197* |       |       |       |       |      |       |      |      |      |      |      |       |
| TST14   | 005160 | 1245* |       |       |       |       |      |       |      |      |      |      |      |       |
| TST15   | 005362 | 1286* |       |       |       |       |      |       |      |      |      |      |      |       |
| TST16   | 005554 | 1322* |       |       |       |       |      |       |      |      |      |      |      |       |
| TST17   | 005750 | 1360* |       |       |       |       |      |       |      |      |      |      |      |       |
| TST2    | 002516 | 707   | 725*  |       |       |       |      |       |      |      |      |      |      |       |
| TST20   | 006246 | 1431* |       |       |       |       |      |       |      |      |      |      |      |       |
| TST21   | 006370 | 1434  | 1456  | 1461* |       |       |      |       |      |      |      |      |      |       |
| TST22   | 006510 | 1464  | 1484  | 1489* |       |       |      |       |      |      |      |      |      |       |
| TST23   | 006622 | 1492  | 1511  | 1516* |       |       |      |       |      |      |      |      |      |       |
| TST24   | 006742 | 1519  | 1539  | 1548* |       |       |      |       |      |      |      |      |      |       |
| TST25   | 007050 | 1551  | 1568  | 1573* |       |       |      |       |      |      |      |      |      |       |
| TST26   | 007176 | 1576  | 1599  | 1604* |       |       |      |       |      |      |      |      |      |       |
| TST27   | 007324 | 1607  | 1630  | 1635* |       |       |      |       |      |      |      |      |      |       |
| TST3    | 002656 | 728   | 760   | 765*  |       |       |      |       |      |      |      |      |      |       |
| TST30   | 007574 | 1638  | 1694  | 1703* |       |       |      |       |      |      |      |      |      |       |
| TST31   | 010000 | 1706  | 1745  | 1750* |       |       |      |       |      |      |      |      |      |       |
| TST32   | 010172 | 1753  | 1789  | 1798* |       |       |      |       |      |      |      |      |      |       |
| TST33   | 010330 | 1801  | 1833* |       |       |       |      |       |      |      |      |      |      |       |
| TST34   | 010516 | 1836  | 1874  | 1883* |       |       |      |       |      |      |      |      |      |       |
| TST35   | 010776 | 1886  | 1946  | 1951* |       |       |      |       |      |      |      |      |      |       |
| TST36   | 011160 | 1954  | 1993  | 1998* |       |       |      |       |      |      |      |      |      |       |
| TST37   | 011412 | 2001  | 2053  | 2071* |       |       |      |       |      |      |      |      |      |       |
| TST4    | 003062 | 768   | 812   | 817*  |       |       |      |       |      |      |      |      |      |       |
| TST40   | 011552 | 2074  | 2076  | 2124* |       |       |      |       |      |      |      |      |      |       |
| TST41   | 012134 | 2127  | 2129  | 2221  | 2226* |       |      |       |      |      |      |      |      |       |
| TST5    | 003342 | 820   | 882   | 909*  |       |       |      |       |      |      |      |      |      |       |
| TST6    | 003544 | 957*  |       |       |       |       |      |       |      |      |      |      |      |       |
| TST7    | 003746 | 1005* |       |       |       |       |      |       |      |      |      |      |      |       |
| TYPE =  | 104401 | 533   | 653   | 670   | 675   | 676   | 2254 | 2356  | 2364 | 2386 | 2395 | 2397 | 2400 | 2402  |
|         |        | 2406  | 2414  | 2480  | 2502  | 2513  | 2527 | 2536  | 2548 | 2551 | 2554 | 2557 | 2560 | 2567  |
|         |        | 2569  | 2959  | 3054  | 3055  | 3058  | 3071 | 3082  | 3101 | 3150 | 3153 | 3157 | 3232 | 3280* |
|         |        | 3327  |       |       |       |       |      |       |      |      |      |      |      |       |
| TYPERR  | 012730 | 2363  | 2386* |       |       |       |      |       |      |      |      |      |      |       |
| TYPOC = | 104402 | 2411  | 2550  | 2553  | 2556  | 2559  | 3057 | 3281* |      |      |      |      |      |       |
| TYPON = | 104404 | 3283* |       |       |       |       |      |       |      |      |      |      |      |       |
| TYPOS = | 104403 | 3282* |       |       |       |       |      |       |      |      |      |      |      |       |
| UNIT =  | 000400 | 200*  | 668   | 2079  | 2080  | 2132  | 2133 | 2887  | 2907 |      |      |      |      |       |
| WAITRE= | 104412 | 711   | 713   | 732   | 755   | 772   | 781  | 783   | 807  | 824  | 826  | 845  | 1293 | 1296  |
|         |        | 1330  | 1367  | 1369  | 1409  | 1438  | 1440 | 1446  | 1448 | 1468 | 1470 | 1476 | 1478 | 1480  |
|         |        | 1482  | 1496  | 1498  | 1504  | 1506  | 1523 | 1525  | 1531 | 1533 | 1535 | 1537 | 1555 | 1561  |
|         |        | 1563  | 1580  | 1586  | 1588  | 1590  | 1592 | 1594  | 1611 | 1617 | 1619 | 1621 | 1623 | 1625  |
|         |        | 1641  | 1643  | 1645  | 1653  | 1655  | 1657 | 1659  | 1709 | 1711 | 1713 | 1721 | 1723 | 1725  |
|         |        | 1727  | 1732  | 1740  | 1756  | 1758  | 1760 | 1768  | 1770 | 1772 | 1774 | 1779 | 1784 | 1805  |
|         |        | 1813  | 1815  | 1829  | 1840  | 1842  | 1855 | 1860  | 1872 | 1890 | 1892 | 1901 | 1903 | 1912  |
|         |        | 1958  | 1966  | 1968  | 1988  | 2005  | 2007 | 2020  | 2025 | 2047 | 2083 | 2085 | 2105 | 2137  |
|         |        | 2139  | 2141  | 2159  | 2164  | 2182  | 2187 | 2192  | 2204 | 2219 | 2231 | 2640 | 2726 | 2757  |
|         |        | 2782  | 2798  | 2851  | 2872  | 3291* |      |       |      |      |      |      |      |       |
| WAITXF= | 104413 | 734   | 775   | 777   | 785   | 828   | 1304 | 1372  | 1442 | 1444 | 1472 | 1474 | 1500 | 1502  |





| Variable | Value  | 3281 | 3282 | 3283 | 3284 | 3285 | 3286 | 3287 | 3288 | 3289 | 3290 | 3291 | 3292 |
|----------|--------|------|------|------|------|------|------|------|------|------|------|------|------|
| STRAC    | 000014 |      |      |      |      |      |      |      |      |      |      |      |      |
| STRAC    | 016532 | 3279 |      |      |      |      |      |      |      |      |      |      |      |
| STRAC    | 001102 | 346  | 2244 | 2273 | 2300 | 2322 | 2327 | 2331 | 2353 | 2382 |      |      |      |
| STRAC    | 016204 | 3144 | 3161 | 3155 |      |      |      |      |      |      |      |      |      |
| STRAC    | 015152 | 2272 | 2280 |      |      |      |      |      |      |      |      |      |      |
| STRAC    | 015322 | 2270 | 2277 | 2482 | 2483 | 3087 |      |      |      |      |      |      |      |
| STRAC    | 015370 | 2290 | 2292 |      |      |      |      |      |      |      |      |      |      |
| STRAC    | 016274 | 2281 |      |      |      |      |      |      |      |      |      |      |      |
| STRAC    | 016310 | 2281 | 3283 |      |      |      |      |      |      |      |      |      |      |
| STRAC    | 016250 | 2282 |      |      |      |      |      |      |      |      |      |      |      |
| STRAC    | 012276 | 2287 |      |      |      |      |      |      |      |      |      |      |      |
| STRAC    | 000000 | 2287 |      |      |      |      |      |      |      |      |      |      |      |
| STRAC    | 016473 | 2298 | 3202 | 3212 | 3247 |      |      |      |      |      |      |      |      |
| STRAC    | 017554 | 2284 | 2260 |      |      |      |      |      |      |      |      |      |      |
| STRAC    |        | 2233 | 227  | 289  | 299  | 321  | 343  | 384  | 494  | 509  | 510  | 545  | 673  |
| STRAC    |        | 2268 | 2330 | 2331 | 2382 | 2417 | 2995 | 3036 | 3165 | 3166 | 3172 | 3309 | 3333 |
| STRAC    |        |      |      |      |      |      |      |      |      |      |      |      | 2265 |
| STRAC    |        |      |      |      |      |      |      |      |      |      |      |      | 3397 |

ERRORS DETECTED: 0  
 DEFAULT GLOBALS GENERATED 0

\*DZTABC,DZTABC/SOL/CRF:SYM=DSKM:SYSMAC.SML(400,1066).DSKZ:DZTABC.NEW(400,1237)  
 RUN-TIME: 45 58 2 SECONDS  
 RUN-TIME RATIO: 125/107=1.1  
 CORE USED: 34K (68 PAGES)

