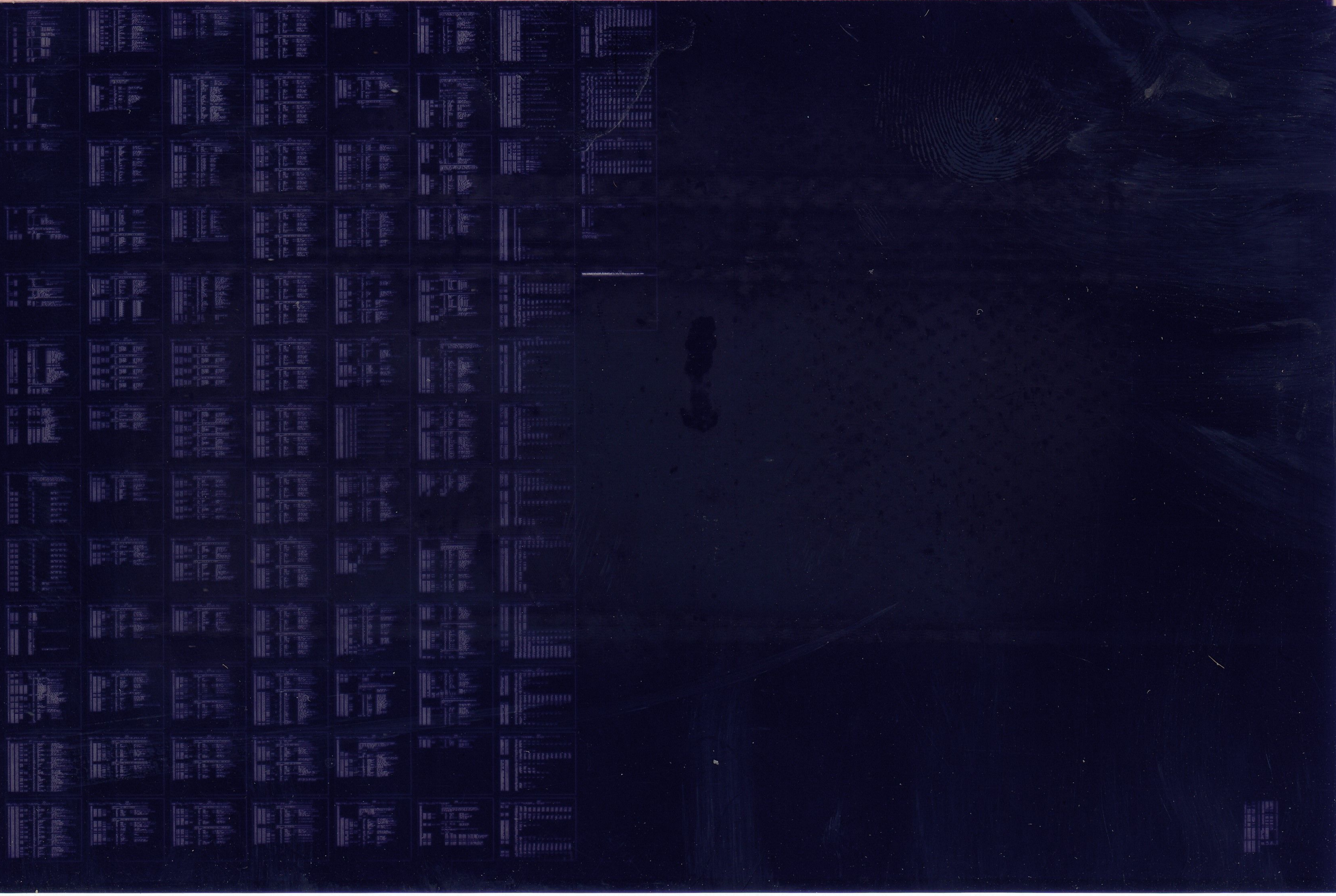


VSV01

VIDEO GRAPHIC TEST
MD-11-DZVSE-A

EP-DZVSE-A-DL-A
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FICHE 1 OF 1

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MADE IN USA




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.TITLE MAINDEC-11-DZVSE-A          VSV01 VIEDO GRAPHIC TEST PROGRAM
.*COPYRIGHT (C) 1976
.*DIGITAL EQUIPMENT CORP.
.*MAYNARD, MASS. 01754
.*
.*PROGRAM BY RAYMOND SHOOP
.*
.*THIS PROGRAM WAS ASSEMBLED USING THE PDP-11 MAINDEC SYSMAC
.*PACKAGE (MAINDEC-11-DZQAC-02), SEPT 14, 1976.
.*
.SBTTL BASIC DEFINITIONS

```

001100

```

.*INITIAL ADDRESS OF THE STACK POINTER *** 1100 ***
STACK= 1100
.EQUIV EMT.ERROR      ::BASIC DEFINITION OF ERROR CALL
.EQUIV IOT.SCOPE     ::BASIC DEFINITION OF SCOPE CALL

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.*MISCELLANEOUS DEFINITIONS
TAB= 11      ::CODE FOR HORIZONTAL TAB
LF= 12      ::CODE FOR LINE FEED
CR= 13      ::CODE FOR CARRIAGE RETURN
CRLF= 200   ::CODE FOR CARRIAGE RETURN-LINE FEED
PS= 177776  ::PROCESSOR STATUS WORD
.EQUIV PS.PSW
SYKLMT= 177774 ::STACK LIMIT REGISTER
PIRQ= 177772  ::PROGRAM INTERRUPT REQUEST REGISTER
DSWR= 177570  ::HARDWARE SWITCH REGISTER
DDISP= 177570 ::HARDWARE DISPLAY REGISTER

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.*GENERAL PURPOSE REGISTER DEFINITIONS
R0= %0      ::GENERAL REGISTER
R1= %1      ::GENERAL REGISTER
R2= %2      ::GENERAL REGISTER
R3= %3      ::GENERAL REGISTER
R4= %4      ::GENERAL REGISTER
R5= %5      ::GENERAL REGISTER
R6= %6      ::GENERAL REGISTER
R7= %7      ::GENERAL REGISTER
SP= %6      ::STACK POINTER
PC= %7      ::PROGRAM COUNTER

```

000000
000040
000100
000140
000200
000240
000300
000340

```

.*PRIORITY LEVEL DEFINITIONS
PR0= 0      ::PRIORITY LEVEL 0
PR1= 40     ::PRIORITY LEVEL 1
PR2= 100    ::PRIORITY LEVEL 2
PR3= 140    ::PRIORITY LEVEL 3
PR4= 200    ::PRIORITY LEVEL 4
PR5= 240    ::PRIORITY LEVEL 5
PR6= 300    ::PRIORITY LEVEL 6
PR7= 340    ::PRIORITY LEVEL 7

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100000
040000
020000
010000

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.*"SWITCH REGISTER" SWITCH DEFINITIONS
SW15= 100000
SW14= 40000
SW13= 20000
SW12= 10000

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.SBTTL COMMON TAGS

::*****
:*THIS TABLE CONTAINS VARIOUS COMMON STORAGE LOCATIONS
:*USED IN THE PROGRAM.

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.SMTAG: .S1100

WORD 0
\$STNM: .BYTE 0000
\$ERFLG: .BYTE 000
\$ICNT: .WORD 000000
\$LPADR: .WORD 000000
\$LPERR: .WORD 000000
\$ERTTL: .WORD 000000
\$ITEMB: .BYTE 000
\$ERMAX: .BYTE 001
\$ERRPC: .WORD 000000
\$GDADR: .WORD 000000
\$BDADR: .WORD 000000
\$GDAT: .WORD 000000
\$BDAT: .WORD 000000
\$AUTOB: .BYTE 000
\$INTAG: .BYTE 000
\$SWR: .WORD 0
\$DISPLAY: .WORD 0DISP
\$TKS: 177560
\$TKB: 177562
\$TPS: 177564
\$TPB: 177566
\$NULL: .BYTE 0
\$FILLS: .BYTE 2
\$FILLC: .BYTE 12
\$TPFLG: .BYTE 0
\$REGAD: .WORD 0
\$REGD: .WORD 0
\$REGI: .WORD 0
\$QUES: .ASCII ??
\$CRLF: .ASCII <15>
\$LF: .ASCII <12>

:::START OF COMMON TAGS
:::CONTAINS THE TEST NUMBER
:::CONTAINS ERROR FLAG
:::CONTAINS SUBTEST ITERATION COUNT
:::CONTAINS SCOPE LOOP ADDRESS
:::CONTAINS SCOPE RETURN FOR ERRORS
:::CONTAINS TOTAL ERRORS DETECTED
:::CONTAINS ITEM CONTROL BYTE
:::CONTAINS MAX. ERRORS PER TEST
:::CONTAINS PC OF LAST ERROR INSTRUCTION
:::CONTAINS ADDRESS OF 'GOOD' DATA
:::CONTAINS ADDRESS OF 'BAD' DATA
:::CONTAINS 'GOOD' DATA
:::CONTAINS 'BAD' DATA
:::RESERVED--NOT TO BE USED
:::AUTOMATIC MODE INDICATOR
:::INTERRUPT MODE INDICATOR
:::ADDRESS OF SWITCH REGISTER
:::ADDRESS OF DISPLAY REGISTER
:::TTY KBD STATUS
:::TTY KBD BUFFER
:::TTY PRINTER STATUS REG. ADDRESS
:::TTY PRINTER BUFFER REG. ADDRESS
:::CONTAINS NULL CHARACTER FOR FILLS
:::CONTAINS # OF FILLER CHARACTERS REQUIRED
:::INSERT FILL CHARS. AFTER A "LINE FEED"
:::"TERMINAL AVAILABLE" FLAG (BIT<07>=0=YES)
:::CONTAINS THE ADDRESS FROM WHICH (\$REGD) WAS OBTAINED
:::CONTAINS ((\$REGAD)+0)
:::CONTAINS ((\$REGAD)+2)
:::QUESTION MARK
:::CARRIAGE RETURN
:::LINE FEED

::*****
.SBTTL APT MAILBOX-ETABLE

:::*****
:EVEN
\$MAIL: .WORD 0
\$MSGTY: .WORD 0
\$FATAL: .WORD 0
\$TESTN: .WORD 0
\$PASS: .WORD 0
\$DEVCT: .WORD 0
\$UNIT: .WORD 0
:::APT MAILBOX
:::MESSAGE TYPE CODE
:::FATAL ERROR NUMBER
:::TEST NUMBER
:::PASS COUNT
:::DEVICE COUNT
:::I/C UNIT NUMBER

001206	000000	\$MSGAD: .WORD	AMSGAD	:: MESSAGE ADDRESS
001210	000000	\$MSGLG: .WORD	AMSGLG	:: MESSAGE LENGTH
001212		\$ETABLE:		:: APT ENVIRONMENT TABLE
001213	000	\$ENV: .BYTE	AENV	:: ENVIRONMENT BYTE
001213	000	\$ENVM: .BYTE	AENVM	:: ENVIRONMENT MODE BITS
001214	000000	\$SWREG: .WORD	ASWREG	:: APT SWITCH REGISTER
001216	000000	\$USWR: .WORD	AUSWR	:: USER SWITCHES
001218	000000	\$CPUOP: .WORD	ACPUOP	:: CPU TYPE, OPTIONS
		*		BITS 15-11=CPU TYPE
		*		11/04=01, 11/05=02, 11/20=03, 11/40=04, 11/45=05
		*		11/70=06, PDQ=07, Q=10
		*		BIT 10=REAL TIME CLOCK
		*		BIT 9=FLOATING POINT PROCESSOR
		*		BIT 8=MEMORY MANAGEMENT
001222	000	\$MAMS1: .BYTE	AMAMS1	:: HIGH ADDRESS, M.S. BYTE
001223	000	\$MTYP1: .BYTE	AMTYP1	:: MEM. TYPE, BLK#1
		*		MEM. TYPE BYTE -- (HIGH BYTE)
		*		900 NSEC CORE=001
		*		300 NSEC BIPOLAR=002
		*		500 NSEC MOS=003
001224	000000	\$MADR1: .WORD	AMADR1	:: HIGH ADDRESS, BLK#1
		*		MEM. LAST ADDR.=3 BYTES, THIS WORD AND LOW OF "TYPE" ABC.E
001226	000	\$MAMS2: .BYTE	AMAMS2	:: HIGH ADDRESS, M.S. BYTE
001227	000	\$MTYP2: .BYTE	AMTYP2	:: MEM. TYPE, BLK#2
001230	000000	\$MADR2: .WORD	AMADR2	:: MEM. LAST ADDRESS, BLK#2
001232	000	\$MAMS3: .BYTE	AMAMS3	:: HIGH ADDRESS, M.S. BYTE
001233	000	\$MTYP3: .BYTE	AMTYP3	:: MEM. TYPE, BLK#3
001234	000000	\$MADR3: .WORD	AMADR3	:: MEM. LAST ADDRESS, BLK#3
001236	000	\$MAMS4: .BYTE	AMAMS4	:: HIGH ADDRESS, M.S. BYTE
001237	000	\$MTYP4: .BYTE	AMTYP4	:: MEM. TYPE, BLK#4
001240	000000	\$MADR4: .WORD	AMADR4	:: MEM. LAST ADDRESS, BLK#4
001242	100360	\$VECT1: .WORD	AVECT1	:: INTERRUPT VECTOR#1, BUS PRIORITY#1
001244	000000	\$VECT2: .WORD	AVECT2	:: INTERRUPT VECTOR#2, BUS PRIORITY#2
001246	172600	\$BASE: .WORD	ABASE	:: BASE ADDRESS OF EQUIPMENT UNDER TEST
001250	000000	\$DEVN: .WORD	ADEVN	:: DEVICE MAP
001251	000000	\$CDW1: .WORD	ACDW1	:: CONTROLLER DESCRIPTION WORD#1
001252	000000	\$CDW2: .WORD	ACDW2	:: CONTROLLER DESCRIPTION WORD#2
001253		\$ETEND:		
001254		.MEXIT		

.SBTTL ERROR POINTER TABLE

:*THIS TABLE CONTAINS THE INFORMATION FOR EACH ERROR THAT CAN OCCUR.
:*THE INFORMATION IS OBTAINED BY USING THE INDEX NUMBER FOUND IN
:*LOCATION \$ITEMB. THIS NUMBER INDICATES WHICH ITEM IN THE TABLE IS PERTINENT.
:*NOTE1: IF \$ITEMB IS 0 THE ONLY PERTINENT DATA IS (\$ERRPC).
:*NOTE2: EACH ITEM IN THE TABLE CONTAINS 4 POINTERS EXPLAINED AS FOLLOWS:

:* EM ::POINTS TO THE ERROR MESSAGE
:* DH ::POINTS TO THE DATA HEADER
:* DT ::POINTS TO THE DATA
:* DF ::POINTS TO THE DATA FORMAT

001256

\$ERRTB:

:ITEM 1

001256 023032
001260 024004
001262 024226
001264 024322

EM1
DH1
DT1
DF0

:BUS ERROR TRAP ON CHARACTER ADDRESSES
:ERRPC ADR
:\$ERRPC \$BDDAT

:ITEM 2

001266 023133
001270 024004
001272 024226
001274 024322

EM2
DH1
DT1
DF0

:BUS ERROR TRAP ON BIT MAP ADDRESSES
:ERRPC ADR
:\$ERRPC \$BDDAT

:ITEM 3

001276 023214
001300 024060
001302 024234
001304 024322

EM3
DH3
DT3
DF0

:CHARACTER STATUS REGISTER ERROR
:ERRPC ADR GOOD BAD
:\$ERRPC VCCSR \$GDDAT \$BDDAT

:ITEM 4

001306 023254
001310 024105
001312 024246
001314 024322

EM4
DH4
DT4
DF0

:VERTICAL SYNC ERROR
:ERRPC ADR TIMED TIME1
:\$ERRPC VCCSR SAVED SAVE2

:ITEM 5

001316 023300
001320 024135
001322 024260
001324 024322

EM5
DH5
DT5
DF0

:VERTICAL SYNC ERROR/
:ERRPC VCCSR
:\$ERRPC VCCSR

:ITEM 6

001326 023336
001330 024151
001332 024266
001334 024322

EM6
DH6
DT6
DF0

:BIT MAP STATUS REGISTER ERROR
:ERRPC ADR GOOD BAD
:\$ERRPC VGCSR \$GDDAT \$BDDAT

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334			:ITEM	7		
335	001336	023374		EM7	:BIT MAP P.C. REGISTER ERROR	
336				DH6	:ERRPC VGCSR GOOD BAD	
337	001340	024151		DT6	:SERRPC VGCSR \$GDDAT \$BDDAT	
338	001342	024266		DF0		
339	001344	024322				
340			:ITEM	10		
341				EM10	:BIT MAP P.C INCREMENT ERROR	
342	001346	023430		DH6	:ERRPC VGCSR GOOD BAD	
343	001350	024151		DT6	:SERRPC VGCSR \$GDDAT \$BDDAT	
344	001352	024266		DF0		
345	001354	024322				
346			:ITEM	11		
347				EM11	:WORD ERROR ON PIXEL BIT 0	
348	001356	023465		DH6	:ERRPC VGCSR GOOD BAD	
349	001360	024151		DT6	:SERRPC VGCSR \$GDDAT \$BDDAT	
350	001362	024266		DF0		
351	001364	024322				
352			:ITEM	12		
353				EM12	:BYTE ERROR ON PIXEL BIT 0	
354	001366	023524		DH6	:ERRPC VGCSR GOOD BAD	
355	001370	024151		DT6	:SERRPC VGCSR \$GDDAT \$BDDAT	
356	001372	024266		DF0		
357	001374	024322				
358			:ITEM	13		
359				EM13	:CHARACTER READY ERROR	
360	001376	023563		DH6	:ERRPC VCCSR GOOD BAD	
361	001400	024151		DT6	:SERRPC VCCSR \$GDDAT \$BDDAT	
362	001402	024266		DF0		
363	001404	024322				
364			:ITEM	14		
365				EM14	:BIT MAP READY ERROR	
366	001406	023611		DH6	:ERRPC VGCSR GOOD BAD	
367	001410	024151		DT6	:SERRPC VGCSR \$GDDAT \$BDDAT	
368	001412	024266		DF0		
369	001414	024322				
370			:ITEM	15		
371				EM14	:BIT MAP READY FAILED TO SET	
372	001416	023611		DH15	:ERRPC VGCSR	
373	001420	024176		DT15		
374	001422	024300		DF0		
375	001424	024322				
376			:ITEM	16		
377				EM16	:PREVIOUSLY EXISTING BIT MAP DOES NOT EXIST NOW	
378	001426	023635		DH16	:ERRPC VSV01	
379	001430	024212		DT16	:SERRPC \$BDDAT	
380	001432	024306		DF0		
381	001434	024322				
382			:ITEM	17		
383				EM17	:PREVIOUSLY EXISTING VTVO1 CONTROLLER DOES NOT EXIST NOW	
384	001436	023714		DH16	:ERRPC VSV01	
385	001440	024212		DT16	:SERRPC \$GDADR	
386	001442	024306		DF0		
387	001444	024322				

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001450 172602
001452 172603
001454 172604
001456 172605
001460 172620
001462 172622
001464 172624
001466 172630

001470 100360
001472 100362

001474 000000
001476 000000
001500 000105
001502 000000
001504 000000
001506 000000
001510 000000
001512 000000
001514 000000
001516 000000
001520 003100
001522 000100
001524 000030
001526 000014
001530 000000
001532 000137
001534 000000
001536 000400
001540 000400
001542 010000
001544 000000
001546 000001
001550 167772
001552 000000
001554 000000
001556 000000
001560 000000
001562 000000

:VTVD1 BUS ADDRESSES AND VECTOR

.SBTTL VSVD1 BUS ADDRESSES AND VECTORS

VCCSR: ABASE
VCHRL0: ABASE+2
VCHRHI: ABASE+3
VCPOS: ABASE+4
VCPOSH: ABASE+5
VGCSR: ABASE+20
VGPC: ABASE+22
VGBUF: ABASE+24
VGCOLR: ABASE+30

VCINT: AVECT1
VCINT1: AVECT1+2

:MISC. LOCATIONS

.SBTTL MISC. LOCATIONS

\$TEMP: 0
\$TEMPO: 0
KE: 105
TEMP1: 0
TEMP2: 0
TEMP3: 0
SAVE0: 0
SAVE1: 0
SAVE2: 0
SAVE3: 0
TOTALC: 1600.
WIDTH: 64.
VHO: 24.
VHI: 12.
STCHAR: 0
LASTCH: 137
TEMPO: 0
XHAIR: 256.
YHAIR: 256.
NUMPIX: 4096.
BURN: 0
COAXSW: 1
COAX: 167772
MSAVE0: 0
MSAVE1: 0
MSAVE2: 0
MSAVE3: 0
MSAVE4: 0

:ASCII CHARACTER USED IN FULL SCREEN PATTERN

:BIT 0-4 SELECT RELAY CONTACTS IN "BURN-IN" MODE
:BUS ADDRESS OF COAX SWITCH


```

439 001564 005237 001544      BURNIN: INC      BURN      :SET BURN-IN MODE FLAG
440 001570 000402              BR      CONT
441 001572 005037 001544      BEGIN: CLR      BURN      :CLEAR "BURN-IN " MODE FLAG
442 001576              CONT:
443
444      .SBTTL INITIALIZE THE COMMON TAGS
      ;;CLEAR THE COMMON TAGS ($CMTAG) AREA
445 001576 012706 001100      MOV      #SCMTAG,R6      ;;FIRST LOCATION TO BE CLEARED
446 001602 005026              CLR      (R6)+           ;;CLEAR MEMORY LOCATION
447 001604 022706 001140      CMP      #SWR,R6 ;;DONE?
448 001610 001374              BNE      -6              ;;LOOP BACK IF NO
449 001612 012706 001100      MOV      #STACK,SP      ;;SETUP THE STACK POINTER
450      ;;INITIALIZE A FEW VECTORS
451 001616 012737 017706 000020      MOV      #SCOPE,@IOTVEC ;; IOT VECTOR FOR SCOPE ROUTINE
452 001624 012737 000340 000022      MOV      #340,@IOTVEC+2 ;; LEVEL 7
453 001632 012737 020036 000030      MOV      #ERROR,@EMTVEC ;; EMT VECTOR FOR ERROR ROUTINE
454 001640 012737 000340 000032      MOV      #340,@EMTVEC+2 ;; LEVEL 7
455 001646 012737 022630 000034      MOV      #TRAP,@TRAPVEC ;; TRAP VECTOR FOR TRAP CALLS
456 001654 012737 000340 000036      MOV      #340,@TRAPVEC+2;LEVEL 7
457 001662 012737 021002 000024      MOV      #SPWRDN,@PWRVEC ;;POWER FAILURE VECTOR
458 001670 012737 000340 000026      MOV      #340,@PWRVEC+2 ;;LEVEL 7
459 001676 013737 003252 003244      MOV      #ENDCT,$EOPCT   ;;SETUP END-OF-PROGRAM COUNTER
460 001704 012737 001704 001106      MOV      #,$SLPADR      ;;INITIALIZE THE LOOP ADDRESS FOR SCOPE
461      ;;SIZE FOR A HARDWARE SWITCH REGISTER. IF NOT FOUND OR IT IS
462      ;;EQUAL TO A "-1", SETUP FOR A SOFTWARE SWITCH REGISTER.
463 001712 013746 000004              MOV      @ERRVEC,-(SF)   ;;SAVE ERROR VECTOR
464 001716 012737 001752 000004      MOV      #64$,@ERRVEC   ;;SET UP ERROR VECTOR
465 001724 012737 177570 001140      MOV      #DSWR,SWR      ;;SETUP FOR A HARDWARE SWICH REGISTER
466 001732 012737 177570 001142      MOV      #DDISP,DISPLAY ;;AND A HARDWARE DISPLAY REGISTER
467 001740 022777 177777 177172      CMP      #-1,@SWR      ;;TRY TO REFERENCE HARDWARE SWR
468 001746 001012              BNE      66$           ;;BRANCH IF NO TIMEOUT TRAP OCCURRED
469      ;;AND THE HARDWARE SWR IS NOT = -1
470 001750 000403              BR      65$           ;;BRANCH IF NO TIMEOUT
471 001752 012716 001760      64$: MOV      #65$,(SP)     ;;SET UP FOR TRAP RETURN
472 001756 000002              RTI
473 001760 012737 000176 001140      65$: MOV      #SWREG,SWR   ;;POINT TO SOFTWARE SWR
474 001766 012737 000174 001142      MOV      #DISPRG,DISPLAY
475 001774 012637 000004      66$: MOV      (SP)+,@ERRVEC ;;RESTORE ERROR VECTOR
476
477 002000 005037 001200              CLR      $PASS          ;;CLEAR PASS COUNT
478 002004 132737 000200 001213      BITB    #APTSIZE,$ENVM  ;;TEST USER SIZE UNDER APT
479 002012 001403              BEQ      67$           ;;YES,USE NON-APT SWITCH
480 002014 012737 001214 001140      MOV      #SWREG,SWR    ;;NO,USE APT SWITCH REGISTER
481 002022
482 002022 005037 001552              CLR      MSAVED         ;;RESET MAP COUNTERS
483 002026 005037 001554              CLR      MSAVE1
484 002032 005037 001556              CLR      MSAVE2         ; AND CONTROLLER
485 002036 005037 001560              CLR      MSAVE3
486 002042 005037 001562              CLR      MSAVE4         ; LOCATIONS
487 002046 005737 000042              TST     @#42           ;;TEST IF IN CHAIN MODE
488 002052 001002              BNE      RESTAT
489 002054 104401              TYPE
490 002056 022712              TITLE
491 002060 000005      RESTAT: RESET

```

MO1

MAINDEC-11-DZVSE-A
DZVSEA.P11 02-JUN-76 00:00

VSVO1 VIEDC GRAPHIC TEST PROGRAM

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```

492          .SBTTL
493          .SBTTL DETERMINE THE NUMBER OF CONTROLLERS TO TEST
494
495 002062 013737 001246 001120 DETERO: MOV    $BASE,$GDADR      ;LOAD BASE ADDR.
496 002070 012737 002242 007366      MOV    #4$,RETURN      ;LOAD RETURN ADDR. AFTER TESTING
497 002076 004737 003340      JSR    PC,DETECT      ;DETERMINE # OF MAPS
498 002102 000453      BR     3$             ;BR IF CONTROLLER DOESN'T EXIST
499 002104 004737 003576      JSR    PC,LOADR      ;FIX THE BUS ADDRESSES
500 002110 005737 001254      TST   $CDW2          ;TEST IF THE OPERATOR HAS SELECTED SOME MAPS
501 002114 001407      BEQ   6$             ;BR IF NOT
502 002116 013737 001254 001252      MOV    $CDW2,$CDW1   ;LOAD HIS SELECTION
503 002124 052737 100000 001552      BIS   #BIT15,MSAVEO ;SET CONTROLLER EXISTS FLAG
504 002132 000430      BR     2$             ;AND TEST THEM
505 002134 105737 001213      6$:  TSTB  $ENVM        ;TEST IF "DO NOT SIZE" IS SET
506 002140 100007      BPL   5$             ;BR IF NOT
507 002142 013737 001250 001252      MOV    $DEVM,$CDW1   ;USE VALUE SUPPLIED BY "APT"
508 002150 052737 100000 001552      BIS   #BIT15,MSAVEO ;SET CONTROLLER EXISTS FLAG
509 002156 000416      BR     2$
510 002160 005737 001200      5$:  TST   $PASS        ;TEST IF FIRST PASS
511 002164 001006      BNE   1$             ;BR IF NOT
512 002166 013737 001512 001552      MOV    SAVE1,MSAVEO ;SAVE THE NUMBER OF MAPS
513 002174 052737 100000 001552      BIS   #BIT15,MSAVEO ;SET "CONTROLLER EXISTS" FLAG
514 002202 123737 001512 001552 1$:  CMPB  SAVE1,MSAVEO ;COMPARE IF ANY CHANGE IN # OF MAPS
515 002210 001401      BEQ   2$             ;BR IF NOT
516 002212 104016      ERROR 16             ;PREVIOUSLY EXISTING BIT MAP DOES NOT EXIST NOW
517 002214 012737 000001 001546 2$:  MOV    #1,COAXSW     ;LOAD SWITCH VALUE
518 002222 005037 001102      CLR   $STNM         ;RESET TEST #
519 002226 000137 003762      JMP   TST1          ;AND TEST THIS CONTROLLER
520 002232 005737 001552      3$:  TST   MSAVEO        ;TEST IF CONTROLLER EXISTED AT ONE TIME
521 002236 100001      BPL   4$             ;BR IF NO
522 002240 104017      ERROR 17             ;PREVIOUSLY EXISTING CONTROLLER AT 172600 DOES NOT EXIST NOW
523 002242 000240      4$:  NOP
524 002244 005737 001552      TST   MSAVEO        ;TEST IF THE FIRST CONTROLLER EXISTS
525 002250 100413      BMI   10$           ;BR IF IT'S THERE
526 002252 012737 002274 001110      MOV    #7$,$LPERR   ;LOAD LOOP RETURN
527 002260 012737 002274 00110E      MOV    #7$,$LPADR   ;LOAD RETURN ADDRESS
528 002266 013737 001246 001126      MOV    $BASE,$BDDAT ;LOAD BASE ADDRESS FOR TYPEOUT
529 002274 104001      7$:  ERROR 1 ;** FATAL ERROR ** ;VTVO1 AT ADDRESS ($BASE) DOESN'T EXIST
530 002276 000776      BR     7$           ;FATAL ERROR
531 002300 005737 001544      10$: TST   BURN         ;TEST IF "BURN-IN" MODE
532 002304 001002      BNE   DETER1        ;BR AND TEST FOR MORE MAPS
533 002306 000137 003222      JMP   DETEC7        ;REPORT "EOP"
534 002312 012737 171600 001120 DETER1: MOV    #171600,$GDADR ;LOAD BUS ADDRESS
535 002320 012737 002472 007366      MOV    #4$,RETURN   ;LOAD RETURN ADDR. AFTER TESTING
536 002326 004737 003340      JSR    PC,DETECT    ;DETERMINE # OF MAPS
537 002332 000453      BR     3$           ;BR IF CONTROLLER DOESN'T EXIST
538 002334 004737 003576      JSR    PC,LOADR     ;FIX THE BUS ADDRESSES
539 002340 005737 001254      TST   $CDW2        ;TEST IF THE OPERATOR HAS SELECTED SOME MAPS
540 002344 001407      BEQ   6$           ;BR IF NOT
541 002346 013737 001254 001252      MOV    $CDW2,$CDW1 ;LOAD HIS SELECTION
542 002354 052737 100000 001554      BIS   #BIT15,MSAVE1 ;SET CONTROLLER EXISTS FLAG
543 002362 000430      BR     2$           ;AND TEST THEM
544 002364 105737 001213      6$:  TSTB  $ENVM        ;TEST IF "DO NOT SIZE" IS SET
545 002370 100007      BPL   5$           ;BR IF NOT
546 002372 013737 001250 001252      MOV    $DEVM,$CDW1 ;USE VALUE SUPPLIED BY "APT"
547 002400 052737 100000 001554      BIS   #BIT15,MSAVE1 ;SET CONTROLLER EXISTS FLAG

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NO1

MAINDEC-11-DZVSE-A
DZVSEA.P11

02-JUN-76 03:00

VSV01 VIEDO GRAPHIC TEST PROGRAM
DETERMINE THE NUMBER OF CONTROLLERS TO TEST

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548 002406 000416          BR      2$
549 002410 005737 001200 5$:  TST      $PASS          ;TEST IF FIRST PASS
550 002414 001006          BNE     1$          ;BR IF NOT
551 002416 013737 001512 001554 MOV     SAVE1,MSAVE1 ;SAVE THE NUMBER OF MAPS
552 002424 052737 100000 001554 BIS     #BIT15,MSAVE1 ;SET "CONTROLLER EXISTS" FLAG
553 002432 123737 001512 001554 1$: CMPB   SAVE1,MSAVE1 ;COMPARE IF ANY CHANGE IN # OF MAPS
554 002440 001401          BEQ     2$          ;BR IF NOT
555 002442 104016          ERROR   16          ;PREVIOUSLY EXISTING BIT MAP DOES NOT EXIST NOW
556 002444 012737 000002 001546 2$:  MOV     #2,COAXSW ;LOAD SWITCH VALUE
557 002452 005037 001102          CLR     $TSTNM ;RESET TEST #
558 002456 000137 003762          JMP     TST1    ;AND TEST THIS CONTROLLER
559 002462 005737 001554          TST     MSAVE1 ;TEST IF CONTROLLER EXISTED AT ONE TIME
560 002466 100001          BPL     4$          ;BR IF NO
561 002470 104017          ERROR   17          ;PREVIOUSLY EXISTING CONTROLLER AT 171600 DOES NOT EXIST NOW
562 002472 000240          NOP
563 002474 012737 171300 001120 DETER2: MOV     #171300,$GDADR ;LOAD BUS ADDRESS
564 002502 012737 002654 007366 MOV     #4$,RETURN ;LOAD RETURN ADDR. AFTER TESTING
565 002510 004737 003340          JSR    PC,DETECT ;DETERMINE # OF MAPS
566 002514 000453          BR     3$          ;BR IF CONTROLLER DOESN'T EXIST
567 002516 004737 003576          JSR    PC,LOADR  ;FIX THE BUS ADDRESSES
568 002522 005737 001254          TST     $CDW2   ;TEST IF THE OPERATOR HAS SELECTED SOME MAPS
569 002526 001407          BEQ     6$          ;BR IF NOT
570 002530 013737 001254 001252 MOV     $CDW2,$CDW1 ;LOAD HIS SELECTION
571 002536 052737 100000 001556 BIS     #BIT15,MSAVE2 ;SET CONTROLLER EXISTS FLAG
572 002544 000430          BR     2$          ;AND TEST THEM
573 002546 105737 001213          TSTB   $ENVM    ;TEST IF "DO NOT SIZE" IS SET
574 002552 100007          BPL     5$          ;BR IF NOT
575 002554 013737 001250 001252 MOV     $DEVM,$CDW1 ;USE VALUE SUPPLIED BY "APT"
576 002562 052737 100000 001556 BIS     #BIT15,MSAVE2 ;SET CONTROLLER EXISTS FLAG
577 002570 000416          BR     2$
578 002572 005737 001200          TST     $PASS   ;TEST IF FIRST PASS
579 002576 001006          BNE     1$          ;BR IF NOT
580 002600 013737 001512 001556 MOV     SAVE1,MSAVE2 ;SAVE THE NUMBER OF MAPS
581 002606 052737 100000 001556 BIS     #BIT15,MSAVE2 ;SET "CONTROLLER EXISTS" FLAG
582 002614 123737 001512 001556 1$: CMPB   SAVE1,MSAVE2 ;COMPARE IF ANY CHANGE IN # OF MAPS
583 002622 001401          BEQ     2$          ;BR IF NOT
584 002624 104016          ERROR   16          ;PREVIOUSLY EXISTING BIT MAP DOES NOT EXIST NOW
585 002626 012737 000004 001546 2$:  MOV     #4,COAXSW ;LOAD SWITCH VALUE
586 002634 005037 001102          CLR     $TSTNM ;RESET TEST #
587 002640 000137 003762          JMP     TST1    ;AND TEST THIS CONTROLLER
588 002644 005737 001556          TST     MSAVE2 ;TEST IF CONTROLLER EXISTED AT ONE TIME
589 002650 100001          BPL     4$          ;BR IF NO
590 002652 104017          ERROR   17          ;PREVIOUSLY EXISTING CONTROLLER AT 171300 DOES NOT EXIST NOW
591 002654 000240          NOP
592 002656 012737 172300 001120 DETER3: MOV     #172300,$GDADR ;LOAD BUS ADDRESS
593 002664 012737 003036 007366 MOV     #4$,RETURN ;LOAD RETURN ADDR. AFTER TESTING
594 002672 004737 003340          JSR    PC,DETECT ;DETERMINE # OF MAPS
595 002676 000453          BR     3$          ;BR IF CONTROLLER DOESN'T EXIST
596 002700 004737 003576          JSR    PC,LOADR  ;FIX THE BUS ADDRESSES
597 002704 005737 001254          TST     $CDW2   ;TEST IF THE OPERATOR HAS SELECTED SOME MAPS
598 002710 001407          BEQ     6$          ;BR IF NOT
599 002712 013737 001254 001252 MOV     $CDW2,$CDW1 ;LOAD HIS SELECTION
600 002720 052737 100000 001560 BIS     #BIT15,MSAVE3 ;SET CONTROLLER EXISTS FLAG
601 002726 000430          BR     2$          ;AND TEST THEM
602 002730 105737 001213          TSTB   $ENVM    ;TEST IF "DO NOT SIZE" IS SET
603 002734 100007          BPL     5$          ;BR IF NOT

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604 002736 013737 001250 001252 MOV $DEVM,$CDW1 ;USE VALUE SUPPLIED BY "APT"
605 002744 052737 100000 001560 BIS #BIT15,MSAVE3 ;SET CONTROLLER EXISTS FLAG
606 002752 000416 BR 25
607 002754 005737 001200 55: TST $PASS ;TEST IF FIRST PASS
608 002760 001006 BNE 15 ;BR IF NOT
609 002762 013737 001512 001560 MOV SAVE1,MSAVE3 ;SAVE THE NUMBER OF MAPS
610 002770 052737 100000 001560 BIS #BIT15,MSAVE3 ;SET "CONTROLLER EXISTS" FLAG
611 002776 123737 001512 001560 15: CMPB SAVE1,MSAVE3 ;COMPARE IF ANY CHANGE IN # OF MAPS
612 003004 001401 BEQ 25 ;BR IF NOT
613 003006 104016 ERROR 16 ;PREVIOUSLY EXISTING BIT MAP DOES NOT EXIST NOW
614 003010 012737 000010 001546 25: MOV #10,COAXSW ;LOAD SWITCH VALUE
615 003016 005037 001102 CLR $STNM ;RESET TEST #
616 003022 000137 003762 JMP TST1 ;AND TEST THIS CONTROLLER
617 003026 005737 001560 35: TST MSAVE3 ;TEST IF CONTROLLER EXISTED AT ONE TIME
618 003032 100001 BPL 45 ;BR IF NO
619 003034 104017 ERROR 17 ;PREVIOUSLY EXISTING CONTROLLER AT 172300 DOES NOT EXIST NOW
620 003036 000240 45: NOP
621 003040 012737 173200 001120 DETER4: MOV #173200,$GDADR ;LOAD BUS ADDRESS
622 003046 012737 003220 007366 MOV #45,RETURN ;LOAD RETURN ADDR. AFTER TESTING
623 003054 004737 003340 JSR PC,DETECT ;DETERMINE # OF MAPS
624 003060 000453 BR 35 ;BR IF CONTROLLER DOESN'T EXIST
625 003062 004737 003576 JSR PC,LOADADR ;FIX THE BUS ADDRESSES
626 003066 005737 001254 TST $CDW2 ;TEST IF THE OPERATOR HAS SELECTED SOME MAPS
627 003072 001407 BEQ 65 ;BR IF NOT
628 003074 013737 001254 001252 MOV $CDW2,$CDW1 ;LOAD HIS SELECTION
629 003102 052737 100000 001562 BIS #BIT15,MSAVE4 ;SET CONTROLLER EXISTS FLAG
630 003110 000430 BR 25 ;AND TEST THEM
631 003112 105737 001213 65: TSTB $ENVM ;TEST IF "DO NOT SIZE" IS SET
632 003116 100007 BPL 55 ;BR IF NOT
633 003120 013737 001250 001252 MOV $DEVM,$CDW1 ;USE VALUE SUPPLIED BY "APT"
634 003126 052737 100000 001562 BIS #BIT15,MSAVE4 ;SET CONTROLLER EXISTS FLAG
635 003134 000416 BR 25
636 003136 005737 001200 55: TST $PASS ;TEST IF FIRST PASS
637 003142 001006 BNE 15 ;BR IF NOT
638 003144 013737 001512 001562 MOV SAVE1,MSAVE4 ;SAVE THE NUMBER OF MAPS
639 003152 052737 100000 001562 BIS #BIT15,MSAVE4 ;SET "CONTROLLER EXISTS" FLAG
640 003160 123737 001512 001562 15: CMPB SAVE1,MSAVE4 ;COMPARE IF ANY CHANGE IN # OF MAPS
641 003166 001401 BEQ 25 ;BR IF NOT
642 003170 104016 ERROR 16 ;PREVIOUSLY EXISTING BIT MAP DOES NOT EXIST NOW
643 003172 012737 000020 001546 25: MOV #20,COAXSW ;LOAD SWITCH VALUE
644 003200 005037 001102 CLR $STNM ;RESET TEST #
645 003204 000137 003762 JMP TST1 ;AND TEST THIS CONTROLLER
646 003210 005737 001562 35: TST MSAVE4 ;TEST IF CONTROLLER EXISTED AT ONE TIME
647 003214 100001 BPL 45 ;BR IF NO
648 003216 104017 ERROR 17 ;PREVIOUSLY EXISTING CONTROLLER AT 173200 DOES NOT EXIST NOW
649 003220 000240 45: NOP
    
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DETEC7:
.SBTTL END OF PASS ROUTINE

:*****
:*INCREMENT THE PASS NUMBER ($PASS)
:*TYPE "END PASS *XXXXX" (WHERE XXXXX IS A DECIMAL NUMBER)
:*IF THERES A MONITOR GO TO IT
:*IF THERE ISN'T JUMP TO RESTAT

SEOP:
NOP
CLR
$STNM          ;;ZERO THE TEST NUMBER
$PASS         ;;INCREMENT THE PASS NUMBER
INC           ;;DON'T ALLOW A NEG. NUMBER
BIC          #10000,$PASS
DEC          (PC)+ ;;LOOP?

SEOPCT: .WORD 1
BGT $DOAGN   ;;YES
MOV (PC)+,2(PC)+ ;;RESTORE COUNTER

SENDCT: .WORD 1
SEOPCT

SENDMG: TYPE $SENDMG ;;TYPE "END PASS *"
MOV $PASS,-(SP) ;;SAVE $PASS FOR TYPEOUT
TYPDS ;;GO TYPE--DECIMAL ASCII WITH SIGN
TYPE $ENULL ;;TYPE A NULL CHARACTER

$GET42: MOV #42,R0 ;;GET MONITOR ADDRESS
BEQ $DOAGN ;;BRANCH IF NO MONITOR
RESET ;;CLEAR THE WORLD
SENDAD: JSR PC,(R0) ;;GO TO MONITOR
NOP ;;SAVE ROOM
NOP ;;FOR
NOP ;;ACT11

$DOAGN: JMP 2(PC)+ ;;RETURN

$RTNAD: .WORD RESTAT
$ENULL: .BYTE -1,-1,0 ;;NULL CHARACTER STRING
$SENDMG: .ASCIZ (<15><12> END PASS *

```

DETERMINE THE NUMBER OF BIT MAP'S ON A VTVO1

```

689          .SBTTL DETERMINE THE NUMBER OF BIT MAP'S ON A VTVO1
690          .SBTTL
691
692 003340 013737 001120 001124 DETECT: MOV      $GDADR,$GDDAT      :GET BASE ADDRESS
693 003346 012737 003570 000004      MOV      #7,$ERRVEC      :LOAD RETURN
694 003354 005777 175544      TST     $GDDAT          :TEST THE CHARACTER STATUS ADDRESS
695 003360 005037 001504      CLR     TEMP2          :THE CONTROLLER EXISTS NOW TEST THE MAPS
696 003364 005037 001506      CLR     TEMP3          :
697 003370 012737 003434 000004      MOV      #2,$ERRVEC      :LOAD BUS TRAP RETURN
698 003376 013737 001124 001126      MOV      $GDDAT,$BDDAT   :GET THE ADDRESS
699 003404 062737 000020 001126 15:      ADD     #20,$BDDAT      :UPDATE THE ADDRESS
700 003412 005777 175510      TST     $BDDAT          :TEST FOR A MAP
701 003416 005237 001504      INC     TEMP2          :YES IT EXISTS UPDATE THE COUNT
702 003422 022737 000006 001504      CMP     #6,TEMP2        :TEST FO LAST
703 003430 001402      BEQ     3$             :
704 003432 000764      BR     1$             :
705 003434 022626      CMP     (SP)+,(SP)+      :POP THE STACK
706 003436 012737 000006 000004 2$:      MOV      #ERRVEC+2,ERRVEC :;RESET BUS TRAP
707 003444 005037 000003      CLR     ERRVEC+2        :
708 003450 005737 000042      TST     #42            :TEST IF "CHAIN MODE"
709 003454 001020      BNE     4$            :BR IF CHAIN MODE
710 003456 005737 001200      TST     $PASS          :TEST IF FIRST PASS
711 003462 001015      BNE     4$            :BR IF NOT
712 003464 104401 022750      TYPE   VTHDR1          :REPORT # OF MAPS
713 003470 013746 001124      MOV     $GDDAT,-(SP)    :ON THE CONTROLLER ADDRESS
714 003474 104402      TYPOC
715 003476 104401 022774      TYPE   VTHDR2          : "HAS"
716 003502 013746 001504      MOV     TEMP2,-(SP)    :#N
717 003506 104403      TYPOS
718 003510      001      000      .BYTE 1,0             :ONE DIGIT
719 003512 104401 023002      TYPE   VTHDR3          : "MAPS INSTALLED"
720 003516 013737 001504 001512 4$:      MOV     TEMP2,SAVE1    :SAVE # OF MAPS
721 003524 001413      BEQ     6$            :BR IF NONE
722 003526 012737 000001 001506      MOV     #1,TEMP3       :LOAD INDICATOR
723 003534 005337 001504 5$:      DEC     TEMP2          :CONVERT FOR LATER
724 003540 001405      BEQ     6$            :
725 003542 006337 001506      ASL    TEMP3          :MOVE
726 003546 005237 001506      INC    TEMP3          :
727 003552 000770      BR     5$            :
728 003554 013737 001506 001252 6$:      MOV     TEMP3,$CDWI     :LOAD THE DETECTED MAP BIT'S
729 003562 062716 000002      ADD     #2,(SP)        :UPDATE EXIT ADDRESS
730 003566 000207      RTS     PC             :RETURN
731
732 003570 022626      7$:      CMP     (SP)+,(SP)+
733 003572 000240      NOP
734 003574 000207      RTS     PC             :RETURN
735
  
```


E02

MAINDEC-11-02VSE-A
02VSEAF11 02-JUN-76

VSWO: VIEDO GRAPHIC TEST PROGRAM
00:00

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736 003576 012700 001446 LODADR: MOV #VCCSR,RO :LOAD ADDRESS POINTER
737 003582 013701 001120 MOV $GDADR,RI :LOAD ADDRESS VALUE
738 003606 010120 10S: MOV R1,(R0)+ :LOAD THE ADDRESSES
739 003610 022700 001470 CMP #VCINT,RO :TEST FOR END
740 003614 001374 BNE 10S
741 003616 062737 000002 001450 ADD #2,VCHRLO :ADJUST THE ADDRESSES
742 003624 062737 000003 001452 ADD #3,VCHRHI
743 003632 062737 000004 001454 ADD #4,VCPOS
744 003640 062737 000005 001456 ADD #5,VCPOSH
745 003646 062737 000020 001460 ADD #20,VGCSR
746 003654 062737 000022 001462 ADD #22,VGPC
747 003662 062737 000024 001464 ADD #24,VGBUF
748 003670 062737 000030 001466 ADD #30,VGCOLR
749 003676 013737 001242 001470 MOV $VECT1,VCINT :LOAD BASIC INTR. VECTOR
750 003704 042737 160000 001470 BIC #160000,VCINT :MASK OFF BP LEVEL BITS
751 003712 013737 001470 001472 MOV VCINT,VCINT1
752 003720 062737 000002 001472 ADD #2,VCINT1
753 003726 000207 RTS :EXIT
754
755 003730 062737 000020 001460 MOVADR: ADD #20,VGCSR :UPDATE BUS ADDRESSES TO NEXT MAP
756 003736 062737 000020 001462 ADD #20,VGPC
757 003744 062737 000020 001464 ADD #20,VGBUF
758 003752 062737 000020 001466 ADD #20,VGCOLR
759 003760 000207 RTS :EXIT
760
761
762
763
764 003762 000004
765 003764 012737 177777 001204
766 003772 012737 004030 001110
767 004000 013737 001120 001124
768 004006 062737 000006 001124
769 004014 013737 001120 001126
770 004022 012737 004054 000004
771 004030 005777 175072
772 004034 023737 001124 001126
773 004042 001407
774 004044 062737 000002 001126
775 004052 000766
776
777 004054 022626 2S: CMP (SP)+,(SP)+ :CLEAN STACK
778 004056 104001 ERRGR 1 :BUS ERROR WITH CHARACTER ADDRESS
779 004060 000763 BR 1S
780
781 004062 012737 000006 000004 3S: MOV #ERRVEC+2,ERRVEC :RESET BUS TRAP
782 004070 005037 000006 CLR #ERRVEC+2
783

```

```

:*****
:*TEST 1 VERIFY ALL BUS ADDRESSES OF THE CHARACTER GEN.
:*****

```

```

↑ST1: SCOPE
MOV #-1,$UNIT :INDICATE A SYNC/ CHARACTER GENERATOR LOGIC ERROR
MOV #1,$SLPERR :LOAD ERROR RETURN
MOV $GDADR,$GDADR :LOAD BASE ADDR.
ADD #5,$GDADR :ADJUST TO LAST ADDRESS
MOV $GDADR,$BDDAT :LOAD TEST ADDRESS
MOV #25,$ERRVEC :LOAD BUS TRAP RETURN
1S: TST $BDDAT :TEST IF ADDRESS EXISTS
CMP $GDADR,$BDDAT :TEST IF FINISHED ALL ADDR.
BEQ 3S ;;BR IF ALL DONE
ADD #2,$BDDAT :ADJUST ADDRESS POINTER
BR 1S :TRY AGAIN
2S:
3S:

```

F02

```

794
795
796
797
798 004074 000004
799 004076 012737 004150 001110
800 004104 013737 001120 001502
801 004112 062737 000020 001502
802 004120 013737 001252 004360
803 004126 042737 177700 004360
804 004134 001426
805 004136 005037 001204
806 004142 012737 000001 001506
807 004150 033737 001506 004360 1$:
808 004156 001402
809 004160 000137 004214
810 004164 062737 000020 001502 2$:
811 004172 005237 001204
812 004176 006337 001506
813 004202 022737 000100 001506
814 004210 001357
815 004212
816 004212 000463 3$:
817
818
819
820
821
822
823
824
825
826
827
828
829
830
831
832
833
834
835
004214 012737 004352 000004 10$:
004222 013737 001502 001126
004230 005777 174672
004234 062737 000002 001126
004242 005777 174660
004246 062737 000002 001126
004254 005777 174646
004260 062737 000002 001126
004266 005777 174634
004272 062737 000002 001126
004300 005777 174622
004304 062737 000002 001126
004312 005777 174610
004316 062737 000002 001126
004324 005777 174576
004330 062737 000002 001126
004336 005777 174564
004342 062737 000002 001126
004350 000705
004352 022626 20$:
004354 104002
004356 000702
004360 000003
  
```

```

:*****
:*TEST 2 TEST ALL EXISTANT BUS ADDRESSES FOR THE SELECTED BIT MAPS
:*****
TST2: SCOPE
MOV #15,$LPERP ;LOAD RETURN IF ERROR
MOV $GDOR,TEMP1 ;GET BASE ADDRESS
ADD #20,TEMP1 ;UPDATE TO THE MAP ADDRESS
MOV $CDW1,DEVTMP ;GET THE SELECTED MAPS
BIC #177700,DEVTMP ;MASK TO LOWER SIX BITS
BEQ 3$ ;BR IF NO MAPS SELECTED
CLR $UNIT ;CLEAR MAP INDICATOR
MOV #BIT0,TEMP3 ;LOAD TESTING BIT
BIT TEMP3,DEVTMP ;TEST IF THIS BIT MAP IS SELECTED
BEQ 2$ ;BR IF NOT
JMP 10$ ;SELECTED - TEST THIS MAP
ADD #20,TEMP1 ;UPDATE TO NEXT BUS ADDRESS
INC $UNIT ;UPDATE MAP INDICATOR
ASL TEMP3 ;MOVE THE TEST BIT
CMP #100,TEMP3 ;TEST IF ANY MORE POSSIBLE MAPS
BNE 1$ ;BR IF MORE
3$: BR TST3 ;:BR TO NEXT TEST

;THE BIT MAP ADDRESS IS IN "TEMP1" - LOAD THE ADDRESS AND
; START TESTING THAT BIT MAP

10$: MOV #20,$#4 ;LOAD FOR BUS TRAP
MOV TEMP1,$BDDAT ;LOAD ADDRESS POINTER
TST $BDDAT ;TEST AN ADDRESS
ADD #2,$BDDAT ;UPDATE ADDRESS
TST $BDDAT ;TEST AN ADDRESS
ADD #2,$BDDAT ;UPDATE ADDRESS
TST $BDDAT ;TEST AN ADDRESS
ADD #2,$BDDAT ;UPDATE ADDRESS
TST $BDDAT ;TEST AN ADDRESS
ADD #2,$BDDAT ;UPDATE ADDRESS
TST $BDDAT ;TEST AN ADDRESS
ADD #2,$BDDAT ;UPDATE ADDRESS
TST $BDDAT ;TEST AN ADDRESS
ADD #2,$BDDAT ;UPDATE ADDRESS
TST $BDDAT ;TEST AN ADDRESS
ADD #2,$BDDAT ;UPDATE ADDRESS
TST $BDDAT ;TEST AN ADDRESS
ADD #2,$BDDAT ;UPDATE ADDRESS
BR 2$ ;RETURN

20$: CMP (SP)+,(SP)+ ;POP STACK
ERROR 2 ;REPORT BUS ERROR TRAP ON A SELECTED BIT MAP ACC
BR 2$ ;RETURN

DEVTMP: ADEVM ;BITS SET INDICATE THE MAPS TO BE TESTED
  
```

G02

```

0036
0037
0038
0039 004362 000004
0040 004364 012737 177777 001204
0041 004372 012737 002000 001124
0042 004400 013777 001124 175040
0043 004406 017737 175034 001126
0044 004414 042737 140000 001126
0045 004422 023737 001124 001126
0046 004430 001401
0047 004432 104003

*****
*TEST 3 TEST THE CHAR CURSOR DISABLE BIT
*****
TST3: SCOPE
MOV #1,SUNIT ;INDICATE A CONTROLLER ERROR
MOV #BIT10,$GDDAT ;LOAD EXPECTED VALUE
MOV $GDDAT,$VCCSR ;LOAD THE REGISTER
MOV $VCCSR,$BDDAT ;READ THE REGISTER
BIC #140000,$BDDAT ;MASK TO BITS 15-14
CMP $GDDAT,$BDDAT ;COMPARE FOR SAME
BEQ TST4 ;;BR IF EXPECTED
ERROR 3 ;'CHAR CURSOR DISABLE' FAILED TO SET

*****
*TEST 4 TEST THE Y CROSS HAIR ENABLE BIT
*****
TST4: SCOPE
MOV #BIT11,$GDDAT ;LOAD EXPECTED VALUE
MOV $GDDAT,$VCCSR ;LOAD THE REGISTER
MOV $VCCSR,$BDDAT ;READ THE REGISTER
BIC #140000,$BDDAT ;MASK TO BITS 15-14
CMP $GDDAT,$BDDAT ;COMPARE FOR SAME
BEQ TST5 ;;BR IF EXPECTED
ERROR 3 ;'Y CROSS HAIR ENABLE' FAILED TO SET

*****
*TEST 5 TEST THE X CROSS HAIR ENABLE BIT
*****
TST5: SCOPE
MOV #BIT12,$GDDAT ;LOAD EXPECTED VALUE
MOV $GDDAT,$VCCSR ;LOAD THE REGISTER
MOV $VCCSR,$BDDAT ;READ THE REGISTER
BIC #140000,$BDDAT ;MASK TO BITS 15-14
CMP $GDDAT,$BDDAT ;COMPARE FOR SAME
BEQ TST6 ;;BR IF EXPECTED
ERROR 3 ;'X CROSS HAIR ENABLE' FAILED TO SET

*****
*TEST 6 TEST THE VERT SYNC INTR. ENABLE BIT
*****
TST6: SCOPE
MOV #340,-(SP) ;RAISE PRIORITY
MOV #15,-(SP)
RTI
15: MOV #BIT13,$GDDAT ;LOAD EXPECTED VALUE
MOV $GDDAT,$VCCSR ;LOAD THE REGISTER
MOV $VCCSR,$BDDAT ;READ THE REGISTER
BIC #140000,$BDDAT ;MASK TO BITS 15-14
CMP $GDDAT,$BDDAT ;COMPARE FOR SAME
BEQ 25 ;;BR IF EXPECTED
ERROR 3 ;'VERT SYNC INTR. ENABLE' FAILED TO SET
23: BIC #BIT13,$VCCSR ;DISABLE INTR.

```

H02

```

009
090
091
092 004630 000004
093 004632 005037 001124
094 004636 012777 036000 174502
095 004644 000005
096 004646 017737 174574 001126
097 004654 042737 141777 001126
098 004662 001401
099 004664 104003
100
101
102
103
104 004666 000004
105 004670 012737 016000 001124
106 004676 013777 001124 174542
107 004704 105077 174536
108 004710 017737 174532 001126
109 004716 042737 140000 001126
110 004724 023737 001124 001126
111 004732 001401
112 004734 104003

```

```

::*****
:*TEST 7 TEST THAT "RESET" CLEARS CHARACTER STATUS REG.
::*****
TST7: SCOPE
CLR $GDDAT ;CLEAR EXPETED
MOV #BIT13!BIT12!BIT11!BIT10,@VCCSR ;LOAD THE REGISTER
RESET ;CLEAR THE WORLD
MOV @VCCSR,$BDDAT ;READ CHARACTER STATUS
BIC #141777,$BDDAT ;MASK TO OTHER BITS
BEQ TST10 ;;BR IF ALL BITS CLEARED
ERROR 3 ;RESET FAILED TO CLEAR CHARACTER STATUS REGISTER

::*****
:*TEST 10 TEST THAT 'CLEAR LOW BYTE' DOES NOT CLEAR HIGH BYTE
::*****
TST10: SCOPE
MOV #16000,$GDDAT ;LOAD EXPECTED READ/WRITE RESULT
MOV $GDDAT,@VCCSR ;LOAD THE REGISTER BITS
CLRB @VCCSR ;CLEAR LOW BYTE
MOV @VCCSR,$BDDAT ;READ THE RESULT OF 'CLRB'
BIC #BIT15!BIT14,$BDDAT ;MASK TO TOP TWO BITS
CMP $GDDAT,$BDDAT ;COMPARE FOR SAME
BEQ TST11 ;;BR IF EXPECTED
ERROR 3 ;CLR LOW BYTE CHANGED THE
;HIGH BYTE OF THE CHAR STATUS REG.

```

```

0:02
914
915
916 004736 000004
917 004740 005077 174502
918 004744 004737 015672
919
920 004750 004737 015676
921 004754 010137 001510
922 004760 004737 015676
923 004764 010137 001514
924 004770 013737 001514 001126
925 004776 163737 001510 001126
926 005004 001001
927 005006 104004
928 005010 013737 001514 001126 1$:
929 005016 006237 001126
930 005022 006237 001126
931 005026 006237 001126
932 005032 023737 001126 001510
933 005040 101016
934 005042 013737 001510 001126
935 005050 006237 001126
936 005054 006237 001126
937 005060 006237 001126
938 005064 023737 001126 001514
939 005072 101001
940 005074 104004
941 005076 000240 2$:

```

```

*****
*TEST 11 TEST THAT 'VERT SYNC' FLAG CHANGES STATES
*****
TST11: SCOPE
CLR JVCCSR ;ENSURE CLEAR REGISTER
JSR PC,COUNTA ;GO WAIT FOR A TRANSITION OF THE VERT SYNC FLAG
;NOW TEST THE RELATIVE TIME DIFFERENCE BETWEEN 'OFF' AND 'ON' STATES
JSR PC,COUNT
MOV R1,SAVE1 ;SAVE THE FIRST TIME
JSR PC,COUNT
MOV R1,SAVE2 ;SAVE THE SECOND TIME
MOV SAVE2,$BDDAT ;LOAD THE BIGGER TIME
SUB SAVE1,$BDDAT ;SUB THE FIRST TIME
BNE IS ;:BR IF NOT SAME
ERROR 4 ;NO TIME DIFFERENCE BETWEEN
MOV SAVE2,$BDDAT ;LOAD BIGGER TIME
ASR $BDDAT ;DIVIDE BY 8
ASR $BDDAT
ASR $BDDAT
CMP $BDDAT,SAVE1 ;:TEST THAT MAGITUDE OF AT LEAST 8 BETWEEN THE TW
BHI 2$ ;:BR IF GREATER
MOV SAVE1,$BDDAT ;LOAD FIRST VALUE
ASR $BDDAT ;ADJUST VALUE
ASR $BDDAT
ASR $BDDAT
CMP $BDDAT,SAVE2 ;COMPARE TO SECOND READ
BHI 2$ ;BR IF MAGITUDE IS GREATER
ERROR 4 ;TIME DIFFERENCE BETWEEN OFF AND ON STATE WRONG
NOP

```


K02

```

969
970
971
972 005202 000004
973 005204 012746 000340
974 005210 012746 005216
975 005214 000002
976 005216 012777 020000 174222 10$:
977 005224 004737 015672
978 005230 012777 005274 174232
979 005236 012777 000340 174226
980 005244 012700 000010
981 005250 005046
982 005252 012746 005260
983 005256 000002
984 005260 005300 1$:
985 005262 001376
986 005264 005077 174156
987 005270 104005
988 005272 000421
989
990 005274 022626
991 005276 012777 005332 174166 2$:
992 005304 012700 000100
993 005310 005046
994 005312 012746 005320
995 005316 000002
996 005320 005300 3$:
997 005322 001376
998 005324 005077 174116
999 005330 000402
1000 005332 022626
1001 005334 104005

```

```

*****
: *TEST 13 TEST FOR 'VERT SYNC' INTERRUPT
*****
TST13: SCOPE
MOV #340,-(SP) ;LOAD PSW RETURN TO LEVEL 7
MOV #10$,-(SP) ;LOAD RETURN P.C.
RTI ;DO THIS TO BE LSI-11 COMPAT.
MOV #BIT13,OVCCSR ;SET INTR ENABLE
JSR PC_COUNTA ;WAIT FOR A COMPLETE 'VERT SYNC' CYCLE
MOV #2$,OVCCINT ;LOAD INTR RETURN VECTOR
MOV #340,OVCCINT1
MOV #10,RO ;LOAD A SHORT DELAY COUNTER
CLR -(SP) ;LOAD PSW RETURN TO LEVEL 0
MOV #1$,-(SP) ;LOAD RETURN P.C.
RTI
1$: DEC RO ;DELAY
BNE 1$ ;BR IF NOT DONE
CLR OVCCSR
ERROR 5 ;'VERT SYNC' FAILED TO CAUSE AN INTR.
BR TST14 ;BR OVER INTR HANDLER
;RETURN HERE WITH PSW=7, LOWER PRIORITY AGAIN AND TEST FOR NO INTERRUPT AGAIN
2$: CMP (SP)+,(SP)+ ;ADJUST THE STACK BY 4
MOV #4$,OVCCINT1 ;RELOAD INTR VECTOR TO FALSE INTR HANDLER
MOV #100,RO ;LOAD DELAY COUNTER
CLR -(SP) ;LOAD PSW RETURN LEVEL TO 0
MOV #3$,-(SP) ;LOAD RETURN P.C.
RTI
3$: DEC RO ;DELAY
BNE 3$
CLR OVCCSR ;CLEAR INTR ENABLE
BR TST14 ;:
4$: CMP (SP)+,(SP)+ ;ADJUST STACK
ERROR 5 ;INTR DONE FAILED TO CLEAR INTR REQUEST

```

L02

```

1002          ;:*****
1003          ;*TEST 14      INTERRUPT LEVEL TEST FOR 'VERT SYNC' INTERRUPT
1004          ;:*****
1005 005336 000004          TEST14: SCOPE
1006 005340 012746 000340      MOV      #340,-(SP)          ;LOAD PSW RETURN TO LEVEL 7
1007 005344 012746 005352      MOV      #10$,-(SP)        ;LOAD RETURN P.C.
1008 005350 000002          RTI          ;DO THIS TO BE LSI-11 COMPAT.
1009 005352 012777 020000 174066 10$: MOV      #BIT13,@VCCSR    ;SET INTR ENABLE
1010 005360 004737 015672      JSR      PC,COUNTA      ;WAIT FOR A COMPLETE 'VERT SYNC' CYCLE
1011 005364 012777 005444 174076      MOV      #2$,@VCINT     ;LOAD INTR RETURN VECTOR
1012 005372 012777 000340 174072      MOV      #340,@VCINT1
1013 005400 012700 000010      MOV      #10,R0        ;LOAD A SHORT DELAY COUNTER
1014 005404 113737 001243 001516      MOVNB   $VECT1+1,SAVE3 ;GET BR LEVEL
1015 005412 042737 177437 001516      BIC      #177437,SAVE3  ;MASK OUT BITS
1016 005420 013746 001516      MOV      SAVE3,-(SP)    ;LOAD BR LEVEL
1017 005424 012746 005432      MOV      #1$,-(SP)     ;LOAD RETURN P.C.
1018 005430 000002          RTI
1019 005432          1$: DEC      R0          ;DELAY
1020 005434 001376          BNE     1$          ;BR IF NOT DONE
1021 005436 005077 174004      CLR     @VCCSR
1022 005442 000404          BR      5$          ;:BR OVER INTR HANDLER
1023          ;RETURN HERE IF AN INTERRUPT OCCURRED ** ERROR **
1024 005444 022626          2$: CMP     (SP)+,(SP)+ ;ADJUST THE STACK BY 4
1025 005446 005077 173774      CLR     @VCCSR
1026 005452 104005          ERROR  5          ;VERT SYNC INTERRUPTED IN ERROR
1027          ;IS BR LEVEL CORRECT ?
1028 005454 012777 005512 174006 5$: MOV      #4$,@VCINT    ;RELOAD INTR VECTOR
1029 005462 012700 000100      MOV      #100,R0      ;LOAD DELAY COUNTER
1030 005466 005046          CLR     -(SP)        ;LOAD PSW RETURN LEVEL TO 0
1031 005470 012746 005476      MOV      #3$,-(SP)    ;LOAD RETURN P.C.
1032 005474 000002          RTI
1033 005476 005300          3$: DEC     R0          ;DELAY
1034 005500 001376          BNE     3$
1035 005502 005077 173740      CLR     @VCCSR      ;CLEAR INTR ENABLE
1036 005506 104005          ERROR  5          ;LOWERING PROIORITY FAILED TO CAUSE AN INTERRUPT
1037 005510 000401          BR      6$
1038 005512 022626          4$: CMP     (SP)+,(SP)+ ;ADJUST STACK
1039 005514 005046          6$: CLR     -(SP)
1040 005516 012746 005524      MOV      #7$,-(SP)
1041 005522 000002          RTI
1042 005524 000240          7$: NOP
    
```

M02

MAINDEC-11-DZVSE-A
DZVSEA.P11 02-JUN-76 00:00

VSV01 VIEDO GRAPHIC TEST PROGRAM
T15 DYNAMIC TESTING OF THE X CROSS HAIR POSITION REGISTERS

MACY11 27(1006) 02-DEC-76 17:43 PAGE 26

```

1043 ::*****
1044 :*TEST 15 DYNAMIC TESTING OF THE X CROSS HAIR POSITION REGISTERS
1045 ::*****
1046 005526 000004 TST15: SCOPE
1047 005530 005737 001544 TST BURN ;TEST IF RUNNING IN "BURN-IN" MCCE
1048 005534 001406 BEQ 3$ ;BR IF NOT
1049 005536 013777 001546 174004 MOV COAXSW, @COAX ;LOAD THE RELAY
1050 005544 004537 016666 JSR R5, DELAY ;LET THE RELAY SETTLE
1051 005550 000012 IO.
1052 005552 3$:
1053 005552 005737 001200 TST $PASS ;TEST IF FIRST PASS OF PROGRAM
1054 005556 001427 BEQ TST16 ;;BR IF FIRST PASS
1055 005560 032777 002000 173352 BIT #SW10, @SWR ;TEST IF INHIBIT VISUAL CHAR. PATTERNS
1056 005566 001023 BNE TST16 ;;BR IF INHIBIT IS SET
1057 005570 012777 010000 173650 2$: MOV #BIT12, @VCCSR ;ENABLE X CROSS HAIRS
1058 005576 005037 001124 CLR $GDDAT ;CLEAR EXPECTED VALUE
1059 005602 005077 173642 CLR @VCHRLO ;CLEAR POS.
1060 005606 113777 001124 173634 1$: MOVB $GDDAT, @VCHRLO ;LOAD THE LOW BYTE (X POSITION)
1061 005614 004537 016666 JSR R5, DELAY
1062 005620 000001 I
1063 005622 005237 001124 INC $GDDAT ;UPDATE X POSITION
1064 005626 023737 001124 001536 CMP $GDDAT, XHAIR ;TEST IF MORE LINES?
1065 005634 001364 BNE 1$
1066 ::*****
1067 :*TEST 16 DYNAMIC TESTING OF THE Y CROSS HAIR POSITION REGISTERS
1068 ::*****
1069 005636 000004 TST16: SCOPE
1070 005640 005737 001200 TST $PASS ;TEST IF FIRST PASS OF PROGRAM
1071 005644 001431 BEQ TST17 ;;BR IF FIRST PASS
1072 005646 032777 002000 173264 BIT #SW10, @SWR ;TEST IF INHIBIT VISUAL CHAR. PATTERNS
1073 005654 001025 BNE TST17 ;;BR IF INHIBIT IS SET
1074 005656 012777 004000 173562 MOV #BIT11, @VCCSR ;ENABLE Y CROSS HAIRS
1075 005664 005037 001124 CLR $GDDAT
1076 005670 005077 173554 CLR @VCHRLO ;CLEAR POSITION
1077 005674 113777 001124 173550 1$: MOVB $GDDAT, @VCHRHI
1078 005702 004537 016666 JSR R5, DELAY
1079 005706 000001 I
1080 005710 105237 001124 INCB $GDDAT
1081 005714 123737 001540 001124 CMPB YHAIR, $GDDAT
1082 005722 001364 BNE 1$
1083 005724 005077 173516 CLR @VCCSR ;DISABLE CROSS HAIRS
1084

```

N02

```

1085      ;:*****
1086      ;*TEST 17      FULL SCREEN OF A CHARACTER
1087      ;:*****
1088      005730 000004      TST17: SCOPE
1089      005732 005737 001200      TST      $PASS      ;TEST IF FIRST PASS OF PROGRAM
1090      005736 001413      BEQ      TST20      ;;BR IF FIRST PASS
1091      005740 032777 002000 173172      BIT      #SW10,2SWR      ;TEST IF INHIBIT VISUAL CHAR. PATTERNS
1092      005746 001007      BNE      TST20      ;;BR IF INHIBIT IS SET
1093      005750 004737 017256      JSR      PC,HOME      ;HOME AND ERASE THE SCREEN
1094      005754 004737 016076      JSR      PC,FILLWC     ;FILL SCREEN WITH A 'E'S
1095      005760 004537 016666      JSR      RS,DELAY
1096      005764 000144      100.
1097
1098      ;:*****
1099      ;*TEST 20      SINGLE CHARACTER ACROSS ALL COLS. <ALL CHARACTERS>
1100      ;:*****
1101      005766 000004      TST20: SCOPE
1102      005770 005737 001200      TST      $PASS      ;TEST IF FIRST PASS OF PROGRAM
1103      005774 001446      BEQ      TST21      ;;BR IF FIRST PASS
1104      005776 032777 002000 173134      BIT      #SW10,2SWR      ;TEST IF INHIBIT VISUAL CHAR. PATTERNS
1105      006004 001042      BNE      TST21      ;;BR IF INHIBIT IS SET
1106      006006 004737 017256      JSR      PC,HOME      ;HOME AND ERASE THE SCREEN
1107      006012 012737 000040 001530 3$:      MOV      #40,STCHAR     ;SET-UP STARTING CHARACTER
1108
1109      006020 013737 001524 001534      MOV      VHD,TEMPO     ;LOAD COUNT
1110      006026 013701 001530 1$:      MOV      STCHAR,R1     ;LOAD R1= TO CHARACTER
1111      006032 004737 016132      JSR      PC,FILBUF     ;LOAD A BUFFER WITH THAT CHARACTER
1112
1113      006036 004737 016032      JSR      PC,XPRNT      ;DISPLAY A FULL LINE FROM THE BUFFER
1114
1115      006042 005337 001534      DEC      TEMPO         ;DONE ?
1116      006046 100010      BPL      2$           ;FINISHED
1117      006050 004537 016666      JSR      RS,DELAY
1118      006054 000144      100.
1119      006056 004737 017256      JSR      PC,HOME      ;HOME AND ERASE THE SCREEN
1120      006062 013737 001524 001534      MOV      VHD,TEMPO
1121      006070 005237 001530 2$:      INC      STCHAR
1122      006074 023737 001532 001530      CMP      LASTCH,STCHAR ;UPDATE THE CHARACTER
1123      006102 001351      BNE      1$           ;TEST FOR FINAL CHARACTER
1124
1125      006104 004537 016666      JSR      RS,DELAY     ;BRANCH IF NOT COMPLETED
1126      006110 000144      100.
1127

```


B03

02-DEC-76 02:58:00
02-DEC-76 02:58:00

VSVC: JEDC GRAPHIC TEST PROGRAM
T21

MACY11 27(1006) 02-DEC-76 17:43 PAGE 29
ROTATING CHARACTERS ACROSS ALL COLS. (ALL CHARACTERS)

```

*****
*TEST 21      ROTATING CHARACTERS ACROSS ALL COLS. (ALL CHARACTERS)
*****
*ST21:  SCOPE
          TST      $PASS      ;TEST IF FIRST PASS OF PROGRAM
          BEQ      TST22      ;;BR IF FIRST PASS
          BIT      #SW10,2SWR   ;TEST IF INHIBIT VISUAL CHAR. PATTERNS
          BNE      TST22      ;;BR IF INHIBIT IS SET
          JSR      PC,HOME     ;HOME AND ERASE THE SCREEN
          MOV      #40,STCHAR  ;SET-UP STARTING CHARACTER

          JSR      PC,HOME     ;HOME AND ERASE THE SCREEN
          MOV      VHD,TEMPO   ;LOAD TEMP
          MOV      STCHAR,R1   ;LOAD R1=TC CHARACTER
          JSR      RS,LIC      ;LOAD A BUFFER STARTING WITH
                                ; THAT CHARACTER AND WIDTH (BYTE)

          JSR      PC,XPRNT    ;DISPLAY A FULL LINE FROM THE BUFFER

          DEC      TEMPO       ;DONE ?
          BPL      2$         ;BR IF YES
          JSR      RS,DELAY

          JSR      PC,HOME     ;HOME AND ERASE THE SCREEN
          MOV      VHD,TEMPO   ;UPDATE THE STARTING CHARACTER
          INC      STCHAR      ;TEST FOR FINAL CHARACTER
          CMP      LASTCH,STCHAR
          BNE      1$         ;BRANCH IF NOT COMPLETED

          JSR      RS,DELAY

          100.

```

C03

MAINDEC-11-02VSE-A
02VSEAF11 02-JUN-76 02:00

VSWO1 VIEDO GRAPHIC TEST PROGRAM
T22

MACY11 27(1006) 02-DEC-76 17:43 PAGE 29
DIRECT CURSOR ADDRESS TEST

```

1159 ::*****
1160 :*TEST 22 DIRECT CURSOR ADDRESS TEST
1161 :*****
1162 006244 000004          †ST22: SCOPE
1163 006246 005737 001200      TST $PASS          :TEST IF FIRST PASS OF PROGRAM
1164 006252 001530          BEQ TST23          :BR IF FIRST PASS
1165 006254 032777 002000 172656 BIT #SW10,JSWR      :TEST IF INHIBIT VISUAL CHAR. PATTERNS
1166 006262 001124          BNE TST23          :BR IF INHIBIT IS SET
1167 006264 004737 017256      JSR PC,HOME        :HOME AND ERASE THE SCREEN
1168 006270 012737 123456 017704 DCATST: MOV #123456,$LONUM :PRIME RANDOM NUMBER GENERATOR
1169 006276 012737 176543 017702 MOV #176543,$HINUM
1170 006304 013737 001520 017152 MOV TOTALC,OVRL   :LOAD CHAR COUNT
1171 006312 013737 001524 01715C MOV VHO,SET       :LOAD LINE COUNT
1172 006320 012700 024350      MOV #BUFFER+20,RO :LOAD DESTINATION BUFFER
1173 006324 012701 017154 25: MOV #MSGTXT,R1    :LOAD MESSAGE POINTER
1174 006330 012120 15: MOV (R1)+,(R0)+   :LOAD 2 CHARACTERS
1175 006332 022701 017254      CMP #MSGTNO,R1    :TEST FOR LAST CHAR
1176 006336 001374          BNE 15            :BR UNTIL DONE 1 LINE
1177 006340 005337 017150      DEC SET          :FINISHED ALL LINES
1178 006344 100367          BPL 25           :BR IF NOT
1179 006346 012737 177777 024330      MOV #-1,BUFFER   :LOAD MESSAGE TERMINATOR
1180 006354 004737 017604          JSR PC,$RAND      :GENERATE RANDOM NUMBER
1181 006360 013700 017702      MOV $HINUM,RO    :GET RANDOM NUMBER
1182 006364 042700 177740      BIC #177740,RO   :RANDOM NO. MUST BE TWO DIGITS
1183 006370 020037 001524      CMP RO,VHO       :NO. MUST NOT BE GREATER THAN VHO
1184 006374 101367          BHI GENER        :GREATER, REGENERATION
1185 006376 010037 017144      MOV RO,1+DDS     :STORE RANDOM Y COORDINATE
1186 006402 012737 024350 017150      MOV #BUFFER+20,SET :LOAD BASE POINTER
1187 006410 010001          MOV RO,R1
1188 006412 001405          BEQ GENRX        :RESULT, MINIMUM Y COORDINATE
1189 006414 063737 001522 017150 15: ADD WIDTH,SET   :SETUP Y INDEX LOCATION FOR PRINTOUT
1190 006422 005301          DEC R1
1191 006424 001372          BNE 15           :Y COORDINATE IS SET
1192 006426 004737 017604          JSR PC,$RAND      :GENERATE RANDOM NUMBER
1193 006432 013700 017702      MOV $HINUM,RO    :GET A RANDOM NUMBER
1194 006436 042700 177700      BIC #177700,RO   :RANDOM NO. MAY BE LESS THAN 200
1195 006442 010037 017146      MOV RO,XADD$     :STORE RANDOM X COORDINATE
1196 006446 060037 017150      ADD RO,SET       :SETUP X COOR, FOR PNTOUT.
1197 006452 013701 017150      MOV SET,R1       :SETUP CHECK
1198 006456 105711          TSTB (R1)        :HAS CURRENT CHAR, ALREADY BEEN USED?
1199 006460 100735          BMI GENER        :YES, REGENERATE
1200 006462 113777 017144 172766      MOVB YADD$,OVCPOSH :LOAD Y COORDINATE
1201 006470 113777 017146 172756      MOVB XADD$,OVCPOS :LOAD X COORDINATE
1202 006476 111137 024330      MOVB (R1),BUFFER :LOAD CHARACTER TO BE PRINTED
1203 006502 152711 000200      BISB #200,(R1)   :INDICATE USE OF CURSOR POSITION
1204 006506 004737 016032      JSR PC,XPRNT     :EXECUTE AND PRINT CHARACTER
1205 006512 104407          CKSWR          :TEST FOR CTRL G
1206 006514 005337 017152      DEC OVRL         :MAXIMUM NO. OF COORDINATES
1207 006520 001315          BNE GENER        :BR BACK UNTIL DONE
1208 006522 004537 016666      JSR RS,DELAY
1209 006526 000144          100.
1210 006530 004737 017256      JSR PC,HOME      :HOME AND ERASE THE SCREEN

```

*TEST 23 CURSOR MOTION TEST

006534 000004
006536 005737 001200
006542 001562
006544 032777 002000 172366
006552 001156
006554 004737 017256
006560 042777 002000 172660

TST23: SCOPE
TST \$PASS ;TEST IF FIRST PASS OF PROGRAM
BEG TST24 ;;BR IF FIRST PASS
BIT \$SW10,\$SWR ;TEST IF INHIBIT VISUAL CHAR. PATTERNS
BNE TST24 ;;BR IF INHIBIT IS SET
JSR PC,HOME ;HOME AND ERASE THE SCREEN
BIC \$BIT10,\$VCCSF ;ENABLE CURSOR

:ROUTINE TO LOAD REFERENCE LINES FOR CURSOR MOTION TEST

006566 012700 024330
006572 013701 001526
006576 005301
006600 004737 006676
006604 004737 006714
006610 112720 000062
006614 004737 006714
006620 004737 006714
006624 112720 000040
006630 112720 000061
006634 004737 006672
006640 004737 006714
006644 112720 000063
006650 004737 006714
006654 004737 006714
006660 112720 000064
006664 112710 000377
006670 000420

LBMT: MOV \$BUFFER,R0 ;LOAD POINTER
MOV VH1,R1 ;LOAD R1
DEC R1
JSR PC,MOVDN1 ;MOVE CURSOR DOWN
JSR PC,MOVRIG ;MOVE RIGHT
MOVB #2,(R0)+ ;LOAD #2
JSR PC,MOVRIG ;MOVE RIGHT
JSR PC,MOVRIG ;MOVE RIGHT
MOVB #40,(R0)+
MOVB #61,(R0)+ ;LOAD #1
JSR PC,MOVDN1 ;MOVE DOWN
JSR PC,MOVRIG ;MOVE RIGHT
MOVB #63,(R0)+ ;LOAD #3
JSR PC,MOVRIG ;MOVE RIGHT
JSR PC,MOVRIG ;MOVE RIGHT
MOVB #64,(R0)+ ;LOAD #4
MOVB #377,(R0) ;TERM
BR LBMT1 ;;BR TO NEXT PART

006672 013701 001526
006676 112720 000015
006702 112720 000012
006706 005301
006710 001372
006712 000207

MOVDN: MOV VH1,R1
MOVDN1: MOVB #15,(R0)+ ;LOAD CR
MOVB #12,(R0)+ ;LOAD LF
DEC R1
BNE MOVDN1 ;LOOP UNTIL DONE
RTS PC ;EXIT

006714 012701 000016
006720 112720 000040
006724 005301
006726 100374
006730 000207

MOVRIG: MOV #16,R1 ;LOAD R1
IS: MOVB #40,(R0)+ ;LOAD SPACES
DEC R1
BPL IS ;LOOP UNTIL DONE
RTS PC ;EXIT

006732 004737 016032
006736 004537 016666
006742 000144

LBMT1: JSR PC,XPRNT ;DISPLAY THIS LINE
JSR RS,DELAY
ICD.

E03

MAINDEC-11-02VSE-A
02VSEB.F11 02-JUN-76

VSNO: XEDC GRAPHIC TEST PROGRAM
T23 CURSOR MOTION TEST

MAY11 27.1006, 02-DEC-76 17:43 PAGE 3:

```

1263 :CURSOR MOTION SUBROUTINE
1264 LCM: MOV VHI,R1 :LOAD COUNT
1265 006744 013701 001526 :LOAD BUFFER POINTER
1266 006750 012700 024330 :LOAD A CURSOR UP
1267 006754 112720 000032 18: MOVB #32,(R0)+ :LOAD UNTIL DONE
1268 006760 005301 :LOAD 'X'
1269 006762 001374 BNE 18 :LOAD COUNT
1270 006764 112720 000130 25: MOVB #130,(R0)+ :LOAD "BACKSPACE (CURSOR LEFT)"
1271 006770 012701 000040 :DONE ALL?
1272 006774 112720 000010 :BR IF NOT
1273 007000 005301 :LOAD 'X'
1274 007002 100374 BPL 25 :LOAD BACKSPACE
1275 007004 112720 000130 35: MOVB #130,(R0)+ :LOAD COUNT
1276 007010 112720 000010 :LOAD CURSOR DOWN
1277 007014 013701 001526 :DONE?
1278 007020 112720 000013 45: MOVE #13,(R0)+ :BR IF NOT
1279 007024 005301 :LOAD 'X'
1280 007026 001374 BNE 35 :LOAD BACKSPACE
1281 007030 112720 000130 :LOAD COUNT
1282 007034 112720 000013 :LOAD "C" CURSOR RIGHT
1283 007040 012701 000036 :LOOP UNTIL DONE
1284 007044 112720 000030 55: MOVB #30,(R0)+ :LOAD 'X'
1285 007050 005301 :DISPLAY THIS LINE
1286 007052 100374 BPL 45
1287 007054 112720 000130 :HOME AND ERASE THE SCREEN
1288 007060 112720 000377 :DISABLE CHAR. CURSOR
1289 007064 004737 016632 JSR PC,XPRNT
1290 007070 004537 016666 JSR RS,DELAY
1291 007074 000144 100.
1292 007076 004737 017256 JSR PC,HOME
1293 007106 052737 002000 BVS #BIT10,AVCCSR

```

```

:*****
: *TEST 24
:*****
*24: SCOPE

```

F03

MAINOC: -11-DVSE-A
 DVSE-A.P11 02-JUN-76

VSVO: VIEDO GRAPHIC TEST PROGRAM
 00:00

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007220	001357	000000	NOCHAR: NOP		
007222	005737	001200	MOV SGOADR,TEMP1		:GET BASE ADDRESS
007226	001455		ADD #20,TEMP1		:UPDATE TO THE MAP ADDRESS
007230	004737	003576	MOV SCDW1,DEVTMP		:GET THE NUMBER OF MAPS SELECTED
007234	013737	001252	BIC #177700,DEVTMP		:MASK TO LOWER SIX BITS
007242	042737	177700	BEQ MAPRTA		:BR IF NO MAPS SELECTED
007250	012700	007346	CLR SUNIT		:CLEAR MAP INDICATOR
007254	005737	007342	MOV #BIT0,TEMP3		:LOAD TESTING BIT
007260	001424		BIT TEMP3,DEVTMP	MAPRTB:	:TEST IF THIS BIT MAP IS SELECTED
007262	006237	007342	BEQ MAPRET		:BR IF NOT
007266	103015		JMP NUMBR		:SELECTED - TEST THIS MAP
007270	004737	016234	ADD #20,TEMP1	MAPRET:	:UPDATE TO NEXT BUS ADDRESS
007274	112037	007344	INC SUNIT		:UPDATE MAP INDICATOR
007300	013777	007344	ASL TEMP3		:MOVE THE TEST BIT
007306	112077	172146	CMR #100,TEMP3		:TEST IF ANY MORE POSSIBLE MAPS
007312	052777	000400	BNE MAPRTB		:BR IF MORE
007320	000401		.SBTTL MULTIPLE MAP PATTERN		
007322	005720		TST SPASS		:TEST IF 1ST PASS
007324	004737	003730	BEQ MAPRTA		:BR IF FIRST PASS
007330	000751		JSR PC,LOADADR		:LOAD THE BUS ADDRESS
007332	004537	016666	MOV SCDW1,105		:GET THE NUMBER OF MAPS
007336	000144		BIC #177700,105		:MASK
007340	000410		MOV #DUALPL,RO		:LOAD POINTER
007342	000000		TST 105	15:	:TEST IF ANY EXIST
007344	000000		BEQ 45		:BR IF DONE
007346	000	005	JSR 105		:TEST FOR NEXT MAP
007350	014	006	BCC 25		:BR IF NEXT NOT SELECTED
007352	060	001	JSR PC,CLRMAP		:CLEAR THE MAP
007354	033	002	MOVB (RO)+,115		:GET COLOR VALUE
007356	017	002	MOV 115,2VGCCLR		:LOAD THE COLOR FOR THIS MAP
007360	063	016	MOVB (RO)+,2VGC5R		:LOAD THE ORGIN FOR THIS MAP
007362	000177	000000	BIS #BITB,2VGC5R		:ENABLE MAP
007366	000000		BR 35		
			TST (RO)+	25:	:ADJUST POINTER
			JSR PC,MOVADR	35:	:LOAD ADR OF NEXT MAP
			BR 15		
			JSR R5,DELAY	45:	
			100.		
			BR MAPRTA		
			0	105:	
			0	115:	
			DUALPL: .BYTE 3,5		:MAP #0 COLOR AND ORGIN
			.BYTE 14,6		:MAP #1 COLOR AND ORGIN
			.BYTE 60,1		:MAP #2
			.BYTE 32,2		:MAP #3
			.BYTE 17,15		:MAP #4
			.BYTE 63,16		:MAP #5
007362	000177	000000	MAPRTA: JMP \$RETURN		
007366	000000		RETURN: 0		:RETURN CONTROL

H03

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007624 000004  
007626 012737 007642 001110  
007634 012737 000001 001124  
007642 013777 001124 171610  
007650 017737 171604 001126  
007656 042737 171360 001126  
007664 023737 001124 001126  
007672 001401  
007674 104006  
007676 005200 001124  
007702 022737 000020 001124  
007710 001354  
  
007712 000004  
007714 012737 006400 001124  
007722 012777 006417 171530  
007730 105077 171524  
007734 017737 171520 001126  
007742 042737 170360 001126  
007750 023737 001124 001126  
007756 001401  
007760 104006  
  
007762 000004  
007764 012737 000017 001124  
007772 012777 006417 171460  
010000 013700 001460  
010004 005200  
010006 105010  
010010 017737 171444 001126  
010016 042737 170360 001126  
010024 023737 001124 001126  
010032 001401  
010034 104006  
  
010036 000004  
010040 005037 001124  
010044 012777 006417 171406  
010052 000005  
010054 017737 171400 001126  
010062 042737 171360 001126  
010070 001401  
010072 104006
```

```
*****  
: *TEST 30 TEST THAT 'ORIGIN POINT' BITS CAN BE SET  
*****  
TST30: SCOPE  
MOV #15,$LPERP ;LOAD THE EXPECTED  
MOV #BIT0,$GDDAT ;LOAD THE REGISTER  
15: MOV $GDDAT,$VGCSSR ;LOAD THE REGISTER  
MOV $VGCSSR,$BDDAT ;READ THE REGISTER  
BIC #171360,$BDDAT ;MASK TO BITS  
CMP $GDDAT,$BDDAT ;COMPARE THE EXPECTED TO RECVD  
BEQ 25 ;:BR IF SET  
ERROR 6 ;'ORIGIN POINT' BIT FAILED TO SET  
25: INC $GDDAT ;UPDATE EXPECTED VALUE OF 'ORIGIN POINT'  
CMP #20,$GDDAT ;TEST IF VALID VALUE FOR 'ORIGIN'  
BNE 15 ;BR IF MORE TO TEST  
*****  
: *TEST 31 TEST THAT CLEAR LOW BYTE DOES NOT CLEAR HIGH BYTE  
*****  
TST31: SCOPE  
MOV #BIT11!BIT10!BIT8,$GDDAT  
MOV #BIT11!BIT10!BIT8+17,$VGCSSR  
CLRB $VGCSSR ;CLEAR LOW BYTE  
MOV $VGCSSR,$BDDAT ;READ THE REGISTER  
BIC #170360,$BDDAT ;MASK TO BITS  
CMP $GDDAT,$BDDAT ;TEST IF SAME  
BEQ TST32 ;:BR IF SAME  
ERROR 6 ;CLEARING LOW BYTE OF STATUS CHANGED THE HIGH BY  
*****  
: *TEST 32 TEST THAT CLEAR HIGH BYTE DOES NOT CLEAR LOW BYTE  
*****  
TST32: SCOPE  
MOV #17,$GDDAT ;LOAD EXPECTED READ VALUE  
MOV #BIT11!BIT10!BIT8+17,$VGCSSR ;LOAD STATUS REGISTER  
MOV $VGCSSR,$R0 ;MAKE ADDRESS  
INC $R0 ;OF HIGH BYTE  
CLRB ($R0) ;CLEAR HIGH BYTE  
MOV $VGCSSR,$BDDAT ;READ REGISTER  
BIC #170360,$BDDAT ;MASK OUT BITS  
CMP $GDDAT,$BDDAT ;COMPARE EXPECT TO RECVD  
BEQ TST33 ;:BR IF SAME  
ERROR 6 ;CLEARING THE HIGH BYTE OF STATUS CHANGED  
;THE LOW BYTE  
*****  
: *TEST 33 TEST THAT "RESET" CLEARS THE BIT MAP STATUS REGISTER  
*****  
TST33: SCOPE  
CLR $GDDAT ;LOAD EXPECTED  
MOV #BIT11!BIT10!BIT8!17,$VGCSSR ;LOAD THE STATUS REGISTER  
RESET ;RESET THE WORLD  
MOV $VGCSSR,$BDDAT ;READ THE REGISTER  
BIC #171360,$BDDAT ;MASK TO OTHER BITS  
BEQ TST34 ;:BR IF CLEARED  
ERROR 6 ;RESET FAILED TO CLEAR BIT MAP STATUS REGISTER
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I03

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010074 000004
010076 005037 001124
010102 005037 001126
010106 004537 016666
010112 100001
010114 052777 001000 171336
010122 117737 171332 001126
010130 100001
010132 104014

*****
*TEST 34 TEST MAP READY AND CLEAR MAP BITS
*****
*ST34: SCOPE
;TEST THAT WRITTING BIT 9 (CLEAR MAP) CLEARS MAP READY (BIT 7)
CLR $GDDAT ;CLEAR EXPECTED
CLR $BDDAT ;CLEAR RESULTS
JSR R5,DELAY ;WAIT FOR VERT SYNC
BIT15!1
BIS #BIT9,$VGCSR ;SET "CLEAR MAP" BIT
MOV $VGCSR,$BDDAT ;READ VG STATUS REG
BPL 15 ;;BR IF CLEARED
ERROR 14 ;BIT MAP READY NOT SET

;TEST "CLEAR MAP" BIT SETS
1$: MOV #BIT9,$GDDAT ;LOAD EXPECTED
JSR R5,DELAY ;WAIT FOR VERT SYNC
BIT15!BIT0
MOV $VGCSR,$BDDAT ;READ MAP STATUS REG.
BIT $GDDAT,$BDDAT ;TEST IF BIT IS SET
BNE 2$ ;;BR IF SET
ERROR 14 ;CLEAR MAP BIT FAILED TO SET

;TEST THAT "MAP READY" CLEARED
2$: CLR $GDDAT ;CLEARED EXPECTED
TSTB $BDDAT ;TEST BIT 7
BPL 3$ ;;BR IF CLEARED
ERROR 14 ;SETTING "CLEAR MAP" BIT FAILED TO CLEAR "MAP RE

;TEST THAT MAP READY RETURNS AFTER 2 VERT SYNC TIMES
3$: JSR R5,DELAY ;WAIT FOR VERT SYNC
BIT15!2
MOV #BIT7,$GDDAT ;LOAD EXPECTED
MOV $VGCSR,$BDDAT ;READ STATUS
BIT $GDDAT,$BDDAT ;TEST IF BIT IS SET
BNE 4$ ;;BR IS SET
ERROR 14 ;MAP READY FAILED TO SET AFTER CLEAR MAP

;TEST THAT "CLEAR MAP" BIT WAS CLEARED
4$: CLR $GDDAT ;CLEAR EXPECTED
BIT #BIT9,$BDDAT ;TEST FOR CLEARED BIT
BEQ TST35 ;;BR IF CLEARED
ERROR 14 ;MAP READY SET BUT "CLEAR MAP" BIT FAILED TO CLE

```

J03

MAINDEC-11-DZVSE-A
DZVSEA.P11 02-JUN-76 00:00

VSVCI VIEDC GRAPHIC TEST PROGRAM
T34

MACY11 27(1006) 02-DEC-76 17:43 PAGE 36
TEST MAP READY AND CLEAR MAP BITS

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010256 000004
010260 012737 010276 001110
010266 005037 001124
010272 005037 001474
010276 013777 001474 171156
010304 017737 171150 001126
010312 042737 177617 001126
010320 023737 001124 001126
010326 001401
010330 104007

010332 062737 000020 001124
010340 005237 001474
010344 022737 000010 001474
010352 001351

010354 000004
010356 012737 010376 001110
010364 012737 000020 001124
010372 005077 171064
010376 012777 177777 171060
010404 012700 000100
010410 105777 171044
010414 100403
010416 005300
010420 001373
010422 104014
010424 017737 171030 001126
010432 042737 177617 001126
010440 023737 001124 001126
010446 001401
010450 104010

010452 062737 000020 001124
010450 022737 000200 001124
010456 001343

```
::*****  
:*TEST 35 READ-WRITE TEST OF THE LOW 3 BITS OF THE MAP P.C.  
:*****  
↑ST35: SCOPE  
MOV #15,$LPERR  
CLR $GDDAT ;CLEAR EXPECTED READ VALUE  
CLR $TEMP ;CLEAR WRITE VALUE  
1$: MOV $TEMP,$VGPC ;LOAD BIT MAP P.C.  
MOV $VGCSP,$BDDAT ;READ STATUS (BIT MAP)  
BIC #177617,$BDDAT ;MASK TO UNUSED BITS  
CMP $GDDAT,$BDDAT ;TEST IF SAME  
BEQ 2$ ;;BR IF SAME  
ERROR 7 ;BIT MAP P.C. DID NOT LOAD  
;CORRECTLY  
2$: ADD #20,$GDDAT ;UPDATE EXPECTED READ VALUE  
INC $TEMP ;UPDATE EXPECTED WRITE VALUE  
CMP #10,$TEMP ;TEST FOR FIRST NON-VALID  
BNE 1$ ;BRANCH IF STILL OK VALUES.  
  
:*****  
:*TEST 36 STATIC BIT MAP P.C. INCREMENT  
:*****  
↑ST36: SCOPE  
MOV #15,$LPERR  
MOV #20,$GDDAT ;LOAD EXPECTED READ VALUE  
CLR $VGPC ;CLEAR MAP P.C.  
1$: MOV #-1,$VGBUF ;LOAD A PIXEL  
MOV #BIT6,RO ;LOAD DELAY COUNT  
3$: TSTB $VGCSP ;TEST IF MAP IS READY  
BMI 4$ ;BR IF YES  
DEC RO ;DELAY  
BNE 3$ ;BR IN NOT EXCEEDED  
ERROR 14 ;MAP READY FAILED TO SET  
4$: MOV $VGCSP,$BDDAT ;READ RESULT VALUE FROM BITS 4-6 OF CSP  
BIC #177617,$BDDAT ;MASK TO UNUSED BITS  
CMP $GDDAT,$BDDAT ;TEST IF SAME  
BEQ 2$ ;;BR IF SAME  
ERROR 10 ;BIT MAP P.C. FAILED TO INCREMENT  
;BY LOADING BUFFER WITHOUT LOADING P.C.  
2$: ADD #BIT4,$GDDAT ;UPDATE EXPECTED READ VALUE  
CMP #BIT7,$GDDAT ;TEST IF MORE BITS TO INCREMENT  
BNE 1$ ;;BR IF MORE BITS
```

K03

MAINDEC-11-DZVSE-A VSWC: VIEDO GRAPHIC TEST PROGRAM MACY11 27(1006) 02-DEC-76 17:43 PAGE 37
 DZVSEAF11 02-JUN-76 00:00 T37 READ-WRITE WORD TEST OF PIXEL 0,1,2 AND 3 -- BIT C

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1544 010470 000004
1545 010472 012737 010514 001110
1546 010500 012737 010000 001124
1547 010506 012737 000001 001474
1548 010514 005077 170742 1$: CLR 0VGPC ;LOAD EXPECTED READ VALUE
1549 010520 005077 170740 CLR 0VGBUF ;LOAD EXPECTED WRITE VALUE
1550 010524 005077 170732 CLR 0VGPC ;RESET P.C.
1551 010530 105777 170724 3$: TSTB 0VGCSP ;CLEAR DATA
1552 010534 100375 BPL 3$ ;RESET PC
1553 010536 013777 001474 170720 4$: MOV $TEMP,0VGBUF ;WAIT FOR MAP READY
1554 010544 105777 170710 TSTB 0VGCSP ;LOAD WRITE VALUE
1555 010550 100375 BPL 4$ ;WAIT FOR READY
1556 010552 017737 170702 001126 MOV 0VGCSP,$BDDAT ;READ RESULT VALUE
1557 010560 042737 007777 001126 BIC #7777,$BDDAT ;MASK TO UNWANTED BITS
1558 010566 023737 001124 001126 CMP $GDDAT,$BDDAT ;TEST IF SAME
1559 010574 001401 BEQ 2$ ;:BR IF SAME
1560 010576 104011 ERROR 11 ;BIT 0 OF A PIXEL FAILED TO SET
1561 010600 006337 001474 2$: ASL $TEMP ;ADJUST
1562 010604 006337 001474 ASL $TEMP ; THE
1563 010610 006337 001474 ASL $TEMP ; WRITE
1564 010614 006337 001474 ASL $TEMP ; VALUE TO THE NEXT PIXEL
1565 010620 006337 001124 ASL $GDDAT ;ADJUST THE EXPECTED READ VALUE
1566 010624 001333 BNE 1$ ;BR IF MORE BITS TO TEST
1567
  
```


L03

```

1568
1569
1570      ;:*****
1571      ;*TEST 40      BASIC LOOK UP TABLE (L.U.T.) TEST
1572      ;:*****
1573      TST40: SCOPE
1574      TST      BURN      ;TEST IF IN "BURN-IN" MODE
1575      BEQ      2$      ;BR IF NOT
1576      MOV      COAXSW, @COAX ;CHANGE THE RELAY
1577      JSR      RS, DELAY ;WAIT FOR RELAY TO SETTLE
1578      IO.
1579      2$:
1580      TST      $PASS      ;TEST IF FIRST PASS OF PROGRAM
1581      BEQ      TST41      ;:BR IF FIRST PASS
1582      BIT      #SW09, @SWR ;TEST IF INHIBIT VISUAL MAP PATTERNS
1583      BNE      TST41      ;:BR IF INHIBIT IS SET
1584      1$:
1585      JSR      PC, TESTID ;DISPLAY MAP # AND TEST #
1586      JSR      PC, CLRMAP ;CLEAR THE BIT MAP
1587      JSR      RS, LODSTA ;LOAD THE SEQUENCE TABLE
1588      0 ;LOC 0 WITH 14 - LOC 1-17 WITH 63
1589      TABLE3
1590      MOV      #BIT10!BIT8, @VGCSSR ;ENABLE THE MAP
1591      JSR      RS, DELAY
1592      IO.
1593
1594      ;:*****
1595      ;*TEST 41      STATIC INTENSITY LEVEL TEST (L.U.T. 0)
1596      ;:*****
1597      TST41: SCOPE
1598      TST      $PASS      ;TEST IF FIRST PASS OF PROGRAM
1599      BEQ      TST42      ;:BR IF FIRST PASS
1600      BIT      #SW09, @SWR ;TEST IF INHIBIT VISUAL MAP PATTERNS
1601      BNE      TST42      ;:BR IF INHIBIT IS SET
1602      JSR      PC, TESTID ;DISPLAY MAP # AND TEST #
1603      JSR      PC, CLRMAP ;ENSURE CLEAR MAP
1604      MOV      #BIT10!BIT8, @VGCSSR ;ENABLE 'MONO' AND 'MAP'
1605      MOV      #17, $TEMP
1606      1$:
1607      MOV      $TEMP, @VGCCLR ;LOAD LOC. 0 WITH A INTENSITY LEVEL
1608      JSR      RS, DELAY
1609      IO.
1610      DEC      $TEMP ;REDUCE THE INTENSITY LEVEL
1611      BNE      1$ ;:BR IF MORE LEVELS TO TEST
1612
1613
1614
1615
1616
1617
    
```

M03

MAINDEC-11-DZYSE-A VSV01 VIEDO GRAPHIC TEST PROGRAM MACY11 27(1006) 02-DEC-76 17:43 PAGE 39
 DZYSEA.P11 02-JUN-76 00:00 T42 DYNAMIC WORD TESTING OF PIXEL 0 (FIRST MAP LOCATION)

```

1618
1619
1620
1621 011012 000004
1622 011014 005737 001200
1623 011020 001432
1624 011022 032777 001000 170110
1625 011030 001026
1626 011032 004737 017306
1627
1628 011036 004737 016234
1629 011042 012777 002000 170410
1630
1631 011050 004537 017062
1632 011054 000000
1633 011056 016526
1634 011060 005077 170376
1635 011064 012777 000001 170372
1636 011072 052777 000400 170360
1637 011100 004537 016666
1638 011104 000144
1639
1640
1641
1642
1643 011106 000004
1644 011110 005737 001200
1645 011114 001432
1646 011116 032777 001000 170014
1647 011124 001026
1648 011126 004737 017306
1649
1650 011132 004737 016234
1651 011136 012777 002000 170314
1652
1653 011144 004537 017062
1654 011150 000000
1655 011152 016526
1656 011154 005077 170302
1657 011160 012777 000020 170276
1658 011166 052777 000400 170264
1659 011174 004537 016666
1660 011200 000144
1661
  
```

```

:*****
:*TEST 42      DYNAMIC WORD TESTING OF PIXEL 0 (FIRST MAP LOCATION)
:*****
TST42: SCOPE
      TST      $PASS      ;TEST IF FIRST PASS OF PROGRAM
      BEQ      TST43      ;;BR IF FIRST PASS
      BIT      #SW09,@SWR  ;TEST IF INHIBIT VISUAL MAP PATTERNS
      BNE      TST43      ;;BR IF INHIBIT IS SET
      JSR      PC,TESTID  ;DISPLAY MAP # AND TEST #
      JSR      PC,CLRMAP  ;CLEAR THE MAP
      MOV      #BIT10,@VGC ;SET 'MONO'
      JSR      R5,LODSTA  ;LOAD 4 INTO L.U.T. 0 AND
      0          ;LOAD #17 INTO L.U.T. 1
      TABLE4
      CLR      @VGPC      ;CLEAR MAP P.C.
      MOV      #1,@VGBUF  ;LOAD PIXEL0 BIT0
      BIS      #BIT8,@VGC ;ENABLE THE MAP
      JSR      R5,DELAY
      100.
:*****
:*TEST 43      DYNAMIC WORD TESTING OF PIXEL 1 (FIRST MAP LOCATION)
:*****
TST43: SCOPE
      TST      $PASS      ;TEST IF FIRST PASS OF PROGRAM
      BEQ      TST44      ;;BR IF FIRST PASS
      BIT      #SW09,@SWR  ;TEST IF INHIBIT VISUAL MAP PATTERNS
      BNE      TST44      ;;BR IF INHIBIT IS SET
      JSR      PC,TESTID  ;DISPLAY MAP # AND TEST #
      JSR      PC,CLRMAP  ;CLEAR THE MAP
      MOV      #BIT10,@VGC ;SET 'MONO'
      JSR      R5,LODSTA  ;LOAD 4 INTO L.U.T. 0 AND
      0          ;LOAD #17 INTO L.U.T. 1
      TABLE4
      CLR      @VGPC      ;CLEAR MAP P.C.
      MOV      #20,@VGBUF  ;LOAD PIXEL0 BIT0
      BIS      #BIT8,@VGC ;ENABLE THE MAP
      JSR      R5,DELAY
      100.
  
```

NO3

MAINDEC-11-DZVSE-A VSV01 VIEDC GRAPHIC TEST PROGRAM MACY11 27(1006) 02-DEC-76 17:43 PAGE 40
 DZVSEA.P11 02-JUN-76 00:00 T44 DYNAMIC WORD TESTING OF PIXEL 2 (FIRST MAP LOCATION)

```

1662
1663
1664
1665 011202 000004
1666 011204 005737 001200
1667 011210 001432
1668 011212 032777 001000 167720
1669 011220 001026
1670 011222 004737 017306
1671
1672 011226 004737 016234
1673 011232 012777 002000 170220
1674
1675 011240 004537 017062
1676 011244 000000
1677 011246 016526
1678 011250 005077 170206
1679 011254 012777 000400 170202
1680 011262 052777 000400 170170
1681 011270 004537 016666
1682 011274 000144
1683
1684
1685
1686
1687 011276 000004
1688 011300 005737 001200
1689 011304 001432
1690 011306 032777 001000 167624
1691 011314 001026
1692 011316 004737 017306
1693
1694 011322 004737 016234
1695 011326 012777 002000 170124
1696
1697 011334 004537 017062
1698 011340 000000
1699 011342 016526
1700 011344 005077 170112
1701 011350 012777 010000 170106
1702 011356 052777 000400 170074
1703 011364 004537 016666
1704 011370 000144
1705
  
```

```

:*****
:*TEST 44 DYNAMIC WORD TESTING OF PIXEL 2 (FIRST MAP LOCATION)
:*****
↑ST44: SCOPE
      TST $PASS ;TEST IF FIRST PASS OF PROGRAM
      BEQ TST45 ;;BR IF FIRST PASS
      BIT #SW09,JSWR ;TEST IF INHIBIT VISUAL MAP PATTERNS
      BNE TST45 ;;BR IF INHIBIT IS SET
      JSR PC,TESTID ;DISPLAY MAP # AND TEST #
      JSR PC,CLMAP ;CLEAR THE MAP
      MOV #BIT10,AVGCSR ;SET 'MONO'
      JSR R5,LODSTA ;LOAD 4 INTO L.U.T. 0 AND
      0 ;LOAD #17 INTO L.U.T. 1
      TABLE4
      CLR AVGPC ;CLEAR MAP P.C.
      MOV #400,AVGBUF ;LOAD PIXEL2 BIT0
      BIS #BIT8,AVGCSR ;ENABLE THE MAP
      JSR R5,DELAY
      100.
  
```

```

1683
1684
1685
1686
1687 011276 000004
1688 011300 005737 001200
1689 011304 001432
1690 011306 032777 001000 167624
1691 011314 001026
1692 011316 004737 017306
1693
1694 011322 004737 016234
1695 011326 012777 002000 170124
1696
1697 011334 004537 017062
1698 011340 000000
1699 011342 016526
1700 011344 005077 170112
1701 011350 012777 010000 170106
1702 011356 052777 000400 170074
1703 011364 004537 016666
1704 011370 000144
1705
  
```

```

:*****
:*TEST 45 DYNAMIC WORD TESTING OF PIXEL 3 (FIRST MAP LOCATION)
:*****
↑ST45: SCOPE
      TST $PASS ;TEST IF FIRST PASS OF PROGRAM
      BEQ TST46 ;;BR IF FIRST PASS
      BIT #SW09,JSWR ;TEST IF INHIBIT VISUAL MAP PATTERNS
      BNE TST46 ;;BR IF INHIBIT IS SET
      JSR PC,TESTID ;DISPLAY MAP # AND TEST #
      JSR PC,CLMAP ;CLEAR THE MAP
      MOV #BIT10,AVGCSR ;SET 'MONO'
      JSR R5,LODSTA ;LOAD 4 INTO L.U.T. 0 AND
      0 ;LOAD #17 INTO L.U.T. 1
      TABLE4
      CLR AVGPC ;CLEAR MAP P.C.
      MOV #10000,AVGBUF ;LOAD PIXEL3 BIT0
      BIS #BIT8,AVGCSR ;ENABLE THE MAP
      JSR R5,DELAY
      100.
  
```

B04

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1760

011372 000004
011374 005737 001200
011400 001441
011402 032777 001000 167530
011410 001035
011412 004737 017306
011418 004737 016234
011422 004537 017062
011426 000000
011430 016466

011432 005077 170024
011436 013700 001464
011442 013701 001542
011446 105777 170006
011452 100375
011454 012710 000001
011460 005301
011462 001371
011464 012777 002400 167766
011472 004737 017554
011476 004537 016666
011502 000144

*TEST 46 DYNAMIC WORD TESTING OF 'PIXELO' (INCREMENT MAP P.C.) TEST L.L.U.T. 1

```
TST46: SCOPE
TST $PASS ;TEST IF FIRST PASS OF PROGRAM
BEQ TST47 ::BR IF FIRST PASS
BIT #SW09, @SWR ;TEST IF INHIBIT VISUAL MAP PATTERNS
BNE TST47 ::BR IF INHIBIT IS SET
JSR PC, TESTID ;DISPLAY MAP * AND TEST *
JSR PC, CLMAP
JSR RS, LODSTA ;LOAD COLOR TABLE
C ;LOC 0 AND 2 THRU 17 WITH 0
TABLE2 ;LOC 1 WITH A 17

CLR @VGPC ;LOAD MAP P.C. TO 0
MOV VGBUF, R0 ;LOAD PIXEL ADDRESS
MOV NUMPIX, R1 ;LOAD # OF PIXELS
TSTB @VGCSR ;WAIT FOR MAP READY
BPL IS
MOV #1, (R0) ;LOAD BIT0 OF PIXEL0
DEC R1 ;FINISHED ALL PIXEL0'S
BNE IS ;BR IF NOT
MOV #BIT10!BIT8, @VGCSR ;ENABLE THE MAP
JSR PC, LDLIN ;LOAD REFERENCE LINE
JSR RS, DELAY
IOC.
```

*TEST 47 DYNAMIC WORD TESTING OF 'PIXEL1' (INCREMENT MAP P.C.) TEST L.L.U.T. 1

```
TST47: SCOPE
TST $PASS ;TEST IF FIRST PASS OF PROGRAM
BEQ TST50 ::BR IF FIRST PASS
BIT #SW09, @SWR ;TEST IF INHIBIT VISUAL MAP PATTERNS
BNE TST50 ::BR IF INHIBIT IS SET
JSR PC, TESTID ;DISPLAY MAP * AND TEST *
JSR PC, CLMAP
JSR RS, LODSTA ;LOAD COLOR TABLE
C ;LOC 0 AND 2 THRU 17 WITH 0
TABLE2 ;LOC 1 WITH A 17

CLR @VGPC ;LOAD MAP P.C. TO 0
MOV VGBUF, R0 ;LOAD PIXEL ADDRESS
MOV NUMPIX, R1 ;LOAD # OF PIXELS
TSTB @VGCSR ;WAIT FOR MAP READY
BPL IS
MOV #20, (R0) ;LOAD BIT0 OF PIXEL1
DEC R1 ;FINISHED ALL PIXEL1'S
BNE IS ;BR IF NOT
MOV #BIT10!BIT8, @VGCSR ;ENABLE THE MAP
JSR PC, LDLIN ;LOAD REFERENCE LINE
JSR RS, DELAY
IOC.
```

```

1760
1761
1762
1763 011616 000004
1764 011620 005737 001200
1765 011624 001441
1766 011626 032777 001000 167304
1767 011634 001035
1768 011636 004737 017306
1769 011642 004737 016234
1770 011646 004537 017062
1771 011652 000000
1772 011654 016466
1773
1774 011656 005077 167600
1775 011662 013700 001464
1776 011666 013701 001542
1777 011672 105777 167562
1778 011676 100375
1779 011700 012710 000400
1780 011704 005301
1781 011706 001371
1782 011710 012777 002400 167542
1783 011716 004737 017554
1784 011722 004537 016666
1785 011726 000144
1786
1787
1788
1789
1790
1791 011730 000004
1792 011732 005737 001200
1793 011736 001441
1794 011740 032777 001000 167172
1795 011746 001035
1796 011750 004737 017306
1797 011754 004737 016234
1798 011760 004537 017062
1799 011764 000000
1800 011766 016466
1801
1802 011770 005077 167466
1803 011774 013700 001464
1804 012000 013701 001542
1805 012004 105777 167450
1806 012010 100375
1807 012012 012710 010000
1808 012016 005301
1809 012020 001371
1810 012022 012777 002400 167430
1811 012030 004737 017554
1812 012034 004537 016666
1813 012040 000144

```

```

*****
*TEST 50 DYNAMIC WORD TESTING OF 'PIXEL2' (INCREMENT MAP P.C.) TEST L.U.T. :
*****

```

```

†STED: SCOPE
      TST $PASS ;TEST IF FIRST PASS OF PROGRAM
      BEQ TST51 ;;BR IF FIRST PASS
      BIT #SW09,JSWR ;TEST IF INHIBIT VISUAL MAP PATTERNS
      BNE TST51 ;;BR IF INHIBIT IS SET
      JSR PC,TESTID ;DISPLAY MAP * AND TEST *
      JSR PC,CLMAP
      JSR RS,LODSTA ;LOAD COLOR TABLE
      O ;LOC 0 AND 2 THRU 17 WITH 0
      TABLE2 ;LOC 1 WITH A 17
      CLR @VGPC ;LOAD MAP P.C. TO C
      MOV VGBUF,RO ;LOAD PIXEL ADDRESS
      MOV NUMPIX,R1 ;LOAD # OF PIXELS
      TSTB @VGCSR ;WAIT FOR MAP READY
      BPL IS
      MOV #400,(RC) ;LOAD BIT0 OF PIXEL2
      DEC R1 ;FINISHED ALL PIXEL2'S
      BNE IS ;BR IF NOT
      MOV #BIT10!BIT8,@VGCSR ;ENABLE THE MAP
      JSR PC,LDLIN ;LOAD REFERENCE LINE
      JSR RS,DELAY
      IO.

```

```

*****
*TEST 51 DYNAMIC WORD TESTING OF 'PIXEL3' (INCREMENT MAP P.C.) TEST L.U.T. 1
*****

```

```

†ST51: SCOPE
      TST $PASS ;TEST IF FIRST PASS OF PROGRAM
      BEQ TST52 ;;BR IF FIRST PASS
      BIT #SW09,JSWR ;TEST IF INHIBIT VISUAL MAP PATTERNS
      BNE TST52 ;;BR IF INHIBIT IS SET
      JSR PC,TESTID ;DISPLAY MAP * AND TEST *
      JSR PC,CLMAP
      JSR RS,LODSTA ;LOAD COLOR TABLE
      O ;LOC 0 AND 2 THRU 17 WITH 0
      TABLE2 ;LOC 1 WITH A 17
      CLR @VGPC ;LOAD MAP P.C. TO C
      MOV VGBUF,RO ;LOAD PIXEL ADDRESS
      MOV NUMPIX,R1 ;LOAD # OF PIXELS
      TSTB @VGCSR ;WAIT FOR MAP READY
      BPL IS
      MOV #10000,(RC) ;LOAD BIT0 OF PIXEL3
      DEC R1 ;FINISHED ALL PIXEL3'S
      BNE IS ;BR IF NOT
      MOV #BIT10!BIT8,@VGCSR ;ENABLE THE MAP
      JSR PC,LDLIN ;LOAD REFERENCE LINE
      JSR RS,DELAY
      IO.

```

012042 000004
012044 005737 001200
012050 001441
012052 032777 001000 16736C
012060 001035
012062 004737 017306
012066 004737 016234
012072 004537 017062
012076 000000
012100 016606
012102 005077 167354
012106 013700 001464
012112 013701 001542
012116 105777 167336
012122 100375
012124 012710 000002
012130 005301
012132 001371
012134 012777 002400 167316
012142 004737 017554
012146 004537 016666
012152 000144

```
*****  
*TEST 52 DYNAMIC WORD TESTING OF 'PIXELO' (INCREMENT MAP P.C.) TEST L.U.T. 2  
*****  
↑TST52: SCOPE  
TST $PASS ;TEST IF FIRST PASS OF PROGRAM  
BEQ TST53 ;;BR IF FIRST PASS  
BIT #SW09,JSWR ;TEST IF INHIBIT VISUAL MAP PATTERNS  
BNE TST53 ;;BR IF INHIBIT IS SET  
JSR PC,TESTID ;DISPLAY MAP * AND TEST *  
JSR PC,CLMAP  
JSR RS,LODSTA ;LOAD COLOR TABLE  
C ;LOC 0,1 AND 3 THRU 17 WITH C  
TABLE6 ;LOC 2 WITH A 17  
CLR @VGPC ;LOAD MAP P.C. TO C  
MOV VGBUF,RO ;LOAD PIXEL ADDRESS  
MOV NUMPIX,R1 ;LOAD # OF PIXELS  
TSTB @VGCSR ;WAIT FOR MAP READY  
BPL IS  
MOV #2,(RO) ;LOAD BIT1 OF PIXEL0  
DEC R1 ;FINISHED ALL PIXEL0'S  
BNE IS ;BR IF NOT  
MOV #BIT10:BIT8,@VGCSR ;ENABLE THE MAP  
JSR PC,LDLIN ;LOAD REFERENCE LINE  
JSR RS,DELAY  
100.
```

012154 000004
012156 005737 001200
012162 001441
012164 032777 001000 166746
012172 001035
012174 004737 017306
012200 004737 016234
012204 004537 017062
012210 000000
012212 016606
012214 005077 167242
012220 013700 001464
012224 013701 001542
012230 105777 167224
012234 100375
012236 012710 000040
012242 005301
012244 001371
012246 012777 002400 167204
012254 004737 017554
012260 004537 016666
012264 000144

```
*****  
*TEST 53 DYNAMIC WORD TESTING OF 'PIXEL1' (INCREMENT MAP P.C.) TEST L.U.T. 2  
*****  
↑TST53: SCOPE  
TST $PASS ;TEST IF FIRST PASS OF PROGRAM  
BEQ TST54 ;;BR IF FIRST PASS  
BIT #SW09,JSWR ;TEST IF INHIBIT VISUAL MAP PATTERNS  
BNE TST54 ;;BR IF INHIBIT IS SET  
JSR PC,TESTID ;DISPLAY MAP * AND TEST *  
JSR PC,CLMAP  
JSR RS,LODSTA ;LOAD COLOR TABLE  
C ;LOC 0,1 AND 3 THRU 17 WITH C  
TABLE6 ;LOC 2 WITH A 17  
CLR @VGPC ;LOAD MAP P.C. TO C  
MOV VGBUF,RO ;LOAD PIXEL ADDRESS  
MOV NUMPIX,R1 ;LOAD # OF PIXELS  
TSTB @VGCSR ;WAIT FOR MAP READY  
BPL IS  
MOV #40,(RO) ;LOAD BIT1 OF PIXEL1  
DEC R1 ;FINISHED ALL PIXEL1'S  
BNE IS ;BR IF NOT  
MOV #BIT10:BIT8,@VGCSR ;ENABLE THE MAP  
JSR PC,LDLIN ;LOAD REFERENCE LINE  
JSR RS,DELAY  
100.
```

E04

012300 000004 001200
012302 005737 001200
012304 001441 001200
012306 032777 001000 166624
012308 001035 001000
012310 004737 017306
012312 004737 016234
012314 004537 017062
012316 000000
012318 016606
012320
012322
012324
012326 005077 167130
012328 013700 001464
012330 013701 001542
012332 105777 167112
012334 100375
012336 012710 001000
012338 005301
012340 001371
012342 012777 002400 167072
012344 004737 017554
012346 004537 016666
012348 000144

```
*****  
*TEST 54 DYNAMIC WORD TESTING OF 'PIXEL2' (INCREMENT MAP P.C.) TEST L.U.T. 2  
*****  
*TEST 54: SCOPE  
TST $PASS ;TEST IF FIRST PASS OF PROGRAM  
BEQ TST55 ::BR IF FIRST PASS  
BIT #SW09,JSWR ;TEST IF INHIBIT VISUAL MAP PATTERNS  
BNE TST55 ::BR IF INHIBIT IS SET  
JSR PC,TESTID ;DISPLAY MAP * AND TEST *  
JSR PC,CLRMAP  
JSR RS,LODSTA ;LOAD COLOR TABLE  
C ;LOC 0,1 AND 3 THRU 17 WITH C  
TABLE6 ;LOC 2 WITH A 17  
  
CLR @VGPC ;LOAD MAP P.C. TO C  
MOV VGBUF,RO ;LOAD PIXEL ADDRESS  
MOV NUMPIX,R1 ;LOAD # OF PIXELS  
1$: TSTB @VGCSR ;WAIT FOR MAP READY  
BPL 1$  
MOV #1000,(RO) ;LOAD BIT1 OF PIXEL2  
DEC R1 ;FINISHED ALL PIXEL2'S  
BNE 1$ ;BR IF NOT  
MOV #BIT10!BIT8,@VGCSR ;ENABLE THE MAP  
JSR PC,LDLIN ;LOAD REFERENCE LINE  
JSR RS,DELAY  
100.
```

012400 000004 001200
012402 005737 001200
012404 001441 001200
012406 032777 001000 166522
012408 001035 001000
012410 004737 017306
012412 004737 016234
012414 004537 017062
012416 000000
012418 016606
012420
012422
012424
012426 005077 167016
012428 013700 001464
012430 013701 001542
012432 105777 167000
012434 100375
012436 012710 020000
012438 005301
012440 001371
012442 012777 002400 166760
012444 004737 017554
012446 004537 016666
012448 000144

```
*****  
*TEST 55 DYNAMIC WORD TESTING OF 'PIXEL3' (INCREMENT MAP P.C.) TEST L.U.T. 2  
*****  
*TEST 55: SCOPE  
TST $PASS ;TEST IF FIRST PASS OF PROGRAM  
BEQ TST56 ::BR IF FIRST PASS  
BIT #SW09,JSWR ;TEST IF INHIBIT VISUAL MAP PATTERNS  
BNE TST56 ::BR IF INHIBIT IS SET  
JSR PC,TESTID ;DISPLAY MAP * AND TEST *  
JSR PC,CLRMAP  
JSR RS,LODSTA ;LOAD COLOR TABLE  
C ;LOC 0,1 AND 3 THRU 17 WITH C  
TABLE6 ;LOC 2 WITH A 17  
  
CLR @VGPC ;LOAD MAP P.C. TO C  
MOV VGBUF,RO ;LOAD PIXEL ADDRESS  
MOV NUMPIX,R1 ;LOAD # OF PIXELS  
1$: TSTB @VGCSR ;WAIT FOR MAP READY  
BPL 1$  
MOV #20000,(RO) ;LOAD BIT1 OF PIXEL3  
DEC R1 ;FINISHED ALL PIXEL3'S  
BNE 1$ ;BR IF NOT  
MOV #BIT10!BIT8,@VGCSR ;ENABLE THE MAP  
JSR PC,LDLIN ;LOAD REFERENCE LINE  
JSR RS,DELAY  
100.
```


F04

NOEC-11-02VSE-A
02VSEAF11

VSV: 02-JUN-76 00:00

VIEDO GRAPHIC TEST PROGRAM
T56

MACY11 27(1006)

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DYNAMIC WORD TESTING OF 'PIXELO' (INCREMENT MAP P.C.) TEST L.U.T. 4

```

1920
1921
1922
1923 012512 000004
1924 012514 005737 001200
1925 012520 001441
1926 012522 032777 001000 166410
1927 012530 001035
1928 012532 004737 017306
1929 012536 004737 016234
1930 012542 004537 017062
1931 012546 000000
1932 012550 016626
1933
1934 012552 005077 166704
1935 012556 013700 001464
1936 012562 013701 001542
1937 012566 105777 166666
1938 012572 100375
1939 012574 012710 000004
1940 012600 005301
1941 012602 001371
1942 012604 012777 002400 166646
1943 012612 004737 017554
1944 012616 004537 016666
1945 012622 000144
1946
1947
1948
1949
1950 012624 000004
1951 012626 005737 001200
1952 012632 001441
1953 012634 032777 001000 166276
1954 012642 001035
1955 012644 004737 017306
1956 012650 004737 016234
1957 012654 004537 017062
1958 012660 000000
1959 012662 016626
1960
1961 012664 005077 166572
1962 012670 013700 001464
1963 012674 013701 001542
1964 012700 105777 166554
1965 012704 100375
1966 012706 012710 000100
1967 012712 005301
1968 012714 001371
1969 012716 012777 002400 166534
1970 012724 004737 017554
1971 012730 004537 016666
1972 012734 000144
1973

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*****
*TEST 56 DYNAMIC WORD TESTING OF 'PIXELO' (INCREMENT MAP P.C.) TEST L.U.T. 4
*****
TST56: SCOPE
TST $PASS ;TEST IF FIRST PASS OF PROGRAM
BEQ TST57 ;;BR IF FIRST PASS
BIT #SW09,JSWR ;TEST IF INHIBIT VISUAL MAP PATTERNS
BNE TST57 ;;BR IF INHIBIT IS SET
JSR PC,TESTID ;DISPLAY MAP * AND TEST *
JSR PC,CLMAP
JSR RS,LODSTA ;LOAD COLOR TABLE
0 ;LOC 0,1,2,3 AND 5 THRU 17 WITH 0
TABLE7 ;LOC 4 WITH A 17

CLR @VGPC ;LOAD MAP P.C. TO 0
MOV VGBUF,R0 ;LOAD PIXEL ADDRESS
MOV NUMPIX,R1 ;LOAD # OF PIXELS
TSTB @VGCSR ;WAIT FOR MAP READY
BPL IS
MOV #4,(R0) ;LOAD BIT2 OF PIXEL0
DEC R1 ;FINISHED ALL PIXEL0'S
BNE IS ;BR IF NOT
MOV #BIT10!BIT8,@VGCSR ;ENABLE THE MAP
JSR PC,LDLIN ;LOAD REFERENCE LINE
JSR RS,DELAY
100.

*****
*TEST 57 DYNAMIC WORD TESTING OF 'PIXEL1' (INCREMENT MAP P.C.) TEST L.U.T. 4
*****
TST57: SCOPE
TST $PASS ;TEST IF FIRST PASS OF PROGRAM
BEQ TST60 ;;BR IF FIRST PASS
BIT #SW09,JSWR ;TEST IF INHIBIT VISUAL MAP PATTERNS
BNE TST60 ;;BR IF INHIBIT IS SET
JSR PC,TESTID ;DISPLAY MAP * AND TEST *
JSR PC,CLMAP
JSR RS,LODSTA ;LOAD COLOR TABLE
0 ;LOC 0,1,2,3 AND 5 THRU 17 WITH 0
TABLE7 ;LOC 4 WITH A 17

CLR @VGPC ;LOAD MAP P.C. TO 0
MOV VGBUF,R0 ;LOAD PIXEL ADDRESS
MOV NUMPIX,R1 ;LOAD # OF PIXELS
TSTB @VGCSR ;WAIT FOR MAP READY
BPL IS
MOV #100,(R0) ;LOAD BIT2 OF PIXEL1
DEC R1 ;FINISHED ALL PIXEL1'S
BNE IS ;BR IF NOT
MOV #BIT10!BIT8,@VGCSR ;ENABLE THE MAP
JSR PC,LDLIN ;LOAD REFERENCE LINE
JSR RS,DELAY
100.

```

G04

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1974
1975
1976
1977 012736 000004
1978 012740 005737 001200
1979 012744 001441
1980 012746 032777 001000 166164
1981 012754 001035
1982 012756 004737 017306
1983 012762 004737 016234
1984 012766 004537 017062
1985 012772 000000
1986 012774 016626
1987
1988 012776 005077 166460
1989 013002 013700 001464
1990 013006 013701 001542
1991 013012 105777 166442
1992 013016 100375
1993 013020 012710 002000
1994 013024 005301
1995 013026 001371
1996 013030 012777 002400 166422
1997 013036 004737 017554
1998 013042 004537 016666
1999 013046 000144
2000
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*****
*TEST 60 DYNAMIC WORD TESTING OF 'PIXEL2' (INCREMENT MAP P.C.) TEST L.U.T. 4
*****
TST60: SCOPE
TST $PASS ;TEST IF FIRST PASS OF PROGRAM
BEQ TST61 ;;BR IF FIRST PASS
BIT #SW09,ASWR ;TEST IF INHIBIT VISUAL MAP PATTERNS
BNE TST61 ;;BR IF INHIBIT IS SET
JSR PC,TESTID ;DISPLAY MAP * AND TEST *
JSR PC,CLMAP
JSR R5,LODSTA ;LOAD COLOR TABLE
0 ;LOC 0,1,2,3 AND 5 THRU 17 WITH 0
TABLE7 ;LOC 41 WITH A 17
CLR @VGPC ;LOAD MAP P.C. TO 0
MOV VGBUF,R0 ;LOAD PIXEL ADDRESS
MOV NUMPIX,R1 ;LOAD # OF PIXELS
TSTB @VGCSCR ;WAIT FOR MAP READY
BPL 1$
MOV #2000,(R0) ;LOAD BIT2 OF PIXEL2
DEC R1 ;FINISHED ALL PIXEL2'S
BNE 1$ ;BR IF NOT
MOV #BIT10:BIT8,@VGCSCR ;ENABLE THE MAP
JSR PC,LDLIN ;LOAD REFERENCE LINE
JSR R5,DELAY
100.

```

```

2007
2008
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*****
*TEST 61 DYNAMIC WORD TESTING OF 'PIXEL3' (INCREMENT MAP P.C.) TEST L.U.T. 4
*****
TST61: SCOPE
TST $PASS ;TEST IF FIRST PASS OF PROGRAM
BEQ TST62 ;;BR IF FIRST PASS
BIT #SW09,ASWR ;TEST IF INHIBIT VISUAL MAP PATTERNS
BNE TST62 ;;BR IF INHIBIT IS SET
JSR PC,TESTID ;DISPLAY MAP * AND TEST *
JSR PC,CLMAP
JSR R5,LODSTA ;LOAD COLOR TABLE
0 ;LOC 0,1,2,3 AND 5 THRU 17 WITH 0
TABLE7 ;LOC 4 WITH A 17
CLR @VGPC ;LOAD MAP P.C. TO 0
MOV VGBUF,R0 ;LOAD PIXEL ADDRESS
MOV NUMPIX,R1 ;LOAD # OF PIXELS
TSTB @VGCSCR ;WAIT FOR MAP READY
BPL 1$
MOV #40000,(R0) ;LOAD BIT2 OF PIXEL3
DEC R1 ;FINISHED ALL PIXEL3'S
BNE 1$ ;BR IF NOT
MOV #BIT10:BIT8,@VGCSCR ;ENABLE THE MAP
JSR PC,LDLIN ;LOAD REFERENCE LINE
JSR R5,DELAY
100.

```

H04

2027
2028
2029
2030 013162 000304
2031 013164 005737 001200
2032 013170 001441
2033 013172 032777 001000 165740
2034 013200 001035
2035 013202 004737 017306
2036 013206 004737 016234
2037 013212 004537 017062
2038 013216 000000
2039 013220 016646
2040
2041 013222 005077 166234
2042 013226 013700 001464
2043 013232 013701 001542
2044 013236 105777 166216
2045 013242 100375
2046 013244 012710 000010
2047 013250 005301
2048 013252 001371
2049 013254 012777 002400 166176
2050 013262 004737 017554
2051 013266 004537 016666
2052 013272 000144
2053
2054
2055
2056
2057 013274 000004
2058 013276 005737 001200
2059 013302 001441
2060 013304 032777 001000 165626
2061 013312 001035
2062 013314 004737 017306
2063 013320 004737 016234
2064 013324 004537 017062
2065 013330 000000
2066 013332 016646
2067
2068 013334 005077 166122
2069 013340 013700 001464
2070 013344 013701 001542
2071 013350 105777 166104
2072 013354 100375
2073 013356 012710 000200
2074 013362 005301
2075 013364 001371
2076 013366 012777 002400 166064
2077 013374 004737 017554
2078 013400 004537 016666
2079 013404 000144
2080

*TEST 62 DYNAMIC WORD TESTING OF 'PIXELO' (INCREMENT MAP P.C.) TEST L.U.T. 10

TST62: SCOPE
TST \$PASS ;TEST IF FIRST PASS OF PROGRAM
BEQ TST63 ;;BR IF FIRST PASS
BIT #SW09,SWR ;TEST IF INHIBIT VISUAL MAP PATTERNS
BNE TST63 ;;BR IF INHIBIT IS SET
JSR PC,TESTID ;DISPLAY MAP # AND TEST #
JSR PC,CLMAP
JSR R5,LODSTA ;LOAD COLOR TABLE
D ;LOC 0,1,2,3,4,5,6,7 AND 11 THRU 17 WITH C
TABLEB ;LOC 10 WITH A 17
CLR @VGPC ;LOAD MAP P.C. TO 0
MOV VGBUF,R0 ;LOAD PIXEL ADDRESS
MOV NUMPIX,R1 ;LOAD # OF PIXELS
1\$: TSTB @VGCSR ;WAIT FOR MAP READY
BPL 1\$
MOV #10,(R0) ;LOAD BITS OF PIXEL0
DEC R1 ;FINISHED ALL PIXEL0'S
BNE 1\$;BR IF NOT
MOV #BIT10!BIT8,@VGCSR ;ENABLE THE MAP
JSR PC,LDLIN ;LOAD REFERENCE LINE
JSR R5,DELAY
100.

*TEST 63 DYNAMIC WORD TESTING OF 'PIXEL1' (INCREMENT MAP P.C.) TEST L.U.T. 10

TST63: SCOPE
TST \$PASS ;TEST IF FIRST PASS OF PROGRAM
BEQ TST64 ;;BR IF FIRST PASS
BIT #SW09,SWR ;TEST IF INHIBIT VISUAL MAP PATTERNS
BNE TST64 ;;BR IF INHIBIT IS SET
JSR PC,TESTID ;DISPLAY MAP # AND TEST #
JSR PC,CLMAP
JSR R5,LODSTA ;LOAD COLOR TABLE
D ;LOC 0,1,2,3,4,5,6,7 AND 11 THRU 17 WITH C
TABLEB ;LOC 10 WITH A 17
CLR @VGPC ;LOAD MAP P.C. TO 0
MOV VGBUF,R0 ;LOAD PIXEL ADDRESS
MOV NUMPIX,R1 ;LOAD # OF PIXELS
1\$: TSTB @VGCSR ;WAIT FOR MAP READY
BPL 1\$
MOV #200,(R0) ;LOAD BITS OF PIXEL1
DEC R1 ;FINISHED ALL PIXEL1'S
BNE 1\$;BR IF NOT
MOV #BIT10!BIT8,@VGCSR ;ENABLE THE MAP
JSR PC,LDLIN ;LOAD REFERENCE LINE
JSR R5,DELAY
100.

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0081
0082
0083
0084 013406 000004
0085 013410 005737 001200
0086 013414 001441
0087 013416 032777 001000 165514
0088 013424 001035
0089 013426 004737 017306
0090 013432 004737 016234
0091 013436 004537 017052
0092 013442 000000
0093 013444 016646
0094
0095 013446 005077 166010
0096 013452 013700 001464
0097 013456 013701 001542
0098 013462 105777 165772
0099 013466 100375
2100 013470 012710 004000
2101 013474 005301
2102 013476 001371
2103 013500 012777 002400 165752
2104 013506 004737 017554
2105 013512 004537 016666
2106 013516 000144
2107
2108
2109
2110
2111 013520 000004
2112 013522 005737 001200
2113 013526 001441
2114 013530 032777 001000 165402
2115 013536 001035
2116 013540 004737 017306
2117 013544 004737 016234
2118 013550 004537 017062
2119 013554 000000
2120 013556 016646
2121
2122 013560 005077 165676
2123 013564 013700 001464
2124 013570 013701 001542
2125 013574 105777 165660
2126 013600 100375
2127 013602 012710 100000
2128 013606 005301
2129 013610 001371
2130 013612 012777 002400 165640
2131 013620 004737 017554
2132 013624 004537 016666
2133 013630 000144

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*****
*TEST 64 DYNAMIC WORD TESTING OF 'PIXEL2' (INCREMENT MAP P.C.) TEST L.U.T. 10
*****

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```

†STE4: SCOPE
TST $PASS ;TEST IF FIRST PASS OF PROGRAM
BEQ TST65 ;;BR IF FIRST PASS
BIT #SW09,@SWR ;TEST IF INHIBIT VISUAL MAP PATTERNS
BNE TST65 ;;BR IF INHIBIT IS SET
JSR PC,TESTID ;DISPLAY MAP * AND TEST *
JSR PC,CLRMAP
JSR R5,LODSTA ;LOAD COLOR TABLE
0 ;LOC 0,1,2,3,4,5,6,7 AND 11 THRU 17 WITH 0
TABLE8 ;LOC 10 WITH A 17
CLR @VGPC ;LOAD MAP P.C. TO C
MOV VGBUF,R0 ;LOAD PIXEL ADDRESS
MOV NUMPIX,R1 ;LOAD # OF PIXELS
1$: TSTB @VGCSR ;WAIT FOR MAP READY
BPL 1$
MOV #4000,(R0) ;LOAD BIT3 OF PIXEL2
DEC R1 ;FINISHED ALL PIXEL2'S
BNE 1$ ;BR IF NOT
MOV #BIT10!BIT8,@VGCSR ;ENABLE THE MAP
JSR PC,LDLIN ;LOAD REFERENCE LINE
JSR R5,DELAY
100.

```

```

*****
*TEST 65 DYNAMIC WORD TESTING OF 'PIXEL3' (INCREMENT MAP P.C.) TEST L.U.T. 10
*****

```

```

†STE5: SCOPE
TST $PASS ;TEST IF FIRST PASS OF PROGRAM
BEQ TST66 ;;BR IF FIRST PASS
BIT #SW09,@SWR ;TEST IF INHIBIT VISUAL MAP PATTERNS
BNE TST66 ;;BR IF INHIBIT IS SET
JSR PC,TESTID ;DISPLAY MAP * AND TEST *
JSR PC,CLRMAP
JSR R5,LODSTA ;LOAD COLOR TABLE
0 ;LOC 0,1,2,3,4,5,6,7 AND 11 THRU 17 WITH 0
TABLE8 ;LOC 10 WITH A 17
CLR @VGPC ;LOAD MAP P.C. TO 0
MOV VGBUF,R0 ;LOAD PIXEL ADDRESS
MOV NUMPIX,R1 ;LOAD # OF PIXELS
1$: TSTB @VGCSR ;WAIT FOR MAP READY
BPL 1$
MOV #100000,(R0) ;LOAD BIT3 OF PIXEL3
DEC R1 ;FINISHED ALL PIXEL3'S
BNE 1$ ;BR IF NOT
MOV #BIT10!BIT8,@VGCSR ;ENABLE THE MAP
JSR PC,LDLIN ;LOAD REFERENCE LINE
JSR R5,DELAY
100.

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2134
2135
2136
2137 013632 000004
2138 013634 005737 001200
2139 013640 001441
2140 013642 032777 001000 165270
2141 013650 001035
2142 013652 004737 017306
2143 013656 004737 016234
2144 013662 004537 017062
2145 013666 000000
2146 013670 016466
2147 013672 005077 165564
2148 013676 013700 001464
2149 013702 013701 001542
2150 013706 105777 165546
2151 013712 100375
2152 013714 112710 000001
2153 013720 005301
2154 013722 001371
2155 013724 012777 002400 165526
2156 013732 004737 017554
2157 013736 004537 016666
2158 013742 000144
2159
2160
2161
2162
2163 013744 000004
2164 013746 005737 001200
2165 013752 001442
2166 013754 032777 001000 165156
2167 013762 001036
2168 013764 004737 017306
2169 013770 004737 016234
2170 013774 004537 017062
2171 014000 000000
2172 014002 016466
2173 014004 005077 165452
2174 014010 013700 001464
2175 014014 005200
2176 014016 013701 001542
2177 014022 105777 165432
2178 014026 100375
2179 014030 112710 000001
2180 014034 005301
2181 014036 001371
2182 014040 012777 002400 165412
2183 014046 004737 017554
2184 014052 004537 016666
2185 014056 000144

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```

*****
*TEST 66 DYNAMIC BYTE TESTING OF 'PIXELO'
*****
TST66: SCOPE
TST $PASS ;TEST IF FIRST PASS OF PROGRAM
BEQ TST67 ;;BR IF FIRST PASS
BIT #SW09,SWR ;TEST IF INHIBIT VISUAL MAP PATTERNS
BNE TST67 ;;BR IF INHIBIT IS SET
JSR PC,TESTID ;DISPLAY MAP * AND TEST *
JSR PC,CLRMAP
JSR R5,LODSTA ;LOAD COLOR TABLE
D ;LOC 0 AND 2 THRU 17 WITH 17
TABLE2 ;LOC 1 WITH A 4
CLR @VGPC ;LOAD MAP P.C. TO 0
MOV VGBUF,R0 ;LOAD PIXEL ADDRESS
MOV NUMPIX,R1 ;LOAD # OF PIXELS
TSTB @VGCSR ;WAIT FOR MAP READY
BPL IS
MOVB #1,(R0) ;LOAD BIT0 OF PIXEL0
DEC R1 ;FINISHED ALL PIXEL C'S
BNE IS ;BR IF NOT
MOV #BIT10:BIT8,@VGCSR ;ENABLE THE MAP
JSR PC,LDLIN ;LOAD REFERENCE LINE
JSR R5,DELAY
100.

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2160
2161
2162
2163 013744 000004
2164 013746 005737 001200
2165 013752 001442
2166 013754 032777 001000 165156
2167 013762 001036
2168 013764 004737 017306
2169 013770 004737 016234
2170 013774 004537 017062
2171 014000 000000
2172 014002 016466
2173 014004 005077 165452
2174 014010 013700 001464
2175 014014 005200
2176 014016 013701 001542
2177 014022 105777 165432
2178 014026 100375
2179 014030 112710 000001
2180 014034 005301
2181 014036 001371
2182 014040 012777 002400 165412
2183 014046 004737 017554
2184 014052 004537 016666
2185 014056 000144

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*****
*TEST 67 DYNAMIC BYTE TESTING OF 'PIXEL1'
*****
TST67: SCOPE
TST $PASS ;TEST IF FIRST PASS OF PROGRAM
BEQ TST70 ;;BR IF FIRST PASS
BIT #SW09,SWR ;TEST IF INHIBIT VISUAL MAP PATTERNS
BNE TST70 ;;BR IF INHIBIT IS SET
JSR PC,TESTID ;DISPLAY MAP * AND TEST *
JSR PC,CLRMAP
JSR R5,LODSTA ;LOAD COLOR TABLE
D ;LOC 0 AND 2 THRU 17 WITH 17
TABLE2 ;LOC 1 WITH A 4
CLR @VGPC ;LOAD MAP P.C. TO 0
MOV VGBUF,R0 ;LOAD PIXEL ADDRESS
INC R0 ;MAKE PIXEL 1 BYTE ADDRESS
MOV NUMPIX,R1 ;LOAD # OF PIXELS
TSTB @VGCSR ;WAIT FOR MAP READY
BPL IS
MOVB #1,(R0) ;LOAD BIT0 OF PIXEL 1
DEC R1 ;FINISHED ALL PIXEL 1'S
BNE IS ;BR IF NOT
MOV #BIT10:BIT8,@VGCSR ;ENABLE THE MAP
JSR PC,LDLIN ;LOAD REFERENCE LINE
JSR R5,DELAY
100.

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K04

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2186
2187
2188
2189 014060 000004
2190 014062 005737 001200
2191 014066 001442
2192 014070 032777 001000 165342
2193 014076 001036
2194 014100 004737 017306
2195 014104 004737 016234
2196 014110 004537 017062
2197 014114 000000
2198 014116 016466
2199 014120 005077 165336
2200 014124 013700 001464
2201 014130 005720
2202 014132 013701 001542
2203 014136 105777 165316
2204 014142 100375
2205 014144 112710 000001
2206 014150 005301
2207 014152 001371
2208 014154 012777 002400 165276
2209 014162 004737 017554
2210 014166 004537 016666
2211 014172 000144
2212
2213
2214
2215
2216 014174 000004
2217 014176 005737 001200
2218 014202 001446
2219 014204 032777 001000 164726
2220 014212 001042
2221 014214 004737 017306
2222 014220 004737 016234
2223 014224 004537 017062
2224 014230 000000
2225 014232 016466
2226 014234 005077 165222
2227 014240 013700 001464
2228 014244 062700 000003
2229 014250 013701 001542
2230 014254 105777 165200
2231 014260 100375
2232 014262 112710 000001
2233 014266 005301
2234 014270 001371
2235 014272 012777 002400 165160
2236 014300 004737 017554
2237 014304 004537 016666
2238 014310 000144
2239 014312 042777 010000 165126

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*****
*TEST 70 DYNAMIC BYTE TESTING OF 'PIXEL2'
*****
TST70: SCOPE
TST $PASS ;TEST IF FIRST PASS OF PROGRAM
BEQ TST71 ;;BR IF FIRST PASS
BIT #SW09,@SWR ;TEST IF INHIBIT VISUAL MAP PATTERNS
BNE TST71 ;;BR IF INHIBIT IS SET
JSR PC,TESTID ;DISPLAY MAP * AND TEST *
JSR PC,CLRMAP
JSR R5,LODSTA ;LOAD COLOR TABLE
0 ;LOC 0 AND 2 THRU 17 WITH 17
TABLE2 ;LOC 1 WITH A 4
CLR @VGPC ;LOAD MAP P.C. TO 0
MOV VGBUF,R0 ;LOAD PIXEL ADDRESS
TST (R0)+ ;MAKE PIXEL 2 BYTE ADDRESS
MOV NUMPIX,R1 ;LOAD # OF PIXELS
TSTB @VGCSR ;WAIT FOR MAP READY
BPL IS
MOVB #1,(R0) ;LOAD BIT0 OF PIXEL 2
DEC R1 ;FINISHED ALL PIXEL 2'S
BNE IS ;BR IF NOT
MOV #BIT10!BIT8,@VGCSR ;ENABLE THE MAP
JSR PC,LDLIN ;LOAD REFERENCE LINE
JSR R5,DELAY
100.

```

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2213
2214
2215
2216 014174 000004
2217 014176 005737 001200
2218 014202 001446
2219 014204 032777 001000 164726
2220 014212 001042
2221 014214 004737 017306
2222 014220 004737 016234
2223 014224 004537 017062
2224 014230 000000
2225 014232 016466
2226 014234 005077 165222
2227 014240 013700 001464
2228 014244 062700 000003
2229 014250 013701 001542
2230 014254 105777 165200
2231 014260 100375
2232 014262 112710 000001
2233 014266 005301
2234 014270 001371
2235 014272 012777 002400 165160
2236 014300 004737 017554
2237 014304 004537 016666
2238 014310 000144
2239 014312 042777 010000 165126

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*****
*TEST 71 DYNAMIC BYTE TESTING OF 'PIXEL3'
*****
TST71: SCOPE
TST $PASS ;TEST IF FIRST PASS OF PROGRAM
BEQ TST72 ;;BR IF FIRST PASS
BIT #SW09,@SWR ;TEST IF INHIBIT VISUAL MAP PATTERNS
BNE TST72 ;;BR IF INHIBIT IS SET
JSR PC,TESTID ;DISPLAY MAP * AND TEST *
JSR PC,CLRMAP
JSR R5,LODSTA ;LOAD COLOR TABLE
0 ;LOC 0 AND 2 THRU 17 WITH 17
TABLE2 ;LOC 1 WITH A 4
CLR @VGPC ;LOAD MAP P.C. TO 0
MOV VGBUF,R0 ;LOAD PIXEL ADDRESS
ADD #3,R0 ;MAKE PIXEL 3 BYTE ADDRESS
MOV NUMPIX,R1 ;LOAD # OF PIXELS
TSTB @VGCSR ;WAIT FOR MAP READY
BPL IS
MOVB #1,(R0) ;LOAD BIT0 OF PIXEL 3
DEC R1 ;FINISHED ALL PIXEL 3'S
BNE IS ;BR IF NOT
MOV #BIT10!BIT8,@VGCSR ;ENABLE THE MAP
JSR PC,LDLIN ;LOAD REFERENCE LINE
JSR R5,DELAY
100.
BIC #BIT12,@VGCSR ;DISABLE REF. LINE

```

2240
2241
2242
2243
2244
2245
2246
2247
2248
2249
2250
2251
2252
2253
2254
2255
2256
2257
2258
2259
2260
2261
2262
2263
2264
2265
2266
2267
2268
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014320 000004
014322 005737 001200
014326 001427
014330 032777 001000 164602
014336 001023
014340 004737 017306
014344 004737 016234
014350 012777 000017 165110
014356 012777 002400 165074
014364 004537 016666
014370 000144
014372 052777 004000 165060
014400 004537 016666
014404 000144

014406 000004
014410 005737 001200
014414 001476
014416 032777 001000 164514
014424 001072
014426 004737 017306
014432 005000
014434 012701 073567
014440 004737 015770
014444 005200 15:
014446 022700 000040
014452 001403
014454 004737 016002
014460 000771
014462 012700 007740 25:
014466 004737 015770
014472 005200 35:
014474 022700 010000
014500 001403
014502 004737 016002
014506 000771
014510 012700 000040 45:
014514 012701 000007 55:
014520 004737 015770
014524 012701 070000
014530 062700 000037
014534 004737 015770
014540 005200
014542 022700 007740
014546 001362
014550 005077 164712
014554 012777 003417 164704

014562 012777 002400 164670
014570 004537 016666
014574 000144

```

:*****
:*TEST 72      TEST THE 'EXPAND' FUNCTION
:*****
TST72:  SCOPE
        TST      $PASS      ;TEST IF FIRST PASS OF PROGRAM
        BEQ      TST73      ;;BR IF FIRST PASS
        BIT      #SW09,2SWR  ;TEST IF INHIBIT VISUAL MAP PATTERNS
        BNE      TST73      ;;BR IF INHIBIT IS SET
        JSR      PC,TESTID   ;DISPLAY MAP * AND TEST *
        JSR      PC,CLRMAP
        MOV      #17,2VGCOLOR ;LOAD MAX INTENSITY INTO LOCATION C
        MOV      #BIT10:BIT8,2VGCSCR ;SET "MONO" AND ENABLE BIT MAP
        JSR      R5,DELAY
        .
        BIS      #BIT11,2VGCSCR ;SET THE 'EXPAND' BIT
        JSR      R5,DELAY
        .
:*****
:*TEST 73      DRAW A BOX UP ON VSVO1 SCREEN
:*****
TST73:  SCOPE
        TST      $PASS      ;TEST IF FIRST PASS OF PROGRAM
        BEQ      TST74      ;;BR IF FIRST PASS
        BIT      #SW09,2SWR  ;TEST IF INHIBIT VISUAL MAP PATTERNS
        BNE      TST74      ;;BR IF INHIBIT IS SET
        JSR      PC,TESTID   ;DISPLAY MAP * AND TEST *
        CLR      R0          ;SET UP BIT MAP ADRS - TOP LINE
        MOV      #73567,R1   ;SET UP PIXEL DATA IN R1 - INT 7
        JSR      PC,DISPY    ;GO LOAD BIT MAP
        INC      R0          ;ADVANCE BIT MAP ADRS
        CMP      #40,R0      ;TOP LINE DONE?
        BEQ      2$         ;BR IF SO
        JSR      PC,DCONT    ;LOAD NEXT PIXEL
        BR       1$         ;NEXT MAP LOAD
        MOV      #7740,R0    ;SET UP BIT MAP ADRS - BOTTOM LINE
        JSR      PC,DISPY    ;GO LOAD BIT MAP
        INC      R0          ;ADVANCE ADRS
        CMP      #10000,R0   ;BOTTOM LINE DONE?
        BEQ      4$         ;BR IF SO
        JSR      PC,DCONT    ;LOAD NEXT PIXEL WORD
        BR       3$         ;NEXT MAP LOAD
        MOV      #40,R0     ;SET UP BIT MAP ADRS SIDE LINES
        MOV      #7,R1      ;SET UP PIXEL 0 DATA IN R1 - INT 7
        JSR      PC,DISPY    ;GO LOAD BIT MAP
        MOV      #70000,R1   ;SET UP PIXEL 3 DATA IN R1 - INT 7
        ADD      #37,R0     ;OFFSET TO RIGHT SIDE LINE
        JSR      PC,DISPY    ;GO LOAD BIT MAP
        INC      R0          ;GO TO NEXT ROW ON LEFT
        CMP      #7740,R0   ;TO BOTTOM YET?
        BNE      5$         ;BR IF NOT
        CLR      2VGCOLOR   ;LOAD L.U.T. 0
        MOV      #3417,2VGCOLOR ;LOAD L.U.T. 7
        .
;NOW DISPLAY NORMAL SIZE BOX
        MOV      #BIT10:BIT8,2VGCSCR ;ENABLE MAP
        JSR      R5,DELAY    ;DELAY FOR OPERATOR
        .

```


M04

MAINDEC-11-DZVSE-A
DZVSEA.P11 02-JUN-76

VSVC: VIEDO GRAPHIC TEST PROGRAM
00:00 T73

MACY11 27(1006)
DRAW A BOX UP ON VSD01 SCREEN

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```

2296 :NOW DISPLAY EXPANDED SIZE BOX
2297 014576 052777 004000 164654 BIS #BIT11,@VGCSR ;EXPAND MAP
2298 014604 004537 016666 JSR RS,DELAY ;DELAY FOR OPERATOR
2299 014610 000144 100.
2300 ;*****
2301 ;*TEST 74 TEST THE 'ORIGIN' FUNCTION
2302 ;*****
2303 †ST74: SCOPE
2304 014612 000004 TST $PASS ;TEST IF FIRST PASS OF PROGRAM
2305 014614 005737 001200 BEQ TST75 ;;BR IF FIRST PASS
2306 014620 001434 BIT #SW09,@SWR ;TEST IF INHIBIT VISUAL MAP PATTERNS
2307 014622 032777 001000 164310 BNE TST75 ;;BR IF INHIBIT IS SET
2308 014630 001030 JSR PC,TESTID ;DISPLAY MAP # AND TEST #
2309 014632 004737 017306 JSR PC,CLRMAP ;CLEAR THE MAP
2310 014636 004737 016234 JSR PC,CLRMAP
2311 014642 012777 000017 164616 MOV #17,@VGCOLOR ;LOAD MAX INTENSITY INTO TABLE LOC. C
2312 014644 012777 002400 164602 MOV #BIT10!BIT8,@VGCSR ;ENABLE THE BIT MAP
2313 014646 005037 001474 CLR $TEMP
2314 014648 113777 001474 164570 1$: MOVB $TEMP,@VGCSR ;LOAD ORIGIN REGISTER
2315 014650 004537 016666 JSR RS,DELAY
2316 014652 000144 100.
2317 014654 105237 001474 INCB $TEMP ;UPDATE THE ORIGIN POSITION
2318 014702 122737 000020 001474 CMPB #20,$TEMP ;TEST FOR A NON-VALID ORIGIN
2319 014710 001364 BNE 1$ ;BR IF A VALID ORIGIN
2320 ;*****
2321 ;*TEST 75 INTENSITY LEVEL TEST-MONOCROME
2322 ;*****
2323 †ST75: SCOPE
2324 014712 000004 TST $PASS ;TEST IF FIRST PASS OF PROGRAM
2325 014714 005737 001200 BEQ TST76 ;;BR IF FIRST PASS
2326 014720 001435 BIT #SW09,@SWR ;TEST IF INHIBIT VISUAL MAP PATTERNS
2327 014722 032777 001000 164210 BNE TST76 ;;BR IF INHIBIT IS SET
2328 014730 001031 JSR PC,TESTID ;DISPLAY MAP # AND TEST #
2329 014732 004737 017306 JSR PC,CLRMAP ;CLEAR THE BIT MAP
2330 014734 004737 016774 JSR PC,LODSEQ ;LOAD TWO WORDS PER LOOK TABLE LOCATION
2331 014736 005037 014764 CLR 2$ ;CLEAR STARTING INTENSITY LEVEL
2332 014738 012777 006400 164500 MOV #BIT11!BIT10!BIT8,@VGCSR ;SET MONOCROME + ENABLE MAP
2333 014740 004537 017062 1$: JSR RS,LODSTA ;LOAD SEQUENCE
2334 014742 000000 2$: 0 ;INDEX INTO DATA VALVE TABLE
2335 014744 016446 TABLE1
2336 014746 004537 016666 JSR RS,DELAY
2337 014748 000144 100.
2338 014750 062737 000001 014764 ADD #1,2$ ;UPDATE INDEX INTO TABLE FOR LOCATION 0'S VALUE
2339 015004 022737 000020 014764 CMP #20,2$ ;TEST IF LAST VLUE FOR MONOCROME
2340 015012 001362 BNE 1$ ;BR IF MORE LEVELS FOR LOCATION 0

```

N04

MAINDEC-11-DZVSE-A
DZVSEA.P11

02-JUN-76 00:00

VSV01 VIEDO GRAPHIC TEST PROGRAM
T76

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TEST THE GREEN BITS

```

2341      ;:*****
2342      ;*TEST 76      TEST THE GREEN BITS
2343      ;:*****
2344      TST76:  SCOPE
2345      TST      $PASS      ;TEST IF FIRST PASS OF PROGRAM
2346      BEQ      TST77      ;;BR IF FIRST PASS
2347      BIT      #SW09,@SWR  ;TEST IF INHIBIT VISUAL MAP PATTERNS
2348      BNE      TST77      ;;BR IF INHIBIT IS SET
2349      JSR      PC,TESTID   ;DISPLAY MAP # AND TEST #
2350      JSR      PC,CLRMAP   ;CLEAR THE MAP
2351      MOV      #BIT11:BIT8,@VGCSPR ;ENABLE MAP AND EXPAND
2352      MOV      #4,@VGCCLR  ;LOAD TABLE POINTER0 - LOW GREEN LEVEL
2353      JSR      R5,DELAY
2354      100.
2355      MOV      #10,@VGCCLR ;SET THE MEDIUM GREEN LEVEL
2356      JSR      R5,DELAY
2357      100.
2358      MOV      #14,@VGCCLR ;SET THE BRIGHTEST BREEN LEVEL
2359      JSR      R5,DELAY
2360      100.
2361
2362      ;:*****
2363      ;*TEST 77      TEST THE RED BITS
2364      ;:*****
2365      TST77:  SCOPE
2366      TST      $PASS      ;TEST IF FIRST PASS OF PROGRAM
2367      BEQ      TST100     ;;BR IF FIRST PASS
2368      BIT      #SW09,@SWR  ;TEST IF INHIBIT VISUAL MAP PATTERNS
2369      BNE      TST100     ;;BR IF INHIBIT IS SET
2370      JSR      PC,TESTID   ;DISPLAY MAP # AND TEST #
2371      JSR      PC,CLRMAP   ;CLEAR THE MAP
2372      MOV      #BIT11:BIT8,@VGCSPR ;ENABLE MAP AND EXPAND
2373      MOV      #1,@VGCCLR  ;SET THE DIMMEST RED LEVEL
2374      JSR      R5,DELAY
2375      100.
2376      MOV      #2,@VGCCLR  ;SET THE MEDIUM RED LEVEL
2377      JSR      R5,DELAY
2378      100.
2379      MOV      #3,@VGCCLR  ;SET THE BRIGHTEST RED LEVEL
2380      JSR      R5,DELAY
2381      100.
2382

```

B05

00000000 015225 000004 001200
00000000 015222 000537 001000 163702
00000000 015226 000143 001000 163702
00000000 015230 002777 001000 163702
00000000 015236 001031 017306
00000000 015240 004737 016234
00000000 015247 004737 004490 164202
00000000 015250 012777 000020 164202
00000000 015256 004537 016666
00000000 015264 000144
00000000 015270 000040 164166
00000000 015276 004537 016666
00000000 015300 000144
00000000 015306 000060 164152
00000000 015314 004537 016666
00000000 015322 000144

```
*****  
*TEST 100 TEST THE BLUE COLOR BITS AT TABLE ADDRESS 0  
*****  
*TEST100: SCOPE  
TST $PASS :TEST IF FIRST PASS OF PROGRAM  
BEG TST101 ::BR IF FIRST PASS  
BIT @SW09,@SWR :TEST IF INHIBIT VISUAL MAP PATTERNS  
BNE TST101 ::BR IF INHIBIT IS SET  
ISR PC,TESTID :DISPLAY MAP * AND TEST *  
ISR PC,CLMAP  
MOV @BIT11:BITS,@VGCSSR :ENABLE MAP AND EXPAND  
MOV @20,@VGCCLR :SET THE DIMMEST BLUE LEVEL  
ISR RS,DELAY  
JCC.  
MOV @40,@VGCCLR :SET THE MEDIUM BLUE LEVEL  
ISR RS,DELAY  
JCC.  
MOV @60,@VGCCLR :SET THE BRIGHTEST BLUE LEVEL  
ISR RS,DELAY  
JCC.  
JCC.
```

C05

015322 000004
015324 005737
015326 001441
015328 032777
015330 001000 163600
015332 001000
015334 001000
015336 004737
015338 004737
015340 004737
015342 004737
015344 004737
015346 004737
015348 004737
015350 005037
015352 005037
015354 005077
015356 004537
015358 000000
015360 016546
015362 012777
015364 004400 164054
015366 004537
015368 000144
015370 000144
015372 062737
015374 000020
015376 001357
015378 005077
015380 164024

```

*****
*TEST 101 COLOR LEVEL TEST - COLORCROME
*****
↑ST101: SCOPE
TST $PASS ;TEST IF FIRST PASS OF PROGRAM
BEQ TST102 ;;BR IF FIRST PASS
BIT @SW09,@SWR ;TEST IF INHIBIT VISUAL MAP PATTERNS
BNE TST102 ;;BR IF INHIBIT IS SET
JSR PC,TESTID ;DISPLAY MAP * AND TEST *
JSR PC,CLMAP ;ENSURE CLEAR MAP
JSR PC,LODSEQ ;LOAD TWO WORDS PER LOOKUP TABLE LOC.
CLR 25 ;CLEAR INDEX POINTER
CLR @VGCSPR ;CLEAR STATUS
JSR RS,LODSTA ;LOAD DATA INTO TABLE
;S:
;S:
TABLES
MOV @BIT11:BIT8,@VGCSPR ;ENABLE MAP
JSR RS,DELAY
100.
ADD #1,25 ;UPDATE LOC 0 INDEX INTO TABLE
CMP #20,25 ;TEST IF FINISHED FOR LOC 0
BNE 15
CLR @VGCSPR ;DISABLE MAP

```

```

*****
*TEST 102 LOGICAL END OF BIT MAP TEST
*****
↑ST102: SCOPE
JMP MAPRET ;RETURN AND TEST IF MORE MAPS

.SBTTL
.SBTTL OPERATOR INTERVENTION AND INSPECTION TESTS
.SBTTL

```

.SBTTL CROSS MATCH TEST PATTERN

```

015442 012706 001100 MATCH: MOV #STACK,SP ;LOAD SP
015446 004737 003576 JSR PC,LOADR ;LOAD BUS ADDRESSES FOR MAP #1
015450 004737 015510 JSR PC,HATCHO ;LOAD MAP #0
015456 012777 004400 163774 MOV #BIT11!BITB,@VGC5R ;ENABLE MAP EXPAND AND ORGIN =0
015464 004737 003730 JSR PC,MOVADR ;ADJUST BUS ADDRESSES TO MAP #1
015470 004737 015510 JSR PC,HATCHO ;LOAD MAP #1
015474 012777 004402 163756 MOV #BIT11!BITB!BIT!,@VGC5R ;ENABLE MAP, EXPAND AND ORIGIN =2
015502 000000 HALT
015504 000137 001572 JMP BEGIN

:ACTUAL LOADING OF CROSS MATCH INTO BIT MAP SUB-ROUTINE
015510 004737 016234 HATCHO: JSR PC,CLRMAP ;ENSURE CLEAR MAP
015514 012737 000022 015636 MOV #22,105 ;LOAD VERT. COUNTER
015522 005077 163734 CLR @VGPC ;CLEAR MAP P.C.
015526 012701 000040 18: MOV #32,R1 ;LOAD WIDTH COUNTER
015532 105777 163722 23: TSTB @VGC5R ;TEST IF MAP READY SET
015536 100375 BPL 25 ;BR IF NOT READY
015540 012777 177777 163716 MOV #-1,@VGBUF ;LOAD #17 INTO ALL PIXELS
015546 005301 DEC R1
015550 001370 BNE 25 ;BR UNTIL FINISHED
015552 012737 000140 015640 MOV #140,115 ;LOAD VERT COUNT
015560 105777 163674 38: TSTB @VGC5R ;TEST IF MAP READY IS SET
015564 100375 BPL 38 ;BR IF NOT
015566 012777 000017 163670 48: MOV #17,@VGBUF ;LOAD VERT LINE PIXEL
015574 105777 163660 TSTB @VGC5R ;TEST IF READY
015600 100375 BPL 48 ;WAIT IF NOT
015602 005077 163656 CLR @VGBUF ;CLEAR PIXELS

015606 005337 015640 DEC 115
015612 001362 BNE 38 ;BR UNTIL FUNISHED SEVERAL ROWS OF PIXELS

015614 005337 015636 DEC 105 ;TEST IF FINISHED THE SCREEN
015620 001342 BNE 15 ;BR IF NOT

015622 005077 163640 CLR @VGCCLR ;CLEAR L.U.T. 0
015626 012777 007477 163632 MOV #7477,@VGCCLR ;LOAD "WHITE" INTO L.U.T. #17
015634 000207 RTS PC ;EXIT

015636 000000 108: 0
015640 000000 118: 0

.SBTTL SWR CONTROL OF X AND Y CROSS HAIRS LOOP
015642 012706 001100 CROSS: MOV #STACK,SP ;LOAD STACK
015646 004737 003576 JSR PC,LOADR ;LOAD DEVICE ADDRESS
015652 012777 014000 163566 MOV #BIT12!BIT11,@VCC5R ;ENABLE CROSS HAIRS
015660 104407 18: CKSWR ;TEST FOR CTRL G
015662 017777 163252 163560 MOV @SWR,@VCHRLO ;LOAD THE CROSS HAIR POSITION REGISTERS
015670 000773 BR 18 ;LOOP BACK

```

E05

MAINDEC-11-DVSE-A
DVSE.A.P11 02-JUN-76 00:00

VSN2: VIDEO GRAPHIC TEST PROGRAM

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015672
015676
015702
015704
015710
015712
015714
015716
015720
015722
015724
015726
015730
015734
015736
015740
015742
015744
015746
015750
015752
015756
015760
015764
015766
015770
015776
016002
016010
016014
016016
016022
016030

005237 015766
012702 040000
005001
013700 001446
030210
001411
005201
001302
104005
000207
030210
001372
005037 015766
000207
005201
001002
104005
000207
030210
001772
005737 015766
001347
005037 015766
000207
000000
042777 000400 163462
010077 163460
042777 000400 163450
105777 163444
100375
010177 163442
052777 000400 163430
000207

```
.SBTTL  
.SBTTL MISC SUBROUTINES  
.SBTTL  
.SBTTL SUBROUTINE TO TEST THAT 'VERT SYNC' BIT CHANGES, REPORT ERROR IF  
:BIT DOES NOT CHANGE STATES (0 TO 1 OR 1 TO 0)  
COUNTA: INC CNTSAV ;SET EXIT FLAG  
COUNT: MOV #BIT14,R2 ;LOAD BIT 14  
CLR R1 ;CLEAR COUNT LOC.  
MOV VCCSR,R0 ;LOAD BUS ADDRESS  
BIT R2,(R0) ;TEST IF BIT IS SET  
BEQ 3$ ;BR IF SET  
:COME HERE IF BIT 14 WAS INITIALLY SET - NOW WAIT FOR IT TO CLEAR  
1$: INC R1 ;COUNT TIME  
BNE 2$ ;BR IF NOT OVERFLOW  
ERROR 5 ;VERTICAL SYNC FLAG ERROR  
RTS PC ;EXIT  
2$: BIT R2,(R0) ;TEST IF BIT 14 IS CLEARED  
BNE 1$ ;BR IF STILL SET  
CLR CNTSAV ;CLEAR EXIT FLAG  
RTS PC ;EXIT  
:COME HERE IF BIT 14 WAS INITIALLY CLEARED - NOW WAIT FOR IT TO SET  
3$: INC R1 ;COUNT TIME  
BNE 4$ ;BR IF NOT OVERFLOW  
ERROR 5 ;VERTICAL SYNC FLAG ERROR  
RTS PC ;EXIT  
4$: BIT R2,(R0) ;TEST IF BIT 14 IS SET  
BEQ 3$ ;BR IF CLEARED  
TST CNTSAV ;TEST IF EXIT FLAG IS SET  
BNE COUNT ;BR IF SET  
CLR CNTSAV ;CLEAR AGAIN  
RTS PC ;EXIT  
CNTSAV: 0  
DISPY: BIC #400,AVGCSR ;DISABLE MAP  
MOV R0,AVGPC ;LOAD MAP P.C.  
DCONT: BIC #400,AVGCSR ;DISABLE MAP  
1$: TSTB AVGCSR ;TEST IF MAP IS READY  
BPL 1$  
MOV R1,AVGBUF ;LOAD A PIXEL  
BIS #400,AVGCSR ;ENABLE MAP  
RTS PC ;EXIT
```

F05

INDEX-11-DVSE-A
 DVSEA.P11 02-JUN-76 00:00

VSV0: VIDEO GRAPHIC TEST PROGRAM
 DISPLAY BUFFER ON VTVD1 SCREEN SUB-ROUTINE

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```

.SBTTL DISPLAY BUFFER ON VTVD1 SCREEN SUB-ROUTINE
2531
2532
2533 016032 012700 024330 XPRNT: MOV #BUFFER, R0 ;LOAD BUFFER POINTER
2534 016036 000240 XPRNTA: NOP
2535 016040 000240 NOP
2536 016042 000240 NOP
2537 016044 005777 163376 1S: TST @VCCSR ;TEST IF READY IS SET
2538 016050 100375 BPL 1S ;WAIT TILL DONE
2539 016052 112077 163370 MOVB (R0)+, @VCCSR ;LOAD THE CHARACTER
2540 016056 122710 000377 CMPS #377, (R0) ;TEST FOR TERM
2541 016062 001370 BNE 1S ;BR IF NOT TERM.
2542 016064 000240 NOP
2543 016066 000240 NOP
2544 016070 000240 NOP
2545 016072 000240 NOP
2546 016074 000207 RTS PC ;EXIT
2547
2548 016076 013701 001500 FILLWC: MOV KE, R1 ;LOAD BASIC CHARACTER
2549 016102 004737 016132 JSR PC, FILBUF ;LOAD BUFFER WITH THE CHARACTER
2550 016106 013737 001524 016130 MOV VHD, 10$ ;LOAD VERTICAL COUNTER
2551 016114 004737 016032 1S: JSR PC, XPRNT ;DISPLAY A LINE
2552 016120 005337 016130 DEC 10$ ;FINISHED ALL VERT LINES ?
2553 016124 100375 BPL 1S ;BR IF NOT
2554 016126 000207 RTS PC ;EXIT
2555 016130 000200 10$: 0
2556
2557 016132 013702 001522 FILBUF: MOV WIDTH, R2 ;LOAD WIDTH VALUE
2558 016136 012700 024330 MOV #BUFFER, R0 ;SET-UP BUFFER POINTER
2559 016142 110120 1S: MOVB R1, (R0)+ ;SAVE THE CHARACTER IN THE BUFFER
2560 016144 005302 DEC R2 ;FINISHED?
2561 016146 001375 BNE 1S ;BRANCH IF NOT COMPLETED
2562 016150 112720 000015 MOVB #15, (R0)+ ;LOAD "CR"
2563 016154 112720 000012 MOVB #12, (R0)+ ;LOAD "LF"
2564 016160 112710 000377 MOVB #377, (R0) ;LOAD TERM.
2565 016164 000207 RTS PC ;EXIT
2566
2567 ;LOAD A INCREMENTING CHARACTER ACROSS THE SCREEN WIDTH
2568 ;ONLY 40 THRU 177 ARE LEGAL CHARACTERS
2569
2570 016166 012700 024330 LIC: MOV #BUFFER, R0 ;SET-UP BUFFER POINTER
2571 016172 013502 MOV @5, R2 ;SET-UP WIDTH
2572 016174 110120 1S: MOVB R1, (R0)+ ;SAVE A CHARACTER IN THE BUFFER
2573 016176 005201 INC R1 ;UPDATE THE CHARACTER
2574 016200 023701 001532 CMP LASTCH, R1 ;TEST FOR
2575 016204 001002 BNE 2$ ;BRANCH IF NOT
2576 016206 012701 000040 MOV #40, R1 ;MAKE A LEGAL CHARACTER
2577 016212 005302 2$: DEC R2 ;DECREMENT COUNT
2578 016214 001367 BNE 1S ;BRANCH IF NOT COMPLETED
2579 016216 112720 000015 MOVB #15, (R0)+ ;LOAD "CR"
2580 016222 112720 000012 MOVB #12, (R0)+ ;LOAD "LF"
2581 016226 112710 000377 MOVB #377, (R0) ;LOAD TERM
2582 016232 000205 RTS R5 ;EXIT
  
```


G05

```
2584 ;SUBROUTINE TO CLEAR THE BIT MAP
2585
2586 016234 012737 000100 016316 CLRMAP: MOV #BIT6,10$ ;LOAD BASE DELAY
2587 016242 005037 015320 CLR 11$ ;CLEAR COUNTER
2588 016246 004537 016666 JSR RS,DELAY
2589 016252 100001 BIT15!1
2590 016254 000240 NOP
2591 016256 000240 NOP
2592 016260 000240 NOP
2593 016262 012777 001000 163170 MOV #BIT9,AVGCSR ;SET 'CLEAR MAP' BIT
2594 016270 105777 163164 1$: TSTB AVGCSR ;TEST READY FLAG
2595 016274 100407 BMI 2$ ;BR IF READY SET
2596 016276 005237 016320 INC 11$ ;UPDATE COUNTER
2597 016302 001372 BNE 1$ ;BR IF NO OVERFLOW
2598 016304 005337 016316 DEC 10$ ;DECREMENT BASE DELAY
2599 016310 001367 BNE 1$ ;BR IF NO OVERFLOW
2600 016312 104015 ERROR 1$ ;MAP READY FAILED TO SET AFTER A GROSS
2601 ;TIME DELAY
2602 016314 000207 2$: RTS PC ;EXIT
2603
2604 016316 000000 10$: 0 ;BASE DELAY
2605 016320 000000 11$: 0 ;COUNT LOCATION
2606
2607 .SBTTL CROSS HAIRS WITH 2 BIT MAPS ENABLED
2608
2609 016322 012706 001100 FRANKD: MOV #STACK,SP
2610 016326 004737 003576 JSR PC,LODADR ;LOAD DEVICE ADDRESS
2611 016332 004737 016234 JSR PC,CLRMAP
2612 016336 012777 000014 163122 MOV #14,AVGCLR ;LOAD LUT TO GREEN
2613 016344 012777 000400 163106 MOV #BIT8,AVGCSR ;ENABLE THE MAP
2614 016352 004737 003730 JSR PC,MOVADR ;UPDATE TO NEXT MAP ADDRESS
2615 016356 004737 016234 JSR PC,CLRMAP
2616 016362 012777 000014 163076 MOV #14,AVGCLR ;LOAD LUT TO GREEN
2617 016370 012777 004404 163062 MOV #BIT11!BIT8!4,AVGCSR ;ENABME MAP - EXPAND - ORIGIN 4
2618 016376 012777 014000 163042 MOV #BIT12!BIT11,AVGCSR ;ENABLE CROSS HAIRS
2619 016404 104407 1$: CKSWR ;TEST FOR CTRL G
2620 016406 017777 162526 163034 MOV #SWR,AVCHRLO ;LOAD CROSS HAIR ORIGIN
2621 016414 000773 BR 1$
2622
2623 .SBTTL BUR TIME-OUT LOOP
2624
2625 016416 012706 001100 BUSOUT: MOV #STACK,SP ;LOAD SP
2626 016422 012737 016440 000004 MOV #2$,ERRVEC ;LOAD BUS TRAP RETURN
2627 016430 005777 162612 1$: TST #BASE ;REFERENCE THE ADDRESS
2628 016434 000775 BR 1$
2629 016436 000240 NOP
2630 016440 022626 2$: CMP (SP)+,(SP)+ ;CLEAN THE STACK
2631 016442 000772 BR 1$
2632 016444 000240 NOP
```

SYNOPSIS - DEVICE-A VSW: WIEDC GRAPHIC TEST PROGRAM
SYNOPSIS.P11 02-JUN-76 00:00 BLR TIME-OUT LOOP

2633	016446	000	001	002	TABLE1: .BYTE	0,1,2,3,4,5,6,7,10,11,12,13,14,15,16,17	
2634	016451	000	004	005			
2635	016454	006	007	010			
2636	016457	011	012	013			
2637	016462	014	015	016			
2638	016465	017					
2639	016466	000	017	000	TABLE2: .BYTE	0,17,0,0,0,0,0,0,0,0,0,0,0,0,0,0	:L.U.(1) = 17 OTHERS = 0
2640	016471	000	000	000			
2641	016474	000	000	000			
2642	016477	000	000	000			
2643	016502	000	000	000			
2644	016505	000					
2645	016506	014	063	063	TABLE3: .BYTE	14,63,63,63,63,63,63,63,63,63,63,63,63,63,63	
2646	016511	063	063	063			
2647	016514	063	063	063			
2648	016517	063	063	063			
2649	016522	063	063	063			
2650	016525	063					
2651	016526	004	017	004	TABLE4: .BYTE	4,17,4,4,4,4,4,4,4,4,4,4,4,4	
2652	016531	004	004	004			
2653	016534	004	004	004			
2654	016537	004	004	004			
2655	016542	004	004	004			
2656	016545	004					
2657	016546	001	002	003	TABLE5: .BYTE	1,2,3,4,10,14,20,40,60,63,74,17,33,25,52,77	
2658	016551	004	010	014			
2659	016554	020	040	060			
2660	016557	063	074	017			
2661	016562	033	025	052			
2662	016565	077					
2663	016566	001	002	003	.BYTE	1,2,3,4,10,14,20,40,60,63,74,17,33,25,52,77	
2664	016571	004	010	014			
2665	016574	020	040	060			
2666	016577	063	074	017			
2667	016602	033	025	052			
2668	016605	077					
2669	016606	000	000	017	TABLE6: .BYTE	0,0,17,0,0,0,0,0,0,0,0,0,0,0,0	:L.U.T. 2 = 17 OTHERS = 0
2670	016611	000	000	000			
2671	016614	000	000	000			
2672	016617	000	000	000			
2673	016622	000	000	000			
2674	016625	000					
2675	016626	000	000	000	TABLE7: .BYTE	0,0,0,0,17,0,0,0,0,0,0,0,0,0,0	:L.U.T. 4 = 17 OTHERS = 0
2676	016631	000	017	000			
2677	016634	000	000	000			
2678	016637	000	000	000			
2679	016642	000	000	000			
2680	016645	000					
2681	016646	000	000	000	TABLE8: .BYTE	0,0,0,0,0,0,0,0,17,0,0,0,0,0,0	:L.U.T. 10 = 17 OTHERS = 0
2682	016651	000	000	000			
2683	016654	000	000	017			
2684	016657	000	000	000			
2685	016662	000	000	000			
2686	016665	000					
2687							

.EVEN

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2691 016666 012777 016772 162574 DELAY: MOV #55,AVCINT ;LOAD INTR. RETURN ADDRESS
2692 016674 012777 000200 162570 MOV #APRIOR,AVCINT! ;LOAD RETURN PRIORITY
2693 016702 012537 016770 MOV (R5)+,10$ ;GET THE ARG.
2694 016706 100003 BPL 2$ ;BR IF POSITIVE
2695
2696 016710 005737 016770 1$: TST 10$ ;TEST IF DELAY CANNOT BE INHIBITED
2697 016714 100404 BMI 3$ ;BR IF IT CANT
2698 016716 032777 010000 162214 2$: BIT #BIT12,ASWR ;TEST IF INHIBIT DELAY SWITCH IS SET
2699 016724 001013 BNE 4$ ;BR IF INHIBIT IS SET
2700 016726 052777 020000 162512 3$: BIS #BIT13,AVCCSR ;ENABLE SYNC INTR.
2701 016734 000001 WAIT ;WAIT FOR INTERRUPT TO HAPPEN
2702 016736 042777 020000 162502 BIC #BIT13,AVCCSR ;DISABLE INTR.
2703 016744 104407 CKSWR ;TEST FOR CTRL G
2704 016746 105337 016770 DECB 10$ ;FINISHED DELAY ^
2705 016752 001356 BNE 1$ ;BR IF NOT
2706 016754 013777 001472 162506 4$: MOV VCINT1,AVCINT ;RESET INTR. VECTOR
2707 016762 005077 162504 CLR AVCINT1 ;
2708 016766 000205 RTS RS ;EXIT
2709
2710 016770 000000 10$: 0
2711 016772 000002 5$: RTI ;CONTINUE
2712
2713
2714 ;LOAD MAP WITH TWO WORDS OF THE SAME VALUE
2715 ; INTO THE ENTIRE BITMAP STARTING AT LOC. 0
2716
2717 016774 010046 LODSEQ: MOV R0,-(SP) ;SAVE R0
2718 016776 010146 MOV R1,-(SP) ;SAVE R1
2719 017000 005077 162456 CLR AVGPC ;ENSURE CLEAR PC
2720 017004 013701 001542 MOV NUMPIX,R1 ;LOAD COUNTER
2721 017010 006201 ASR R1 ;ADJUST COUNT
2722
2723 017012 005000 1$: CLR R0 ;START WITH PIXEL VALUE OF 0
2724 017014 105777 162440 2$: TSTB AVGCSR ;ENSURE MAP IS READY
2725 017020 100375 BPL 2$
2726 017022 010077 162436 MOV R0,AVGBUF ;LOAD A PIXEL WORD
2727 017026 105777 162426 3$: TSTB AVGCSR ;ENSURE MAP IS READY
2728 017032 100375 BPL 3$
2729 017034 010077 162424 MOV R0,AVGBUF ;LOAD 2ND PIXEL WORD
2730 017040 005301 DEC R1 ;FINISHED ALL PIXELS ^
2731 017042 100404 BMI 4$ ;BR IF FINISHED
2732 017044 062700 010421 ADD #10421,R0 ;UPDATE PIXEL DATA TO NEXT L.U.T. ADDRESS
2733 017050 103361 BCC 2$ ;BR IF MORE ADDRESSES TO LOAD
2734 017052 000757 BR 1$ ;BR TO RESET PIXEL DATA
2735
2736 017054 012601 4$: MOV (SP)+,R1 ;RESTORE R1
2737 017056 012600 MOV (SP)+,R0 ;RESTORE R0
2738 017060 000207 RTS PC ;EXIT
2739
  
```

J05

MAINDEC-11-DZYSE-A
DZYSEA.P11

02-JUN-76 00:00

VSV01 VIEDC GRAPHIC TEST PROGRAM

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SUBROUTINE TO LOAD THE L.U.T. WITH THE VALUE OF A TABLE

```

2740 .SPTTL SUBROUTINE TO LOAD THE L.U.T. WITH THE VALUE OF A TABLE
2741
2742 017062 010046 LODSTA: MOV RO, -(SP) ;SAVE RO
2743 017064 012537 017136 MOV (R5)+, 10$ ;GET STARTING LUT ADDRESS
2744 017070 012500 MOV (R5)+, RO ;GET TABLE ADDRESS
2745 017072 012737 000020 017140 MOV #16, 11$ ;LOAD COUNTER
2746
2747 017100 113737 017136 017143 MOVB 10$, 12$+1 ;LOAD HIGH BYTE WITH LUT ADDRESS
2748 017106 112037 017142 1$: MOVB (RO)+, 12$ ;LOAD VALUE INTO LOW BYTE
2749 017112 013777 017142 162346 MOV 12$, SVGCOLA ;LOAD THE TABLE
2750 017120 105237 017143 INCB 12$+1 ;UPDATE THE LUT POINTER
2751 017124 005337 017140 DEC 11$ ;FINISHED ALL L.U.T. ADDRESSES
2752 017130 001366 BNE 1$ ;BR IF NOT
2753 017132 012600 MOV (SP)+, RO ;RESTORE RO
2754 017134 000205 RTS #5 ;EXIT
2755
2756 017136 000000 10$: 0
2757 017140 000020 11$: 16. ;COUNTER OF L.U.T.
2758 017142 000000 12$: 0 ;TEMP LOCATION
2759
2760 017144 000000 YADD: 0
2761 017146 000000 XADD: 0
2762 017150 000000 SET: 0
2763 017152 000000 OVRAL: 0
2764 017154 051526 030126 026461 MSGTXT: .ASCII \VSV01-DYNAMIC-CURSOR-ADDRESS-TEST-\
2765 017162 054504 040516 044515
2766 017170 026503 052503 051522
2767 017176 051117 040455 042104
2768 017204 042522 051523 052055
2769 017212 051505 026524
2770 017216 042055 043511 052111 .ASCII \-DIGITAL-EQUIPMENT-CORPORATION\
2771 017224 046101 042455 052521
2772 017232 050111 042515 052116
2773 017240 041455 051117 047520
2774 017246 040522 044524 047117
2775 017254 100000 MSGTND: BIT15
    
```

K05

MAINDEC-11-DZVSE-A
DZVSEA.P11

02-JUN-76

VSV0: VIEDO GRAPHIC TEST PROGRAM
00:00

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HOME THE DISPLAY SUBROUTINE

```

2776 .SBTTL HOME THE DISPLAY SUBROUTINE
2777 017256 004537 016666 HOME: JSR R5,DELAY ;WAIT FOR VERT SYNC
2778 017262 100001 BIT15:BIT0
2779 017264 012737 177435 024330 MOV #177435,BUFFER ;LOAD "HOME" AND TERMINATOR
2780 017272 004737 016032 JSR PC,XPRNT ;EXECUTE
2781 017276 005777 162144 1$: TST @VCCSR ;WAIT FOR CHAR READY
2782 017302 100375 BPL 1$
2783 017304 000207 RTS PC ;RETURN
2784
2785 .SBTTL DISPLAY BIT MAP # AND TEST #
2786 017306 032777 010000 161624 TESTID: BIT #BIT12,@SWR ;TEST IF INHIBIT DELAY IS SET
2787 017314 001071 BNE 3$ ;BR AND DONT DISPLAY THIS STUFF
2788 017316 112737 000060 017525 MOVB #0,MAPIND-1 ;LOAD 1 DIGIT VALUE
2789 017324 013737 001546 017502 MOV COAXSW,10$ ;SAVE COAX SWITCH VALUE
2790 017332 006237 017502 4$: ASR 10$ ;CONVERT TO A ASCII DIGIT
2791 017336 103403 BCS 5$
2792 017340 105237 017525 INCB MAPIND-1
2793 017344 000772 BR 4$
2794 017346 012737 000060 017502 5$: MOV #0,10$ ;LOAD MAP # INDICATOR
2795 017354 063737 001204 017502 ADD $UNIT,10$ ;ADD CURRENT MAP I.D.
2796 017352 113737 017502 017526 MOVB 10$,MAPIND ;SAVE IN A MESSAGE
2797 017370 013700 001102 1$: MOV $TSTNM,R0 ;LOAD TEST #
2798 017374 112737 000060 017550 MOVB #0,TSTIND ;LOAD 0
2799 017402 032700 000100 BIT #BIT6,R0 ;TEST IF TEST # > 100
2800 017406 001402 BEQ 2$
2801 017410 105237 017550 INCB TSTIND ;MAKE TEST # 100
2802 017414 010001 2$: MOV R0,R1
2803 017416 042701 177770 BIC #177770,R1 ;MASK LOWER BITS
2804 017422 062701 000060 ADD #60,R1 ;MAKE IT ASCII
2805 017426 110137 017552 MOVB R1,TSTIND+2 ;LOAD MESSAGE
2806 017432 006200 ASR R0
2807 017434 006200 ASR R0
2808 017436 006200 ASR R0
2809 017440 042700 177770 BIC #177770,R0 ;MASK LOWER BITS
2810 017444 062700 000060 ADD #60,R0 ;UPDATE TO ASCII
2811 017450 110037 017551 MOVB R0,TSTIND+1 ;UPDATE MESSAGE
2812 017454 112777 000014 161772 MOVB #14,@VCPOS ;LOAD X POSITION
2813 017462 112777 000030 161766 MOVB #24,@VCPOSH ;LOAD Y POSITION
2814 017470 012700 017504 MOV #BRIAN,R0 ;LOAD MESSAGE POINTER
2815 017474 004737 016036 JSR PC,XPRNTA ;DISPLAY MESSAGE
2816 017500 000207 3$: RTS PC ;EXIT
2817 017502 000000 10$: 0
2818 017504 042524 052123 047111 BRIAN: .ASCII /TESTING BIT MAP #/
2819 017512 020107 044502 020124
2820 017520 040515 020120 043
2821 017525 060 .BYTE 60 ;1 DIGIT OF MAP #
2822 017526 060 MAPIND: .BYTE 60 ;2 DIGIT OF MAP #
2823 017527 040 020040 052522 .ASCII / RUNNING TEST #/
2824 017534 047116 047111 020107
2825 017542 042524 052123 021440
2826 017550 060 060 TSTIND: .BYTE 60,60,60,377
2827 017553 377 .EVEN

```

```

2829
2830 017554 012777 000202 161666 LDLIN: MOV #202,@VCHRLO ;LOAD X CROSS HAIR LINE
2831 017562 052777 010000 161656 BIS #BIT12,@VCCSR ;ENABLE X CROSS HAIR
2832 017570 000207 RTS PC ;EXIT
2833
2834 017572 050000 BYTST: BIT14!BIT12
2835 017574 120000 BIT15!BIT13
2836 017576 050000 BIT14!BIT12
2837 017500 120000 BIT15!BIT13
2838 017602 000000 0
2839
2840 .SBTT_ RANDOM NUMBER GENERATOR ROUTINE
2841
2842 ;*****
2843 ;*THIS ROUTINE IS A DOUBLE PRECISION PSEUDO RANDOM NUMBER GENERATOR
2844 ;*WITH A RANGE OF 0 TO 2(+33)-1.
2845 ;*CALL:
2846 ;* JSR PC,$RAND ;:CALL THE ROUTINE
2847 ;* RETURN ;:RETURN HERE THE RANDOM
2848 ;* ;:NUMBER WILL BE IN
2849 ;* ;:$HINUM,$LONUM
2850
2851 $RAND:
2852 017604 MOV RO,-(SP) ;:PUSH RO ON STACK
2853 017606 MOV R1,-(SP) ;:PUSH R1 ON STACK
2854 017610 MOV R2,-(SP) ;:PUSH R2 ON STACK
2855 017612 013700 017704 MOV $LONUM,R0 ;:SET R0 WITH LOW
2856 017616 013701 017702 MOV $HINUM,R1 ;:SET R1 WITH HIGH
2857 017622 012702 177771 MOV #-7,R2 ;:SET SHIFT COUNT
2858 017626 006300 1$: ASL RO ;:SHIFT R0 LEFT AND
2859 017630 006101 ROL R1 ;:ROTATE CARRY INTO R1 AND
2860 017632 005202 INC R2 ;:CHECK FOR DONE
2861 017634 001374 BNE 1$ ;:CONTINUE SHIFT LOOP
2862 017636 063700 017704 ADD $LONUM,R0 ;:ADD NUMBER TO MAKE X 129
2863 017642 005501 ADC R1 ;:PROPOGATE CARRY
2864 017644 063701 017702 ADD $HINUM,R1 ;:ADD NUMBER TO MAKE X 129
2865 017650 062700 001057 ADD #1057,R0 ;:ADD LOW CONSTANT
2866 017654 005501 ADC R1 ;:PROPOGATE CARRY
2867 017656 062701 047401 ADD #47401,R1 ;:ADD HIGH CONSTANT
2868 017662 010037 017704 MOV RO,$LONUM ;:SAVE R0
2869 017666 010137 017702 MOV R1,$HINUM ;:SAVE R1
2870 017672 012602 MOV (SP)+,R2 ;:POP STACK INTO R2
2871 017674 012601 MOV (SP)+,R1 ;:POP STACK INTO R1
2872 017676 012600 MCV (SP)+,RO ;:POP STACK INTO RO
2873 017700 000207 RTS PC ;:RETURN
2874 017702 176543 $HINUM: .WORD 176543
2875 017704 123456 $LONUM: .WORD 123456

```

M05

MAINDEC-11-DZVSE-A
DZVSEA.P11

02-JUN-76 00:00

VSV0: VIEDC GRAPHIC TEST PROGRAM
RANDOM NUMBER GENERATOR ROUTINE

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017706
017706 104407
017710 104407
017712 032777 040000 161220
017720 001040
017722 000416
017724 013746 000004
017730 012737 017750 000004
017736 005737 177060
017742 012637 000004
017746 000414
017750 022626
017752 012637 000004
017756 000421
017760
017760 032777 000400 161152
017766 001404
017770 127737 161144 001102
017776 001411
020000 105237 001102
020004 113737 001102 001176
020012 011637 001106
020016 105037 001103
020022 013777 001102 161112
020030 013716 001106
020034 000002

.SBTTL SCOPE HANDLER ROUTINE

```
::*****  
:*THIS ROUTINE CONTROLS THE LOOPING OF SUBTESTS. IT WILL INCREMENT  
:*AND LOAD THE TEST NUMBER($STNM) INTO THE DISPLAY REG.(DISPLAY<7:0>,  
:*AND LOAD THE ERROR FLAG ($ERFLG) INTO DISPLAY<15:08>  
:*THE SWITCH OPTIONS PROVIDED BY THIS ROUTINE ARE:  
:*SW14=1 LOOP ON TEST  
:*SW08=1 LOOP ON TEST IN SWR<7:0>  
:*CALL  
:* SCOPE ::SCOPE=IOT  
$SCOPE:  
CKSWR ::TEST FOR CHANGE IN SOFT-SWR  
CKSWR  
1$: BIT #BIT14,@SWR ::LOOP ON PRESENT TEST?  
BNE $OVER ::YES IF SW14=1  
:****START OF CODE FOR THE XOR TESTER****  
$XTSTR: BR 6$ ::IF RUNNING ON THE "XOR" TESTER CHANGE  
::THIS INSTRUCTION TO A "NOP" (NOP=240)  
MOV @#ERRVEC, -(SP) ::SAVE THE CONTENTS OF THE ERROR VECTOR  
MOV #5$, @#ERRVEC ::SET FOR TIMEOUT  
TST @#177060 ::TIME OUT ON XOR?  
MOV (SP)+, @#ERRVEC ::RESTORE THE ERROR VECTOR  
BR $SVLAD ::GO TO THE NEXT TEST  
5$: CMP (SP)+, (SP)+ ::CLEAR THE STACK AFTER A TIME OUT  
MOV (SP)+, @#ERRVEC ::RESTORE THE ERROR VECTOR  
BR $OVER ::LOOP ON THE PRESENT TEST  
6$:;****END OF CODE FOR THE XOR TESTER****  
BIT #BIT08,@SWR ::LOOP ON SPEC. TEST?  
BEQ $SVLAD ::BR IF NO  
CMPB @SWR,$STNM ::ON THE RIGHT TEST? SWR<7:0>  
BEQ $OVER ::BR IF YES  
$SVLAD: INCB $STNM ::COUNT TEST NUMBERS  
MOVB $STNM,$TESTN ::SET TEST NUMBER IN APT MAILBOX  
MOV (SP),$LPADR ::SAVE SCOPE LOOP ADDRESS  
CLRB $ERFLG ::ZERO THE ERROR FLAG  
$OVER: MOV $STNM,@DISPLAY ::DISPLAY TEST NUMBER  
MOV $LPADR,(SP) ::FUDGE RETURN ADDRESS  
RTI ::FIXES PS
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020036 104407
020040 105237 001103
020044 001775
020046 013777 001102 161066
020054 005237 001112
020060 011637 001116
020064 162737 000002 001116
020072 117737 161020 001114
020100 032777 020000 161032
020106 001004
020110 004737 020174
020114 104401 001167
020120
020120 122737 000001 001212
020126 001007
020130 113737 001114 020142
020136 004737 021522
020142 000
020143 000
020144 000777
020146 005777 160766
020152 100002
020154 000000
020156 104407
020160
020160 022737 003304 000042
020166 001001
020170 000000
020172
020172 000002

```

.SBTTL ERROR HANDLER ROUTINE

:*****
:*THIS ROUTINE WILL INCREMENT THE ERROR FLAG AND THE ERROR COUNT.
:*SAVE THE ERROR ITEM NUMBER AND THE ADDRESS OF THE ERROR CALL
:*AND GO TO $ERRTYP ON ERROR
:*THE SWITCH OPTIONS PROVIDED BY THIS ROUTINE ARE:
:*SW15=1 HALT ON ERROR
:*SW13=1 INHIBIT ERROR TYPEOUTS
:*CALL
:* ERROR N ;:ERROR=EMT AND N=ERROR ITEM NUMBER

$ERROR:
7$: CKSWR ;:TEST FOR CHANGE IN SOFT-SWR
INCB $ERFLG ;:SET THE ERROR FLAG
BEQ 7$ ;:DON'T LET THE FLAG GO TO ZERO
MOV $TSTNM,$DISPLAY ;:DISPLAY TEST NUMBER AND ERROR FLAG
INC $ERTTL ;:INC THE ERROR COUNT
MOV (SP),$ERRPC ;:GET ADDRESS OF ERROR INSTRUCTION
SUB #2,$ERRPC
MOVB @,$ERRPC,$ITEMB ;:STRIP AND SAVE THE ERROR ITEM CODE
BIT #BIT13,$SWR ;:SKIP TYPEOUT IF SET
BNE 20$ ;:SKIP TYPEOUTS
JSR PC,$ERRTYP ;:GO TO USER ERROR ROUTINE
TYPE , $CRLF

20$: CMPB #APTENV,$ENV ;:RUNNING IN APT MODE
BNE 2$ ;:NO SKIP APT ERROR REPORT
MOVB $ITEMB,21$ ;:SET ITEM NUMBER AS ERROR NUMBER
JSR PC,$ATY4 ;:REPORT FATAL ERROR TO APT

21$: .BYTE 0
.BYTE 0

22$: BR 22$ ;:APT ERROR LOOP
TST @SWR ;:HALT ON ERROR
BPL 3$ ;:SKIP IF CONTINUE
HALT ;:HALT ON ERROR!
CKSWR ;:TEST FOR CHANGE IN SOFT-SWR

3$: CMP # $ENDAD,@#42 ;:ACT-11 AUTO-ACCEPT?
BNE 6$ ;:BRANCH IF NC
HALT ;:YES

6$: RTI ;:RETURN

```

.SRTL ERROR MESSAGE TYPEOUT ROUTINE

*THIS ROUTINE USES THE "ITEM CONTROL BYTE" (SITEMB) TO DETERMINE WHICH
*ERROR IS TO BE REPORTED. IT THEN OBTAINS, FROM THE "ERROR TABLE" (SERRTB),
*AND REPORTS THE APPROPRIATE INFORMATION CONCERNING THE ERROR.

SERRTYP:

020174	104401	001167	TYPE	SCRLF	:: "CARRIAGE RETURN" & "LINE FEED"
020200	013046		MOV	RC, -(SP)	:: SAVE RC
020202	005000		CLR	RC	:: PICKUP THE ITEM INDEX
020204	153700	001114	BISB	2(SITEMB, RC)	
020210	001004		BNE	IS	:: IF ITEM NUMBER IS ZERO, JUST
					:: TYPE THE PC OF THE ERROR
020212	013746	001116	MOV	SERRPC, -(SP)	:: SAVE SERRPC FOR TYPEOUT
					:: ERROR ADDRESS
020216	104402		TYPOC		:: GO TYPE--OCTAL ASCII(ALL DIGITS)
020220	000426		BR	6S	:: GET OUT
020222	005300	1S:	DEC	RC	:: ADJUST THE INDEX SO THAT IT WILL
020224	006300		ASL	RC	:: WORK FOR THE ERROR TABLE
020226	006300		ASL	RC	
020230	006300		ASL	RC	
020232	062700	001256	ADD	SERRTB, RC	:: FORM TABLE POINTER
020236	012037	020246	MOV	(RC)+, 2S	:: PICKUP "ERROR MESSAGE" POINTER
020242	001404		BEG	3S	:: SKIP TYPEOUT IF NO POINTER
020244	104401		TYPE		:: TYPE THE "ERROR MESSAGE"
020246	000000	2S:	.WORD	0	:: "ERROR MESSAGE" POINTER GOES HERE
020250	104401	001167	TYPE	SCRLF	:: "CARRIAGE RETURN" & "LINE FEED"
020254	012037	020264	MOV	(RC)+, 4S	:: PICKUP "DATA HEADER" POINTER
020260	001404		BEG	5S	:: SKIP TYPEOUT IF 0
020262	104401		TYPE		:: TYPE THE "DATA HEADER"
020264	000000	4S:	.WORD	0	:: "DATA HEADER" POINTER GOES HERE
020266	104401	001167	TYPE	SCRLF	:: "CARRIAGE RETURN" & "LINE FEED"
020272	011000	5S:	MOV	(RC), RC	:: PICKUP "DATA TABLE" POINTER
020274	001004		BNE	7S	:: GO TYPE THE DATA
020276	012600	6S:	MOV	(SP)+, RC	:: RESTORE RC
020300	104401	001167	TYPE	SCRLF	:: "CARRIAGE RETURN" & "LINE FEED"
020304	000207		RTS	PC	:: RETURN
020306		7S:			
020306	013046		MOV	2(RC)+, -(SP)	:: SAVE 2(RC)+ FOR TYPEOUT
020310	104402		TYPOC		:: GO TYPE--OCTAL ASCII(ALL DIGITS)
020312	005710		TST	(RC)	:: IS THERE ANOTHER NUMBER?
020314	001770		BEG	6S	:: BR IF NO
020316	104401	020324	TYPE	8S	:: TYPE TWO(2) SPACES
020322	000771		BR	7S	:: LOOP
020324	020040	000	.ASCIZ	/' '	:: TWO(2) SPACES
020330	020330		.EVEN		

.SBTTL BINARY TO OCTAL (ASCII) AND TYPE

```

*****
*THIS ROUTINE IS USED TO CHANGE A 16-BIT BINARY NUMBER TO A 6-DIGIT
*OCTAL (ASCII) NUMBER AND TYPE IT.
*STYPOS---ENTER HERE TO SETUP SUPPRESS ZEROS AND NUMBER OF DIGITS TO TYPE
*CALL:
*   MOV     NUM,-(SP)      ;;NUMBER TO BE TYPED
*   TYPOS   N              ;;CALL FOR TYPEOUT
*   .BYTE  N              ;;N=1 TO 6 FOR NUMBER OF DIGITS TO TYPE
*   .BYTE  M              ;;M=1 OR 0
*                               ;;1=TYPE LEADING ZEROS
*                               ;;0=SUPPRESS LEADING ZEROS

```

```

*STYPON---ENTER HERE TO TYPE OUT WITH THE SAME PARAMETERS AS THE LAST
*STYPOS OR STYPOC

```

```

*CALL:
*   MOV     NUM,-(SP)      ;;NUMBER TO BE TYPED
*   TYPON   N              ;;CALL FOR TYPEOUT

```

```

*STYPOC---ENTER HERE FOR TYPEOUT OF A 16 BIT NUMBER
*CALL:
*   MOV     NUM,-(SP)      ;;NUMBER TO BE TYPED
*   TYPOC   N              ;;CALL FOR TYPEOUT

```

020330 017646 000000
020334 116637 000001
020337 112637 020555
020338 062716 000002
020352 000406
020354 112737 000001
020362 112737 000006
020370 112737 000005
020376 010346
020400 010446
020402 010546
020404 113704 020555
020410 005404
020412 062704 000006
020416 110437 020554
020422 113704 020553
020426 016605 000012
020432 005003
020434 006105
020436 000404
020440 006105
020442 006105
020444 006105
020446 010503
020450 006103
020452 105337 020554
020456 100016
020460 042703 177770
020464 001002
020466 005704

```

STYPOS: MOV     2(SP),-(SP)      ;;PICKUP THE MODE
        MOVB   1(SP),%SOFILL    ;;LOAD ZERO FILL SWITCH
        MOVB   (SP)+,%SOMODE+1  ;;NUMBER OF DIGITS TO TYPE
        ADD    #2,(SP)          ;;ADJUST RETURN ADDRESS
        BR     STYPON
STYPOC: MOVB   #1,%SOFILL       ;;SET THE ZERO FILL SWITCH
        MOVB   #6,%SOMODE+1     ;;SET FOR SIX(6) DIGITS
STYPON: MOVB   #5,%SOCNT        ;;SET THE ITERATION COUNT
        MOV    R3,-(SP)         ;;SAVE R3
        MOV    R4,-(SP)         ;;SAVE R4
        MOV    R5,-(SP)         ;;SAVE R5
        MOVB   %SOMODE+1,R4     ;;GET THE NUMBER OF DIGITS TO TYPE
        NEG    R4
        ADD    #6,R4            ;;SUBTRACT IT FOR MAX. ALLOWED
        MOVB   R4,%SOMODE       ;;SAVE IT FOR USE
        MOVB   %SOFILL,R4       ;;GET THE ZERO FILL SWITCH
        MOV    12(SP),R5        ;;PICKUP THE INPUT NUMBER
        CLR   R3                ;;CLEAR THE OUTPUT WORD
        ROL   R5                ;;ROTATE MSB INTO "C"
        BR    3$                ;;GO DO MSB
        ROL   R5                ;;FORM THIS DIGIT
        ROL   R5
        ROL   R5
        ROL   R5
        MOV    R5,R3
3$:    ROL   R3                ;;GET LSB OF THIS DIGIT
        DECB  %SOMODE           ;;TYPE THIS DIGIT?
        BPL   7$                ;;BR IF NO
        BIC   #177770,R3       ;;GET RID OF JUNK
        BNE   4$                ;;TEST FOR 0
        TST   R4                ;;SUPPRESS THIS 0

```


E06

NOV02-11-DVSE-A
DVSEAF.P11 02-JUN-76 00:00

VSV0: VIEDO GRAPHIC TEST PROGRAM
CONVERT BINARY TO DECIMAL AND TYPE ROUTINE

MAY11 27(1006) 02-DEC-76 17:43 PAGE 70

01	020646	001002		BNE	5\$:: FALL THROUGH IF C
02	020650	105716		TSTB	(SP)	:: STILL DOING LEADING 0'S?
03	020652	100407		BMI	7\$:: BR IF YES
04	020654	106316	5\$:	ASLB	(SP)	:: MSD?
05	020656	103003		BCC	6\$:: BR IF NO
06	020660	116663	000001 177777	MOVB	1(SP),-1(R3)	:: YES--SET THE SIGN
07	020666	052702	000060	BIS	#'0,R2	:: MAKE THE BCD DIGIT ASCII
08	020672	052702	000040	BIS	#'R2	:: MAKE IT A SPACE IF NOT ALREADY A DIGIT
09	020676	110223		MOVB	R2,(R3)+	:: PUT THIS CHARACTER IN THE OUTPUT BUFFER
10	020700	005720		TST	(R0)+	:: JUST INCREMENTING
11	020702	020027	000010	CMP	R0,#'0	:: CHECK THE TABLE INDEX
12	020706	002746		BLT	8\$:: GO DO THE NEXT DIGIT
13	020710	003002		BGT	8\$:: GO TO EXIT
14	020712	010502		MOV	R5,R2	:: GET THE LSD
15	020714	000764		BR	6\$:: GO CHANGE TO ASCII
16	020716	105726	8\$:	TSTB	(SP)+	:: WAS THE LSD THE FIRST NON-ZERO?
17	020720	100003		BPL	9\$:: BR IF NO
18	020722	116663	177777 177776	MOVB	-1(SP),-2(R3)	:: YES--SET THE SIGN FOR TYPING
19	020730	105013	9\$:	CLRB	(R3)	:: SET THE TERMINATOR
20	020732	012605		MOV	(SP)+,R5	:: POP STACK INTO R5
21	020734	012603		MOV	(SP)+,R3	:: POP STACK INTO R3
22	020736	012602		MOV	(SP)+,R2	:: POP STACK INTO R2
23	020740	012601		MOV	(SP)+,R1	:: POP STACK INTO R1
24	020742	012600		MOV	(SP)+,R0	:: POP STACK INTO R0
25	020744	104401	020772	TYPE	\$DBLK	:: NOW TYPE THE NUMBER
26	020750	016666	000002 000004	MOV	2(SP),4(SP)	:: ADJUST THE STACK
27	020756	012616		MOV	(SP)+,(SP)	
28	020760	000002		RTI		:: RETURN TO USER
29	020762	023420	\$DTBL:	10000.		
30	020764	001750		1000.		
31	020766	000144		100.		
32	020770	000012		10.		
33	020772	000007	\$DELK:	.BLFW	4	

F06

.SBTTL POWER DOWN AND UP ROUTINES

:*****

POWER DOWN ROUTINE

	021002	012737	021146	000024	\$PWRDN: MOV	\$SILLUP, @PWRVEC	:: SET FOR FAST UP
	021010	012737	000340	000026	MOV	#340, @PWRVEC+2	:: PRI0:7
	021016	010046			MOV	RO, -(SP)	:: PUSH RO ON STACK
	021020	010146			MOV	R1, -(SP)	:: PUSH R1 ON STACK
	021022	010246			MOV	R2, -(SP)	:: PUSH R2 ON STACK
	021024	010346			MOV	R3, -(SP)	:: PUSH R3 ON STACK
	021026	010446			MOV	R4, -(SP)	:: PUSH R4 ON STACK
	021030	010546			MOV	R5, -(SP)	:: PUSH R5 ON STACK
	021032	017746	160102		MOV	@SWR, -(SP)	:: PUSH @SWR ON STACK
	021036	010637	021152		MOV	SP, \$SAVR6	:: SAVE SP
	021042	012737	021054	000024	MOV	\$PWRUP, @PWRVEC	:: SET UP VECTOR
	021050	000000			HALT		
	021052	000776			BR	.-2	:: HANG UP

:*****

POWER UP ROUTINE

	021054	012737	021146	000024	\$PWRUP: MOV	\$SILLUP, @PWRVEC	:: SET FOR FAST DOWN
	021052	012706	021152		MOV	\$SAVR6, SP	:: GET SP
	021056	005037	021152		CLR	\$SAVR6	:: WAIT LOOP FOR THE TTY
	021072	005237	021152		1\$: INC	\$SAVR6	:: WAIT FOR THE INC
	021076	001375			BNE	1\$:: OF WORD
	021100	012677	160034		MOV	(SP)+, @SWR	:: POP STACK INTO @SWR
	021104	012605			MOV	(SP)+, R5	:: POP STACK INTO R5
	021106	012604			MOV	(SP)+, R4	:: POP STACK INTO R4
	021110	012603			MOV	(SP)+, R3	:: POP STACK INTO R3
	021112	012602			MOV	(SP)+, R2	:: POP STACK INTO R2
	021114	012601			MOV	(SP)+, R1	:: POP STACK INTO R1
	021116	012600			MOV	(SP)+, RO	:: POP STACK INTO RO
	021120	012737	021002	000024	MOV	\$PWRDN, @PWRVEC	:: SET UP THE POWER DOWN VECTOR
	021126	012737	000340	000026	MOV	#340, @PWRVEC+2	:: PRI0:7
	021134	104401			TYPE		:: REPORT THE POWER FAILURE
	021136	021154			\$PWRMG: .WORD	PWRMSG	:: POWER FAIL MESSAGE POINTER
	021140	012716			MOV	(PC)+, (SP)	:: RESTART AT BEGIN
	021142	001572			\$PWRAD: .WORD	BEGIN	:: RESTART ADDRESS
	021144	000002			RTI		
	021146	000000			\$SILLUP: HALT		:: THE POWER UP SEQUENCE WAS STARTED
	021150	000776			BR	.-2	:: BEFORE THE POWER DOWN WAS COMPLETE
	021152	000000			\$SAVR6: 0		:: PUT THE SP HERE
	021154	005015	042522	052123	PWRMSG: .ASCII	<15><12>	RESTARTING AFTER A POWER FAILURE<15><12>
	021162	051101	044524	043516			
	021170	040440	052106	051105			
	021176	040440	050040	053517			
	021204	051105	043040	044501			
	021212	052514	042522	005015			
	021220	000					
	021222						.EVEN

.SBTTL TYPE ROUTINE

::*****
::*ROUTINE TO TYPE ASCIZ MESSAGE. MESSAGE MUST TERMINATE WITH A 0 BYTE.
::*THE ROUTINE WILL INSERT A NUMBER OF NULL CHARACTERS AFTER A LINE FEED.
::*NOTE1: \$NULL CONTAINS THE CHARACTER TO BE USED AS THE FILLER CHARACTER.
::*NOTE2: \$FILLS CONTAINS THE NUMBER OF FILLER CHARACTERS REQUIRED.
::*NOTE3: \$FILLC CONTAINS THE CHARACTER TO FILL AFTER.

::*CALL:
::*1) USING A TRAP INSTRUCTION
::* TYPE .MESADR ::MESADR IS FIRST ADDRESS OF AN ASCIZ STRING
::*CF
::* TYPE
::* MESADR
::*

021222 105737 001157
021226 100002
021230 000000
021232 000430
021234 010046
021236 017600 000002
021242 122737 000001 001212
021250 001011
021252 132737 000100 001213
021260 001405
021262 010037 021272
021266 004737 021512
021272 000000
021274 132737 000040 001213
021302 001003
021304 112046
021306 001005
021310 005726
021312 012600
021314 062716 000002
021320 000002
021322 122716 000011
021326 001430
021330 122716 000200
021334 001006
021336 005726
021340 104401
021342 001167
021344 105037 021500
021350 000755
021352 004737 021434
021356 123726 001156
021362 001350
021364 013746 001154
021370 105366 000001
021374 002770
021376 004737 021434
021402 105337 021500

\$TYPE: TSTB \$TFFLG :: IS THERE A TERMINAL?
BPL \$S :: BR IF YES
HALT \$S :: HALT HERE IF NO TERMINAL
BR \$S :: LEAVE
1\$: MOV RO, -(SP) :: SAVE RO
MOV \$2(SP), RO :: GET ADDRESS OF ASCIZ STRING
CMPB #APTENV, \$ENV :: RUNNING IN APT MODE
BNE \$S :: NO, GO CHECK FOR APT CONSOLE
BITB #APTPOOL, \$ENVM :: SPOOL MESSAGE TO APT
BEQ \$S :: NO, GO CHECK FOR CONSOLE
MOV RO, \$1S :: SETUP MESSAGE ADDRESS FOR APT
JSR PC, \$ATY3 :: SPOOL MESSAGE TO APT
61\$: .WORD 0 :: MESSAGE ADDRESS
62\$: BITB #APTCSUP, \$ENVM :: APT CONSOLE SUPPRESSED
BNE \$S :: YES, SKIP TYPE OUT
2\$: MOVB (RO)+, -(SP) :: PUSH CHARACTER TO BE TYPED ONTO STACK
BNE \$S :: BR IF IT ISN'T THE TERMINATOR
TST (SP)+ :: IF TERMINATOR POP IT OFF THE STACK
60\$: MOV (SP)+, RO :: RESTORE RO
3\$: ADD #2, (SP) :: ADJUST RETURN PC
RTI :: RETURN
4\$: CMPB #HT, (SP) :: BRANCH IF <HT>
BEQ \$S
CMPB #CRLF, (SP) :: BRANCH IF NOT <CRLF>
BNE \$S
TST (SP)+ :: POP <CR><LF> EQUIV
TYPE \$CRLF :: TYPE A CR AND LF
C_LRB \$CHARCNT :: CLEAR CHARACTER COUNT
BR \$S :: GET NEXT CHARACTER
5\$: JSR PC, \$TYPEC :: GO TYPE THIS CHARACTER
6\$: CMPB \$FILLC, (SP)+ :: IS IT TIME FOR FILLER CHARS.?
BNE \$S :: IF NO GO GET NEXT CHAR.
MOV \$NULL, -(SP) :: GET # OF FILLER CHARS. NEEDED
AND THE NULL CHAR.
7\$: DECB 1(SP) :: DOES A NULL NEED TO BE TYPED?
BLT \$S :: BR IF NO--GO POP THE NULL OFF OF STACK
JSR PC, \$TYPEC :: GO TYPE A NULL
DECB \$CHARCNT :: DO NOT COUNT AS A COUNT


```

021406 000770 BR 75 ::LOOP
: HORIZONTAL TAB PROCESSOR
021410 112716 000040 95: MOV B' (SP) ::REPLACE TAB WITH SPACE
021414 004737 021434 95: JSR PC,$TYPEC ::TYPE A SPACE
021420 132737 000007 021500 BITB #7,$CHARCNT ::BRANCH IF NOT AT
021426 001372 BNE 95 ::TAB STOP
021430 005726 TST (SP)+ ::POP SPACE OFF STACK
021432 000724 BR 25 ::GET NEXT CHARACTER
021434 105777 157510 $TYPEC: TSTB 2$TPS ::WAIT UNTIL PRINTER IS READY
021440 100375 BPL $TYPEC
021442 116677 000002 157502 MOV B 2(SP),2$TPB ::LOAD CHAR TO BE TYPED INTO DATA REG.
021450 122766 000015 000002 CMPB #CR,2(SP) ::IS CHARACTER A CARRIAGE RETURN?
021456 001003 BNE 1$ ::BRANCH IF NO
021460 105037 021500 CLRB $CHARCNT ::YES--CLEAR CHARACTER COUNT
021464 000406 BR $TYPEX ::EXIT
021466 122766 000012 000002 1$: CMPB #LF,2(SP) ::IS CHARACTER A LINE FEED?
021474 001402 BEQ $TYPEX ::BRANCH IF YES
021476 105227 INCB (PC)+ ::COUNT THE CHARACTER
021500 000000 $CHARCNT: .WORD 0 ::CHARACTER COUNT STORAGE
021502 000207 $TYPEX: RTS PC

.SBTTL APT COMMUNICATIONS ROUTINE
:*****
021504 112737 000001 021750 $ATY1: MOV B #1,$FFLG ::TO REPORT FATAL ERROR
021512 112737 000001 021746 $ATY3: MOV B #1,$MFLG ::TO TYPE A MESSAGE
021520 000403 BR $ATYC
021522 112737 000001 021750 $ATY4: MOV B #1,$FFLG ::TO ONLY REPORT FATAL ERROR
021530 $ATYC:
021530 010046 MOV R0,-(SP) ::PUSH R0 ON STACK
021532 010146 MOV R1,-(SP) ::PUSH R1 ON STACK
021534 105737 021746 TSTB $MFLG ::SHOULD TYPE A MESSAGE?
021540 001450 BEQ 5$ ::IF NOT: BR
021542 122737 000001 001212 CMPB #APTENV,$ENV ::OPERATING UNDER APT?
021550 001031 BNE 3$ ::IF NOT: BR
021552 132737 000100 001213 BITB #APTSPOOL,$ENVM ::SHOULD SPOOL MESSAGES?
021560 001425 BEQ 3$ ::IF NOT: BR
021562 017600 000004 MOV 24(SP),R0 ::GET MESSAGE ADDR.
021566 062766 000002 000004 ADD #2,4(SP) ::BUMP RETURN ADDR.
021574 005737 001172 1$: TST $MSGTYPE ::SEE IF DONE W/ LAST XMISSION?
021600 001375 BNE 1$ ::IF NOT: WAIT
021602 010037 001206 MOV R0,$MSGAD ::PUT ADDR IN MAILBOX
021606 105720 2$: TSTB (R0)+ ::FIND END OF MESSAGE
021610 001376 BNE 2$
021612 163700 001206 SUB $MSGAD,R0 ::SUB START OF MESSAGE
021616 006200 ASR R0 ::GET MESSAGE LNTH IN WORDS
021620 010037 001210 MOV R0,$MSGLGT ::PUT LENGTH IN MAILBOX
021624 012737 000004 001172 MOV #4,$MSGTYPE ::TELL APT TO TAKE MSG.
021632 000413 BR 5$
021634 017637 000004 021660 3$: MOV 24(SP),4$ ::PUT MSG ADDR IN JSR LINKAGE
021642 062766 000002 000004 ADD #2,4(SP) ::BUMP RETURN ADDRESS
021650 013746 177776 MOV 177776,-(SP) ::PUSH 177776 ON STACK
021654 004737 021222 JSR PC,$TYPE ::CALL TYPE MACRO
021660 000000 4$: .WORD 0

```

```

0017 021662          SS:
0018 021662 105737 021750 105:  TSTB  SFFLG      :: SHOULD REPORT FATAL ERROR?
0019 021666 001416          BEQ  125      :: IF NOT: BR
0020 021670 005737 001212 105:  TST  SENV      :: RUNNING UNDER APT?
0021 021674 001413          BEQ  125      :: IF NOT: BR
0022 021676 005737 001172 115:  TST  SMSGTYPE :: FINISHED LAST MESSAGE?
0023 021702 001375          BNE  115      :: IF NOT: WAIT
0024 021704 017637 000004 001174  MOV  24(SP), SFATAL :: GET ERROR #
0025 021712 062766 000002 000004  ADD  #2, 4(SP)      :: BUMP RETURN ADDR.
0026 021720 005237 001172 125:  INC  SMSGTYPE      :: TELL APT TO TAKE ERROR
0027 021724 105037 021750  CLRB SFFLG        :: CLEAR FATAL FLAG
0028 021730 105037 021747  CLRB SLFLG        :: CLEAR LOG FLAG
0029 021734 105037 021746  CLRB SMFLG        :: CLEAR MESSAGE FLAG
0030 021740 012601          MOV  (SP)+, R1      :: POP STACK INTO R1
0031 021742 012600          MOV  (SP)+, R0      :: POP STACK INTO R0
0032 021744 000207          RTS  PC           :: RETURN
0033 021746          SMFLG: .BYTE 0      :: MESSG. FLAG
0034 021747          SLFLG: .BYTE 0      :: LOG FLAG
0035 021750          SFFLG: .BYTE 0      :: FATAL FLAG
                                .EVEN
0036          APTSIZE=200
0037          APTENV=001
0038          APTSPool=100
0039          APTCSUF=040

```


3397 022164 002420
 3398 022166 021627 000067
 3399 022172 003015
 3400 022174 042726 000060
 3401 022200 005766 000002
 3402 022204 001403
 3403 022206 006316
 3404 022210 006316
 3405 022212 006316
 3406 022214 005266 000002
 3407 022220 056616 177776
 3408 022224 000707
 3409 022226 104401 001166
 3410 022232 000720

```

BLT      18$          ;;BRANCH IF YES
CMP      (SP),#67    ;;CHAR > 7?
BGT      18$          ;;BRANCH IF YES
BIC      #60,(SP)+   ;;STRIP-OFF ASCII
TST      2(SP)       ;;IS THIS THE FIRST CHAR
BEQ      17$          ;;BRANCH IF YES
ASL      (SP)        ;;NO, SHIFT PRESENT
ASL      (SP)        ;;CHAR OVER TO MAKE
ASL      (SP)        ;;ROOM FOR NEW ONE.
17$: INC      2(SP)    ;;KEEP COUNT OF CHAR
BIS      -2(SP),(SP) ;;SET IN NEW CHAR
BR       7$          ;;GET THE NEXT ONE
18$: TYPE 50QUES     ;;TYPE ?<CR><LF>
BR       20$         ;;SIMULATE CONTROL-U
.DSABL   LSB
  
```

THIS ROUTINE WILL INPUT A SINGLE CHARACTER FROM THE TTY

*CALL:

```

* RDCHR          ;; INPUT A SINGLE CHARACTER FROM THE TTY
* RETURN HERE   ;; CHARACTER IS ON THE STACK
*              ;; WITH PARITY BIT STRIPPED OFF
  
```

3422 022234 011646
 3423 022236 016666 000004 000002
 3424 022244 105777 156674
 3425 022250 100375
 3426 022252 117766 156670 000004
 3427 022260 042766 177600 000004
 3428 022266 026627 000004 000023
 3429 022274 001013
 3430 022276 105777 156642
 3431 022302 100375
 3432 022304 117746 156636
 3433 022310 042716 177600
 3434 022314 022627 000021
 3435 022320 001366
 3436 022322 000750
 3437 022324 026627 000004 000140
 3438 022332 002407
 3439 022334 026627 000004 000175
 3440 022342 003003
 3441 022344 042766 000040 000004
 3442 022352 000002

```

$RDCHR: MOV      (SP),-(SP)    ;;PUSH DOWN THE PC
          MOV      4(SP),2(SP)  ;;SAVE THE PS
1$: TST      0$TKS          ;;WAIT FOR
          BPL      1$          ;;A CHARACTER
          MOVB     0$TKB,4(SP)  ;;READ THE TTY
          BIC      #1C<177>,4(SP) ;;GET RID OF JUNK IF ANY
          CMP      4(SP),#23    ;;IS IT A CONTROL-S?
          BNE      3$          ;;BRANCH IF NO
          TST      0$TKS          ;;WAIT FOR A CHARACTER
          BPL      2$          ;;LOOP UNTIL ITS THERE
          MOVB     0$TKB,-(SP)  ;;GET CHARACTER
          BIC      #1C177,(SP)  ;;MAKE IT 7-BIT ASCII
          CMP      (SP)+,#21    ;;IS IT A CONTROL-Q?
          BNE      2$          ;;IF NOT DISCARD IT
          BR       1$          ;;YES, RESUME
          CMP      4(SP),#140   ;;IS IT UPPER CASE?
          BLT      4$          ;;BRANCH IF YES
          CMP      4(SP),#175   ;;IS IT A SPECIAL CHAR?
          BGT      4$          ;;BRANCH IF YES
          BIC      #40,4(SP)    ;;MAKE IT UPPER CASE
          RTI                    ;;GO BACK TO USER
          4$:
  
```

THIS ROUTINE WILL INPUT A STRING FROM THE TTY

*CALL:

```

* RDLIN          ;; INPUT A STRING FROM THE TTY
* RETURN HERE   ;; ADDRESS OF FIRST CHARACTER WILL BE ON THE STACK
*              ;; TERMINATOR WILL BE A BYTE OF ALL C'S
  
```

3450 022354 010346
 3451 022356 012703 022462
 3452 022362 022703 022472

```

$RDLIN: MOV      R3,-(SP)      ;;SAVE R3
1$: MOV      #1$TTYIN,R3      ;;GET ADDRESS
2$: CMP      #1$TTYIN+8.,R3    ;;BUFFER FULL?
  
```

```

3453 022366 101405      BLOS      4$      ;;BR IF YES
3454 022370 104410      RDCHR     ;;GO READ ONE CHARACTER FROM THE TTY
3455 022372 112613      MOVB     (SP)+,(R3)  ;;GET CHARACTER
3456 022374 122713 00C177 10$: CMPB     #177,(R3)  ;;IS IT A RUBOUT
3457 022400 001003      SNE      3$      ;;SKIP IF NOT
3458 022402 104401 001166 4$:  TYPE     ,SQUES  ;;TYPE A '?'
3459 022406 000763      BR       1$      ;;CLEAR THE BUFFER AND LOOP
3460 022410 111337 022460 3$:  MOVB     (R3),9$  ;;ECHO THE CHARACTER
3461 022414 104401 022460      TYPE     9$
3462 022420 122723 000015      CMPB     #15,(R3)+  ;;CHECK FOR RETURN
3463 022424 001356      BNE      2$      ;;LOOP IF NOT RETURN
3464 022426 105063 177777      CLRB     -1(R3)    ;;CLEAR RETURN (THE 15)
3465 022432 104401 001170      TYPE     ,SLF     ;;TYPE A LINE FEED
3466 022436 012603      MOV      (SP)+,R3  ;;RESTORE R3
3467 022440 011646      MOV      (SP)-,(SP)  ;;ADJUST THE STACK AND PUT ADDRESS OF THE
3468 022442 016666 000004 000002      MOV      4(SP),2(SP)  ;; FIRST ASCII CHARACTER ON IT
3469 022450 012766 022462 000004      MOV      #STTYIN,4(SP)
3470 022456 000002      RTI      ;;RETURN
3471 022460 000      9$:  .BYTE     0      ;;STORAGE FOR ASCII CHAR. TO TYPE
3472 022461 000      .BYTE     0      ;;TERMINATOR
3473 022462 000010      $TTYIN: .BLKB     8.  ;;RESERVE 8 BYTES FOR TTY INPUT
3474 022472 052536 005015 000      $CNTLU: .ASCIZ  /↑U<<15><12>  ;;CONTROL "U"
3475 022477 136 006507 000012      $CNTLG: .ASCIZ  /↑G<<15><12>  ;;CONTROL "G"
3476 022504 005015 053523 020122      $MSWR:  .ASCIZ  <15><12>/SWR = /
3477 022512 020075 000
3478 022515 040 047040 053505      $MNEW:  .ASCIZ  / NEW = /
3479 022522 036440 000040
3480 .SBTTL READ AN OCTAL NUMBER FROM THE TTY
3481
3482 ;;*****
3483 ;;*THIS ROUTINE WILL READ AN OCTAL (ASCII) NUMBER FROM THE TTY AND
3484 ;;*CHANGE IT TO BINARY.
3485 ;;*CALL:
3486 ;;*      RDOCT
3487 ;;*      RETURN HERE
3488 ;;*      ;;READ AN OCTAL NUMBER
3489 ;;*      ;;LOW ORDER BITS ARE ON TOP OF THE STACK
3490 ;;*      ;;HIGH ORDER BITS ARE IN $HI OCT
3491
3490 022526 011646      $RDOCT: MOV      (SP)-,(SP)  ;;PROVIDE SPACE FOR THE
3491 022530 016666 000004 000002      MOV      4(SP),2(SP)  ;;INPUT NUMBER
3492 022536 010046      MOV      R0,-(SP)    ;;PUSH R0 ON STACK
3493 022540 010146      MOV      R1,-(SP)    ;;PUSH R1 ON STACK
3494 022542 010246      MOV      R2,-(SP)    ;;PUSH R2 ON STACK
3495 022544 104411 1$:  RDLIN     ;;READ AN ASCII LINE
3496 022546 012600      MOV      (SP)+,R0    ;;GET ADDRESS OF 1ST CHARACTER
3497 022550 005001      CLR      R1          ;;CLEAR DATA WORD
3498 022552 005002      CLR      R2
3499 022554 112046 2$:  MOVB     (R0)+,-(SP)  ;;PICKUP THIS CHARACTER
3500 022556 001412      BEQ      3$          ;;IF ZERO GET OUT
3501 022560 006301      ASL      R1          ;;*2
3502 022562 006102      ROL      R2
3503 022564 006301      ASL      R1          ;;*4
3504 022566 006102      ROL      R2
3505 022570 006301      ASL      R1          ;;*8
3506 022572 006102      ROL      R2
3507 022574 042716 177770      BIC      #↑C7,(SP)   ;;STRIP THE ASCII JUNK
3508 022600 062601      ADD      (SP)+,R1   ;;ADD IN THIS DIGIT

```

M06

MAINDEC-11-DZVSE-A
DZVSEA.P11

02-JUN-76

VSV01 VIEDO GRAPHIC TEST PROGRAM
00:00

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READ AN OCTAL NUMBER FROM THE TTY

3509	022602	000764		BR	2\$::LOOP
3510	022604	005726		3\$: TST	(SP)+	::CLEAN TERMINATOR FROM STACK
3511	022606	010166	000012	MOV	R1,12(SP)	::SAVE THE RESULT
3512	022612	010237	022626	MOV	R2,\$HI OCT	
3513	022616	012602		MOV	(SP)+,R2	::POP STACK INTO R2
3514	022620	012601		MOV	(SP)+,R1	::POP STACK INTO R1
3515	022622	012600		MOV	(SP)+,R0	::POP STACK INTO R0
3516	022624	000002		RTI		::RETURN
3517	022626	000000		\$HI OCT: .WORD	0	::HIGH ORDER BITS GO HERE

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.SBTTL TRAP DECODER

::*****
:*THIS ROUTINE WILL PICKUP THE LOWER BYTE OF THE "TRAP" INSTRUCTION
:*AND USE IT TO INDEX THROUGH THE TRAP TABLE FOR THE STARTING ADDRESS
:*OF THE DESIRED ROUTINE. THEN USING THE ADDRESS OBTAINED IT WILL
:*GO TO THAT ROUTINE.

\$TRAP: MOV RO,-(SP) ;;SAVE RO
MOV 2(SP),RO ;;GET TRAP ADDRESS
TST -(RO) ;;BACKUP BY 2
MOV (RO),RO ;;GET RIGHT BYTE OF TRAP
ASL RO ;;POSITION FOR INDEXING
MOV \$TRPAD(RO),RO ;;INDEX TO TABLE
RTS RO ;;GO TO ROUTINE

;;THIS IS USE TO HANDLE THE "GETPRI" MACRO

\$TRAP2: MOV (SP),-(SP) ;;MOVE THE PC DOWN
MOV 4(SP),2(SP) ;;MOVE THE PSW DOWN
RTI ;;RESTORE THE PSW

.SBTTL TRAP TABLE

*THIS TABLE CONTAINS THE STARTING ADDRESSES OF THE ROUTINES CALLED
*BY THE "TRAP" INSTRUCTION.

: ROUTINE
:-----
\$TRPAD: .WORD \$TRAP2
\$TYPE ;;CALL=TYPE TRAP+1(104401) TTY TYPEOUT ROUTINE
\$TYPOC ;;CALL=TYPOC TRAP+2(104402) TYPE OCTAL NUMBER (WITH LEADING ZERCS)
\$TYPOS ;;CALL=TYPOS TRAP+3(104403) TYPE OCTAL NUMBER (NO LEADING ZEROS)
\$TYPON ;;CALL=TYPON TRAP+4(104404) TYPE OCTAL NUMBER (AS PER LAST CALL)
\$TYPDS ;;CALL=TYPDS TRAP+5(104405) TYPE DECIMAL NUMBER (WITH SIGN)
\$GTSWR ;;CALL=GTSWR TRAP+6(104406) GET SOFT-SWR SETTING
\$CKSWR ;;CALL=CKSWR TRAP+7(104407) TEST FOR CHANGE IN SOFT-SWR
\$RDCHR ;;CALL=RDCHR TRAP+10(104410) TTY TYPEIN CHARACTER ROUTINE
\$RDLIN ;;CALL=RDLIN TRAP+11(104411) TTY TYPEIN STRING ROUTINE
\$RDOCT ;;CALL=RDOCT TRAP+12(104412) READ AN OCTAL NUMBER FROM TTY

022630 010046
022632 016600 000002
022636 005740
022640 111000
022642 006300
022644 016000 022664
022650 000200

022652 011646
022654 016666 000004 000002
022662 000002

022664 022652
022666 021222
022670 020354
022672 020330
022674 020370
022676 020556

022700 022022
022702 021752
022704 022234
022706 022354
022710 022526

3563 0227112 025015 046412 026504
 3564 0227120 030461 042055 052132
 3565 0227128 042523 040455 024462
 3566 0227134 052501 027107 030440
 3567 0227142 032471 006466 000012
 3568 0227150 015 012
 3569 0227158 052126 030126 020061
 3570 0227160 052101 040440 042104
 3571 0227166 042522 051523 000040
 3572 0227174 044040 051501 000040
 3573 023002 041040 052111 046440
 3574 023010 050101 051450 020051
 3575 023016 047111 052123 046101
 3576 023024 042514 006504 000012
 3577 023032 047516 041040 051525
 3578 023040 051040 051505 047520
 3579 023046 051516 020105 051106
 3580 023054 046517 040440 041440
 3581 023062 040510 040522 052103
 3582 023070 051105 041040 051525
 3583 023076 051040 043505 051511
 3584 023104 042524 020122 025040
 3585 023112 020052 040506 040524
 3586 023120 020114 051105 047522
 3587 023126 020122 025052 000
 3588 023133 116 020117 052502
 3589 023140 020123 042522 050123
 3590 023146 047117 042523 043040
 3591 023154 047522 020115 020101
 3592 023162 042523 042514 052103
 3593 023170 042105 041040 052111
 3594 023176 046440 050101 051040
 3595 023204 043505 051511 042524
 3596 023212 000122
 3597 023214 044103 051101 041501
 3598 023222 042524 020122 052123
 3599 023230 052101 051525 051040
 3600 023236 043505 051511 042524
 3601 023244 020122 051105 047522
 3602 023252 000122
 3603 023254 042526 052122 041511
 3604 023262 046101 051440 047131
 3605 023270 020103 051105 047522
 3606 023276 000122
 3607 023300 042526 052122 041511
 3608 023306 046101 051440 047131
 3609 023314 020103 047111 042524
 3610 023322 051122 050125 020124
 3611 023330 051105 047522 000122
 3612 023336 044502 020124 040515
 3613 023344 020120 052123 052101
 3614 023352 051525 051040 043505
 3615 023360 051511 042524 020122
 3616 023366 051105 047522 000122
 3617 023374 044502 020124 040515

SBTTL ASCII MESSAGES
 TITLE: .ASCIIZ <15><12><12> /MO-11-DZVSE-AD AUG. 1976 <15><12>

VTHDR1: .BYTE 15,12
 .ASCIIZ /VIVO! AT ADDRESS /

VTHDR2: .ASCIIZ / HAS /
 VTHDR3: .ASCIIZ / BIT MAP(S) INSTALLED/<15><12>

EM1: .ASCIIZ /NO BUS RESPONSE FROM A CHARACTER BUS REGISTER ** FATAL ERROR **/

EM2: .ASCIIZ /NO BUS RESPONSE FROM A SELECTED BIT MAP REGISTER/

EM3: .ASCIIZ /CHARACTER STATUS REGISTER ERROR/

EM4: .ASCIIZ /VERTICAL SYNC ERROR/

EM5: .ASCIIZ /VERTICAL SYNC INTERRUPT ERROR/

EM6: .ASCIIZ /BIT MAP STATUS REGISTER ERROR/

EM7: .ASCIIZ /BIT MAP P.C. REGISTER ERROR/

02VSEB.P11 02-JUN-76

VSV01 VIEDO GRAPHIC TEST PROGRAM
ASCII MESSAGES

3618	023402	020120	027120	027103		
3619	023410	051040	043505	051511		
3620	023416	042524	020122	051105		
3621	023424	047522	000122			
3622	023430	044502	020124	040515	EM10:	.ASCIZ /BIT MAP P.C. INCREMENT ERROR/
3623	023436	020120	027120	027103		
3624	023444	044440	041516	042522		
3625	023452	042515	052116	042440		
3626	023460	051122	051117	000		
3627	023465	127	051117	020104	EM11:	.ASCIZ /WORD TEST OF PIXEL BIT 0 ERROR/
3628	023472	042524	052123	047440		
3629	023500	020106	044520	042530		
3630	023506	020114	044502	020124		
3631	023514	020060	051105	047522		
3632	023522	000122				
3633	023524	054502	042524	052040	EM12:	.ASCIZ /BYTE TEST OF PIXEL BIT 0 ERROR/
3634	023532	051505	020124	043117		
3635	023540	050040	054111	046105		
3636	023546	041040	052111	030040		
3637	023554	042440	051122	051117		
3638	023562	000				
3639	023563	103	040510	040522	EM13:	.ASCIZ /CHARACTER READY ERROR/
3640	023570	052103	051105	051040		
3641	023576	040505	054504	042440		
3642	023604	051122	051117	000		
3643	023611	102	052111	046440	EM14:	.ASCIZ /BIT MAP READY ERROR/
3644	023616	050101	051040	040505		
3645	023624	054504	042440	051122		
3646	023632	051117	000			
3647	023635	120	042522	044526	EM16:	.ASCIZ /PREVIOUSLY EXISTING BIT MAP DOES NOT EXIST NOW/
3648	023642	052517	046123	020131		
3649	023650	054105	051511	044524		
3650	023656	043516	041040	052111		
3651	023664	046440	050101	042040		
3652	023672	042517	020123	047516		
3653	023700	020124	054105	051511		
3654	023706	020124	047516	000127		
3655	023714	051120	053105	047511	EM17:	.ASCIZ /PREVIOUSLY EXISTING VTVD1 CONTROLLER DOES NOT EXIST NOW/
3656	023722	051525	054514	042440		
3657	023730	044530	052123	047111		
3658	023736	020107	052126	030126		
3659	023744	020061	047503	052116		
3660	023752	047522	046114	051105		
3661	023760	042040	042517	020123		
3662	023766	047516	020124	054105		
3663	023774	051511	020124	047516		
3664	024002	000127				
3665	024004	051105	050122	004503	DH1:	.ASCIZ /ERRPC DEVICE ADDRESS THAT CAUSED A BUS TRAP/
3666	024012	042504	044526	042503		
3667	024020	040440	042104	042522		
3668	024026	051523	052040	040510		
3669	024034	020124	040503	051525		
3670	024042	042105	040440	041040		
3671	024050	051525	052040	040522		
3672	024056	000120				
3673	024060	051105	050122	004503	DH3:	.ASCIZ /ERRPC VCCSF GOOD BAD/

VSVO: VIDEO GRAPHIC TEST PROGRAM
ASCII MESSAGES

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024100 051503 034522
024101 042117 041011
024102 051122 041520 DM4: .ASCIZ /ERRPC VCCSR TIME1 TIME2
024103 053011 041503 051123
024104 052011 046511 030505
024105 052011 046511 031105
024106 000
024107 051122 041520 DM5: .ASCIZ /ERRPC VCCSR/
024108 053011 041503 051123
024109 000
024110 051122 041520 DM6: .ASCIZ /ERRPC VGCSR GOOD BAD/
024111 053011 041507 051123
024112 043411 047517 004504
024113 040502 000104
024114 051105 050122 004503 DM15: .ASCIZ /ERRPC VGCSR/
024115 043526 051503 000122
024116 051105 050122 004503 DM16: .ASCIZ /ERRPC VTVO1/
024117 052126 030126 000061
024118 .EVEN
024119 001116 001126 000000 DT1: $ERRPC,$BDDAT,0
024120 001116 001446 001124 DT3: $ERRPC,VCCSR,$GDDAT,$BDDAT,0
024121 001126 000000
024122 001116 001446 001510 DT4: $ERRPC,VCCSR,SAVE0,SAVE2,0
024123 001514 000000
024124 001116 001446 000000 DT5: $ERRPC,VCCSR,0
024125 001116 001460 001124 DT6: $ERRPC,VGCSR,$GDDAT,$BDDAT,0
024126 001126 000000
024127 001116 001460 000000 DT15: $ERRPC,VGCSR,0
024128 001116 001120 000000 DT16: $ERRPC,$GDADR,0,0,0,C
024129 000000 000000 000000
024130 000 000 000 DFO: .BYTE 0,0,0,0,0,0
024131 000 000 000
024132 000000 BUFFER: 0
024133 000000 .END

```


VGPC	001462	399*	746*	756*	1352*	1355*	1505*	1523*	1548*	1550*	1634*	1656*	1678*	1700*
		1720*	1747*	1774*	1801*	1827*	1854*	1881*	1908*	1934*	1961*	1988*	2015*	2041*
		2068*	2095*	2122*	2147*	2173*	2195*	2226*	2454*	2524*	2719*			
VHO	001524	423*	1109	1120	1140	1152	1171	1183	2550					
VH1	001526	424*	1225	1243	1261	1273								
VTHDR1	022750	712	3568*											
VTHDR2	022774	715	3572*											
VTHDR3	023002	719	3573*											
WIDTH	001522	422*	1143	1189	2557									
XADDS	017146	1195*	1201	2761*										
XHAIR	00153E	429*	1064											
XPRNT	016032	1113	1145	1204	1256	1285	2533*	2551	2780					
XPRNTA	016036	2534*	2815											
YADDS	017144	1185*	1200	2760*										
YHAIR	001540	429*	1081											
\$APTHD	001000	173	179*											
\$ASTAT=	***** U	3318	3333											
\$ATYC	021530	3289	3291*											
\$ATY1	021504	3287*												
\$ATY3	021512	3233	3288*											
\$ATY4	021522	2947	3290*											
\$AUTOB	001134	210*	3359	3480										
\$BASE	001246	275*	495	528	2627									
\$BDADR	001122	205*												
\$BDADR	001126	207*	528*	698*	699*	700	769*	771	772	774*	811*	812	813*	814
		815*	816	817*	818	819*	820	821*	822	823*	824	825*	826	827*
		843*	844*	845	855*	856*	857	867*	868*	859	882*	883*	884	896*
		897*	907*	908*	909	924*	925*	928*	929*	930*	931*	932	934*	935*
		936*	937*	938	948*	956*	962*	1366*	1367*	1368	1378*	1379*	1380	1390*
		1391*	1392	1403*	1404*	1405	1418*	1419*	1420	1432*	1433*	1434	1445*	1446*
		1456*	1460*	1469*	1470	1477	1486*	1487	1494	1506*	1507*	1508	1531*	1532*
		1533	1556*	1557*	1558	3694	3695	3700						
\$CDW1	001252	277*	502*	507*	541*	546*	570*	575*	599*	604*	628*	633*	728*	791
		1298	1315											
\$CDW2	001254	278*	500	502	539	541	568	570	597	599	626	628		
\$CHARC	021500	3250*	3260*	3267	3276*	3281*								
\$CKSWR	021752	3351*	3557											
\$CMTAG	001100	193*	444	445	453	459	460							
\$CM1 =	000002	225*	226*	227*										
\$CM2 =	000004	225*	226*	227*										
\$CM3 =	000002	223*	225											
\$CNTLG	022477	3362	3475*											
\$CNTLU	022472	3379	3474*											
\$CPUOP	001220	249*												
\$CRLF	001167	228*	2942	2961	2970	2989	2994	2998	3249	3284	3390	3474		
\$DBLK	020772	3111	3145	3153*										
\$DEVCT	001202	240*												
\$DEVM	001250	276*	507	546	575	604	633							
\$DOAGN	003314	666	675	681*										
\$DTBL	020762	3114	3149*											
\$ENDAD	003304	159	677*	2956										
\$ENDCT	003252	459	668*											
\$ENDMG	003323	670	685*											
\$ENULL	003320	673	684*											
\$ENV	001212	245*	2944	3228	3296	3320								
\$ENVYM	001213	246*	478	505	544	573	602	631	3230	3235	3298			

\$MTYP4	001237	271#												
\$NULL	001154	219#	3255	3284										
\$NWTST=	000001	761#	784#	936#	849#	861#	873#	889#	900#	913#	942#	969#	1002#	1043#
		1066#	1085#	1098#	1128#	1159#	1211#	1291#	1360#	1372#	1384#	1396#	1411#	1423#
		1438#	1450#	1498#	1517#	1541#	1569#	1597#	1618#	1640#	1662#	1684#	1706#	1733#
		1760#	1787#	1813#	1840#	1867#	1894#	1920#	1947#	1974#	2001#	2027#	2054#	2081#
		2108#	2134#	2160#	2186#	2213#	2240#	2257#	2300#	2320#	2341#	2362#	2383#	2404#
		2429#												
\$OCNT	020552	3042*	3071*	3084#										
\$OMCODE	020554	3037*	3041*	3046	3049*	3060*	3086#							
\$OVER	020022	2893	2904	2909	2914#									
\$PASS	001200	239#	477*	510	549	578	607	636	662*	663*	671	684	710	1053
		1070	1089	1102	1132	1163	1215	1312	1579	1601	1622	1644	1666	1688
		1710	1737	1764	1791	1817	1844	1871	1898	1924	1951	1978	2005	2031
		2058	2085	2112	2138	2164	2190	2217	2244	2261	2304	2324	2345	2366
		2387	2408											
\$PASTM	001006	183#												
\$FWRAD	021142	3192#												
\$PWRDN	021002	457	3159#	3187										
\$PWRMG	021136	3190#												
\$PWRUP	021054	3169	3175#											
\$QUES	001166	227#	2961	3284	3409	3458	3474							
\$RAND	017604	1180	1192	2851#										
\$RDCHR	022234	3422#	3558											
\$RDDEC=	***** U	3561												
\$RDLIN	022354	3450#	3559											
\$RDOCT	022526	3490#	3560											
\$RDSZ =	000010	3443#												
\$REGAD	001160	223#												
\$REGO	001162	225#												
\$REG1	001164	226#												
\$RTNAD	003316	683#												
\$R2A =	***** U	3561												
\$SAVRE=	***** U	3561												
\$SAVRE	021152	3168*	3176	3177*	3178*	3196#								
\$SCOPE	017706	451	2889#											
\$SETUP=	000137	441#	450	451	453	455	457	459	460	661	2890	2931	2954	2956
		3346	3480											
\$STUP =	177777	441#												
\$SVLAC	020000	2901	2907	2910#										
\$SVPC =	000230	157#	162											
\$SWR =	160400	1#	11	130	131	132	133	134	135	227	460	461	656	662
		676	682	684	765	788	840	853	865	877	893	904	917	946
		973	1006	1047	1070	1089	1102	1132	1163	1215	1295	1364	1376	1388
		1400	1415	1427	1442	1454	1502	1521	1545	1573	1601	1622	1644	1666
		1688	1710	1737	1764	1791	1817	1844	1871	1898	1924	1951	1978	2005
		2031	2058	2085	2112	2138	2164	2190	2217	2244	2261	2304	2324	2345
		2366	2387	2408	2433	2883	2884	2885	2892	2904	2906	2907	2910	2913
		2917	2924	2925	2926	2927	2935	2939	2951	2955	2961	3193		
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		1615*	2312*	2313	2316*	2317								
\$TEMPO	001476	412#												
\$TESTN	001176	238#	2911*											
\$TKB	001146	216#	3344	3355	3372	3426	3432							

