



Fury Interface Board

Pre-Release User Manual

10/28/2007

The following pages describe the Fury I/O interface board (IB).

Note: In this pre-release document, only the general description of each schematic will be included. The Production board will have the complete specs.

Page 1, is the layout and multiplexing of the input from the EFC and 10MHz signals. These are provided by the Fury board.

Here we see the connections for the 3 different OCXO's supported.

J14, J15, J16 and J17 are all SMA connectors.

J14 is the SMA connector to the Fury EFC. J15 can be used to sample the signal and TP1 is the test point for a possible oscilloscope or DVM.

J16 is the SMA connector to the Fury 10MHz signal. J17 can be used to sample the signal and TP2 is the test point for a possible oscilloscope or DVM.

The connector, for the HP10544A and the 10811-xxxx OCXO's, is labeled U3. HP OCXO's require +24V and +12V to function properly.

The other connection is for the 4 pin, 36x27mm type of OCXO. Many different manufacturers provide this kind of footprint. MTI and Crystek are just a few.

If the OCXO you are using is none of the above, then you can either connect straight from the Fury or connect from the Fury to the IB and use the SMA breakouts (J15, J17) from there. This will reduce the wear on the Fury SMA connectors.

Note that only one OCXO can be connected at any time.

Page 2, describes the circuit to drive the EFC voltage for the different oscillators. The Fury EFC output swings between 0-5VDC. In order to properly interface with OCXO's, with different EFC range, a simple OPAMP circuit is needed.

Care must be taken to select the right kind of OPAMP. Some considerations should be offset drift and noise parameters.

We picked the LT1208 because we have been very pleased with the device in the past.

Note, that any 8 pin, dual OPAMP should fit, as the pinouts are similar for most industry devices.

Description of the circuit:

U1A and associated components perform input voltage translation between 0-5V and +/-10V. U2 (TL431) is used as the voltage reference. By changing R1,R2 and R5,R6, different voltage offsets can be achieved. Make sure that $R1=R5$ and $R2=R6$.

U1B and associated components perform input voltage translation between 0-5V and 0-10V. By changing R8,R9 and R10,R11, different voltage offsets can be achieved. Make sure that $R8=R11$ and $R9=R10$.

The input from the Fury board can be switched, (via J8, J9 and J10) to the different parts of the circuit. Only one of these jumpers should be connected at any time.

(J8, J11), (J9, J12) and (J10, J13) should be connected in pairs.

If J8 is not connected then J21 should be, in order to ground the input of U1A.

If J10 is not connected then J23 should be, in order to ground the input of U1B.

Page 3, Describes the GPS antenna, 10MHz output and 1PPS output connections.

The Fury normally provides a cable with a MMCX to female BNC connectors. This cable is connected to the GPS receiver antenna input.

We prefer to use a MMCX to SMA antenna cable and have the female BNC connector on the IB. This way, all the connectors are situated on the two boards.

Basically, what we have done here is provide an alternate way to connect the GPS antenna.

If you prefer to use the MMCX to BNC cable, then you can still use the IB and attach the cable to, J3. J3 is a board mounted male BNC connector. This way you can still have all the connectors on the two boards.

J5 is a board mounted BNC connector, which provides that 10MHz signal, from the Fury.

J7 is a board mounted BNC connector, which provides that 1PPS signal, from the Fury.

Page 4, Describes the power supply.

The following options are available:

Option 1. HP OCXO is used, +/- 24VDC is provided.

In this case you should build the entire schematic.

Option 2. HP OCXO is used, +/- 24VDC and +/- 12VDC are provided.

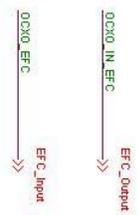
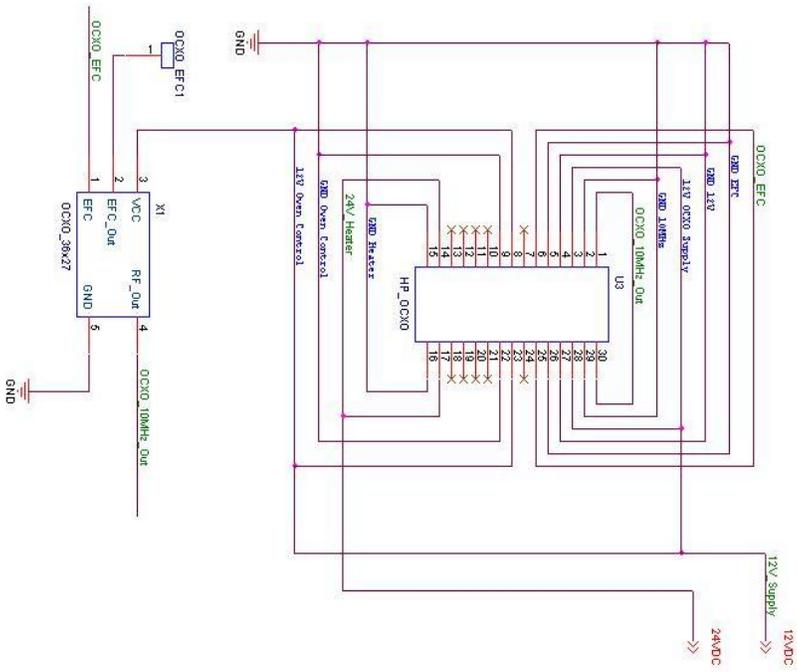
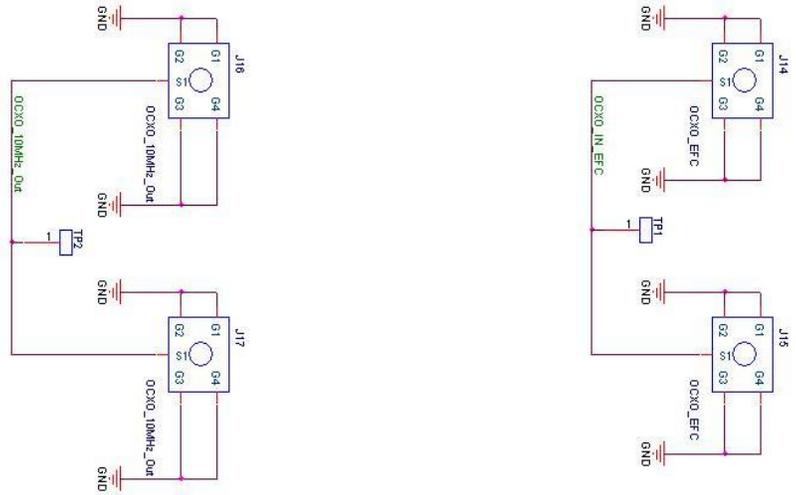
In this case build everything except the two voltage regulators.

Option 3, Non-HP OCXO is used. In this case just provide the +/-12VDC and build everything after the voltage regulators. This includes: C7, C8, D1, C13, C14 and D2.

Optionally, you can build the power indicator circuit.

Page 5, is the circuit used to do the PSPICE modeling of the OPAMP EFC interface.

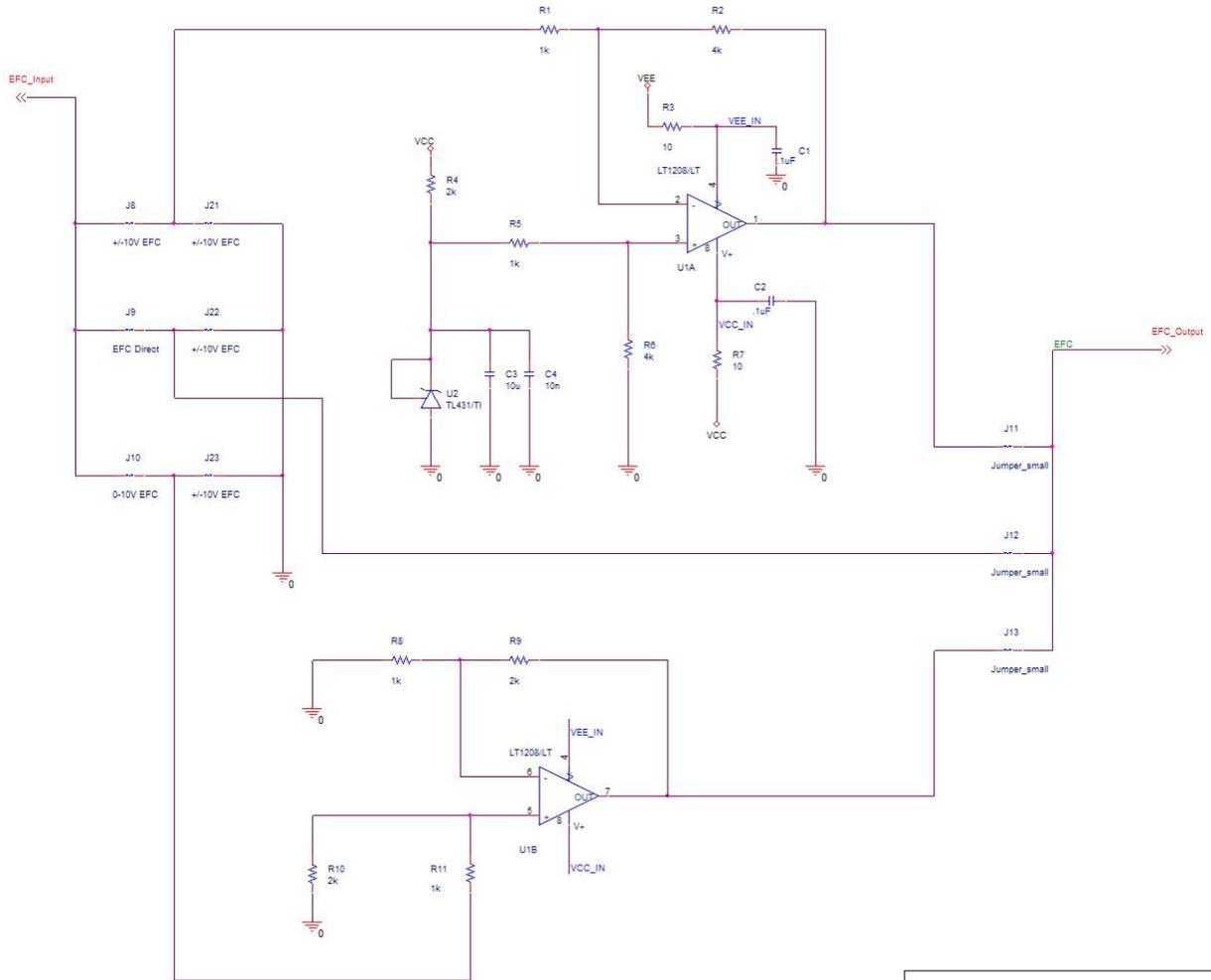
Page 6, is the PSPICE output of the circuit on page 5.



Notes:

Some OCKX0's use Pin2 as EFC_Out. Some have it as NC.

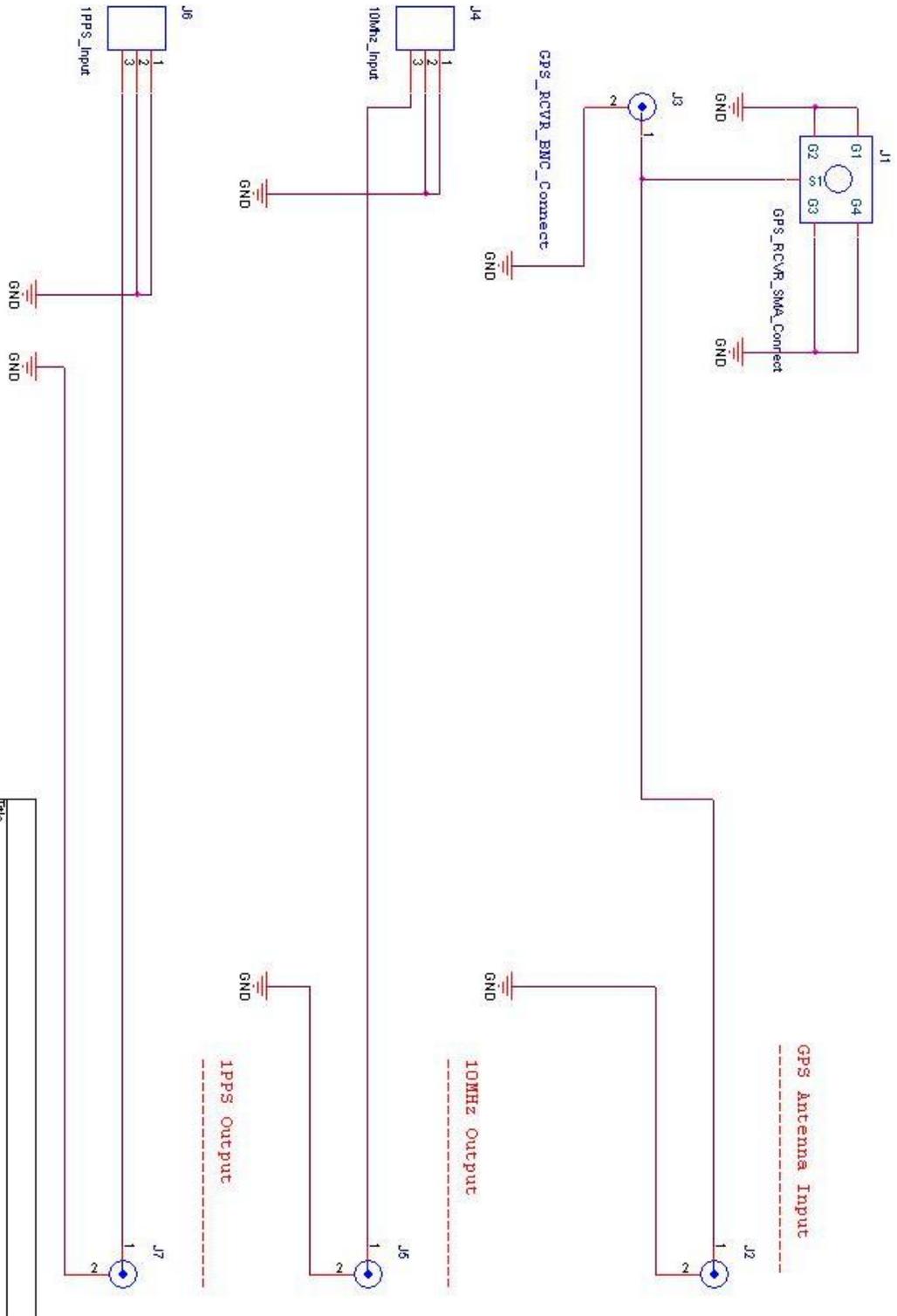
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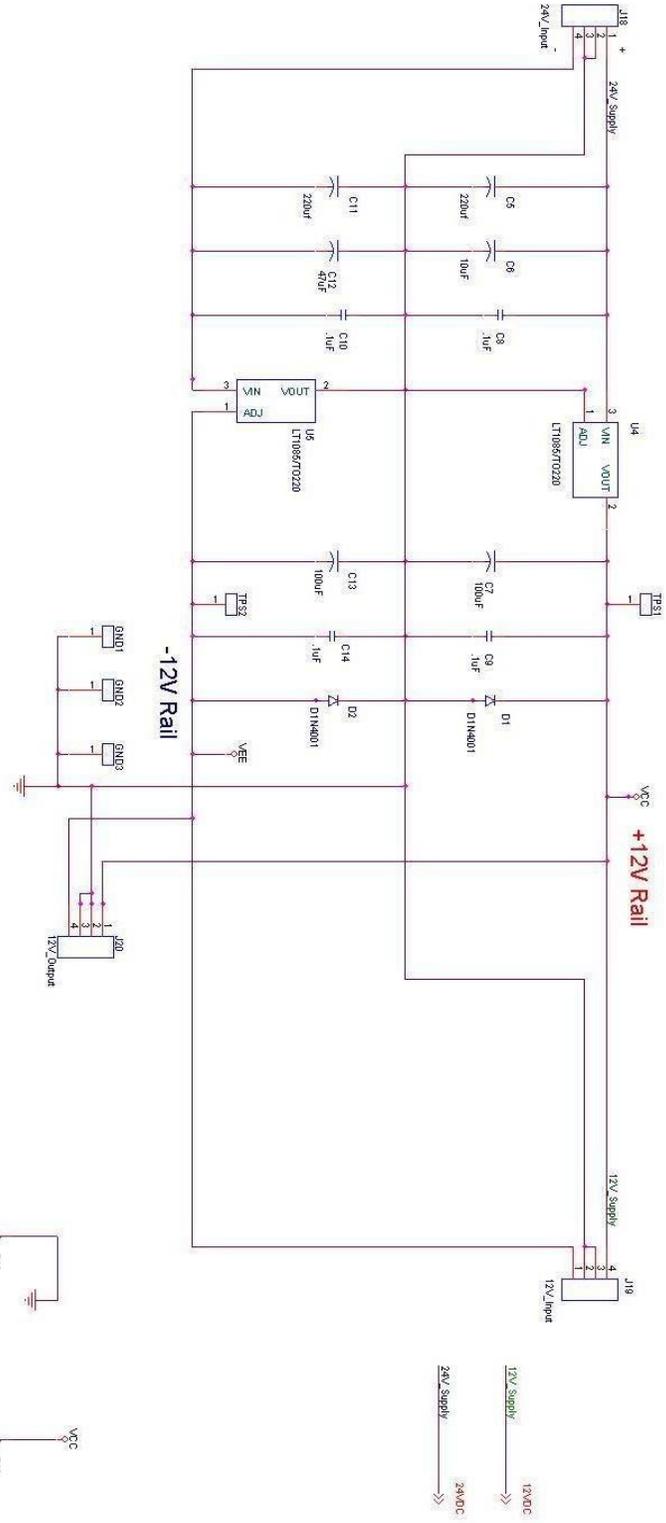
Note:

Input frequency is 10Hz. It approximates a slow moving signal, such as the one from a DAC.

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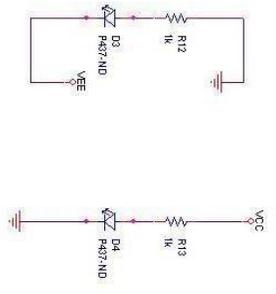


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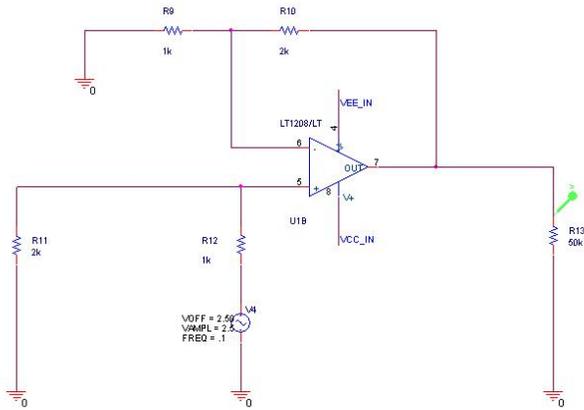
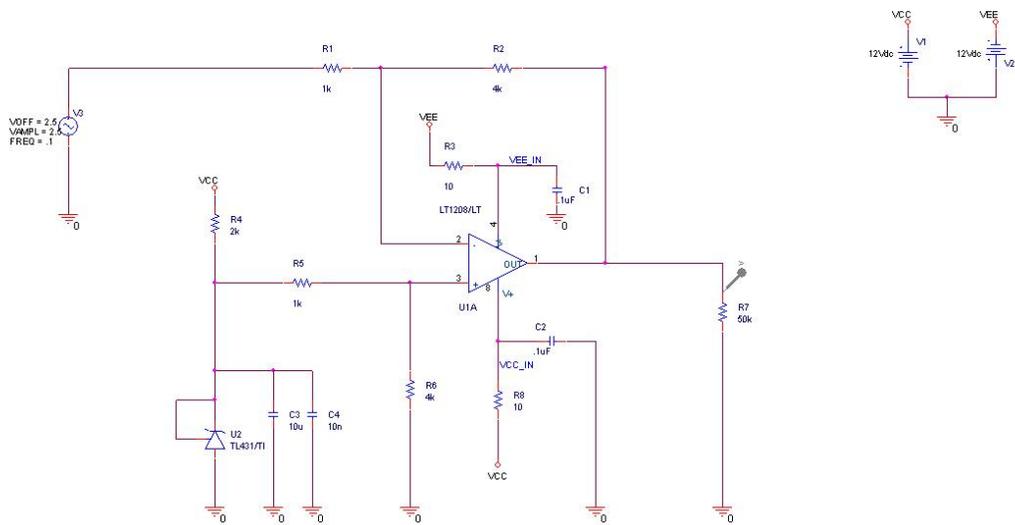


WARNING:

You cannot supply both 12V and 24V. One excludes the other. See user manual for details.
 If a 12V supply is used, then the voltage regulators should not be present



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Note:
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Input frequency is .10Hz. It approximates a slow moving signal, such as the one from a DAC.

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