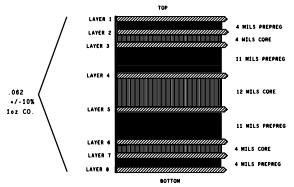


LAYER STACKING

SCALE: NONE

NOTES: UNLESS OTHERWISE SPECIFIED:

1 THIS IS A 8 LAYER BOARD WITH COMPONENTS ON BOTH SIDES.

2 RINTED CIRCUIT BOARD SHALL BE 1 OUNCE COPPER

3 SMT LAND PATTERNS THAT HAVE A FINISHED PAD WIDTH RANGING FROM .012 TO .015 MUST USE THE FOLLOWING TOLERANCE, +.002-.0005. PB VENDOR TO ADJUST ARTWORK TO COMPENSATE FOR ETCH PROCESS.

4 MANUFACTURE/FABRICATE TO MEET EU RoHS DIRECTIVE. ALL MATERIALS AND SOLDERMASK TO BE COMPLIANT TO EU RoHS DIRECTIVE 20002/95/EC

LAMINATE AND RESIN MATERIAL PARAMETERS
Tg > 170 C
Td > 294 C

5 SILKSCREEN BOTH SIDES WITH WHITE NON CONDUCTIVE EPOXY BASED INK

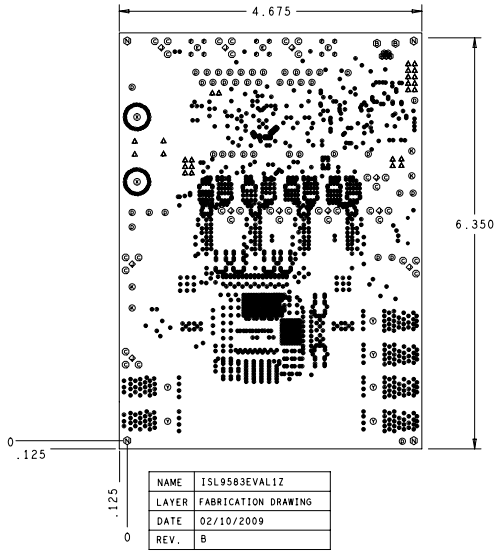
6 IMMERSION GOLD FINISH

7 MINIMUM SPACE .005"/TRACE .008"

8 ALL VIAS ARE TINTED

9 BLUE SOLDER MASK

10 55 OHM CONTROLLED IMPEDANCE-8 MILS LINES: LAYER 6 REFERENCED TO LAYER 7
LAYER 3 REFERENCED TO LAYER 2.



NAME	ISL9583EVAL1Z
LAYER	FABRICATION DRAWING
DATE	02/10/2009
REV.	B

DRILL CHART: TOP to BOTTOM
ALL UNITS ARE IN MILS

FIGURE	SIZE	PLATED	QTY
•	8.0	PLATED	1615
⊕	28.0	PLATED	5
◎	28.0	PLATED	2
⊙	37.0	PLATED	33
◍	40.0	PLATED	42
△	41.0	PLATED	26
◘	50.0	PLATED	3
●	68.0	PLATED	12
◈	93.0	PLATED	11
⊗	100.0	PLATED	4
⊛	155.0	PLATED	2
⊘	200.0	PLATED	6
▪	40.0	NON-PLATED	2
⊠	128.0	NON-PLATED	4

Drawn By: MARGARET SAVIEO	Date Drawn: 2-10-09	Engineer: LIU YA	
Released By:	Date Released:	ISL9583EVAL1Z	
Updated By:	Date Updated:	MASK#	HDWR ID / REV. B
intersil	FILENAME: ISL9583EVAL1ZREVB	SHEET 1 of 1	