

Release Notes for Intel® TXT and BtG BIOS AC 1.13.19 and SINIT AC 1.13.19 for Comet Lake S Platform

April 2020

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Revision History

Date	Revision	Changes
September 10 th , 2019	1.11.1	Initial Release
October 5 th , 2019	1.12.2	WW39 BKC
January 2020	BIOSAC: 1.12.34 & SINIT: 1.12.35	PV Release
January 2020	1.13.0	See Release Notes
February 2020	1.13.11	See Release Notes
March 2020	1.13.15	See Release Notes
March 2020	1.13.16	See Release Notes
March 2020	1.13.17	See Release Notes
March 2020	1.13.18	See Release Notes
April 2020	1.13.19	See Release Notes



ACM Details

Name:	CML_S_BIOSAC_v1.13.19 CML_S_SINIT_v1.13.19
Description:	Startup-ACM for Intel Comet Lake Platforms
Requirements:	Important notes: <ul style="list-style-type: none"> • Must have 0xCA MCU Patch or newer for CML-S
Release Information	
BIOS ACM Rev: 1.13.19	<p>Sighting fixed this revision:</p> <ul style="list-style-type: none"> • Memory unlock failed after clear CMOS with BtG Profile 0T <p>Crashcode Tool:</p> <p>crash_1.3.zip – Password “I accept”</p> <p>Notes:</p> <ul style="list-style-type: none"> • Memory unlock failed after clear CMOS with BtG Profile 0T • Added special handling for profile-0T with CMOS clear and tpmestablishment=0, • ACM will force txtenabled=1, so that IBB can be verified
SINIT ACM Rev: 1.13.19	<p>Sighting fixed this revision:</p> <ul style="list-style-type: none"> • Added more Debug output for Trace ACMs. <p>Crashcode Tool:</p> <p>crash_1.3.zip – Password “I accept”</p>
Name:	CML_S_BIOSAC_v1.13.18 CML_S_SINIT_v1.13.15
Description:	Startup-ACM for Intel Comet Lake Platforms
Requirements:	Important notes: <ul style="list-style-type: none"> • Must have 0xCA MCU Patch or newer for CML-S
Release Information	

BIOS ACM Rev: 1.13.18	<p>Sighting fixed this revision:</p> <ul style="list-style-type: none"> • Fix memory lock down issue <p>Crashcode Tool: crash_1.3.zip – Password “I accept”</p> <p>Notes:</p> <ul style="list-style-type: none"> • If tpmestablishment=0, send LT-CMD-NO-SECRETS before determining txtenabled flag, this will clear BLOCK-MEM-STS which will prevent incorrect determination of RTC clear. • Fixed various other issues related with this to prevent ACM to enter incorrectly into power down flow. Before ACM exit, make sure WAKE-ERROR-STS = 0 to prevent memory lock.
SINIT ACM Rev: 1.13.15	<p>Sighting fixed this revision:</p> <ul style="list-style-type: none"> • ACM forces PS attributes to be same as legacy platform for vendor WA <p>Crashcode Tool: crash_1.3.zip – Password “I accept”</p>
Name:	CML_S_BIOSAC_v1.13.17 CML_S_SINIT_v1.13.15
Description:	Startup-ACM for Intel Comet Lake Platforms
Requirements:	Important notes: <ul style="list-style-type: none"> • Must have 0xCA MCU Patch for CML-S
Release Information	
BIOS ACM Rev: 1.13.17	<p>Sighting fixed this revision:</p> <ul style="list-style-type: none"> • Fixed issues regarding PCR extend • Fixed Measurement issue with profile0T/4T <p>Crashcode Tool:</p> <ul style="list-style-type: none"> • crash_1.3.zip – Password “I accept”



SINIT ACM Rev: 1.13.15	<p>Sighting fixed this revision:</p> <ul style="list-style-type: none"> ACM forces PS attributes to be same as legacy platform for vendor WA <p>Crashcode Tool:</p> <ul style="list-style-type: none"> crash_1.3.zip – Password “I accept”
Name:	<p>CML_S_BIOSAC_v1.13.16</p> <p>CML_S_SINIT_v1.13.15</p>
Description:	Startup-ACM for Intel Comet Lake Platforms
Requirements:	<p>Important notes:</p> <ul style="list-style-type: none"> Must have 0xCA MCU Patch for CML-S
Release Information	
BIOS ACM Rev: 1.13.16	<p>Sighting fixed this revision:</p> <ul style="list-style-type: none"> Fix issue with platform reset during S4 <p>Crashcode Tool:</p> <ul style="list-style-type: none"> crash_1.3.zip – Password “I accept”
SINIT ACM Rev: 1.13.15	<p>Sighting fixed this revision:</p> <ul style="list-style-type: none"> ACM forces PS attributes to be same as legacy platform for vendor WA <p>Crashcode Tool:</p> <ul style="list-style-type: none"> crash_1.3.zip – Password “I accept”
Name:	<p>CML_S_BIOSAC_v1.13.15</p> <p>CML_S_SINIT_v1.13.15</p>
Description:	Startup-ACM for Intel Comet Lake Platforms
Requirements:	<p>Important notes:</p> <ul style="list-style-type: none"> Must have 0xCA MCU Patch for CML-S
Release Information	

BIOS ACM Rev: 1.13.15	<p>Sighting fixed this revision:</p> <ul style="list-style-type: none"> • Adding workaround for Setting PCR18_EXTEND bit based on incoming Policy Digest <p>Crashcode Tool:</p> <ul style="list-style-type: none"> • crash_1.3.zip – Password “I accept”
SINIT ACM Rev: 1.13.15	<p>Sighting fixed this revision:</p> <ul style="list-style-type: none"> • ACM forces PS attributes to be same as legacy platform for vendor WA <p>Crashcode Tool:</p> <ul style="list-style-type: none"> • crash_1.3.zip – Password “I accept”
Name:	CML_S_BIOSAC_v1.13.11 CML_S_SINIT_v1.13.0
Description:	Startup-ACM for Intel Comet Lake Platforms
Requirements:	<p>Important notes:</p> <ul style="list-style-type: none"> • Must have 0xCA MCU Patch for CML-S
Release Information	
BIOS ACM Rev: 1.13.11	<p>Sighting fixed this revision:</p> <ul style="list-style-type: none"> • Observing WinPSEM Error - Minidump - WHEA un correctable error, Bug check 124 , GenuineIntel during S4 / WR cycles <p>Crashcode Tool:</p> <ul style="list-style-type: none"> • crash_1.3.zip – Password “I accept”
SINIT ACM Rev: 1.13.0	<p>Sighting fixed this revision:</p> <ul style="list-style-type: none"> • Updated ACM to support all CML CPUs <p>Crashcode Tool:</p> <ul style="list-style-type: none"> • crash_1.3.zip – Password “I accept”
Name:	CML_S_BIOSAC_v1.13.0



	CML_S_SINIT_v1.13.0
Description:	Startup-ACM for Intel Comet Lake Platforms
Requirements:	Important notes: <ul style="list-style-type: none"> • Must have 0xCA MCU Patch for CML-S
Release Information	
BIOS ACM Rev: 1.13.0	<p>Sighting fixed this revision:</p> <ul style="list-style-type: none"> • Implement SINIT code changes to support PCR18 extend of SINIT SVN value <p>Crashcode Tool:</p> <ul style="list-style-type: none"> • crash_1.3.zip – Password “I accept”
SINIT ACM Rev: 1.13.0	<p>Sighting fixed this revision:</p> <ul style="list-style-type: none"> • Updated ACM to support all CML CPUs <p>Crashcode Tool:</p> <ul style="list-style-type: none"> • crash_1.3.zip – Password “I accept”
Name:	CML_S_BIOSAC_v1.12.34_DBG.bin CML_S_BIOSAC_v1.12.34_PW.bin CML_S_SINIT_v1.12.35_DBG.bin CML_S_SINIT_v1.12.35_PW.bin
Description:	Startup-ACM for Intel Comet Lake Platforms
Requirements:	Important notes: <ul style="list-style-type: none"> • Must have 0xCA MCU Patch for CML-S • Must have this BIOS Change, LINK.
Release Information	

BIOS ACM Rev: 1.12.34	Sighting fixed this revision: <ul style="list-style-type: none"> Updated ACM to support all CML CPUs Crashcode Tool: <ul style="list-style-type: none"> crash_1.3.zip – Password “I accept”
SINIT ACM Rev: 1.12.36	Sighting fixed this revision: <ul style="list-style-type: none"> Updated ACM to support all CML CPUs Crashcode Tool: <ul style="list-style-type: none"> crash_1.3.zip – Password “I accept”
Name:	CML_S_BIOSAC_v1.12.2_DBG.bin CML_S_SINIT_v1.12.2_DBG.bin
Description:	Startup-ACM for Intel Comet Lake Platforms
Requirements:	Important notes: <ul style="list-style-type: none"> N/A
Release Information	
BIOS ACM Rev: 1.12.2	Sighting fixed this revision: <ul style="list-style-type: none"> Updated ACM to support all CML CPUs Crashcode Tool: <ul style="list-style-type: none"> crash_1.3.zip – Password “I accept” Notes: <ul style="list-style-type: none"> Initial Release



SINIT ACM Rev: 1.12.2	Sighting fixed this revision: <ul style="list-style-type: none"> Updated ACM to support all CML CPUs Crashcode Tool: <ul style="list-style-type: none"> crash_1.3.zip – Password “I accept” Notes: <ul style="list-style-type: none"> Initial Release
Name:	CML_S_BIOSAC_v1.11.1_DBG.bin CML_S_BIOSAC_v1.11.1_NPW.bin CML_S_SINIT_v1.11.1_DBG.bin CML_S_SINIT_v1.11.1_NPW.bin
Description:	Startup-ACM for Intel Comet Lake Platforms
Requirements:	Important notes: <ul style="list-style-type: none"> N/A
Release Information	
BIOS ACM Rev: 1.11.1	Sighting fixed this revision: <ul style="list-style-type: none"> N/A. Crashcode Tool: <ul style="list-style-type: none"> crash_1.3.zip – Password “I accept” Notes: <ul style="list-style-type: none"> Initial Release
SINIT ACM Rev: 1.11.1	Sighting fixed this revision: <ul style="list-style-type: none"> N/A. Crashcode Tool: <ul style="list-style-type: none"> crash_1.3.zip – Password “I accept” Notes: <ul style="list-style-type: none"> Initial Release

