Video Bios Release Notes Icelake Build# 14.0.1003

Nov 20, 2018

INTEL CONFIDENTIAL

INFORMATION IN THIS DOCUMENT IS PROVIDED IN CONNECTION WITH INTEL PRODUCTS. NO LICENSE, EXPRESS OR IMPLIED, BY ESTOPPEL OR OTHERWISE, TO ANY INTELLECTUAL PROPERTY RIGHTS IS GRANTED BY THIS DOCUMENT. EXCEPT AS PROVIDED IN INTEL'S TERMS AND CONDITIONS OF SALE FOR SUCH PRODUCTS, INTEL ASSUMES NO LIABILITY WHATSOEVER AND INTEL DISCLAIMS ANY EXPRESS OR IMPLIED WARRANTY, RELATING TO SALE AND/OR USE OF INTEL PRODUCTS INCLUDING LIABILITY OR WARRANTIES RELATING TO FITNESS FOR A PARTICULAR PURPOSE, MERCHANTABILITY, OR INFRINGEMENT OF ANY PATENT, COPYRIGHT OR OTHER INTELLECTUAL PROPERTY RIGHT.

UNLESS OTHERWISE AGREED IN WRITING BY INTEL, THE INTEL PRODUCTS ARE NOT DESIGNED NOR INTENDED FOR ANY APPLICATION IN WHICH THE FAILURE OF THE INTEL PRODUCT COULD CREATE A SITUATION WHERE PERSONAL INJURY OR DEATH MAY OCCUR.

Intel may make changes to specifications and product descriptions at any time, without notice. Designers must not rely on the absence or characteristics of any features or instructions marked "reserved" or "undefined." Intel reserves these for future definition and shall have no responsibility whatsoever for conflicts or incompatibilities arising from future changes to them. The information here is subject to change without notice. Do not finalize a design with this information.

This document contains information on products in the design phase of development.

The products described in this document may contain design defects or errors known as errata, which may cause the product to deviate from published specifications. Current characterized errata are available on request.

Contact your local Intel sales office or your distributor to obtain the latest specifications and before placing your product order.

Intel is a trademark of Intel Corporation in the U.S. and other countries.

*Other names and brands may be claimed as the property of others. Copyright © 2018, Intel Corporation. All rights reserved.

Video BIOS release contains support for the following Intel Chipsets/Processors:

• Icelake

Revision History

Date	Build Number	Description
Nov 20, 2018	14.0.1003	Alpha
Oct 26, 2018	14.0.1002	Pre-Alpha
Sept 11 , 2018	14.0.1001	Pre-Alpha

Change Logs

Change Logs			
DCN #	BUG/RCR	DCN Title	Version
[CCN][1606735892]	NA	Verify and Update DP enabling sequence	14.0.1003
[CCN][1606732231]	NA	Code clean up	14.0.1003
[CCN][1606730722]	NA	Re-design the display enumeration functionality in ICL VBIOS	14.0.1003
[CCN][1606730638]	NA	Correction to link training sequence	14.0.1003
[CCN][1606709317]	NA	Modify the 8254 chip based delay to DE time stamp counter.	14.0.1002
[CCN][1606707615]	NA	Remove obsolete VBT blocks as per the VBT Spec 223	14.0.1002
[CCN][1606699389]	NA	Fix for VBE "4F11, 01- Return Flat Panel Information" Causing error during KVMR session enabled without EDP connected	14.0.1002
[CCN][1606683483]	NA	Adding changes as per VBT spec to align with GOP Changes done from 210 to 220	14.0.1002
[CCN] [1606661560]	NA	Bug fixed for EDP 1.4/1.2 panel	14.0.1001
[CCN] [1606470624]	NA	Display blank out in Clone mode	14.0.1001
[CCN] [1606472237]	NA	Display corruption observed after executing DIR and CLS command in DOS.	14.0.1001
[CCN] [1506653761]	NA	System Hang and error observed in case of warm boot.	14.0.1001

[CCN] [1606069800]	NA	32Bit MMIO access in DOS.	14.0.1001
[CCN][1605462408]	NA	Combo PHY changes for EDP	14.0.1001
[CCN] [1605281755]	NA	Verify and Update HDMI1.4 enabling sequence	14.0.1001
[CCN] [1604578452]	NA	Generic PLL programming	14.0.1001

Known Issues

1	Hot plug in DOS OS will not work	
2	Dynamic CD CLK feature is not added	

Unsupported features

1	No display support on Type-C port	
2	2 No Display on MG PHY ports	