

INITIAL/LOGO			ABBREVIATIONS		
	LVL	LPF Switch for Auto Focus	P	P SW	Power Switch
M	M1-3	Motor Coil Terminal 1 to 3		PB1-3	PNP Base 1-3
	MA0-5	Microprocessor Address Data 0-5		PBCTL	Play Back Control
	Mbps	Megahertz Bit Per Second		PBCTL	Pre-Branking Control
	MD	Modulation		PBH	Head Amp Switch
	MD0-7	Microprocessor Data 0-7		PBLK	Pre-Blanking (Pulse)
	MDT0-7	Microprocessor Data 0-7		PC1-3	Corrector of PNP Transistor
	ME (TAPE)	Metal Evaporated (Tape)		PCBM	Carrier Balance
	MENB	Focus Motor Enable		PCH	Phase Compensator (Hall AMP)
	MFF	Manual Focus Far		PCI	Phase Compensator (Current)
	MFN	Manual Focus Near		PCO	Phase Compensator Out
	MHSYNC	Monitor Horizontal Sync Signal		PCS	Switching Power Control
	MIC	Memory In Cassette		PCV	Phase Compensator (Voltage)
	MIG	Meta In Gap		PE	Emitter of PNP Transistor
	MIX N.R.D.	Non Rec Data Mix		PED	Pedestal
	MOD	Modulation		PEDECNT	Pedestal Control
	MOUT	Mic Out		PENO	Alarm (L)
	MP (TAPE)	Metal Particle (Tape)		PFP	Pilot Frame Position
	MPEG	Moving Picture Image Cording Experts Group		PGA, B	Power Ground A, B
	MPEG2	Moving Picture Image Cording Experts Group Phase 2		PGC	Pulse Generator Comparator
	MRST	Focus Motor Reset		PGI	Pulse Generator Input
N	MSB	Most Signal Bit		PGMM	Pulse Generator Monostable Multivibrator
	MVSYNC	Monitor Vertical Sync Signal		PGO	Output of Pulse Generator AMP
	N/F	Near/Far Focus		PMODE	Select Signal for Normal / Wide Screen
	N/P	NTSC/PAL		PON	Power On
	NB1-3	Base for NPN Transistor		POR	Power On Reset
	NC	No Connection		POSCOM	Common Position
	NC1-3	Corrector of NPN Transistor		PREAMP	Pre-AMP
	NCLR	Power On Reset		PREBLK	Pre-Blanking
	NCP1	Clamp Pulse		PT	Protect for V Voltage
	NCP2+VDH	Clamp Pulse + Horizontal Drive Pulse		PWM	Pulse Width Modulation
	NCP2+VDM	Clamp Pulse + Gate Pulse		PWMB	Pulse Width Modulation Pulse
	NDE	Non Liner De-Emphasis	Q	Q2H	Source Output Select
	NE	Emitor of NPN Transistor			
	NLE	Non Liner Emphasis	R	R CTL P	Recorded Control Pulse (+)
	NR	Noise Reduction		R CTL R	Recorded Control Pulse (–)
	NRD	Non Rec Data		R/B	Read/Busy
	NRD BLK	Non Rec Data Blanking		R/L	Direction Control for Data Transmition
	NRD CLK	No Rec Data Clock		RA	Recording AMP
	NRE	Read Enable Input (Low Active)		RA1	Rec AMP 1
O	NWE	Write Enable (Low Active)		RAC AC	Rec Audio Current
	OB	Optical Black		RAD	Read Address Data
	OBCNT	Optical Black Control		RAE	Read Address Enable
	OBREF	Reference Voltage for Optical Black Control		RB	Read Busy
	OE	Output Enable		R-B	R Bias
	OFH	Horizontal Counted Down Clock Signal (Reference)		RCB	R Carrier Balance
	OFS	Offset		RE	Read Enable
	OP	Operation AMP Output		RE(F), (S)	Rotary Erase Head Transformer
	OSD	ON Screen Display		REB	R Bias
	OVL	Overlap Pulse		REC CC	Rec Current Control
	OZ	Optical Zoom		REC CCNT	Rec Current Control
				RECCTRL	Recording Control Pulse
				RECI	Rec Amp Switch