

| INITIAL/LOGO | | ABBREVIATIONS | INITIAL/LOGO | | ABBREVIATIONS |
|--------------|-------------|---|--------------|-----------------|--|
| | CRA | Pre Aperature Gain Control | | DISCS | Dis Chip Select |
| | CRST | Camera Reset | | DISP | Display |
| | CS | Chip Select | | DL | Delay Line |
| | CS 0-7 | Chrominance Signal Out 0-7 | | DOBCK | Audio A/D Converter Bit Clock |
| | CSEL | Clock Phase Select | | DOCTL | Data Output Control Signal |
| | CSI 0-7 | Chrominance Signal In 0-7 | | DODAT | Serial Data (to D/A Converter) |
| | CTSW | Crosstalk Switch | | DOLRCK | Audio A/D Converter LR Clock |
| | CURR | Current | | DOLRCK | L/R Clock (to D/A Converter) |
| | CW | Clockwise | | DOMCK | Audio A/D Converter Master Clock |
| | CYL EC | Cylinder Motor Trque Control | | DOMCK | Master Clock (to D/A Converter) |
| | CYL PG | Cylinder Motor PG | | DQ 1-16 | Memory Data |
| | CYL VM | Cylinder Motor Current or Power | | DRAM CAS | D-RAM Colum Address Strobe |
| D | D CLK | Digital Clock | | DRAM OE | D-RAM Out Enable |
| | D MODE | Digital Mode Switch Signal | | DRAM RAS | D-RAM Read Address Strobe |
| | D01-03 | Zoom 01-03 | | DREC | AV Delayed REC Start Pulse |
| | DA UV SEL | D/A Convertor U/V Select | | DRK | Dark (LPF Switch for Auto Focus) |
| | DAC | Digital Analogue Converter | | DS1, 2 | Double Sampling Pulse |
| | DAG | Digital Analogue Ground | | DSF 0-7 | Data In/Out for Shaffling Memory |
| | DB0-7 | Data 0-7 | | DSF 0-7 | Input/Output Data to Shuffling Memory (18MHz) |
| | DB0-7 | Microprocessor Data | | DSP | Digital Signal Processor |
| | DCC | DC Clamp Control | | DSP R/B | DSP IC Rady/Busy |
| | DCCNT | DC Control | | DSP-48K-H | DSP IC Clock Select |
| | DCI | Digital Channel Cording IC | | DSTB | Data Stobe Signal |
| | DCLR | Digital Clear | | DSV | Digital Sum Variation |
| | DCP | Digital Clamp Pulse | | DV | Digital Video |
| | DCS-CLK, DA | CAS & DV I/F Serial Clock | | DVB | Digital Video Broadcast |
| | DC-STP1 | DCS Serial Start | | DVC | Digital Video Cassette |
| | DC-STP2 | DCS Serial Stop | | DVDD | Digital VDD |
| | DCT | Discrete Cosine Transform (Compression) | | DVIO | Digital Video Input Output |
| | DCX7 | Serial Data | | DVSS | Digital Ground |
| | DEDP 0-3 | Playback Data | | DX | Shift Data for X Direction (for LCD) |
| | DEDR 0-3 | Rec Data | | DY | Shift Data for Y Direction (for LCD) |
| | DEMO | Demodulation | | DY | TFT Y-axis Shift Data |
| | DEMP | A/D Convertor Empahsis Control | | DZ | Digital Zoom |
| | DEMP | De-Emphasis | E | E Snap | Electric Snap Shot |
| | DFD 0-7 | Encode Data In/Out Between Shaffling Memory | | E ZM | Electric Zoom |
| | DFD0-7 | Encode Input/Output Signal for Shuffling Memory | | E2 CS or E2P CS | EEPROM Chip Select |
| | DIBDCK | Bit Clock | | E2 R/B | EEPROM Rady/Busy |
| | DICLK | Digital Clock | | E2P | EEPROM |
| | DIDAT | Serial Data | | EARP | Earphone |
| | DIDAT | Serial Data Durring Digital Audio In | | EC | Torque Control |
| | DIF | Digital Interface | | ECC | Error Correction Cording |
| | DILRCK | L/R Clock | | ECM | Electric Condencer Mic |
| | DILRCK | Serial Clock Durring Digital Audio In | | ECR | Reference Voltage for Capstan Torque |
| | DIMCK | Master Clock | | EDA | Error Correction, DCI, ATF Servo |
| | DIMCK | Mater Clock Durring Digital Audio In | | EE CS | EEPROM Chip Select |
| | DIO 1-8 | Data In/Out | | EE R/B | EEPROM Read (H)/Busy (L) |
| | DIOS | Data In/Out Select Control Signal | | EEPROM | Electric Erasable Programable Read Only Memory |
| | DIOS | Select Signal for Digital In/Out | | EIS | Electric Image Stabilizer (DIS) |
| | DIS | Digital Image Stabilizer | | EMP | A/D Convertor Emphasis Control |
| | DIS R/B | Digital Image Stabilizer Read (H)/Busy (L) | | ENAB | Enable |
| | DIS R/B | DIS IC Rady/Busy | | ENV | Enverope |
| | DIS/KAND | Digital Image Stabilizer/Sensitivity | | EOB | End of Block |