

**346.**—A. H. STRoud & DON SECREST, "Approximate Integration Formulas for Certain Spherically Symmetric Regions," *Math. Comp.*, v. 17, 1963 p. 105–135.

The following errors have been noted on page 121 in Table 4, "Quadrature Formulas for the Integral  $\int_{-\infty}^{\infty} |r|^{n-1} e^{-r^2} f(r) dr$ ."

<i>n</i>	<i>h</i>	row	column	<i>for</i>	<i>read</i>
3	8	1	1	... 187(-1)	... 188(-1)
3	16	1	2	... 806(-1)	... 810(-1)
3	16	2	2	... 719(-1)	... 714(-1)
3	16	3	2	... 853(-1)	... 854(-1)
3	16	4	2	... 162(-2)	... 160(-2)
3	16	6	2	... 012(-5)	... 009(-5)
3	16	7	2	... 067(-7)	... 069(-7)
3	16	8	2	... 564(-9)	... 563(-9)

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## NOTE

### NEW JOURNAL

The Information Processing Society of Japan has announced the annual publication, beginning in 1961, of an English-language compilation of selected papers from bimonthly Japanese journals devoted to information processing. The new journal is entitled *Information Processing in Japan*. The publisher's address is c/o JEIDA, 35 Nishikubo-tomoecho, Minato-ku, Tokyo.

The first issue contains the following papers:

- T. Kasami: Systematic Codes Using Binary Shift Register Sequences
- T. Kasami: A Systematic Code for Non-Independent Errors
- J. Baba & S. Hayashi: Evaluation of Errors at [sic] Numerical Integration of Ordinary Differential Equations
- H. Takahashi & Y. Ishibashi: A New Method for "Exact Calculation" by a Digital Computer (An Application of Modulo  $p$  Arithmetics)
- T. Norimatsu & T. Deido: Investigation of Error Accumulation in Runge-Kutta Integration Process by Circle Test
- M. Takata: The Programmed Digital Differential Analyzer
- M. Hosaka: On Block Operations Using Delay Lines
- K. Fuchi & H. Nishino: System Design of ETL MK-4B, an Input-Output Computer
- S. Muroga, K. Takashima, I. Toda & M. Yamada: The Magnetic Tape Device for the Parametron Digital Computer M-1
- T. Deido, M. Ito & T. Norimatsu: Quasi-Optimum Automatic Design for a Feed-back Control System by Use of the Digital Computer
- K. Fuchi & H. Nishino: Automatic Data Processing in the Wiring of a Digital Computer
- K. Mori: Simulation Experiment for Japanese Economy: 1953–1957.